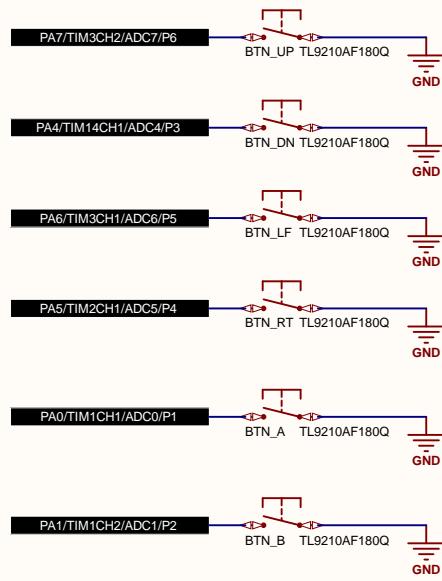
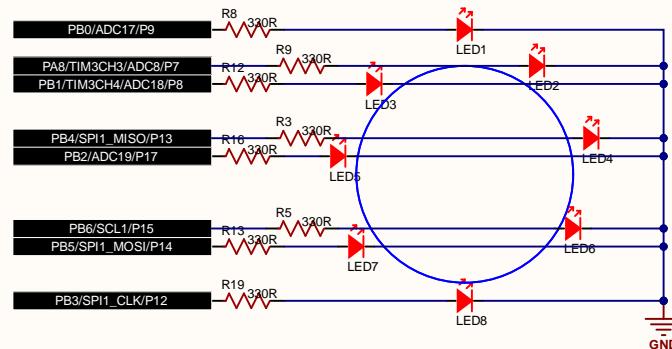


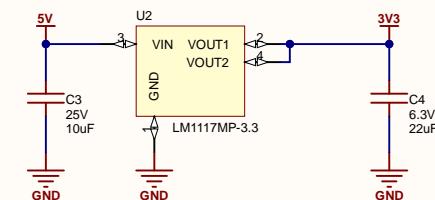
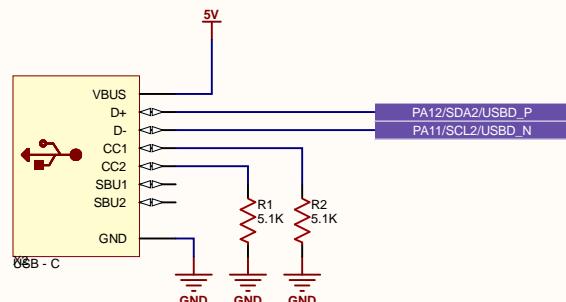
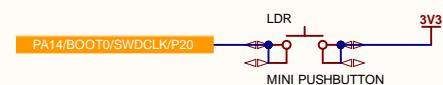
A



B



C



D

Title: *Controller*

Part #: GDL-SPCTRLR-B

Revision: B

Date: 12/4/2025

Sheet 1 of 4



A

A

B

B

C

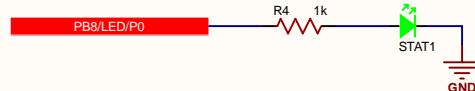
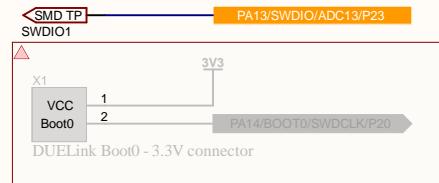
C

D

D

PB8/LED/P0
PA0/TIM1CH1/ADC0/P1
PA1/TIM1CH2/ADC1/P2
PA4/TIM14CH1/ADC4/P3
PA5/TIM2CH1/ADC5/P4
PA6/TIM3CH1/ADC6/P5
PA7/TIM3CH2/ADC7/P6
PA8/TIM3CH3/ADC8/P7
PB1/TIM3CH4/ADC18/P8
PB0/ADC17/P9
PC15/P10
PC6/TIM2CH3/P11
PB3/SPI1_CLK/P12

PB4/SPI1_MISO/P13
PB5/SPI1_MOSI/P14
PB6/SCL1/P15
PB7/SDA1/P16
PB2/ADC19/P17
PB9/P18
PA15/P19
PA14/BOOT0/SWDCLK/P20
PA9/UART1_TX/P21
PA10/UART1_RX/P22
PA13/SWDIO/ADC13/P23
PC14/P24
PA2/UART2_TX/P25
PA3/UART2_RX/P26
NRST/PF2/P27



Title: MISC

Part #: N/A

Revision: B

Date: 12/4/2025 Sheet 2 of 4



A

## Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

\* P1, P2: TIM1

\* P3: TIM14

\* P4, P11: TIM2

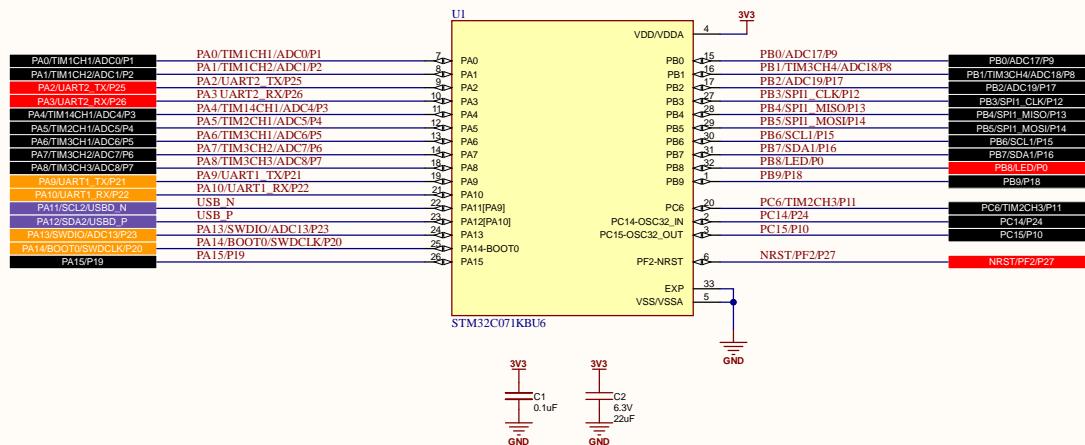
\* P5, 6, 7, 8: TIM3

SW UART: Pins 1 RX, 2 TX

Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

B

UART1 is available when not used  
in UpLink

C

D

A

A

B

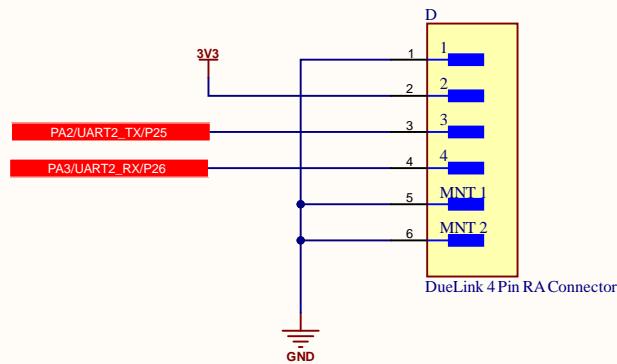
B

C

C

D

D

Title: *DUELink Downlink*

Part #: N/A

Revision: A

Date: 12/4/2025

Sheet 4 of 4


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 electronics  
Where Hardware Meets Software