

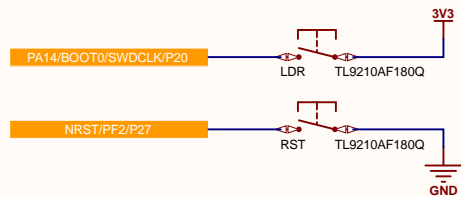
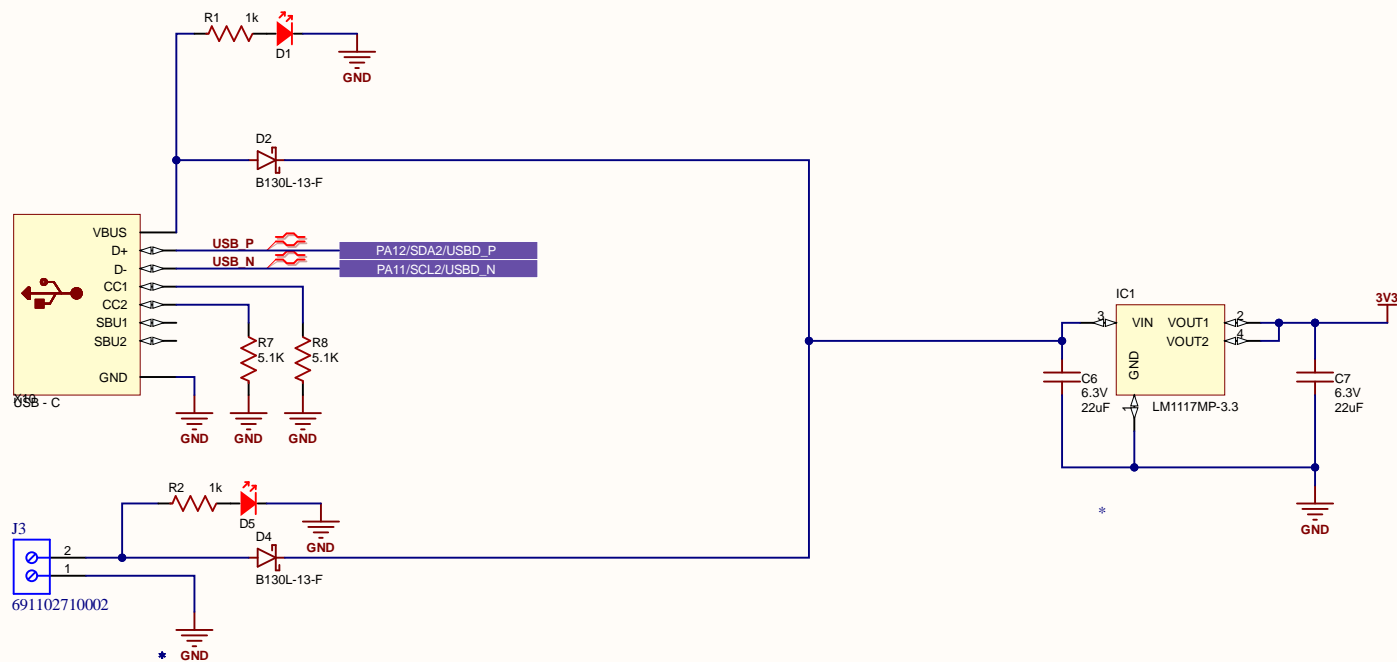
Table 6: Chip Boot Mode Control

Boot Mode	GPIO0	GPIO2
SPI Boot Mode	1	Any value
Joint Download Boot Mode <sup>2</sup>	0	0

- <sup>1</sup> Bold marks the default value and configuration.  
<sup>2</sup> Joint Download Boot mode supports the following download methods:
- SDIO Download Boot
  - UART Download Boot

Table 5: ESP32-PICO Series Hardware Connection Pinout

Function of Connection	ESP32 Board or Module Pins	Other Device Pins
Download/Log output <sup>1</sup>	<b>UART0</b> <ul style="list-style-type: none"> <li>• GPIO3 (RX)</li> <li>• GPIO1 (TX)</li> </ul>	<b>PC</b> <ul style="list-style-type: none"> <li>• TX</li> <li>• RX</li> </ul>
AT command/response <sup>2</sup>	<b>UART1</b> <ul style="list-style-type: none"> <li>• GPIO19 (RX)</li> <li>• GPIO22 (TX)</li> <li>• GPIO15 (CTS)</li> <li>• GPIO14 (RTS)</li> </ul>	<b>USB to serial converter</b> <ul style="list-style-type: none"> <li>• TX</li> <li>• RX</li> <li>• RTS</li> <li>• CTS</li> </ul>



1

2

3

4

A

A

B

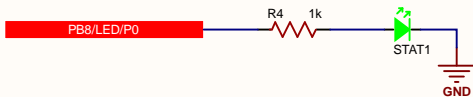
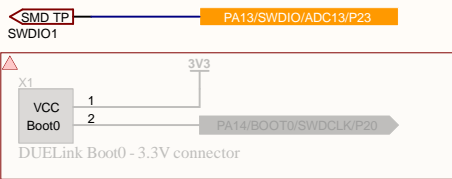
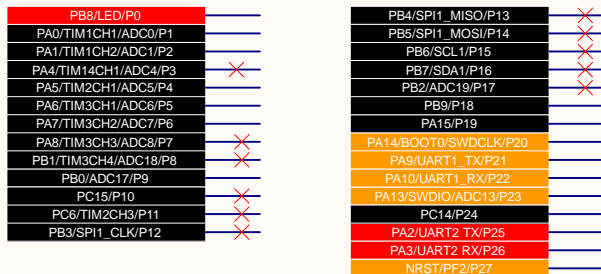
B

C

C

D

D



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil

Title: <i>MISC</i>		
Part #: N/A		
Revision: B	Date: 9/23/2025	Sheet 3 of 5

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Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

\* P1, P2: TIM1

\* P3: TIM14

\* P4, P11: TIM2

\* P5,6,7,8: TIM3

SW UART: Pins 1 RX, 2 TX

Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

UART1 is available when not used in Uplink

