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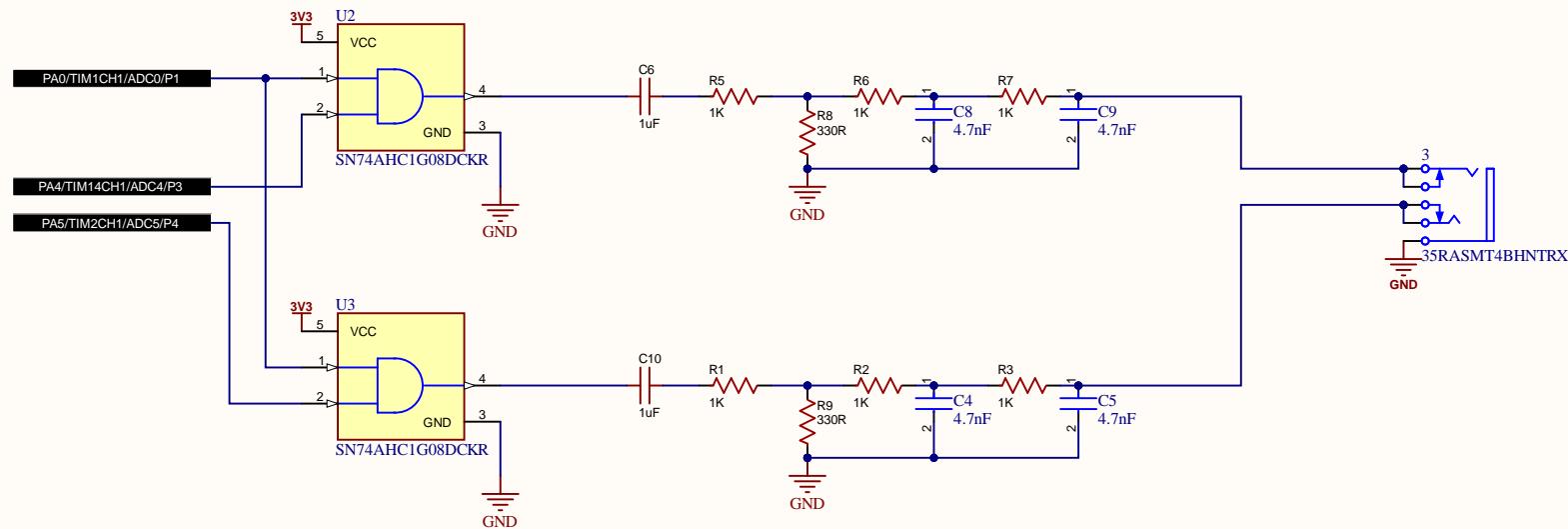
B

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D



A

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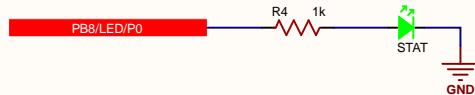
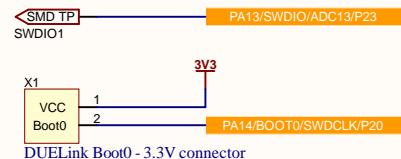
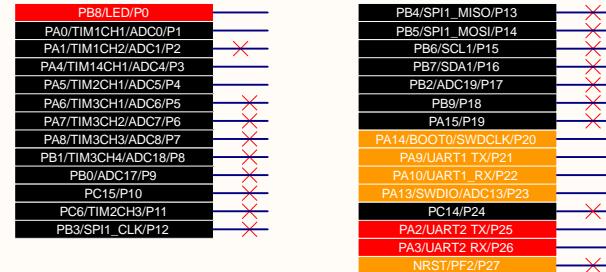
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Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil

Title: MISC

Part #: N/A

Revision: A

Date: 12/9/2025

Sheet 2 of 5



A

Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

* P1, P2: TIM1

* P3: TIM14

* P4, P11: TIM2

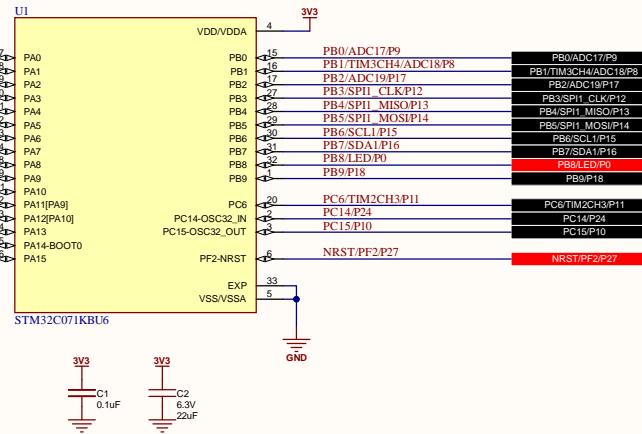
* P5, 6, 7, 8: TIM3

SW UART: Pins 1 RX, 2 TX

Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

B



UART1 is available when not used in UpLink

C

PA0/TIM1CH1/ADC0/P1	PA0/TIM1CH2/ADC1/P2	PA1/TIM1CH2/ADC1/P2
PA2/UART2_RX/P25	PA2/UART2_RX/P25	PA3/UART2_RX/P26
PA3/UART2_RX/P26		
PA4/TIM1CH1/ADC4/P3	PA4/TIM1CH1/ADC4/P3	PA4/TIM1CH1/ADC4/P3
PA5/TIM2CH1/ADC5/P4	PA5/TIM2CH1/ADC5/P4	PA5/TIM2CH1/ADC5/P4
PA6/TIM3CH1/ADC6/P5	PA6/TIM3CH1/ADC6/P5	PA6/TIM3CH1/ADC6/P5
PA7/TIM3CH2/ADC7/P6	PA7/TIM3CH2/ADC7/P6	PA7/TIM3CH2/ADC7/P6
PA8/TIM3CH3/ADC8/P7	PA8/TIM3CH3/ADC8/P7	PA8/TIM3CH3/ADC8/P7
PA9/UART1_RX/P21	PA9/UART1_RX/P21	PA9/UART1_RX/P21
PA10/UART1_RX/P22	PA10/UART1_RX/P22	PA10/UART1_RX/P22
PA11/SC12/USBD_N	PA11/SC12/USBD_N	PA11/PA9]
PA12/SC12/USBD_P	PA12/SC12/USBD_P	PA12/PA10]
PA13/SWDIO/ADC13/P23	PA13/SWDIO/ADC13/P23	PA13/PA13]
PA14/BOOT0/SWDCLK/P20	PA14/BOOT0/SWDCLK/P20	PA14/PA14-BOOT0
PA15/P19	PA15/P19	PA15/PA15]

D

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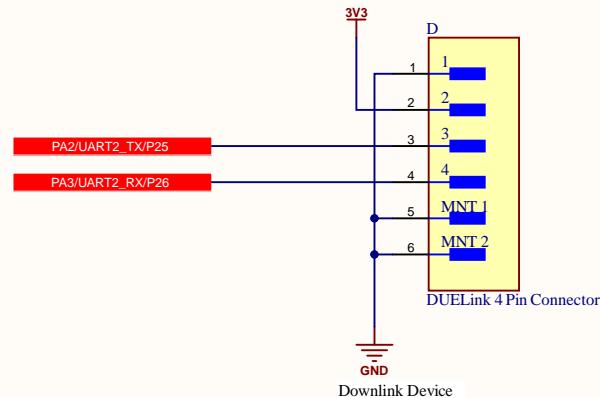
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Title: *DUELink Downlink*

Part #: N/A

Revision: A Date: 12/9/2025 Sheet 4 of 5


GHI
 electronics
Where Hardware Meets Software

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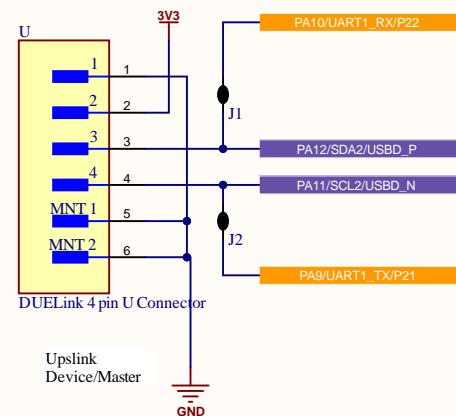
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Title: *DUELink Uplink*

Part #: N/A

Revision: A

Date: 12/9/2025

Sheet 5 of 5

