

Table 6: Chip Boot Mode Control

Boot Mode	GPIO0	GPIO2
SPI Boot Mode	1	Any value
Joint Download Boot Mode ²	0	0

- ¹ Bold marks the default value and configuration.
² Joint Download Boot mode supports the following download methods:
- SDIO Download Boot
 - UART Download Boot

Table 5: ESP32-PICO Series Hardware Connection Pinout

Function of Connection	ESP32 Board or Module Pins	Other Device Pins
Download/Log output ¹	UART0 <ul style="list-style-type: none"> • GPIO3 (RX) • GPIO1 (TX) 	PC <ul style="list-style-type: none"> • TX • RX
AT command/response ²	UART1 <ul style="list-style-type: none"> • GPIO19 (RX) • GPIO22 (TX) • GPIO15 (CTS) • GPIO14 (RTS) 	USB to serial converter <ul style="list-style-type: none"> • TX • RX • RTS • CTS

1

2

3

4

A

A

B

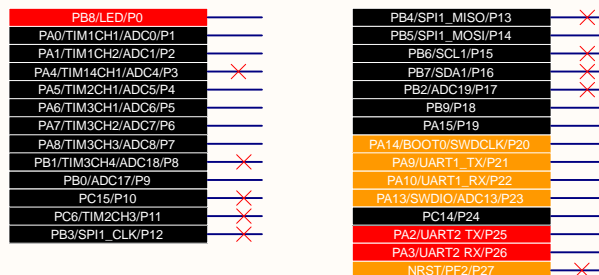
B

C

C

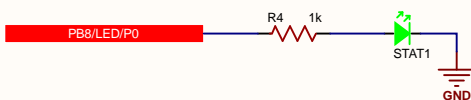
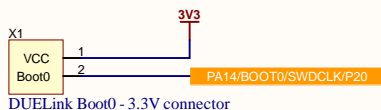
D

D



SMD TP
SWDIO1

PA13/SWDIO/ADC13/P23



FID1
Fiducial 40 Mil



FID2
Fiducial 40 Mil



FID3
Fiducial 40 Mil



FID4
Fiducial 40 Mil



FID5
Fiducial 40 Mil



FID6
Fiducial 40 Mil

Title: *MISC*

Part #: N/A

Revision: N/A

Date: 10/8/2025

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Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

- * P1, P2: TIM1
- * P3: TIM14
- * P4, P11: TIM2
- * P5, 6, 7, 8: TIM3

SW UART: Pins 1 RX, 2 TX

Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

UART1 is available when not used in Uplink





