

A

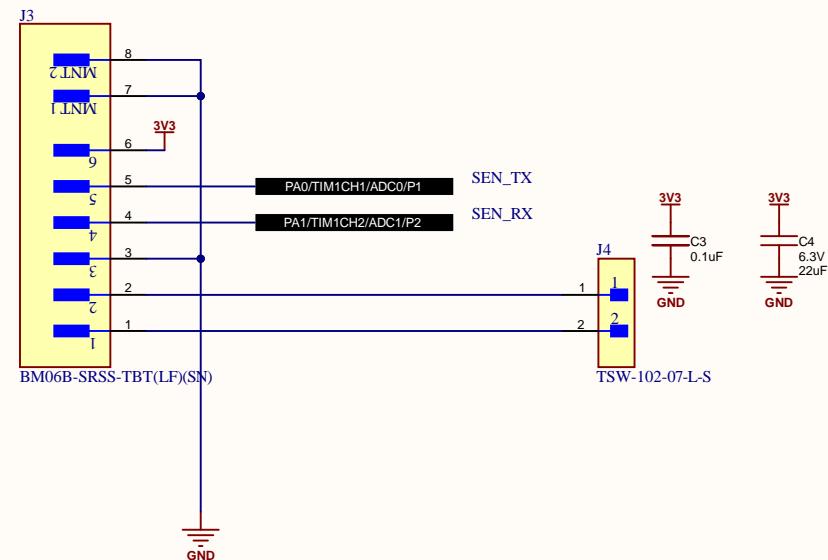


Interface Definition (1.0mm 6pin)

No.	Definition	Instruction
1	VCC	Power input 3.3V
2	TXD	Serial port send signal (TTL Level)
3	RXD	Serial port receiver signal (TTL Level)
4	GND	Signal ground
5	D-	USB data input
6	D+	USB data output

1 6

Note:
The line order has nothing to do with color.



A

A

B

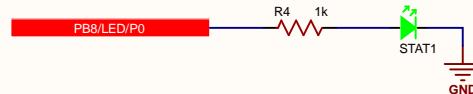
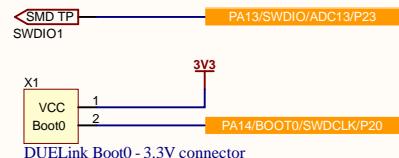
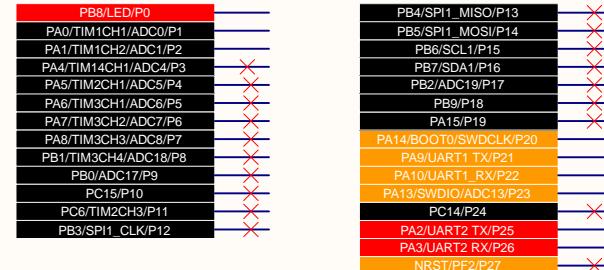
B

C

C

D

D



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil



Fiducial 40 Mil

Title: MISC

Part #: N/A

Revision: A

Date: 11/5/2025

Sheet 2 of 5



A

Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

* P1, P2: TIM1

* P3: TIM14

* P4, P11: TIM2

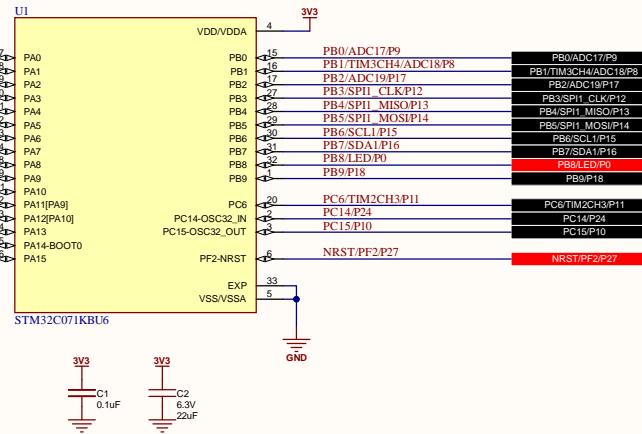
* P5, 6, 7, 8: TIM3

SW UART: Pins 1 RX, 2 TX

Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

B



UART1 is available when not used in UpLink

C

D

A

A

B

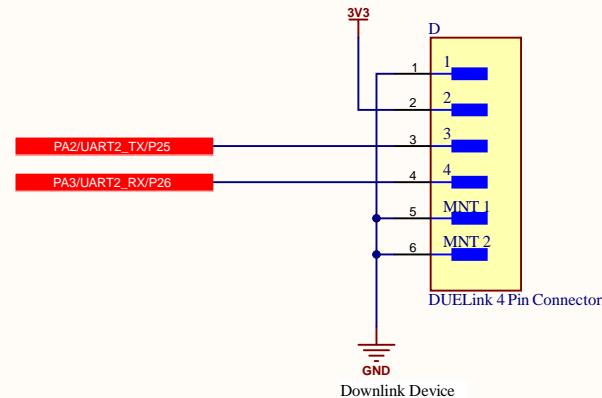
B

C

C

D

D

Title: *DUELink Downlink*

Part #: N/A

Revision: A

Date: 11/5/2025

Sheet 4 of 5



A

A

B

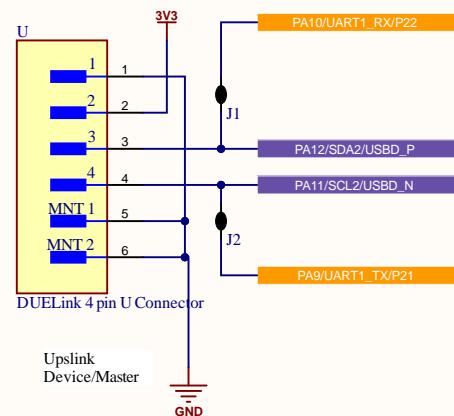
B

C

C

D

D

Title: *DUELink Uplink*

Part #: N/A

Revision: A Date: 11/5/2025 Sheet 5 of 5


GHI
 electronics
Where Hardware Meets Software