

Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

* P1, P2: TIM1

* P3: TIM14

* P4, P11: TIM2

* P5,6,7,8: TIM3

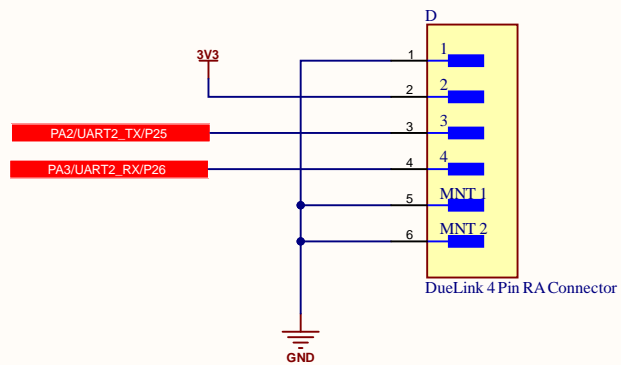
SW UART: Pins 1 RX, 2 TX



Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

UART1 is available when not used in Uplink





Title: <i>DUELink Downlink</i>				 Where Hardware Meets Software
Part #: N/A				
Revision: A	Date: 9/25/2025	Sheet 4 of 5	©2025 GHI Electronics, LLC - Michigan, USA	

