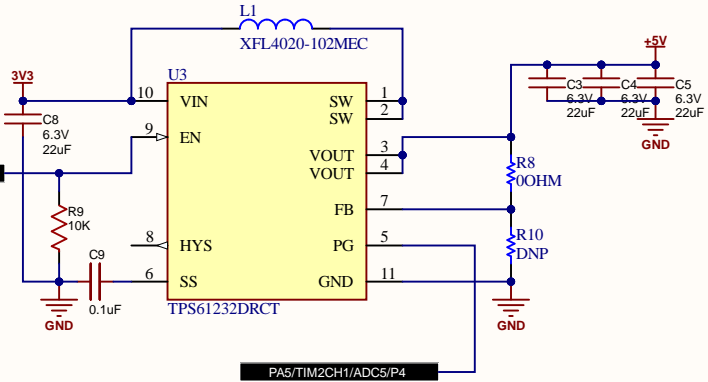
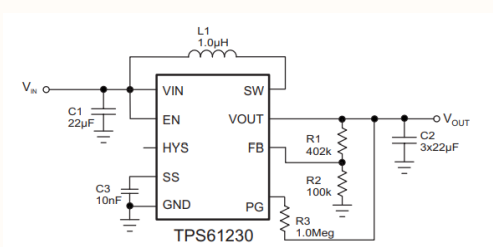


Table 5. List of Inductors				
INDUCTANCE [μH]	CURRENT RATING [A]	DC RESISTANCE [mΩ]	PART NUMBER	MANUFACTURER
1.0	5.4	10.8	XFL4020-102ME	Coilcraft
1.0	7.5	9	LQH6PPN1R0	muRata
0.47	6.6	7.6	XFL4015-471ME	Coilcraft



The system memory contains an embedded boot loader. It manages the flash memory reprogramming through one of the following interfaces:

- USART on pins PA9/PA10 or PA3/PA2
- I²C-bus on pins PB6/PB7 or PB10/PB11
- SPI on pins PA4/PA5/PA6/PA7 (PA2/PA4/PA5/PA6 on TSSOP20 N package) or PB12/PB13/PB14/PB15
- USB on pins PA11/PA12

1

2

3

4

A

A

B

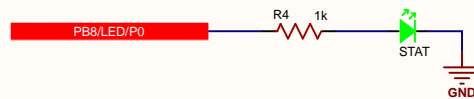
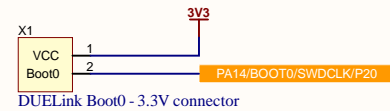
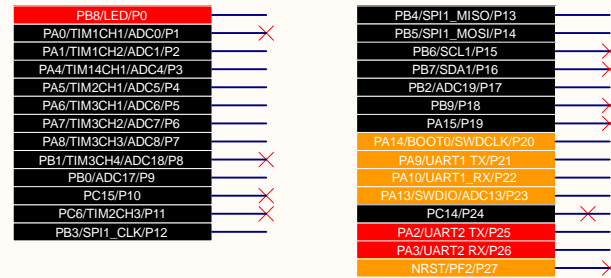
B

C

C

D

D



Title: MISC		
Part #: N/A		
Revision: B	Date: 10/9/2025	Sheet 2 of 5

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Software Features:

ADC: P1, P2, P3, P4, P5, P6, P7, P8, P9, P17

Touch: Can be any pin but hardware need 100pF + 1Mohm

HW PWM: P1, P2, P3, P4, P5, P6, P7, P8, P11

* P1, P2: TIM1

* P3: TIM14

* P4, P11: TIM2

* P5,6,7,8: TIM3

SW UART: Pins 1 RX, 2 TX

Wakeup Pins: P1, P3

Interrupts on: P1, P2, P3, P4, P5, P6, P7, P12

UART1 is available when not used in Uplink





