/usr/lib32/libmcrypt.so.4.4.8: file format elf32-littlearm

Disassembly of section .init:

00002214 <.init>:

2214: e92d4008 push {r3, lr}

2218: eb000065 bl 23b4 <mcrypt\_enc\_get\_key\_size@plt+0xc>

221c: e8bd8008 pop {r3, pc}

Disassembly of section .plt:

00002220 <calloc@plt-0x14>:

2220: e52de004 push {lr} ; (str lr, [sp, #-4]!)

2224: e59fe004 ldr lr, [pc, #4] ; 2230 <calloc@plt-0x4>

2228: e08fe00e add lr, pc, lr

222c: e5bef008 ldr pc, [lr, #8]!

2230: 00031dd0 ldrdeq r1, [r3], -r0

00002234 <calloc@plt>:

2234: e28fc600 add ip, pc, #0, 12

2238: e28cca31 add ip, ip, #200704 ; 0x31000

223c: e5bcfdd0 ldr pc, [ip, #3536]! ; 0xdd0

00002240 <raise@plt>:

2240: e28fc600 add ip, pc, #0, 12

2244: e28cca31 add ip, ip, #200704 ; 0x31000

2248: e5bcfdc8 ldr pc, [ip, #3528]! ; 0xdc8

0000224c <strcmp@plt>:

224c: e28fc600 add ip, pc, #0, 12

2250: e28cca31 add ip, ip, #200704 ; 0x31000

2254: e5bcfdc0 ldr pc, [ip, #3520]! ; 0xdc0

00002258 <\_\_cxa\_finalize@plt>:

2258: e28fc600 add ip, pc, #0, 12

225c: e28cca31 add ip, ip, #200704 ; 0x31000

2260: e5bcfdb8 ldr pc, [ip, #3512]! ; 0xdb8

00002264 <printf@plt>:

2264: e28fc600 add ip, pc, #0, 12

2268: e28cca31 add ip, ip, #200704 ; 0x31000

226c: e5bcfdb0 ldr pc, [ip, #3504]! ; 0xdb0

00002270 <memmove@plt>:

2270: e28fc600 add ip, pc, #0, 12

2274: e28cca31 add ip, ip, #200704 ; 0x31000

2278: e5bcfda8 ldr pc, [ip, #3496]! ; 0xda8

0000227c <free@plt>:

227c: e28fc600 add ip, pc, #0, 12

2280: e28cca31 add ip, ip, #200704 ; 0x31000

2284: e5bcfda0 ldr pc, [ip, #3488]! ; 0xda0

00002288 <memcpy@plt>:

2288: e28fc600 add ip, pc, #0, 12

228c: e28cca31 add ip, ip, #200704 ; 0x31000

2290: e5bcfd98 ldr pc, [ip, #3480]! ; 0xd98

00002294 <memcmp@plt>:

2294: e28fc600 add ip, pc, #0, 12

2298: e28cca31 add ip, ip, #200704 ; 0x31000

229c: e5bcfd90 ldr pc, [ip, #3472]! ; 0xd90

000022a0 <stpcpy@plt>:

22a0: e28fc600 add ip, pc, #0, 12

22a4: e28cca31 add ip, ip, #200704 ; 0x31000

22a8: e5bcfd88 ldr pc, [ip, #3464]! ; 0xd88

000022ac <strdup@plt>:

22ac: e28fc600 add ip, pc, #0, 12

22b0: e28cca31 add ip, ip, #200704 ; 0x31000

22b4: e5bcfd80 ldr pc, [ip, #3456]! ; 0xd80

000022b8 <realloc@plt>:

22b8: e28fc600 add ip, pc, #0, 12

22bc: e28cca31 add ip, ip, #200704 ; 0x31000

22c0: e5bcfd78 ldr pc, [ip, #3448]! ; 0xd78

000022c4 <fwrite@plt>:

22c4: e28fc600 add ip, pc, #0, 12

22c8: e28cca31 add ip, ip, #200704 ; 0x31000

22cc: e5bcfd70 ldr pc, [ip, #3440]! ; 0xd70

000022d0 <munlock@plt>:

22d0: e28fc600 add ip, pc, #0, 12

22d4: e28cca31 add ip, ip, #200704 ; 0x31000

22d8: e5bcfd68 ldr pc, [ip, #3432]! ; 0xd68

000022dc <strcpy@plt>:

22dc: e28fc600 add ip, pc, #0, 12

22e0: e28cca31 add ip, ip, #200704 ; 0x31000

22e4: e5bcfd60 ldr pc, [ip, #3424]! ; 0xd60

000022e8 <puts@plt>:

22e8: e28fc600 add ip, pc, #0, 12

22ec: e28cca31 add ip, ip, #200704 ; 0x31000

22f0: e5bcfd58 ldr pc, [ip, #3416]! ; 0xd58

000022f4 <malloc@plt>:

22f4: e28fc600 add ip, pc, #0, 12

22f8: e28cca31 add ip, ip, #200704 ; 0x31000

22fc: e5bcfd50 ldr pc, [ip, #3408]! ; 0xd50

00002300 <\_\_gmon\_start\_\_@plt>:

2300: e28fc600 add ip, pc, #0, 12

2304: e28cca31 add ip, ip, #200704 ; 0x31000

2308: e5bcfd48 ldr pc, [ip, #3400]! ; 0xd48

0000230c <\_\_ctype\_b\_loc@plt>:

230c: e28fc600 add ip, pc, #0, 12

2310: e28cca31 add ip, ip, #200704 ; 0x31000

2314: e5bcfd40 ldr pc, [ip, #3392]! ; 0xd40

00002318 <mcrypt\_enc\_get\_block\_size@plt>:

2318: e28fc600 add ip, pc, #0, 12

231c: e28cca31 add ip, ip, #200704 ; 0x31000

2320: e5bcfd38 ldr pc, [ip, #3384]! ; 0xd38

00002324 <strlen@plt>:

2324: e28fc600 add ip, pc, #0, 12

2328: e28cca31 add ip, ip, #200704 ; 0x31000

232c: e5bcfd30 ldr pc, [ip, #3376]! ; 0xd30

00002330 <mcrypt\_module\_close@plt>:

2330: e28fc600 add ip, pc, #0, 12

2334: e28cca31 add ip, ip, #200704 ; 0x31000

2338: e5bcfd28 ldr pc, [ip, #3368]! ; 0xd28

0000233c <mcrypt\_enc\_is\_block\_algorithm@plt>:

233c: e28fc600 add ip, pc, #0, 12

2340: e28cca31 add ip, ip, #200704 ; 0x31000

2344: e5bcfd20 ldr pc, [ip, #3360]! ; 0xd20

00002348 <memset@plt>:

2348: e28fc600 add ip, pc, #0, 12

234c: e28cca31 add ip, ip, #200704 ; 0x31000

2350: e5bcfd18 ldr pc, [ip, #3352]! ; 0xd18

00002354 <mlock@plt>:

2354: e28fc600 add ip, pc, #0, 12

2358: e28cca31 add ip, ip, #200704 ; 0x31000

235c: e5bcfd10 ldr pc, [ip, #3344]! ; 0xd10

00002360 <readdir\_r@plt>:

2360: e28fc600 add ip, pc, #0, 12

2364: e28cca31 add ip, ip, #200704 ; 0x31000

2368: e5bcfd08 ldr pc, [ip, #3336]! ; 0xd08

0000236c <sprintf@plt>:

236c: e28fc600 add ip, pc, #0, 12

2370: e28cca31 add ip, ip, #200704 ; 0x31000

2374: e5bcfd00 ldr pc, [ip, #3328]! ; 0xd00

00002378 <mcrypt\_enc\_get\_supported\_key\_sizes@plt>:

2378: e28fc600 add ip, pc, #0, 12

237c: e28cca31 add ip, ip, #200704 ; 0x31000

2380: e5bcfcf8 ldr pc, [ip, #3320]! ; 0xcf8

00002384 <mcrypt\_enc\_get\_iv\_size@plt>:

2384: e28fc600 add ip, pc, #0, 12

2388: e28cca31 add ip, ip, #200704 ; 0x31000

238c: e5bcfcf0 ldr pc, [ip, #3312]! ; 0xcf0

00002390 <strncat@plt>:

2390: e28fc600 add ip, pc, #0, 12

2394: e28cca31 add ip, ip, #200704 ; 0x31000

2398: e5bcfce8 ldr pc, [ip, #3304]! ; 0xce8

0000239c <mcrypt\_enc\_is\_block\_algorithm\_mode@plt>:

239c: e28fc600 add ip, pc, #0, 12

23a0: e28cca31 add ip, ip, #200704 ; 0x31000

23a4: e5bcfce0 ldr pc, [ip, #3296]! ; 0xce0

000023a8 <mcrypt\_enc\_get\_key\_size@plt>:

23a8: e28fc600 add ip, pc, #0, 12

23ac: e28cca31 add ip, ip, #200704 ; 0x31000

23b0: e5bcfcd8 ldr pc, [ip, #3288]! ; 0xcd8

Disassembly of section .text:

000023b4 <mcrypt\_free\_p@@Base-0x240>:

23b4: e59f3014 ldr r3, [pc, #20] ; 23d0 <mcrypt\_enc\_get\_key\_size@plt+0x28>

23b8: e59f2014 ldr r2, [pc, #20] ; 23d4 <mcrypt\_enc\_get\_key\_size@plt+0x2c>

23bc: e08f3003 add r3, pc, r3

23c0: e7932002 ldr r2, [r3, r2]

23c4: e3520000 cmp r2, #0

23c8: 012fff1e bxeq lr

23cc: eaffffcb b 2300 <\_\_gmon\_start\_\_@plt>

23d0: 00031c3c andeq r1, r3, ip, lsr ip

23d4: 000000bc strheq r0, [r0], -ip

23d8: e59f002c ldr r0, [pc, #44] ; 240c <mcrypt\_enc\_get\_key\_size@plt+0x64>

23dc: e59f302c ldr r3, [pc, #44] ; 2410 <mcrypt\_enc\_get\_key\_size@plt+0x68>

23e0: e08f0000 add r0, pc, r0

23e4: e08f3003 add r3, pc, r3

23e8: e1530000 cmp r3, r0

23ec: e59f3020 ldr r3, [pc, #32] ; 2414 <mcrypt\_enc\_get\_key\_size@plt+0x6c>

23f0: e08f3003 add r3, pc, r3

23f4: 012fff1e bxeq lr

23f8: e59f2018 ldr r2, [pc, #24] ; 2418 <mcrypt\_enc\_get\_key\_size@plt+0x70>

23fc: e7933002 ldr r3, [r3, r2]

2400: e3530000 cmp r3, #0

2404: 012fff1e bxeq lr

2408: e12fff13 bx r3

240c: 00033134 andeq r3, r3, r4, lsr r1

2410: 00033130 andeq r3, r3, r0, lsr r1

2414: 00031c08 andeq r1, r3, r8, lsl #24

2418: 00000090 muleq r0, r0, r0

241c: e59f0038 ldr r0, [pc, #56] ; 245c <mcrypt\_enc\_get\_key\_size@plt+0xb4>

2420: e59f3038 ldr r3, [pc, #56] ; 2460 <mcrypt\_enc\_get\_key\_size@plt+0xb8>

2424: e08f0000 add r0, pc, r0

2428: e08f3003 add r3, pc, r3

242c: e0431000 sub r1, r3, r0

2430: e59f302c ldr r3, [pc, #44] ; 2464 <mcrypt\_enc\_get\_key\_size@plt+0xbc>

2434: e1a01141 asr r1, r1, #2

2438: e08f3003 add r3, pc, r3

243c: e0811fa1 add r1, r1, r1, lsr #31

2440: e1b010c1 asrs r1, r1, #1

2444: 012fff1e bxeq lr

2448: e59f2018 ldr r2, [pc, #24] ; 2468 <mcrypt\_enc\_get\_key\_size@plt+0xc0>

244c: e7933002 ldr r3, [r3, r2]

2450: e3530000 cmp r3, #0

2454: 012fff1e bxeq lr

2458: e12fff13 bx r3

245c: 000330f0 strdeq r3, [r3], -r0

2460: 000330ec andeq r3, r3, ip, ror #1

2464: 00031bc0 andeq r1, r3, r0, asr #23

2468: 000000d4 ldrdeq r0, [r0], -r4

246c: e59f304c ldr r3, [pc, #76] ; 24c0 <mcrypt\_enc\_get\_key\_size@plt+0x118>

2470: e59f204c ldr r2, [pc, #76] ; 24c4 <mcrypt\_enc\_get\_key\_size@plt+0x11c>

2474: e08f3003 add r3, pc, r3

2478: e08f2002 add r2, pc, r2

247c: e5d33000 ldrb r3, [r3]

2480: e3530000 cmp r3, #0

2484: 112fff1e bxne lr

2488: e59f3038 ldr r3, [pc, #56] ; 24c8 <mcrypt\_enc\_get\_key\_size@plt+0x120>

248c: e92d4010 push {r4, lr}

2490: e7923003 ldr r3, [r2, r3]

2494: e3530000 cmp r3, #0

2498: 0a000002 beq 24a8 <mcrypt\_enc\_get\_key\_size@plt+0x100>

249c: e59f3028 ldr r3, [pc, #40] ; 24cc <mcrypt\_enc\_get\_key\_size@plt+0x124>

24a0: e79f0003 ldr r0, [pc, r3]

24a4: ebffff6b bl 2258 <\_\_cxa\_finalize@plt>

24a8: ebffffca bl 23d8 <mcrypt\_enc\_get\_key\_size@plt+0x30>

24ac: e59f301c ldr r3, [pc, #28] ; 24d0 <mcrypt\_enc\_get\_key\_size@plt+0x128>

24b0: e3a02001 mov r2, #1

24b4: e08f3003 add r3, pc, r3

24b8: e5c32000 strb r2, [r3]

24bc: e8bd8010 pop {r4, pc}

24c0: 000330a0 andeq r3, r3, r0, lsr #1

24c4: 00031b80 andeq r1, r3, r0, lsl #23

24c8: 0000008c andeq r0, r0, ip, lsl #1

24cc: 00031c3c andeq r1, r3, ip, lsr ip

24d0: 00033060 andeq r3, r3, r0, rrx

24d4: eaffffd0 b 241c <mcrypt\_enc\_get\_key\_size@plt+0x74>

24d8: e92d4070 push {r4, r5, r6, lr}

24dc: e1a04000 mov r4, r0

24e0: e1a05001 mov r5, r1

24e4: ebffff88 bl 230c <\_\_ctype\_b\_loc@plt>

24e8: e5d43000 ldrb r3, [r4]

24ec: e3530030 cmp r3, #48 ; 0x30

24f0: e5901000 ldr r1, [r0]

24f4: 0a00000f beq 2538 <mcrypt\_enc\_get\_key\_size@plt+0x190>

24f8: e1a00004 mov r0, r4

24fc: e3a02000 mov r2, #0

2500: ea000001 b 250c <mcrypt\_enc\_get\_key\_size@plt+0x164>

2504: e083208e add r2, r3, lr, lsl #1

2508: e5f03001 ldrb r3, [r0, #1]!

250c: e1a0c083 lsl ip, r3, #1

2510: e082e102 add lr, r2, r2, lsl #2

2514: e191c0bc ldrh ip, [r1, ip]

2518: e2433030 sub r3, r3, #48 ; 0x30

251c: e31c0b02 tst ip, #2048 ; 0x800

2520: 1afffff7 bne 2504 <mcrypt\_enc\_get\_key\_size@plt+0x15c>

2524: e3520000 cmp r2, #0

2528: e5852000 str r2, [r5]

252c: a8bd8070 popge {r4, r5, r6, pc}

2530: e3a00000 mov r0, #0

2534: e8bd8070 pop {r4, r5, r6, pc}

2538: e5d42001 ldrb r2, [r4, #1]

253c: e1a02082 lsl r2, r2, #1

2540: e19120b2 ldrh r2, [r1, r2]

2544: e3120b02 tst r2, #2048 ; 0x800

2548: 0affffea beq 24f8 <mcrypt\_enc\_get\_key\_size@plt+0x150>

254c: eafffff7 b 2530 <mcrypt\_enc\_get\_key\_size@plt+0x188>

2550: e92d4070 push {r4, r5, r6, lr}

2554: e1a04002 mov r4, r2

2558: e1a05003 mov r5, r3

255c: ebffffdd bl 24d8 <mcrypt\_enc\_get\_key\_size@plt+0x130>

2560: e3500000 cmp r0, #0

2564: 0a00000e beq 25a4 <mcrypt\_enc\_get\_key\_size@plt+0x1fc>

2568: e5d03000 ldrb r3, [r0]

256c: e353002e cmp r3, #46 ; 0x2e

2570: 1a00000b bne 25a4 <mcrypt\_enc\_get\_key\_size@plt+0x1fc>

2574: e1a01004 mov r1, r4

2578: e2800001 add r0, r0, #1

257c: ebffffd5 bl 24d8 <mcrypt\_enc\_get\_key\_size@plt+0x130>

2580: e3500000 cmp r0, #0

2584: 0a000006 beq 25a4 <mcrypt\_enc\_get\_key\_size@plt+0x1fc>

2588: e5d03000 ldrb r3, [r0]

258c: e353002e cmp r3, #46 ; 0x2e

2590: 1a000003 bne 25a4 <mcrypt\_enc\_get\_key\_size@plt+0x1fc>

2594: e1a01005 mov r1, r5

2598: e2800001 add r0, r0, #1

259c: e8bd4070 pop {r4, r5, r6, lr}

25a0: eaffffcc b 24d8 <mcrypt\_enc\_get\_key\_size@plt+0x130>

25a4: e3a00000 mov r0, #0

25a8: e8bd8070 pop {r4, r5, r6, pc}

25ac: e92d4010 push {r4, lr}

25b0: e24ddb89 sub sp, sp, #140288 ; 0x22400

25b4: e24dd098 sub sp, sp, #152 ; 0x98

25b8: e28d4e49 add r4, sp, #1168 ; 0x490

25bc: e2844008 add r4, r4, #8

25c0: e3a03000 mov r3, #0

25c4: e28d2004 add r2, sp, #4

25c8: e28d1008 add r1, sp, #8

25cc: e58d3004 str r3, [sp, #4]

25d0: ebffff62 bl 2360 <readdir\_r@plt>

25d4: e5140494 ldr r0, [r4, #-1172] ; 0xfffffb6c

25d8: e3500000 cmp r0, #0

25dc: 0a000001 beq 25e8 <mcrypt\_enc\_get\_key\_size@plt+0x240>

25e0: e280000b add r0, r0, #11

25e4: ebffff30 bl 22ac <strdup@plt>

25e8: e28ddb89 add sp, sp, #140288 ; 0x22400

25ec: e28dd098 add sp, sp, #152 ; 0x98

25f0: e8bd8010 pop {r4, pc}

000025f4 <mcrypt\_free\_p@@Base>:

25f4: e3510000 cmp r1, #0

25f8: e92d4070 push {r4, r5, r6, lr}

25fc: e1a06000 mov r6, r0

2600: da000005 ble 261c <mcrypt\_free\_p@@Base+0x28>

2604: e2404004 sub r4, r0, #4

2608: e0845101 add r5, r4, r1, lsl #2

260c: e5b40004 ldr r0, [r4, #4]!

2610: ebffff19 bl 227c <free@plt>

2614: e1540005 cmp r4, r5

2618: 1afffffb bne 260c <mcrypt\_free\_p@@Base+0x18>

261c: e1a00006 mov r0, r6

2620: e8bd4070 pop {r4, r5, r6, lr}

2624: eaffff14 b 227c <free@plt>

2628: e1903001 orrs r3, r0, r1

262c: e92d4070 push {r4, r5, r6, lr}

2630: e24dd080 sub sp, sp, #128 ; 0x80

2634: 0a000029 beq 26e0 <mcrypt\_free\_p@@Base+0xec>

2638: e28d503c add r5, sp, #60 ; 0x3c

263c: e1a03000 mov r3, r0

2640: e3a02000 mov r2, #0

2644: e1a00005 mov r0, r5

2648: eb000341 bl 3354 <mcrypt\_module\_close@@Base+0x90>

264c: e3500000 cmp r0, #0

2650: 0a000022 beq 26e0 <mcrypt\_free\_p@@Base+0xec>

2654: e28d404c add r4, sp, #76 ; 0x4c

2658: e1a0e004 mov lr, r4

265c: e59f6098 ldr r6, [pc, #152] ; 26fc <mcrypt\_free\_p@@Base+0x108>

2660: e8be000f ldm lr!, {r0, r1, r2, r3}

2664: e1a0c00d mov ip, sp

2668: e08f6006 add r6, pc, r6

266c: e58d6034 str r6, [sp, #52] ; 0x34

2670: e8ac000f stmia ip!, {r0, r1, r2, r3}

2674: e8be000f ldm lr!, {r0, r1, r2, r3}

2678: e8ac000f stmia ip!, {r0, r1, r2, r3}

267c: e8be000f ldm lr!, {r0, r1, r2, r3}

2680: e59ee000 ldr lr, [lr]

2684: e8ac000f stmia ip!, {r0, r1, r2, r3}

2688: e895000f ldm r5, {r0, r1, r2, r3}

268c: e58ce000 str lr, [ip]

2690: eb0002f1 bl 325c <mcrypt\_free@@Base+0x2f8>

2694: e3500000 cmp r0, #0

2698: 0a000012 beq 26e8 <mcrypt\_free\_p@@Base+0xf4>

269c: e12fff30 blx r0

26a0: e1a0c004 mov ip, r4

26a4: e1a0e00d mov lr, sp

26a8: e1a06000 mov r6, r0

26ac: e8bc000f ldm ip!, {r0, r1, r2, r3}

26b0: e8ae000f stmia lr!, {r0, r1, r2, r3}

26b4: e8bc000f ldm ip!, {r0, r1, r2, r3}

26b8: e8ae000f stmia lr!, {r0, r1, r2, r3}

26bc: e8bc000f ldm ip!, {r0, r1, r2, r3}

26c0: e59cc000 ldr ip, [ip]

26c4: e8ae000f stmia lr!, {r0, r1, r2, r3}

26c8: e895000f ldm r5, {r0, r1, r2, r3}

26cc: e58ec000 str ip, [lr]

26d0: eb0002dc bl 3248 <mcrypt\_free@@Base+0x2e4>

26d4: e1a00006 mov r0, r6

26d8: e28dd080 add sp, sp, #128 ; 0x80

26dc: e8bd8070 pop {r4, r5, r6, pc}

26e0: e3e06000 mvn r6, #0

26e4: eafffffa b 26d4 <mcrypt\_free\_p@@Base+0xe0>

26e8: e1a0c004 mov ip, r4

26ec: e1a0e00d mov lr, sp

26f0: e8bc000f ldm ip!, {r0, r1, r2, r3}

26f4: e3e06000 mvn r6, #0

26f8: eaffffec b 26b0 <mcrypt\_free\_p@@Base+0xbc>

26fc: 0001bdf8 strdeq fp, [r1], -r8

00002700 <mcrypt\_list\_algorithms@@Base>:

2700: e59f30dc ldr r3, [pc, #220] ; 27e4 <mcrypt\_list\_algorithms@@Base+0xe4>

2704: e59f20dc ldr r2, [pc, #220] ; 27e8 <mcrypt\_list\_algorithms@@Base+0xe8>

2708: e08f3003 add r3, pc, r3

270c: e92d41f0 push {r4, r5, r6, r7, r8, lr}

2710: e3a06000 mov r6, #0

2714: e5816000 str r6, [r1]

2718: e7934002 ldr r4, [r3, r2]

271c: e1a07001 mov r7, r1

2720: e5945000 ldr r5, [r4]

2724: e5941004 ldr r1, [r4, #4]

2728: e3550000 cmp r5, #0

272c: e2844008 add r4, r4, #8

2730: 0a000017 beq 2794 <mcrypt\_list\_algorithms@@Base+0x94>

2734: e3510000 cmp r1, #0

2738: 1afffff8 bne 2720 <mcrypt\_list\_algorithms@@Base+0x20>

273c: e1a00005 mov r0, r5

2740: ebffffb8 bl 2628 <mcrypt\_free\_p@@Base+0x34>

2744: e3500000 cmp r0, #0

2748: e1a00006 mov r0, r6

274c: dafffff3 ble 2720 <mcrypt\_list\_algorithms@@Base+0x20>

2750: e5971000 ldr r1, [r7]

2754: e2811001 add r1, r1, #1

2758: e1a01101 lsl r1, r1, #2

275c: ebfffed5 bl 22b8 <realloc@plt>

2760: e2506000 subs r6, r0, #0

2764: e1a00005 mov r0, r5

2768: 0a00000d beq 27a4 <mcrypt\_list\_algorithms@@Base+0xa4>

276c: e5975000 ldr r5, [r7]

2770: ebfffecd bl 22ac <strdup@plt>

2774: e5973000 ldr r3, [r7]

2778: e2831001 add r1, r3, #1

277c: e7860105 str r0, [r6, r5, lsl #2]

2780: e7962103 ldr r2, [r6, r3, lsl #2]

2784: e3520000 cmp r2, #0

2788: 0a000006 beq 27a8 <mcrypt\_list\_algorithms@@Base+0xa8>

278c: e5871000 str r1, [r7]

2790: eaffffe2 b 2720 <mcrypt\_list\_algorithms@@Base+0x20>

2794: e3510000 cmp r1, #0

2798: 1affffe0 bne 2720 <mcrypt\_list\_algorithms@@Base+0x20>

279c: e1a00006 mov r0, r6

27a0: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

27a4: e5973000 ldr r3, [r7]

27a8: e3530000 cmp r3, #0

27ac: c2465004 subgt r5, r6, #4

27b0: c3a04000 movgt r4, #0

27b4: da000005 ble 27d0 <mcrypt\_list\_algorithms@@Base+0xd0>

27b8: e5b50004 ldr r0, [r5, #4]!

27bc: ebfffeae bl 227c <free@plt>

27c0: e5973000 ldr r3, [r7]

27c4: e2844001 add r4, r4, #1

27c8: e1530004 cmp r3, r4

27cc: cafffff9 bgt 27b8 <mcrypt\_list\_algorithms@@Base+0xb8>

27d0: e1a00006 mov r0, r6

27d4: e3a06000 mov r6, #0

27d8: ebfffea7 bl 227c <free@plt>

27dc: e1a00006 mov r0, r6

27e0: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

27e4: 000318f0 strdeq r1, [r3], -r0

27e8: 000000b0 strheq r0, [r0], -r0 ; <UNPREDICTABLE>

27ec: e1903001 orrs r3, r0, r1

27f0: e92d4070 push {r4, r5, r6, lr}

27f4: e24dd080 sub sp, sp, #128 ; 0x80

27f8: 0a000029 beq 28a4 <mcrypt\_list\_algorithms@@Base+0x1a4>

27fc: e28d503c add r5, sp, #60 ; 0x3c

2800: e1a03000 mov r3, r0

2804: e3a02000 mov r2, #0

2808: e1a00005 mov r0, r5

280c: eb0002d0 bl 3354 <mcrypt\_module\_close@@Base+0x90>

2810: e3500000 cmp r0, #0

2814: 0a000022 beq 28a4 <mcrypt\_list\_algorithms@@Base+0x1a4>

2818: e28d404c add r4, sp, #76 ; 0x4c

281c: e1a0e004 mov lr, r4

2820: e59f6098 ldr r6, [pc, #152] ; 28c0 <mcrypt\_list\_algorithms@@Base+0x1c0>

2824: e8be000f ldm lr!, {r0, r1, r2, r3}

2828: e1a0c00d mov ip, sp

282c: e08f6006 add r6, pc, r6

2830: e58d6034 str r6, [sp, #52] ; 0x34

2834: e8ac000f stmia ip!, {r0, r1, r2, r3}

2838: e8be000f ldm lr!, {r0, r1, r2, r3}

283c: e8ac000f stmia ip!, {r0, r1, r2, r3}

2840: e8be000f ldm lr!, {r0, r1, r2, r3}

2844: e59ee000 ldr lr, [lr]

2848: e8ac000f stmia ip!, {r0, r1, r2, r3}

284c: e895000f ldm r5, {r0, r1, r2, r3}

2850: e58ce000 str lr, [ip]

2854: eb000280 bl 325c <mcrypt\_free@@Base+0x2f8>

2858: e3500000 cmp r0, #0

285c: 0a000012 beq 28ac <mcrypt\_list\_algorithms@@Base+0x1ac>

2860: e12fff30 blx r0

2864: e1a0c004 mov ip, r4

2868: e1a0e00d mov lr, sp

286c: e1a06000 mov r6, r0

2870: e8bc000f ldm ip!, {r0, r1, r2, r3}

2874: e8ae000f stmia lr!, {r0, r1, r2, r3}

2878: e8bc000f ldm ip!, {r0, r1, r2, r3}

287c: e8ae000f stmia lr!, {r0, r1, r2, r3}

2880: e8bc000f ldm ip!, {r0, r1, r2, r3}

2884: e59cc000 ldr ip, [ip]

2888: e8ae000f stmia lr!, {r0, r1, r2, r3}

288c: e895000f ldm r5, {r0, r1, r2, r3}

2890: e58ec000 str ip, [lr]

2894: eb00026b bl 3248 <mcrypt\_free@@Base+0x2e4>

2898: e1a00006 mov r0, r6

289c: e28dd080 add sp, sp, #128 ; 0x80

28a0: e8bd8070 pop {r4, r5, r6, pc}

28a4: e3e06000 mvn r6, #0

28a8: eafffffa b 2898 <mcrypt\_list\_algorithms@@Base+0x198>

28ac: e1a0c004 mov ip, r4

28b0: e1a0e00d mov lr, sp

28b4: e8bc000f ldm ip!, {r0, r1, r2, r3}

28b8: e3e06000 mvn r6, #0

28bc: eaffffec b 2874 <mcrypt\_list\_algorithms@@Base+0x174>

28c0: 00019a94 muleq r1, r4, sl

000028c4 <mcrypt\_list\_modes@@Base>:

28c4: e59f30dc ldr r3, [pc, #220] ; 29a8 <mcrypt\_list\_modes@@Base+0xe4>

28c8: e59f20dc ldr r2, [pc, #220] ; 29ac <mcrypt\_list\_modes@@Base+0xe8>

28cc: e08f3003 add r3, pc, r3

28d0: e92d41f0 push {r4, r5, r6, r7, r8, lr}

28d4: e3a06000 mov r6, #0

28d8: e5816000 str r6, [r1]

28dc: e7934002 ldr r4, [r3, r2]

28e0: e1a07001 mov r7, r1

28e4: e5945000 ldr r5, [r4]

28e8: e5941004 ldr r1, [r4, #4]

28ec: e3550000 cmp r5, #0

28f0: e2844008 add r4, r4, #8

28f4: 0a000017 beq 2958 <mcrypt\_list\_modes@@Base+0x94>

28f8: e3510000 cmp r1, #0

28fc: 1afffff8 bne 28e4 <mcrypt\_list\_modes@@Base+0x20>

2900: e1a00005 mov r0, r5

2904: ebffffb8 bl 27ec <mcrypt\_list\_algorithms@@Base+0xec>

2908: e3500000 cmp r0, #0

290c: e1a00006 mov r0, r6

2910: dafffff3 ble 28e4 <mcrypt\_list\_modes@@Base+0x20>

2914: e5971000 ldr r1, [r7]

2918: e2811001 add r1, r1, #1

291c: e1a01101 lsl r1, r1, #2

2920: ebfffe64 bl 22b8 <realloc@plt>

2924: e2506000 subs r6, r0, #0

2928: e1a00005 mov r0, r5

292c: 0a00000d beq 2968 <mcrypt\_list\_modes@@Base+0xa4>

2930: e5975000 ldr r5, [r7]

2934: ebfffe5c bl 22ac <strdup@plt>

2938: e5973000 ldr r3, [r7]

293c: e2831001 add r1, r3, #1

2940: e7860105 str r0, [r6, r5, lsl #2]

2944: e7962103 ldr r2, [r6, r3, lsl #2]

2948: e3520000 cmp r2, #0

294c: 0a000006 beq 296c <mcrypt\_list\_modes@@Base+0xa8>

2950: e5871000 str r1, [r7]

2954: eaffffe2 b 28e4 <mcrypt\_list\_modes@@Base+0x20>

2958: e3510000 cmp r1, #0

295c: 1affffe0 bne 28e4 <mcrypt\_list\_modes@@Base+0x20>

2960: e1a00006 mov r0, r6

2964: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

2968: e5973000 ldr r3, [r7]

296c: e3530000 cmp r3, #0

2970: c2465004 subgt r5, r6, #4

2974: c3a04000 movgt r4, #0

2978: da000005 ble 2994 <mcrypt\_list\_modes@@Base+0xd0>

297c: e5b50004 ldr r0, [r5, #4]!

2980: ebfffe3d bl 227c <free@plt>

2984: e5973000 ldr r3, [r7]

2988: e2844001 add r4, r4, #1

298c: e1530004 cmp r3, r4

2990: cafffff9 bgt 297c <mcrypt\_list\_modes@@Base+0xb8>

2994: e1a00006 mov r0, r6

2998: e3a06000 mov r6, #0

299c: ebfffe36 bl 227c <free@plt>

29a0: e1a00006 mov r0, r6

29a4: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

29a8: 0003172c andeq r1, r3, ip, lsr #14

29ac: 000000b0 strheq r0, [r0], -r0 ; <UNPREDICTABLE>

000029b0 <mcrypt\_check\_version@@Base>:

29b0: e92d4030 push {r4, r5, lr}

29b4: e2504000 subs r4, r0, #0

29b8: e24dd01c sub sp, sp, #28

29bc: 0a000024 beq 2a54 <mcrypt\_check\_version@@Base+0xa4>

29c0: e59f009c ldr r0, [pc, #156] ; 2a64 <mcrypt\_check\_version@@Base+0xb4>

29c4: e28d3008 add r3, sp, #8

29c8: e28d2004 add r2, sp, #4

29cc: e1a0100d mov r1, sp

29d0: e08f0000 add r0, pc, r0

29d4: ebfffedd bl 2550 <mcrypt\_enc\_get\_key\_size@plt+0x1a8>

29d8: e2505000 subs r5, r0, #0

29dc: 0a00000b beq 2a10 <mcrypt\_check\_version@@Base+0x60>

29e0: e28d100c add r1, sp, #12

29e4: e1a00004 mov r0, r4

29e8: e28d3014 add r3, sp, #20

29ec: e28d2010 add r2, sp, #16

29f0: ebfffed6 bl 2550 <mcrypt\_enc\_get\_key\_size@plt+0x1a8>

29f4: e2501000 subs r1, r0, #0

29f8: 0a000004 beq 2a10 <mcrypt\_check\_version@@Base+0x60>

29fc: e59d2000 ldr r2, [sp]

2a00: e59d300c ldr r3, [sp, #12]

2a04: e1520003 cmp r2, r3

2a08: ca000011 bgt 2a54 <mcrypt\_check\_version@@Base+0xa4>

2a0c: 0a000002 beq 2a1c <mcrypt\_check\_version@@Base+0x6c>

2a10: e3a00000 mov r0, #0

2a14: e28dd01c add sp, sp, #28

2a18: e8bd8030 pop {r4, r5, pc}

2a1c: e59d2004 ldr r2, [sp, #4]

2a20: e59d3010 ldr r3, [sp, #16]

2a24: e1520003 cmp r2, r3

2a28: ca000009 bgt 2a54 <mcrypt\_check\_version@@Base+0xa4>

2a2c: 1afffff7 bne 2a10 <mcrypt\_check\_version@@Base+0x60>

2a30: e59d2008 ldr r2, [sp, #8]

2a34: e59d3014 ldr r3, [sp, #20]

2a38: e1520003 cmp r2, r3

2a3c: ca000004 bgt 2a54 <mcrypt\_check\_version@@Base+0xa4>

2a40: 1afffff2 bne 2a10 <mcrypt\_check\_version@@Base+0x60>

2a44: e1a00005 mov r0, r5

2a48: ebfffdff bl 224c <strcmp@plt>

2a4c: e3500000 cmp r0, #0

2a50: baffffee blt 2a10 <mcrypt\_check\_version@@Base+0x60>

2a54: e59f000c ldr r0, [pc, #12] ; 2a68 <mcrypt\_check\_version@@Base+0xb8>

2a58: e08f0000 add r0, pc, r0

2a5c: e28dd01c add sp, sp, #28

2a60: e8bd8030 pop {r4, r5, pc}

2a64: 00019380 andeq r9, r1, r0, lsl #7

2a68: 000192f8 strdeq r9, [r1], -r8

2a6c: e92d4070 push {r4, r5, r6, lr}

2a70: e1a04000 mov r4, r0

2a74: e5906090 ldr r6, [r0, #144] ; 0x90

2a78: ebfffe4a bl 23a8 <mcrypt\_enc\_get\_key\_size@plt>

2a7c: e3a05000 mov r5, #0

2a80: e1a01000 mov r1, r0

2a84: e1a00006 mov r0, r6

2a88: eb000155 bl 2fe4 <mcrypt\_free@@Base+0x80>

2a8c: e5845090 str r5, [r4, #144] ; 0x90

2a90: e1a00004 mov r0, r4

2a94: e5946088 ldr r6, [r4, #136] ; 0x88

2a98: eb00024d bl 33d4 <mcrypt\_module\_close@@Base+0x110>

2a9c: e1a01000 mov r1, r0

2aa0: e1a00006 mov r0, r6

2aa4: eb00014e bl 2fe4 <mcrypt\_free@@Base+0x80>

2aa8: e5845088 str r5, [r4, #136] ; 0x88

2aac: e594108c ldr r1, [r4, #140] ; 0x8c

2ab0: e1a00004 mov r0, r4

2ab4: eb0003bf bl 39b8 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x10c>

2ab8: e594508c ldr r5, [r4, #140] ; 0x8c

2abc: e3550000 cmp r5, #0

2ac0: 0a000004 beq 2ad8 <mcrypt\_check\_version@@Base+0x128>

2ac4: e1a00004 mov r0, r4

2ac8: eb00025c bl 3440 <mcrypt\_module\_close@@Base+0x17c>

2acc: e1a01000 mov r1, r0

2ad0: e1a00005 mov r0, r5

2ad4: eb000142 bl 2fe4 <mcrypt\_free@@Base+0x80>

2ad8: e3a00000 mov r0, #0

2adc: e584008c str r0, [r4, #140] ; 0x8c

2ae0: e8bd8070 pop {r4, r5, r6, pc}

00002ae4 <mcrypt\_generic\_init@@Base>:

2ae4: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

2ae8: e24dd010 sub sp, sp, #16

2aec: e1a06002 mov r6, r2

2af0: e1a08003 mov r8, r3

2af4: e1a09001 mov r9, r1

2af8: e1a04000 mov r4, r0

2afc: ebfffe29 bl 23a8 <mcrypt\_enc\_get\_key\_size@plt>

2b00: e16f3f16 clz r3, r6

2b04: e1a032a3 lsr r3, r3, #5

2b08: e1560000 cmp r6, r0

2b0c: d1a05003 movle r5, r3

2b10: c3835001 orrgt r5, r3, #1

2b14: e3550000 cmp r5, #0

2b18: 1a00006c bne 2cd0 <mcrypt\_generic\_init@@Base+0x1ec>

2b1c: e28d100c add r1, sp, #12

2b20: e1a00004 mov r0, r4

2b24: ebfffe13 bl 2378 <mcrypt\_enc\_get\_supported\_key\_sizes@plt>

2b28: e250a000 subs sl, r0, #0

2b2c: 0a000059 beq 2c98 <mcrypt\_generic\_init@@Base+0x1b4>

2b30: e59d100c ldr r1, [sp, #12]

2b34: e3510000 cmp r1, #0

2b38: da00000b ble 2b6c <mcrypt\_generic\_init@@Base+0x88>

2b3c: e59a7000 ldr r7, [sl]

2b40: e1560007 cmp r6, r7

2b44: 0a00001a beq 2bb4 <mcrypt\_generic\_init@@Base+0xd0>

2b48: e1a03005 mov r3, r5

2b4c: e1a0200a mov r2, sl

2b50: ea000002 b 2b60 <mcrypt\_generic\_init@@Base+0x7c>

2b54: e5b27004 ldr r7, [r2, #4]!

2b58: e1560007 cmp r6, r7

2b5c: 0a000014 beq 2bb4 <mcrypt\_generic\_init@@Base+0xd0>

2b60: e2833001 add r3, r3, #1

2b64: e1530001 cmp r3, r1

2b68: 1afffff9 bne 2b54 <mcrypt\_generic\_init@@Base+0x70>

2b6c: e1a00004 mov r0, r4

2b70: ebfffe0c bl 23a8 <mcrypt\_enc\_get\_key\_size@plt>

2b74: e1a07000 mov r7, r0

2b78: e59d000c ldr r0, [sp, #12]

2b7c: e3500000 cmp r0, #0

2b80: da00000b ble 2bb4 <mcrypt\_generic\_init@@Base+0xd0>

2b84: e59a3000 ldr r3, [sl]

2b88: e1560003 cmp r6, r3

2b8c: da000051 ble 2cd8 <mcrypt\_generic\_init@@Base+0x1f4>

2b90: e1a0200a mov r2, sl

2b94: e3a03000 mov r3, #0

2b98: ea000002 b 2ba8 <mcrypt\_generic\_init@@Base+0xc4>

2b9c: e5b21004 ldr r1, [r2, #4]!

2ba0: e1560001 cmp r6, r1

2ba4: da000047 ble 2cc8 <mcrypt\_generic\_init@@Base+0x1e4>

2ba8: e2833001 add r3, r3, #1

2bac: e1530000 cmp r3, r0

2bb0: 1afffff9 bne 2b9c <mcrypt\_generic\_init@@Base+0xb8>

2bb4: e1a0000a mov r0, sl

2bb8: ebfffdaf bl 227c <free@plt>

2bbc: e1a00004 mov r0, r4

2bc0: ebfffdf8 bl 23a8 <mcrypt\_enc\_get\_key\_size@plt>

2bc4: e1a01000 mov r1, r0

2bc8: e3a00001 mov r0, #1

2bcc: eb0000f2 bl 2f9c <mcrypt\_free@@Base+0x38>

2bd0: e3500000 cmp r0, #0

2bd4: e5840090 str r0, [r4, #144] ; 0x90

2bd8: 0a000040 beq 2ce0 <mcrypt\_generic\_init@@Base+0x1fc>

2bdc: e1a02006 mov r2, r6

2be0: e1a01009 mov r1, r9

2be4: ebfffda1 bl 2270 <memmove@plt>

2be8: e1a00004 mov r0, r4

2bec: eb0001f8 bl 33d4 <mcrypt\_module\_close@@Base+0x110>

2bf0: e1a01000 mov r1, r0

2bf4: e3a00001 mov r0, #1

2bf8: eb0000e7 bl 2f9c <mcrypt\_free@@Base+0x38>

2bfc: e3500000 cmp r0, #0

2c00: e5840088 str r0, [r4, #136] ; 0x88

2c04: 0a000043 beq 2d18 <mcrypt\_generic\_init@@Base+0x234>

2c08: e1a00004 mov r0, r4

2c0c: eb00020b bl 3440 <mcrypt\_module\_close@@Base+0x17c>

2c10: e2501000 subs r1, r0, #0

2c14: da00001d ble 2c90 <mcrypt\_generic\_init@@Base+0x1ac>

2c18: e3a00001 mov r0, #1

2c1c: eb0000de bl 2f9c <mcrypt\_free@@Base+0x38>

2c20: e3500000 cmp r0, #0

2c24: e1a01000 mov r1, r0

2c28: e584008c str r0, [r4, #140] ; 0x8c

2c2c: 0a00003d beq 2d28 <mcrypt\_generic\_init@@Base+0x244>

2c30: e58d8000 str r8, [sp]

2c34: e1a02009 mov r2, r9

2c38: e1a03007 mov r3, r7

2c3c: e1a00004 mov r0, r4

2c40: eb000334 bl 3918 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x6c>

2c44: e3500000 cmp r0, #0

2c48: 1a00002a bne 2cf8 <mcrypt\_generic\_init@@Base+0x214>

2c4c: e3580000 cmp r8, #0

2c50: e5945088 ldr r5, [r4, #136] ; 0x88

2c54: e5946090 ldr r6, [r4, #144] ; 0x90

2c58: 0a000001 beq 2c64 <mcrypt\_generic\_init@@Base+0x180>

2c5c: e1a00004 mov r0, r4

2c60: ebfffdc7 bl 2384 <mcrypt\_enc\_get\_iv\_size@plt>

2c64: e58d0004 str r0, [sp, #4]

2c68: e58d8000 str r8, [sp]

2c6c: e1a03007 mov r3, r7

2c70: e1a02006 mov r2, r6

2c74: e1a01005 mov r1, r5

2c78: e1a00004 mov r0, r4

2c7c: eb0002ca bl 37ac <mcrypt\_enc\_is\_block\_algorithm@@Base+0x6c>

2c80: e3500000 cmp r0, #0

2c84: 1a000017 bne 2ce8 <mcrypt\_generic\_init@@Base+0x204>

2c88: e28dd010 add sp, sp, #16

2c8c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

2c90: e594108c ldr r1, [r4, #140] ; 0x8c

2c94: eaffffe5 b 2c30 <mcrypt\_generic\_init@@Base+0x14c>

2c98: e59d300c ldr r3, [sp, #12]

2c9c: e3530000 cmp r3, #0

2ca0: 0a000003 beq 2cb4 <mcrypt\_generic\_init@@Base+0x1d0>

2ca4: e1a00004 mov r0, r4

2ca8: ebfffdbe bl 23a8 <mcrypt\_enc\_get\_key\_size@plt>

2cac: e1a07006 mov r7, r6

2cb0: eaffffbf b 2bb4 <mcrypt\_generic\_init@@Base+0xd0>

2cb4: e1a00004 mov r0, r4

2cb8: ebfffdba bl 23a8 <mcrypt\_enc\_get\_key\_size@plt>

2cbc: e1560000 cmp r6, r0

2cc0: dafffff9 ble 2cac <mcrypt\_generic\_init@@Base+0x1c8>

2cc4: eafffff6 b 2ca4 <mcrypt\_generic\_init@@Base+0x1c0>

2cc8: e1a07001 mov r7, r1

2ccc: eaffffb8 b 2bb4 <mcrypt\_generic\_init@@Base+0xd0>

2cd0: e3e00002 mvn r0, #2

2cd4: eaffffeb b 2c88 <mcrypt\_generic\_init@@Base+0x1a4>

2cd8: e1a07003 mov r7, r3

2cdc: eaffffb4 b 2bb4 <mcrypt\_generic\_init@@Base+0xd0>

2ce0: e3e00003 mvn r0, #3

2ce4: eaffffe7 b 2c88 <mcrypt\_generic\_init@@Base+0x1a4>

2ce8: e1a00004 mov r0, r4

2cec: ebffff5e bl 2a6c <mcrypt\_check\_version@@Base+0xbc>

2cf0: e3e00000 mvn r0, #0

2cf4: eaffffe3 b 2c88 <mcrypt\_generic\_init@@Base+0x1a4>

2cf8: e5940090 ldr r0, [r4, #144] ; 0x90

2cfc: ebfffd5e bl 227c <free@plt>

2d00: e5940088 ldr r0, [r4, #136] ; 0x88

2d04: ebfffd5c bl 227c <free@plt>

2d08: e594008c ldr r0, [r4, #140] ; 0x8c

2d0c: ebfffd5a bl 227c <free@plt>

2d10: e3e00000 mvn r0, #0

2d14: eaffffdb b 2c88 <mcrypt\_generic\_init@@Base+0x1a4>

2d18: e5940090 ldr r0, [r4, #144] ; 0x90

2d1c: ebfffd56 bl 227c <free@plt>

2d20: e3e00003 mvn r0, #3

2d24: eaffffd7 b 2c88 <mcrypt\_generic\_init@@Base+0x1a4>

2d28: e5940090 ldr r0, [r4, #144] ; 0x90

2d2c: ebfffd52 bl 227c <free@plt>

2d30: e5940088 ldr r0, [r4, #136] ; 0x88

2d34: ebfffd50 bl 227c <free@plt>

2d38: e3e00003 mvn r0, #3

2d3c: eaffffd1 b 2c88 <mcrypt\_generic\_init@@Base+0x1a4>

00002d40 <mcrypt\_generic@@Base>:

2d40: e1a03002 mov r3, r2

2d44: e1a02001 mov r2, r1

2d48: e590108c ldr r1, [r0, #140] ; 0x8c

2d4c: ea000337 b 3a30 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x184>

00002d50 <mdecrypt\_generic@@Base>:

2d50: e1a03002 mov r3, r2

2d54: e1a02001 mov r2, r1

2d58: e590108c ldr r1, [r0, #140] ; 0x8c

2d5c: ea000347 b 3a80 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x1d4>

00002d60 <mcrypt\_generic\_end@@Base>:

2d60: e92d4010 push {r4, lr}

2d64: e2504000 subs r4, r0, #0

2d68: 0a000007 beq 2d8c <mcrypt\_generic\_end@@Base+0x2c>

2d6c: e5943090 ldr r3, [r4, #144] ; 0x90

2d70: e3530000 cmp r3, #0

2d74: 0a000000 beq 2d7c <mcrypt\_generic\_end@@Base+0x1c>

2d78: ebffff3b bl 2a6c <mcrypt\_check\_version@@Base+0xbc>

2d7c: e1a00004 mov r0, r4

2d80: ebfffd6a bl 2330 <mcrypt\_module\_close@plt>

2d84: e3a00000 mov r0, #0

2d88: e8bd8010 pop {r4, pc}

2d8c: e3e00000 mvn r0, #0

2d90: e8bd8010 pop {r4, pc}

00002d94 <mcrypt\_generic\_deinit@@Base>:

2d94: e2503000 subs r3, r0, #0

2d98: 0a000006 beq 2db8 <mcrypt\_generic\_deinit@@Base+0x24>

2d9c: e5933090 ldr r3, [r3, #144] ; 0x90

2da0: e3530000 cmp r3, #0

2da4: 0a000003 beq 2db8 <mcrypt\_generic\_deinit@@Base+0x24>

2da8: e92d4010 push {r4, lr}

2dac: ebffff2e bl 2a6c <mcrypt\_check\_version@@Base+0xbc>

2db0: e3a00000 mov r0, #0

2db4: e8bd8010 pop {r4, pc}

2db8: e3e00000 mvn r0, #0

2dbc: e12fff1e bx lr

00002dc0 <mcrypt\_perror@@Base>:

2dc0: e59f30ec ldr r3, [pc, #236] ; 2eb4 <mcrypt\_perror@@Base+0xf4>

2dc4: e2800006 add r0, r0, #6

2dc8: e08f3003 add r3, pc, r3

2dcc: e3500005 cmp r0, #5

2dd0: 908ff100 addls pc, pc, r0, lsl #2

2dd4: ea000035 b 2eb0 <mcrypt\_perror@@Base+0xf0>

2dd8: ea00000c b 2e10 <mcrypt\_perror@@Base+0x50>

2ddc: ea000013 b 2e30 <mcrypt\_perror@@Base+0x70>

2de0: ea00001a b 2e50 <mcrypt\_perror@@Base+0x90>

2de4: ea000021 b 2e70 <mcrypt\_perror@@Base+0xb0>

2de8: ea000028 b 2e90 <mcrypt\_perror@@Base+0xd0>

2dec: eaffffff b 2df0 <mcrypt\_perror@@Base+0x30>

2df0: e59f00c0 ldr r0, [pc, #192] ; 2eb8 <mcrypt\_perror@@Base+0xf8>

2df4: e3a0200f mov r2, #15

2df8: e3a01001 mov r1, #1

2dfc: e7933000 ldr r3, [r3, r0]

2e00: e59f00b4 ldr r0, [pc, #180] ; 2ebc <mcrypt\_perror@@Base+0xfc>

2e04: e5933000 ldr r3, [r3]

2e08: e08f0000 add r0, pc, r0

2e0c: eafffd2c b 22c4 <fwrite@plt>

2e10: e59f00a0 ldr r0, [pc, #160] ; 2eb8 <mcrypt\_perror@@Base+0xf8>

2e14: e3a02013 mov r2, #19

2e18: e3a01001 mov r1, #1

2e1c: e7933000 ldr r3, [r3, r0]

2e20: e59f0098 ldr r0, [pc, #152] ; 2ec0 <mcrypt\_perror@@Base+0x100>

2e24: e5933000 ldr r3, [r3]

2e28: e08f0000 add r0, pc, r0

2e2c: eafffd24 b 22c4 <fwrite@plt>

2e30: e59f0080 ldr r0, [pc, #128] ; 2eb8 <mcrypt\_perror@@Base+0xf8>

2e34: e3a0200e mov r2, #14

2e38: e3a01001 mov r1, #1

2e3c: e7933000 ldr r3, [r3, r0]

2e40: e59f007c ldr r0, [pc, #124] ; 2ec4 <mcrypt\_perror@@Base+0x104>

2e44: e5933000 ldr r3, [r3]

2e48: e08f0000 add r0, pc, r0

2e4c: eafffd1c b 22c4 <fwrite@plt>

2e50: e59f0060 ldr r0, [pc, #96] ; 2eb8 <mcrypt\_perror@@Base+0xf8>

2e54: e3a0201a mov r2, #26

2e58: e3a01001 mov r1, #1

2e5c: e7933000 ldr r3, [r3, r0]

2e60: e59f0060 ldr r0, [pc, #96] ; 2ec8 <mcrypt\_perror@@Base+0x108>

2e64: e5933000 ldr r3, [r3]

2e68: e08f0000 add r0, pc, r0

2e6c: eafffd14 b 22c4 <fwrite@plt>

2e70: e59f0040 ldr r0, [pc, #64] ; 2eb8 <mcrypt\_perror@@Base+0xf8>

2e74: e3a02019 mov r2, #25

2e78: e3a01001 mov r1, #1

2e7c: e7933000 ldr r3, [r3, r0]

2e80: e59f0044 ldr r0, [pc, #68] ; 2ecc <mcrypt\_perror@@Base+0x10c>

2e84: e5933000 ldr r3, [r3]

2e88: e08f0000 add r0, pc, r0

2e8c: eafffd0c b 22c4 <fwrite@plt>

2e90: e59f0020 ldr r0, [pc, #32] ; 2eb8 <mcrypt\_perror@@Base+0xf8>

2e94: e3a02027 mov r2, #39 ; 0x27

2e98: e3a01001 mov r1, #1

2e9c: e7933000 ldr r3, [r3, r0]

2ea0: e59f0028 ldr r0, [pc, #40] ; 2ed0 <mcrypt\_perror@@Base+0x110>

2ea4: e5933000 ldr r3, [r3]

2ea8: e08f0000 add r0, pc, r0

2eac: eafffd04 b 22c4 <fwrite@plt>

2eb0: e12fff1e bx lr

2eb4: 00031230 andeq r1, r3, r0, lsr r2

2eb8: 000000ac andeq r0, r0, ip, lsr #1

2ebc: 00018f50 andeq r8, r1, r0, asr pc

2ec0: 00018fb0 ; <UNDEFINED> instruction: 0x00018fb0

2ec4: 00018f80 andeq r8, r1, r0, lsl #31

2ec8: 00018f44 andeq r8, r1, r4, asr #30

2ecc: 00018f08 andeq r8, r1, r8, lsl #30

2ed0: 00018ec0 andeq r8, r1, r0, asr #29

00002ed4 <mcrypt\_strerror@@Base>:

2ed4: e2800006 add r0, r0, #6

2ed8: e3500005 cmp r0, #5

2edc: 908ff100 addls pc, pc, r0, lsl #2

2ee0: ea000017 b 2f44 <mcrypt\_strerror@@Base+0x70>

2ee4: ea000007 b 2f08 <mcrypt\_strerror@@Base+0x34>

2ee8: ea000009 b 2f14 <mcrypt\_strerror@@Base+0x40>

2eec: ea00000b b 2f20 <mcrypt\_strerror@@Base+0x4c>

2ef0: ea00000d b 2f2c <mcrypt\_strerror@@Base+0x58>

2ef4: ea00000f b 2f38 <mcrypt\_strerror@@Base+0x64>

2ef8: eaffffff b 2efc <mcrypt\_strerror@@Base+0x28>

2efc: e59f0048 ldr r0, [pc, #72] ; 2f4c <mcrypt\_strerror@@Base+0x78>

2f00: e08f0000 add r0, pc, r0

2f04: e12fff1e bx lr

2f08: e59f0040 ldr r0, [pc, #64] ; 2f50 <mcrypt\_strerror@@Base+0x7c>

2f0c: e08f0000 add r0, pc, r0

2f10: e12fff1e bx lr

2f14: e59f0038 ldr r0, [pc, #56] ; 2f54 <mcrypt\_strerror@@Base+0x80>

2f18: e08f0000 add r0, pc, r0

2f1c: e12fff1e bx lr

2f20: e59f0030 ldr r0, [pc, #48] ; 2f58 <mcrypt\_strerror@@Base+0x84>

2f24: e08f0000 add r0, pc, r0

2f28: e12fff1e bx lr

2f2c: e59f0028 ldr r0, [pc, #40] ; 2f5c <mcrypt\_strerror@@Base+0x88>

2f30: e08f0000 add r0, pc, r0

2f34: e12fff1e bx lr

2f38: e59f0020 ldr r0, [pc, #32] ; 2f60 <mcrypt\_strerror@@Base+0x8c>

2f3c: e08f0000 add r0, pc, r0

2f40: e12fff1e bx lr

2f44: e3a00000 mov r0, #0

2f48: e12fff1e bx lr

2f4c: 00018e58 andeq r8, r1, r8, asr lr

2f50: 00018ecc andeq r8, r1, ip, asr #29

2f54: 00018eb0 ; <UNDEFINED> instruction: 0x00018eb0

2f58: 00018e88 andeq r8, r1, r8, lsl #29

2f5c: 00018e60 andeq r8, r1, r0, ror #28

2f60: 00018e2c andeq r8, r1, ip, lsr #28

00002f64 <mcrypt\_free@@Base>:

2f64: eafffcc4 b 227c <free@plt>

2f68: e1a02001 mov r2, r1

2f6c: e3a01000 mov r1, #0

2f70: eafffcf4 b 2348 <memset@plt>

2f74: eafffcf6 b 2354 <mlock@plt>

2f78: e92d4070 push {r4, r5, r6, lr}

2f7c: e1a05000 mov r5, r0

2f80: ebfffcdb bl 22f4 <malloc@plt>

2f84: e2504000 subs r4, r0, #0

2f88: 0a000001 beq 2f94 <mcrypt\_free@@Base+0x30>

2f8c: e1a01005 mov r1, r5

2f90: ebfffff7 bl 2f74 <mcrypt\_free@@Base+0x10>

2f94: e1a00004 mov r0, r4

2f98: e8bd8070 pop {r4, r5, r6, pc}

2f9c: e92d4070 push {r4, r5, r6, lr}

2fa0: e1a05001 mov r5, r1

2fa4: ebfffca2 bl 2234 <calloc@plt>

2fa8: e2504000 subs r4, r0, #0

2fac: 0a000001 beq 2fb8 <mcrypt\_free@@Base+0x54>

2fb0: e1a01005 mov r1, r5

2fb4: ebffffee bl 2f74 <mcrypt\_free@@Base+0x10>

2fb8: e1a00004 mov r0, r4

2fbc: e8bd8070 pop {r4, r5, r6, pc}

2fc0: e92d4070 push {r4, r5, r6, lr}

2fc4: e1a05001 mov r5, r1

2fc8: ebfffcba bl 22b8 <realloc@plt>

2fcc: e2504000 subs r4, r0, #0

2fd0: 0a000001 beq 2fdc <mcrypt\_free@@Base+0x78>

2fd4: e1a01005 mov r1, r5

2fd8: ebffffe5 bl 2f74 <mcrypt\_free@@Base+0x10>

2fdc: e1a00004 mov r0, r4

2fe0: e8bd8070 pop {r4, r5, r6, pc}

2fe4: e92d4070 push {r4, r5, r6, lr}

2fe8: e1a04000 mov r4, r0

2fec: e1a05001 mov r5, r1

2ff0: ebffffdc bl 2f68 <mcrypt\_free@@Base+0x4>

2ff4: e1a01005 mov r1, r5

2ff8: e1a00004 mov r0, r4

2ffc: ebfffcb3 bl 22d0 <munlock@plt>

3000: e1a00004 mov r0, r4

3004: e8bd4070 pop {r4, r5, r6, lr}

3008: eafffc9b b 227c <free@plt>

300c: e92d4070 push {r4, r5, r6, lr}

3010: e1a05001 mov r5, r1

3014: e1a04000 mov r4, r0

3018: ebfffcc1 bl 2324 <strlen@plt>

301c: e1a06000 mov r6, r0

3020: e1a00005 mov r0, r5

3024: ebfffcbe bl 2324 <strlen@plt>

3028: e1560000 cmp r6, r0

302c: 1a000014 bne 3084 <mcrypt\_free@@Base+0x120>

3030: e3560000 cmp r6, #0

3034: 0a000014 beq 308c <mcrypt\_free@@Base+0x128>

3038: e2442001 sub r2, r4, #1

303c: e0820006 add r0, r2, r6

3040: e2451001 sub r1, r5, #1

3044: ea000006 b 3064 <mcrypt\_free@@Base+0x100>

3048: e353002d cmp r3, #45 ; 0x2d

304c: 035c005f cmpeq ip, #95 ; 0x5f

3050: 0a000001 beq 305c <mcrypt\_free@@Base+0xf8>

3054: e153000c cmp r3, ip

3058: 1a000009 bne 3084 <mcrypt\_free@@Base+0x120>

305c: e1520000 cmp r2, r0

3060: 0a000009 beq 308c <mcrypt\_free@@Base+0x128>

3064: e5f23001 ldrb r3, [r2, #1]!

3068: e5f1c001 ldrb ip, [r1, #1]!

306c: e353005f cmp r3, #95 ; 0x5f

3070: 1afffff4 bne 3048 <mcrypt\_free@@Base+0xe4>

3074: e35c002d cmp ip, #45 ; 0x2d

3078: 0afffff7 beq 305c <mcrypt\_free@@Base+0xf8>

307c: e153000c cmp r3, ip

3080: 0afffff5 beq 305c <mcrypt\_free@@Base+0xf8>

3084: e3e00000 mvn r0, #0

3088: e8bd8070 pop {r4, r5, r6, pc}

308c: e3a00000 mov r0, #0

3090: e8bd8070 pop {r4, r5, r6, pc}

3094: e92d4070 push {r4, r5, r6, lr}

3098: e24ddc06 sub sp, sp, #1536 ; 0x600

309c: e24dd008 sub sp, sp, #8

30a0: e3a03000 mov r3, #0

30a4: e3510000 cmp r1, #0

30a8: e1a06000 mov r6, r0

30ac: e1a04002 mov r4, r2

30b0: e5cd3004 strb r3, [sp, #4]

30b4: 0a000009 beq 30e0 <mcrypt\_free@@Base+0x17c>

30b8: e28d5004 add r5, sp, #4

30bc: e3a02c02 mov r2, #512 ; 0x200

30c0: e1a00005 mov r0, r5

30c4: ebfffcb1 bl 2390 <strncat@plt>

30c8: e1a00005 mov r0, r5

30cc: ebfffc94 bl 2324 <strlen@plt>

30d0: e59f3034 ldr r3, [pc, #52] ; 310c <mcrypt\_free@@Base+0x1a8>

30d4: e08f3003 add r3, pc, r3

30d8: e1d330b0 ldrh r3, [r3]

30dc: e18530b0 strh r3, [r5, r0]

30e0: e3540000 cmp r4, #0

30e4: 0a000003 beq 30f8 <mcrypt\_free@@Base+0x194>

30e8: e1a01004 mov r1, r4

30ec: e3a02c02 mov r2, #512 ; 0x200

30f0: e28d0004 add r0, sp, #4

30f4: ebfffca5 bl 2390 <strncat@plt>

30f8: e3a00000 mov r0, #0

30fc: e5860000 str r0, [r6]

3100: e28ddc06 add sp, sp, #1536 ; 0x600

3104: e28dd008 add sp, sp, #8

3108: e8bd8070 pop {r4, r5, r6, pc}

310c: 00018d18 andeq r8, r1, r8, lsl sp

3110: e59f3050 ldr r3, [pc, #80] ; 3168 <mcrypt\_free@@Base+0x204>

3114: e59f2050 ldr r2, [pc, #80] ; 316c <mcrypt\_free@@Base+0x208>

3118: e08f3003 add r3, pc, r3

311c: e92d4070 push {r4, r5, r6, lr}

3120: e1a05000 mov r5, r0

3124: e7934002 ldr r4, [r3, r2]

3128: e5941000 ldr r1, [r4]

312c: e5940004 ldr r0, [r4, #4]

3130: e3510000 cmp r1, #0

3134: e2844008 add r4, r4, #8

3138: 0a000007 beq 315c <mcrypt\_free@@Base+0x1f8>

313c: e3500000 cmp r0, #0

3140: 1afffff8 bne 3128 <mcrypt\_free@@Base+0x1c4>

3144: e1a00005 mov r0, r5

3148: ebffffaf bl 300c <mcrypt\_free@@Base+0xa8>

314c: e3500000 cmp r0, #0

3150: 1afffff4 bne 3128 <mcrypt\_free@@Base+0x1c4>

3154: e3e00000 mvn r0, #0

3158: e8bd8070 pop {r4, r5, r6, pc}

315c: e3500000 cmp r0, #0

3160: 1afffff0 bne 3128 <mcrypt\_free@@Base+0x1c4>

3164: e8bd8070 pop {r4, r5, r6, pc}

3168: 00030ee0 andeq r0, r3, r0, ror #29

316c: 000000b0 strheq r0, [r0], -r0 ; <UNPREDICTABLE>

3170: e24dd010 sub sp, sp, #16

3174: e92d41f0 push {r4, r5, r6, r7, r8, lr}

3178: e24ddb01 sub sp, sp, #1024 ; 0x400

317c: e28dce41 add ip, sp, #1040 ; 0x410

3180: e28cc004 add ip, ip, #4

3184: e1a0600d mov r6, sp

3188: e98c000f stmib ip, {r0, r1, r2, r3}

318c: e28d1e41 add r1, sp, #1040 ; 0x410

3190: e281100c add r1, r1, #12

3194: e1a00006 mov r0, r6

3198: e59d445c ldr r4, [sp, #1116] ; 0x45c

319c: ebfffc3f bl 22a0 <stpcpy@plt>

31a0: e59f3094 ldr r3, [pc, #148] ; 323c <mcrypt\_free@@Base+0x2d8>

31a4: e1a01004 mov r1, r4

31a8: e08f3003 add r3, pc, r3

31ac: e59f408c ldr r4, [pc, #140] ; 3240 <mcrypt\_free@@Base+0x2dc>

31b0: e08f4004 add r4, pc, r4

31b4: e1a02000 mov r2, r0

31b8: e5930000 ldr r0, [r3]

31bc: e5d33004 ldrb r3, [r3, #4]

31c0: e5820000 str r0, [r2]

31c4: e2820005 add r0, r2, #5

31c8: e5c23004 strb r3, [r2, #4]

31cc: ebfffc42 bl 22dc <strcpy@plt>

31d0: e59f206c ldr r2, [pc, #108] ; 3244 <mcrypt\_free@@Base+0x2e0>

31d4: e1a03004 mov r3, r4

31d8: e3a04000 mov r4, #0

31dc: e7935002 ldr r5, [r3, r2]

31e0: e2858004 add r8, r5, #4

31e4: ea000003 b 31f8 <mcrypt\_free@@Base+0x294>

31e8: ebffff87 bl 300c <mcrypt\_free@@Base+0xa8>

31ec: e3500000 cmp r0, #0

31f0: 0a00000c beq 3228 <mcrypt\_free@@Base+0x2c4>

31f4: e2844001 add r4, r4, #1

31f8: e7953184 ldr r3, [r5, r4, lsl #3]

31fc: e1a00006 mov r0, r6

3200: e2531000 subs r1, r3, #0

3204: e0857184 add r7, r5, r4, lsl #3

3208: 1afffff6 bne 31e8 <mcrypt\_free@@Base+0x284>

320c: e7980184 ldr r0, [r8, r4, lsl #3]

3210: e3500000 cmp r0, #0

3214: 1afffff6 bne 31f4 <mcrypt\_free@@Base+0x290>

3218: e28ddb01 add sp, sp, #1024 ; 0x400

321c: e8bd41f0 pop {r4, r5, r6, r7, r8, lr}

3220: e28dd010 add sp, sp, #16

3224: e12fff1e bx lr

3228: e5970004 ldr r0, [r7, #4]

322c: e28ddb01 add sp, sp, #1024 ; 0x400

3230: e8bd41f0 pop {r4, r5, r6, r7, r8, lr}

3234: e28dd010 add sp, sp, #16

3238: e12fff1e bx lr

323c: 00018c48 andeq r8, r1, r8, asr #24

3240: 00030e48 andeq r0, r3, r8, asr #28

3244: 000000b0 strheq r0, [r0], -r0 ; <UNPREDICTABLE>

3248: e24dd010 sub sp, sp, #16

324c: e24dc004 sub ip, sp, #4

3250: e28dd010 add sp, sp, #16

3254: e98c000f stmib ip, {r0, r1, r2, r3}

3258: e12fff1e bx lr

325c: e24dd010 sub sp, sp, #16

3260: e3700001 cmn r0, #1

3264: e92d4030 push {r4, r5, lr}

3268: e24dd03c sub sp, sp, #60 ; 0x3c

326c: e28d4048 add r4, sp, #72 ; 0x48

3270: e884000f stm r4, {r0, r1, r2, r3}

3274: 13a00000 movne r0, #0

3278: 1a00000d bne 32b4 <mcrypt\_free@@Base+0x350>

327c: e28de058 add lr, sp, #88 ; 0x58

3280: e59d508c ldr r5, [sp, #140] ; 0x8c

3284: e8be000f ldm lr!, {r0, r1, r2, r3}

3288: e1a0c00d mov ip, sp

328c: e58d5034 str r5, [sp, #52] ; 0x34

3290: e8ac000f stmia ip!, {r0, r1, r2, r3}

3294: e8be000f ldm lr!, {r0, r1, r2, r3}

3298: e8ac000f stmia ip!, {r0, r1, r2, r3}

329c: e8be000f ldm lr!, {r0, r1, r2, r3}

32a0: e59ee000 ldr lr, [lr]

32a4: e8ac000f stmia ip!, {r0, r1, r2, r3}

32a8: e894000f ldm r4, {r0, r1, r2, r3}

32ac: e58ce000 str lr, [ip]

32b0: ebffffae bl 3170 <mcrypt\_free@@Base+0x20c>

32b4: e28dd03c add sp, sp, #60 ; 0x3c

32b8: e8bd4030 pop {r4, r5, lr}

32bc: e28dd010 add sp, sp, #16

32c0: e12fff1e bx lr

000032c4 <mcrypt\_module\_close@@Base>:

32c4: e92d4030 push {r4, r5, lr}

32c8: e2504000 subs r4, r0, #0

32cc: e24dd03c sub sp, sp, #60 ; 0x3c

32d0: 0a00001d beq 334c <mcrypt\_module\_close@@Base+0x88>

32d4: e284e010 add lr, r4, #16

32d8: e1a0c00d mov ip, sp

32dc: e8be000f ldm lr!, {r0, r1, r2, r3}

32e0: e2845044 add r5, r4, #68 ; 0x44

32e4: e8ac000f stmia ip!, {r0, r1, r2, r3}

32e8: e8be000f ldm lr!, {r0, r1, r2, r3}

32ec: e8ac000f stmia ip!, {r0, r1, r2, r3}

32f0: e8be000f ldm lr!, {r0, r1, r2, r3}

32f4: e8ac000f stmia ip!, {r0, r1, r2, r3}

32f8: e59e3000 ldr r3, [lr]

32fc: e58c3000 str r3, [ip]

3300: e894000f ldm r4, {r0, r1, r2, r3}

3304: ebffffcf bl 3248 <mcrypt\_free@@Base+0x2e4>

3308: e284e054 add lr, r4, #84 ; 0x54

330c: e1a0c00d mov ip, sp

3310: e8be000f ldm lr!, {r0, r1, r2, r3}

3314: e8ac000f stmia ip!, {r0, r1, r2, r3}

3318: e8be000f ldm lr!, {r0, r1, r2, r3}

331c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3320: e8be000f ldm lr!, {r0, r1, r2, r3}

3324: e8ac000f stmia ip!, {r0, r1, r2, r3}

3328: e59e3000 ldr r3, [lr]

332c: e58c3000 str r3, [ip]

3330: e895000f ldm r5, {r0, r1, r2, r3}

3334: ebffffc3 bl 3248 <mcrypt\_free@@Base+0x2e4>

3338: e1a00004 mov r0, r4

333c: ebfffbce bl 227c <free@plt>

3340: e3a00000 mov r0, #0

3344: e28dd03c add sp, sp, #60 ; 0x3c

3348: e8bd8030 pop {r4, r5, pc}

334c: e3e00000 mvn r0, #0

3350: eafffffb b 3344 <mcrypt\_module\_close@@Base+0x80>

3354: e92d41f0 push {r4, r5, r6, r7, r8, lr}

3358: e2534000 subs r4, r3, #0

335c: 0a000015 beq 33b8 <mcrypt\_module\_close@@Base+0xf4>

3360: e5d4c000 ldrb ip, [r4]

3364: e35c0000 cmp ip, #0

3368: 0a000012 beq 33b8 <mcrypt\_module\_close@@Base+0xf4>

336c: e1a05000 mov r5, r0

3370: e1a00004 mov r0, r4

3374: e1a07002 mov r7, r2

3378: e1a06001 mov r6, r1

337c: ebfffbe8 bl 2324 <strlen@plt>

3380: e350003f cmp r0, #63 ; 0x3f

3384: 8a00000b bhi 33b8 <mcrypt\_module\_close@@Base+0xf4>

3388: e2802001 add r2, r0, #1

338c: e1a01004 mov r1, r4

3390: e2850004 add r0, r5, #4

3394: ebfffbbb bl 2288 <memcpy@plt>

3398: e1a00004 mov r0, r4

339c: ebffff5b bl 3110 <mcrypt\_free@@Base+0x1ac>

33a0: e3500000 cmp r0, #0

33a4: 0a000005 beq 33c0 <mcrypt\_module\_close@@Base+0xfc>

33a8: e3e03000 mvn r3, #0

33ac: e5853000 str r3, [r5]

33b0: e1a00003 mov r0, r3

33b4: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

33b8: e3a00000 mov r0, #0

33bc: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

33c0: e1a02007 mov r2, r7

33c4: e1a01006 mov r1, r6

33c8: e1a00005 mov r0, r5

33cc: e8bd41f0 pop {r4, r5, r6, r7, r8, lr}

33d0: eaffff2f b 3094 <mcrypt\_free@@Base+0x130>

33d4: e59f3060 ldr r3, [pc, #96] ; 343c <mcrypt\_module\_close@@Base+0x178>

33d8: e92d4010 push {r4, lr}

33dc: e280e010 add lr, r0, #16

33e0: e24dd038 sub sp, sp, #56 ; 0x38

33e4: e08f3003 add r3, pc, r3

33e8: e58d3034 str r3, [sp, #52] ; 0x34

33ec: e1a04000 mov r4, r0

33f0: e8be000f ldm lr!, {r0, r1, r2, r3}

33f4: e1a0c00d mov ip, sp

33f8: e8ac000f stmia ip!, {r0, r1, r2, r3}

33fc: e8be000f ldm lr!, {r0, r1, r2, r3}

3400: e8ac000f stmia ip!, {r0, r1, r2, r3}

3404: e8be000f ldm lr!, {r0, r1, r2, r3}

3408: e59ee000 ldr lr, [lr]

340c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3410: e894000f ldm r4, {r0, r1, r2, r3}

3414: e58ce000 str lr, [ip]

3418: ebffff8f bl 325c <mcrypt\_free@@Base+0x2f8>

341c: e3500000 cmp r0, #0

3420: 0a000002 beq 3430 <mcrypt\_module\_close@@Base+0x16c>

3424: e28dd038 add sp, sp, #56 ; 0x38

3428: e8bd4010 pop {r4, lr}

342c: e12fff10 bx r0

3430: e3e00000 mvn r0, #0

3434: e28dd038 add sp, sp, #56 ; 0x38

3438: e8bd8010 pop {r4, pc}

343c: 0001af9c muleq r1, ip, pc ; <UNPREDICTABLE>

3440: e59f3064 ldr r3, [pc, #100] ; 34ac <mcrypt\_module\_close@@Base+0x1e8>

3444: e92d4010 push {r4, lr}

3448: e280e054 add lr, r0, #84 ; 0x54

344c: e24dd038 sub sp, sp, #56 ; 0x38

3450: e08f3003 add r3, pc, r3

3454: e58d3034 str r3, [sp, #52] ; 0x34

3458: e1a04000 mov r4, r0

345c: e8be000f ldm lr!, {r0, r1, r2, r3}

3460: e1a0c00d mov ip, sp

3464: e2844044 add r4, r4, #68 ; 0x44

3468: e8ac000f stmia ip!, {r0, r1, r2, r3}

346c: e8be000f ldm lr!, {r0, r1, r2, r3}

3470: e8ac000f stmia ip!, {r0, r1, r2, r3}

3474: e8be000f ldm lr!, {r0, r1, r2, r3}

3478: e59ee000 ldr lr, [lr]

347c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3480: e894000f ldm r4, {r0, r1, r2, r3}

3484: e58ce000 str lr, [ip]

3488: ebffff73 bl 325c <mcrypt\_free@@Base+0x2f8>

348c: e3500000 cmp r0, #0

3490: 0a000002 beq 34a0 <mcrypt\_module\_close@@Base+0x1dc>

3494: e28dd038 add sp, sp, #56 ; 0x38

3498: e8bd4010 pop {r4, lr}

349c: e12fff10 bx r0

34a0: e3e00000 mvn r0, #0

34a4: e28dd038 add sp, sp, #56 ; 0x38

34a8: e8bd8010 pop {r4, pc}

34ac: 00018e50 andeq r8, r1, r0, asr lr

000034b0 <mcrypt\_enc\_set\_state@@Base>:

34b0: e59f3078 ldr r3, [pc, #120] ; 3530 <mcrypt\_enc\_set\_state@@Base+0x80>

34b4: e92d40f0 push {r4, r5, r6, r7, lr}

34b8: e280e054 add lr, r0, #84 ; 0x54

34bc: e24dd03c sub sp, sp, #60 ; 0x3c

34c0: e08f3003 add r3, pc, r3

34c4: e58d3034 str r3, [sp, #52] ; 0x34

34c8: e1a05000 mov r5, r0

34cc: e1a06001 mov r6, r1

34d0: e1a07002 mov r7, r2

34d4: e8be000f ldm lr!, {r0, r1, r2, r3}

34d8: e1a0c00d mov ip, sp

34dc: e2854044 add r4, r5, #68 ; 0x44

34e0: e8ac000f stmia ip!, {r0, r1, r2, r3}

34e4: e8be000f ldm lr!, {r0, r1, r2, r3}

34e8: e8ac000f stmia ip!, {r0, r1, r2, r3}

34ec: e8be000f ldm lr!, {r0, r1, r2, r3}

34f0: e59ee000 ldr lr, [lr]

34f4: e8ac000f stmia ip!, {r0, r1, r2, r3}

34f8: e894000f ldm r4, {r0, r1, r2, r3}

34fc: e58ce000 str lr, [ip]

3500: ebffff55 bl 325c <mcrypt\_free@@Base+0x2f8>

3504: e2503000 subs r3, r0, #0

3508: 0a000005 beq 3524 <mcrypt\_enc\_set\_state@@Base+0x74>

350c: e1a02007 mov r2, r7

3510: e1a01006 mov r1, r6

3514: e595008c ldr r0, [r5, #140] ; 0x8c

3518: e28dd03c add sp, sp, #60 ; 0x3c

351c: e8bd40f0 pop {r4, r5, r6, r7, lr}

3520: e12fff13 bx r3

3524: e3e00000 mvn r0, #0

3528: e28dd03c add sp, sp, #60 ; 0x3c

352c: e8bd80f0 pop {r4, r5, r6, r7, pc}

3530: 00018cec andeq r8, r1, ip, ror #25

00003534 <mcrypt\_enc\_get\_state@@Base>:

3534: e59f3078 ldr r3, [pc, #120] ; 35b4 <mcrypt\_enc\_get\_state@@Base+0x80>

3538: e92d40f0 push {r4, r5, r6, r7, lr}

353c: e280e054 add lr, r0, #84 ; 0x54

3540: e24dd03c sub sp, sp, #60 ; 0x3c

3544: e08f3003 add r3, pc, r3

3548: e58d3034 str r3, [sp, #52] ; 0x34

354c: e1a05000 mov r5, r0

3550: e1a06001 mov r6, r1

3554: e1a07002 mov r7, r2

3558: e8be000f ldm lr!, {r0, r1, r2, r3}

355c: e1a0c00d mov ip, sp

3560: e2854044 add r4, r5, #68 ; 0x44

3564: e8ac000f stmia ip!, {r0, r1, r2, r3}

3568: e8be000f ldm lr!, {r0, r1, r2, r3}

356c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3570: e8be000f ldm lr!, {r0, r1, r2, r3}

3574: e59ee000 ldr lr, [lr]

3578: e8ac000f stmia ip!, {r0, r1, r2, r3}

357c: e894000f ldm r4, {r0, r1, r2, r3}

3580: e58ce000 str lr, [ip]

3584: ebffff34 bl 325c <mcrypt\_free@@Base+0x2f8>

3588: e2503000 subs r3, r0, #0

358c: 0a000005 beq 35a8 <mcrypt\_enc\_get\_state@@Base+0x74>

3590: e1a02007 mov r2, r7

3594: e1a01006 mov r1, r6

3598: e595008c ldr r0, [r5, #140] ; 0x8c

359c: e28dd03c add sp, sp, #60 ; 0x3c

35a0: e8bd40f0 pop {r4, r5, r6, r7, lr}

35a4: e12fff13 bx r3

35a8: e3e00000 mvn r0, #0

35ac: e28dd03c add sp, sp, #60 ; 0x3c

35b0: e8bd80f0 pop {r4, r5, r6, r7, pc}

35b4: 00018c84 andeq r8, r1, r4, lsl #25

000035b8 <mcrypt\_enc\_get\_block\_size@@Base>:

35b8: e59030a4 ldr r3, [r0, #164] ; 0xa4

35bc: e12fff13 bx r3

35c0: e59f3060 ldr r3, [pc, #96] ; 3628 <mcrypt\_enc\_get\_block\_size@@Base+0x70>

35c4: e92d4010 push {r4, lr}

35c8: e280e010 add lr, r0, #16

35cc: e24dd038 sub sp, sp, #56 ; 0x38

35d0: e08f3003 add r3, pc, r3

35d4: e58d3034 str r3, [sp, #52] ; 0x34

35d8: e1a04000 mov r4, r0

35dc: e8be000f ldm lr!, {r0, r1, r2, r3}

35e0: e1a0c00d mov ip, sp

35e4: e8ac000f stmia ip!, {r0, r1, r2, r3}

35e8: e8be000f ldm lr!, {r0, r1, r2, r3}

35ec: e8ac000f stmia ip!, {r0, r1, r2, r3}

35f0: e8be000f ldm lr!, {r0, r1, r2, r3}

35f4: e59ee000 ldr lr, [lr]

35f8: e8ac000f stmia ip!, {r0, r1, r2, r3}

35fc: e894000f ldm r4, {r0, r1, r2, r3}

3600: e58ce000 str lr, [ip]

3604: ebffff14 bl 325c <mcrypt\_free@@Base+0x2f8>

3608: e3500000 cmp r0, #0

360c: 0a000002 beq 361c <mcrypt\_enc\_get\_block\_size@@Base+0x64>

3610: e28dd038 add sp, sp, #56 ; 0x38

3614: e8bd4010 pop {r4, lr}

3618: e12fff10 bx r0

361c: e3e00000 mvn r0, #0

3620: e28dd038 add sp, sp, #56 ; 0x38

3624: e8bd8010 pop {r4, pc}

3628: 00019974 andeq r9, r1, r4, ror r9

0000362c <mcrypt\_enc\_get\_key\_size@@Base>:

362c: e59f3060 ldr r3, [pc, #96] ; 3694 <mcrypt\_enc\_get\_key\_size@@Base+0x68>

3630: e92d4010 push {r4, lr}

3634: e280e010 add lr, r0, #16

3638: e24dd038 sub sp, sp, #56 ; 0x38

363c: e08f3003 add r3, pc, r3

3640: e58d3034 str r3, [sp, #52] ; 0x34

3644: e1a04000 mov r4, r0

3648: e8be000f ldm lr!, {r0, r1, r2, r3}

364c: e1a0c00d mov ip, sp

3650: e8ac000f stmia ip!, {r0, r1, r2, r3}

3654: e8be000f ldm lr!, {r0, r1, r2, r3}

3658: e8ac000f stmia ip!, {r0, r1, r2, r3}

365c: e8be000f ldm lr!, {r0, r1, r2, r3}

3660: e59ee000 ldr lr, [lr]

3664: e8ac000f stmia ip!, {r0, r1, r2, r3}

3668: e894000f ldm r4, {r0, r1, r2, r3}

366c: e58ce000 str lr, [ip]

3670: ebfffef9 bl 325c <mcrypt\_free@@Base+0x2f8>

3674: e3500000 cmp r0, #0

3678: 0a000002 beq 3688 <mcrypt\_enc\_get\_key\_size@@Base+0x5c>

367c: e28dd038 add sp, sp, #56 ; 0x38

3680: e8bd4010 pop {r4, lr}

3684: e12fff10 bx r0

3688: e3e00000 mvn r0, #0

368c: e28dd038 add sp, sp, #56 ; 0x38

3690: e8bd8010 pop {r4, pc}

3694: 0001ad9c muleq r1, ip, sp

00003698 <mcrypt\_enc\_get\_supported\_key\_sizes@@Base>:

3698: e59f309c ldr r3, [pc, #156] ; 373c <mcrypt\_enc\_get\_supported\_key\_sizes@@Base+0xa4>

369c: e92d4070 push {r4, r5, r6, lr}

36a0: e280e010 add lr, r0, #16

36a4: e24dd038 sub sp, sp, #56 ; 0x38

36a8: e08f3003 add r3, pc, r3

36ac: e58d3034 str r3, [sp, #52] ; 0x34

36b0: e1a04000 mov r4, r0

36b4: e1a05001 mov r5, r1

36b8: e8be000f ldm lr!, {r0, r1, r2, r3}

36bc: e1a0c00d mov ip, sp

36c0: e8ac000f stmia ip!, {r0, r1, r2, r3}

36c4: e8be000f ldm lr!, {r0, r1, r2, r3}

36c8: e8ac000f stmia ip!, {r0, r1, r2, r3}

36cc: e8be000f ldm lr!, {r0, r1, r2, r3}

36d0: e59ee000 ldr lr, [lr]

36d4: e8ac000f stmia ip!, {r0, r1, r2, r3}

36d8: e894000f ldm r4, {r0, r1, r2, r3}

36dc: e58ce000 str lr, [ip]

36e0: ebfffedd bl 325c <mcrypt\_free@@Base+0x2f8>

36e4: e2504000 subs r4, r0, #0

36e8: 05854000 streq r4, [r5]

36ec: 0a00000f beq 3730 <mcrypt\_enc\_get\_supported\_key\_sizes@@Base+0x98>

36f0: e1a00005 mov r0, r5

36f4: e12fff34 blx r4

36f8: e2506000 subs r6, r0, #0

36fc: 01a04006 moveq r4, r6

3700: 0a00000a beq 3730 <mcrypt\_enc\_get\_supported\_key\_sizes@@Base+0x98>

3704: e5954000 ldr r4, [r5]

3708: e3540000 cmp r4, #0

370c: 0a000007 beq 3730 <mcrypt\_enc\_get\_supported\_key\_sizes@@Base+0x98>

3710: e1a00104 lsl r0, r4, #2

3714: ebfffaf6 bl 22f4 <malloc@plt>

3718: e2504000 subs r4, r0, #0

371c: 0a000003 beq 3730 <mcrypt\_enc\_get\_supported\_key\_sizes@@Base+0x98>

3720: e5952000 ldr r2, [r5]

3724: e1a01006 mov r1, r6

3728: e1a02102 lsl r2, r2, #2

372c: ebfffad5 bl 2288 <memcpy@plt>

3730: e1a00004 mov r0, r4

3734: e28dd038 add sp, sp, #56 ; 0x38

3738: e8bd8070 pop {r4, r5, r6, pc}

373c: 0001ad50 andeq sl, r1, r0, asr sp

00003740 <mcrypt\_enc\_is\_block\_algorithm@@Base>:

3740: e59f3060 ldr r3, [pc, #96] ; 37a8 <mcrypt\_enc\_is\_block\_algorithm@@Base+0x68>

3744: e92d4010 push {r4, lr}

3748: e280e010 add lr, r0, #16

374c: e24dd038 sub sp, sp, #56 ; 0x38

3750: e08f3003 add r3, pc, r3

3754: e58d3034 str r3, [sp, #52] ; 0x34

3758: e1a04000 mov r4, r0

375c: e8be000f ldm lr!, {r0, r1, r2, r3}

3760: e1a0c00d mov ip, sp

3764: e8ac000f stmia ip!, {r0, r1, r2, r3}

3768: e8be000f ldm lr!, {r0, r1, r2, r3}

376c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3770: e8be000f ldm lr!, {r0, r1, r2, r3}

3774: e59ee000 ldr lr, [lr]

3778: e8ac000f stmia ip!, {r0, r1, r2, r3}

377c: e894000f ldm r4, {r0, r1, r2, r3}

3780: e58ce000 str lr, [ip]

3784: ebfffeb4 bl 325c <mcrypt\_free@@Base+0x2f8>

3788: e3500000 cmp r0, #0

378c: 0a000002 beq 379c <mcrypt\_enc\_is\_block\_algorithm@@Base+0x5c>

3790: e28dd038 add sp, sp, #56 ; 0x38

3794: e8bd4010 pop {r4, lr}

3798: e12fff10 bx r0

379c: e3e00000 mvn r0, #0

37a0: e28dd038 add sp, sp, #56 ; 0x38

37a4: e8bd8010 pop {r4, pc}

37a8: 0001ac6c andeq sl, r1, ip, ror #24

37ac: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

37b0: e24dd03c sub sp, sp, #60 ; 0x3c

37b4: e1a05001 mov r5, r1

37b8: e1a06002 mov r6, r2

37bc: e1a07003 mov r7, r3

37c0: e1a04000 mov r4, r0

37c4: e1cd85d8 ldrd r8, [sp, #88] ; 0x58

37c8: ebfffadb bl 233c <mcrypt\_enc\_is\_block\_algorithm@plt>

37cc: e3500000 cmp r0, #0

37d0: 1a000019 bne 383c <mcrypt\_enc\_is\_block\_algorithm@@Base+0xfc>

37d4: e59f30c8 ldr r3, [pc, #200] ; 38a4 <mcrypt\_enc\_is\_block\_algorithm@@Base+0x164>

37d8: e284e010 add lr, r4, #16

37dc: e08f3003 add r3, pc, r3

37e0: e58d3034 str r3, [sp, #52] ; 0x34

37e4: e8be000f ldm lr!, {r0, r1, r2, r3}

37e8: e1a0c00d mov ip, sp

37ec: e8ac000f stmia ip!, {r0, r1, r2, r3}

37f0: e8be000f ldm lr!, {r0, r1, r2, r3}

37f4: e8ac000f stmia ip!, {r0, r1, r2, r3}

37f8: e8be000f ldm lr!, {r0, r1, r2, r3}

37fc: e59ee000 ldr lr, [lr]

3800: e8ac000f stmia ip!, {r0, r1, r2, r3}

3804: e894000f ldm r4, {r0, r1, r2, r3}

3808: e58ce000 str lr, [ip]

380c: ebfffe92 bl 325c <mcrypt\_free@@Base+0x2f8>

3810: e250e000 subs lr, r0, #0

3814: 0a00001f beq 3898 <mcrypt\_enc\_is\_block\_algorithm@@Base+0x158>

3818: e58d9058 str r9, [sp, #88] ; 0x58

381c: e1a03008 mov r3, r8

3820: e1a02007 mov r2, r7

3824: e1a01006 mov r1, r6

3828: e1a00005 mov r0, r5

382c: e1a0c00e mov ip, lr

3830: e28dd03c add sp, sp, #60 ; 0x3c

3834: e8bd43f0 pop {r4, r5, r6, r7, r8, r9, lr}

3838: e12fff1c bx ip

383c: e59f3064 ldr r3, [pc, #100] ; 38a8 <mcrypt\_enc\_is\_block\_algorithm@@Base+0x168>

3840: e284e010 add lr, r4, #16

3844: e08f3003 add r3, pc, r3

3848: e58d3034 str r3, [sp, #52] ; 0x34

384c: e8be000f ldm lr!, {r0, r1, r2, r3}

3850: e1a0c00d mov ip, sp

3854: e8ac000f stmia ip!, {r0, r1, r2, r3}

3858: e8be000f ldm lr!, {r0, r1, r2, r3}

385c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3860: e8be000f ldm lr!, {r0, r1, r2, r3}

3864: e59ee000 ldr lr, [lr]

3868: e8ac000f stmia ip!, {r0, r1, r2, r3}

386c: e894000f ldm r4, {r0, r1, r2, r3}

3870: e58ce000 str lr, [ip]

3874: ebfffe78 bl 325c <mcrypt\_free@@Base+0x2f8>

3878: e2503000 subs r3, r0, #0

387c: 0a000005 beq 3898 <mcrypt\_enc\_is\_block\_algorithm@@Base+0x158>

3880: e1a02007 mov r2, r7

3884: e1a01006 mov r1, r6

3888: e1a00005 mov r0, r5

388c: e28dd03c add sp, sp, #60 ; 0x3c

3890: e8bd43f0 pop {r4, r5, r6, r7, r8, r9, lr}

3894: e12fff13 bx r3

3898: e3e00001 mvn r0, #1

389c: e28dd03c add sp, sp, #60 ; 0x3c

38a0: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

38a4: 0001ab5c andeq sl, r1, ip, asr fp

38a8: 0001aaf4 strdeq sl, [r1], -r4

000038ac <mcrypt\_enc\_get\_algorithms\_name@@Base>:

38ac: e59f3060 ldr r3, [pc, #96] ; 3914 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x68>

38b0: e92d4010 push {r4, lr}

38b4: e280e010 add lr, r0, #16

38b8: e24dd038 sub sp, sp, #56 ; 0x38

38bc: e08f3003 add r3, pc, r3

38c0: e58d3034 str r3, [sp, #52] ; 0x34

38c4: e1a04000 mov r4, r0

38c8: e8be000f ldm lr!, {r0, r1, r2, r3}

38cc: e1a0c00d mov ip, sp

38d0: e8ac000f stmia ip!, {r0, r1, r2, r3}

38d4: e8be000f ldm lr!, {r0, r1, r2, r3}

38d8: e8ac000f stmia ip!, {r0, r1, r2, r3}

38dc: e8be000f ldm lr!, {r0, r1, r2, r3}

38e0: e59ee000 ldr lr, [lr]

38e4: e8ac000f stmia ip!, {r0, r1, r2, r3}

38e8: e894000f ldm r4, {r0, r1, r2, r3}

38ec: e58ce000 str lr, [ip]

38f0: ebfffe59 bl 325c <mcrypt\_free@@Base+0x2f8>

38f4: e2503000 subs r3, r0, #0

38f8: 0a000003 beq 390c <mcrypt\_enc\_get\_algorithms\_name@@Base+0x60>

38fc: e12fff33 blx r3

3900: e28dd038 add sp, sp, #56 ; 0x38

3904: e8bd4010 pop {r4, lr}

3908: eafffa67 b 22ac <strdup@plt>

390c: e28dd038 add sp, sp, #56 ; 0x38

3910: e8bd8010 pop {r4, pc}

3914: 0001ab64 andeq sl, r1, r4, ror #22

3918: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

391c: e1a05000 mov r5, r0

3920: e280e054 add lr, r0, #84 ; 0x54

3924: e59f0088 ldr r0, [pc, #136] ; 39b4 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x108>

3928: e24dd03c sub sp, sp, #60 ; 0x3c

392c: e08f0000 add r0, pc, r0

3930: e58d0034 str r0, [sp, #52] ; 0x34

3934: e1a06001 mov r6, r1

3938: e1a07002 mov r7, r2

393c: e1a08003 mov r8, r3

3940: e8be000f ldm lr!, {r0, r1, r2, r3}

3944: e1a0c00d mov ip, sp

3948: e2854044 add r4, r5, #68 ; 0x44

394c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3950: e8be000f ldm lr!, {r0, r1, r2, r3}

3954: e59d9058 ldr r9, [sp, #88] ; 0x58

3958: e8ac000f stmia ip!, {r0, r1, r2, r3}

395c: e8be000f ldm lr!, {r0, r1, r2, r3}

3960: e59ee000 ldr lr, [lr]

3964: e8ac000f stmia ip!, {r0, r1, r2, r3}

3968: e894000f ldm r4, {r0, r1, r2, r3}

396c: e58ce000 str lr, [ip]

3970: ebfffe39 bl 325c <mcrypt\_free@@Base+0x2f8>

3974: e2504000 subs r4, r0, #0

3978: 0a00000a beq 39a8 <mcrypt\_enc\_get\_algorithms\_name@@Base+0xfc>

397c: e1a00005 mov r0, r5

3980: ebfffa64 bl 2318 <mcrypt\_enc\_get\_block\_size@plt>

3984: e1a03009 mov r3, r9

3988: e1a02008 mov r2, r8

398c: e1a01007 mov r1, r7

3990: e1a0c004 mov ip, r4

3994: e58d0058 str r0, [sp, #88] ; 0x58

3998: e1a00006 mov r0, r6

399c: e28dd03c add sp, sp, #60 ; 0x3c

39a0: e8bd43f0 pop {r4, r5, r6, r7, r8, r9, lr}

39a4: e12fff1c bx ip

39a8: e3e00000 mvn r0, #0

39ac: e28dd03c add sp, sp, #60 ; 0x3c

39b0: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

39b4: 000188b8 ; <UNDEFINED> instruction: 0x000188b8

39b8: e59f306c ldr r3, [pc, #108] ; 3a2c <mcrypt\_enc\_get\_algorithms\_name@@Base+0x180>

39bc: e92d4030 push {r4, r5, lr}

39c0: e280e054 add lr, r0, #84 ; 0x54

39c4: e24dd03c sub sp, sp, #60 ; 0x3c

39c8: e08f3003 add r3, pc, r3

39cc: e58d3034 str r3, [sp, #52] ; 0x34

39d0: e1a04000 mov r4, r0

39d4: e1a05001 mov r5, r1

39d8: e8be000f ldm lr!, {r0, r1, r2, r3}

39dc: e1a0c00d mov ip, sp

39e0: e2844044 add r4, r4, #68 ; 0x44

39e4: e8ac000f stmia ip!, {r0, r1, r2, r3}

39e8: e8be000f ldm lr!, {r0, r1, r2, r3}

39ec: e8ac000f stmia ip!, {r0, r1, r2, r3}

39f0: e8be000f ldm lr!, {r0, r1, r2, r3}

39f4: e59ee000 ldr lr, [lr]

39f8: e8ac000f stmia ip!, {r0, r1, r2, r3}

39fc: e894000f ldm r4, {r0, r1, r2, r3}

3a00: e58ce000 str lr, [ip]

3a04: ebfffe14 bl 325c <mcrypt\_free@@Base+0x2f8>

3a08: e2503000 subs r3, r0, #0

3a0c: 0a000003 beq 3a20 <mcrypt\_enc\_get\_algorithms\_name@@Base+0x174>

3a10: e1a00005 mov r0, r5

3a14: e28dd03c add sp, sp, #60 ; 0x3c

3a18: e8bd4030 pop {r4, r5, lr}

3a1c: e12fff13 bx r3

3a20: e3e00000 mvn r0, #0

3a24: e28dd03c add sp, sp, #60 ; 0x3c

3a28: e8bd8030 pop {r4, r5, pc}

3a2c: 00018834 andeq r8, r1, r4, lsr r8

3a30: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

3a34: e24dd014 sub sp, sp, #20

3a38: e1a04000 mov r4, r0

3a3c: e1a06001 mov r6, r1

3a40: e1a08002 mov r8, r2

3a44: e1a09003 mov r9, r3

3a48: e5905094 ldr r5, [r0, #148] ; 0x94

3a4c: ebfffa31 bl 2318 <mcrypt\_enc\_get\_block\_size@plt>

3a50: e594e09c ldr lr, [r4, #156] ; 0x9c

3a54: e594c088 ldr ip, [r4, #136] ; 0x88

3a58: e59470a0 ldr r7, [r4, #160] ; 0xa0

3a5c: e1a02009 mov r2, r9

3a60: e1a01008 mov r1, r8

3a64: e58d7008 str r7, [sp, #8]

3a68: e88d5000 stm sp, {ip, lr}

3a6c: e1a03000 mov r3, r0

3a70: e1a00006 mov r0, r6

3a74: e12fff35 blx r5

3a78: e28dd014 add sp, sp, #20

3a7c: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

3a80: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

3a84: e24dd014 sub sp, sp, #20

3a88: e1a04000 mov r4, r0

3a8c: e1a06001 mov r6, r1

3a90: e1a08002 mov r8, r2

3a94: e1a09003 mov r9, r3

3a98: e5905098 ldr r5, [r0, #152] ; 0x98

3a9c: ebfffa1d bl 2318 <mcrypt\_enc\_get\_block\_size@plt>

3aa0: e594e09c ldr lr, [r4, #156] ; 0x9c

3aa4: e594c088 ldr ip, [r4, #136] ; 0x88

3aa8: e59470a0 ldr r7, [r4, #160] ; 0xa0

3aac: e1a02009 mov r2, r9

3ab0: e1a01008 mov r1, r8

3ab4: e58d7008 str r7, [sp, #8]

3ab8: e88d5000 stm sp, {ip, lr}

3abc: e1a03000 mov r3, r0

3ac0: e1a00006 mov r0, r6

3ac4: e12fff35 blx r5

3ac8: e28dd014 add sp, sp, #20

3acc: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

00003ad0 <mcrypt\_enc\_get\_modes\_name@@Base>:

3ad0: e59f3064 ldr r3, [pc, #100] ; 3b3c <mcrypt\_enc\_get\_modes\_name@@Base+0x6c>

3ad4: e92d4010 push {r4, lr}

3ad8: e280e054 add lr, r0, #84 ; 0x54

3adc: e24dd038 sub sp, sp, #56 ; 0x38

3ae0: e08f3003 add r3, pc, r3

3ae4: e58d3034 str r3, [sp, #52] ; 0x34

3ae8: e1a04000 mov r4, r0

3aec: e8be000f ldm lr!, {r0, r1, r2, r3}

3af0: e1a0c00d mov ip, sp

3af4: e2844044 add r4, r4, #68 ; 0x44

3af8: e8ac000f stmia ip!, {r0, r1, r2, r3}

3afc: e8be000f ldm lr!, {r0, r1, r2, r3}

3b00: e8ac000f stmia ip!, {r0, r1, r2, r3}

3b04: e8be000f ldm lr!, {r0, r1, r2, r3}

3b08: e59ee000 ldr lr, [lr]

3b0c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3b10: e894000f ldm r4, {r0, r1, r2, r3}

3b14: e58ce000 str lr, [ip]

3b18: ebfffdcf bl 325c <mcrypt\_free@@Base+0x2f8>

3b1c: e2503000 subs r3, r0, #0

3b20: 0a000003 beq 3b34 <mcrypt\_enc\_get\_modes\_name@@Base+0x64>

3b24: e12fff33 blx r3

3b28: e28dd038 add sp, sp, #56 ; 0x38

3b2c: e8bd4010 pop {r4, lr}

3b30: eafff9dd b 22ac <strdup@plt>

3b34: e28dd038 add sp, sp, #56 ; 0x38

3b38: e8bd8010 pop {r4, pc}

3b3c: 000187a0 andeq r8, r1, r0, lsr #15

00003b40 <mcrypt\_enc\_is\_block\_mode@@Base>:

3b40: e59f3064 ldr r3, [pc, #100] ; 3bac <mcrypt\_enc\_is\_block\_mode@@Base+0x6c>

3b44: e92d4010 push {r4, lr}

3b48: e280e054 add lr, r0, #84 ; 0x54

3b4c: e24dd038 sub sp, sp, #56 ; 0x38

3b50: e08f3003 add r3, pc, r3

3b54: e58d3034 str r3, [sp, #52] ; 0x34

3b58: e1a04000 mov r4, r0

3b5c: e8be000f ldm lr!, {r0, r1, r2, r3}

3b60: e1a0c00d mov ip, sp

3b64: e2844044 add r4, r4, #68 ; 0x44

3b68: e8ac000f stmia ip!, {r0, r1, r2, r3}

3b6c: e8be000f ldm lr!, {r0, r1, r2, r3}

3b70: e8ac000f stmia ip!, {r0, r1, r2, r3}

3b74: e8be000f ldm lr!, {r0, r1, r2, r3}

3b78: e59ee000 ldr lr, [lr]

3b7c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3b80: e894000f ldm r4, {r0, r1, r2, r3}

3b84: e58ce000 str lr, [ip]

3b88: ebfffdb3 bl 325c <mcrypt\_free@@Base+0x2f8>

3b8c: e3500000 cmp r0, #0

3b90: 0a000002 beq 3ba0 <mcrypt\_enc\_is\_block\_mode@@Base+0x60>

3b94: e28dd038 add sp, sp, #56 ; 0x38

3b98: e8bd4010 pop {r4, lr}

3b9c: e12fff10 bx r0

3ba0: e3e00000 mvn r0, #0

3ba4: e28dd038 add sp, sp, #56 ; 0x38

3ba8: e8bd8010 pop {r4, pc}

3bac: 000186f4 strdeq r8, [r1], -r4

00003bb0 <mcrypt\_enc\_mode\_has\_iv@@Base>:

3bb0: e59f3064 ldr r3, [pc, #100] ; 3c1c <mcrypt\_enc\_mode\_has\_iv@@Base+0x6c>

3bb4: e92d4010 push {r4, lr}

3bb8: e280e054 add lr, r0, #84 ; 0x54

3bbc: e24dd038 sub sp, sp, #56 ; 0x38

3bc0: e08f3003 add r3, pc, r3

3bc4: e58d3034 str r3, [sp, #52] ; 0x34

3bc8: e1a04000 mov r4, r0

3bcc: e8be000f ldm lr!, {r0, r1, r2, r3}

3bd0: e1a0c00d mov ip, sp

3bd4: e2844044 add r4, r4, #68 ; 0x44

3bd8: e8ac000f stmia ip!, {r0, r1, r2, r3}

3bdc: e8be000f ldm lr!, {r0, r1, r2, r3}

3be0: e8ac000f stmia ip!, {r0, r1, r2, r3}

3be4: e8be000f ldm lr!, {r0, r1, r2, r3}

3be8: e59ee000 ldr lr, [lr]

3bec: e8ac000f stmia ip!, {r0, r1, r2, r3}

3bf0: e894000f ldm r4, {r0, r1, r2, r3}

3bf4: e58ce000 str lr, [ip]

3bf8: ebfffd97 bl 325c <mcrypt\_free@@Base+0x2f8>

3bfc: e3500000 cmp r0, #0

3c00: 0a000002 beq 3c10 <mcrypt\_enc\_mode\_has\_iv@@Base+0x60>

3c04: e28dd038 add sp, sp, #56 ; 0x38

3c08: e8bd4010 pop {r4, lr}

3c0c: e12fff10 bx r0

3c10: e3e00000 mvn r0, #0

3c14: e28dd038 add sp, sp, #56 ; 0x38

3c18: e8bd8010 pop {r4, pc}

3c1c: 00018674 andeq r8, r1, r4, ror r6

00003c20 <mcrypt\_enc\_is\_block\_algorithm\_mode@@Base>:

3c20: e59f3064 ldr r3, [pc, #100] ; 3c8c <mcrypt\_enc\_is\_block\_algorithm\_mode@@Base+0x6c>

3c24: e92d4010 push {r4, lr}

3c28: e280e054 add lr, r0, #84 ; 0x54

3c2c: e24dd038 sub sp, sp, #56 ; 0x38

3c30: e08f3003 add r3, pc, r3

3c34: e58d3034 str r3, [sp, #52] ; 0x34

3c38: e1a04000 mov r4, r0

3c3c: e8be000f ldm lr!, {r0, r1, r2, r3}

3c40: e1a0c00d mov ip, sp

3c44: e2844044 add r4, r4, #68 ; 0x44

3c48: e8ac000f stmia ip!, {r0, r1, r2, r3}

3c4c: e8be000f ldm lr!, {r0, r1, r2, r3}

3c50: e8ac000f stmia ip!, {r0, r1, r2, r3}

3c54: e8be000f ldm lr!, {r0, r1, r2, r3}

3c58: e59ee000 ldr lr, [lr]

3c5c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3c60: e894000f ldm r4, {r0, r1, r2, r3}

3c64: e58ce000 str lr, [ip]

3c68: ebfffd7b bl 325c <mcrypt\_free@@Base+0x2f8>

3c6c: e3500000 cmp r0, #0

3c70: 0a000002 beq 3c80 <mcrypt\_enc\_is\_block\_algorithm\_mode@@Base+0x60>

3c74: e28dd038 add sp, sp, #56 ; 0x38

3c78: e8bd4010 pop {r4, lr}

3c7c: e12fff10 bx r0

3c80: e3e00000 mvn r0, #0

3c84: e28dd038 add sp, sp, #56 ; 0x38

3c88: e8bd8010 pop {r4, pc}

3c8c: 0001862c andeq r8, r1, ip, lsr #12

00003c90 <mcrypt\_module\_open@@Base>:

3c90: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

3c94: e1a07000 mov r7, r0

3c98: e24dd03c sub sp, sp, #60 ; 0x3c

3c9c: e1a05001 mov r5, r1

3ca0: e3a00001 mov r0, #1

3ca4: e3a010a8 mov r1, #168 ; 0xa8

3ca8: e1a09002 mov r9, r2

3cac: e1a06003 mov r6, r3

3cb0: ebfff95f bl 2234 <calloc@plt>

3cb4: e2504000 subs r4, r0, #0

3cb8: 0a000076 beq 3e98 <mcrypt\_module\_open@@Base+0x208>

3cbc: e1a03007 mov r3, r7

3cc0: e1a02006 mov r2, r6

3cc4: e1a01005 mov r1, r5

3cc8: ebfffda1 bl 3354 <mcrypt\_module\_close@@Base+0x90>

3ccc: e2507000 subs r7, r0, #0

3cd0: 0a00007e beq 3ed0 <mcrypt\_module\_open@@Base+0x240>

3cd4: e2848044 add r8, r4, #68 ; 0x44

3cd8: e1a01005 mov r1, r5

3cdc: e1a03009 mov r3, r9

3ce0: e1a02006 mov r2, r6

3ce4: e1a00008 mov r0, r8

3ce8: ebfffd99 bl 3354 <mcrypt\_module\_close@@Base+0x90>

3cec: e2505000 subs r5, r0, #0

3cf0: 0a000080 beq 3ef8 <mcrypt\_module\_open@@Base+0x268>

3cf4: e59f3224 ldr r3, [pc, #548] ; 3f20 <mcrypt\_module\_open@@Base+0x290>

3cf8: e2845010 add r5, r4, #16

3cfc: e08f3003 add r3, pc, r3

3d00: e58d3034 str r3, [sp, #52] ; 0x34

3d04: e8b5000f ldm r5!, {r0, r1, r2, r3}

3d08: e1a0c00d mov ip, sp

3d0c: e2847010 add r7, r4, #16

3d10: e2846054 add r6, r4, #84 ; 0x54

3d14: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d18: e8b5000f ldm r5!, {r0, r1, r2, r3}

3d1c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d20: e8b5000f ldm r5!, {r0, r1, r2, r3}

3d24: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d28: e5953000 ldr r3, [r5]

3d2c: e58c3000 str r3, [ip]

3d30: e894000f ldm r4, {r0, r1, r2, r3}

3d34: ebfffd48 bl 325c <mcrypt\_free@@Base+0x2f8>

3d38: e59f31e4 ldr r3, [pc, #484] ; 3f24 <mcrypt\_module\_open@@Base+0x294>

3d3c: e1a0e007 mov lr, r7

3d40: e08f3003 add r3, pc, r3

3d44: e1a0c00d mov ip, sp

3d48: e584009c str r0, [r4, #156] ; 0x9c

3d4c: e58d3034 str r3, [sp, #52] ; 0x34

3d50: e8be000f ldm lr!, {r0, r1, r2, r3}

3d54: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d58: e8be000f ldm lr!, {r0, r1, r2, r3}

3d5c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d60: e8be000f ldm lr!, {r0, r1, r2, r3}

3d64: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d68: e5953000 ldr r3, [r5]

3d6c: e58c3000 str r3, [ip]

3d70: e894000f ldm r4, {r0, r1, r2, r3}

3d74: ebfffd38 bl 325c <mcrypt\_free@@Base+0x2f8>

3d78: e59f31a8 ldr r3, [pc, #424] ; 3f28 <mcrypt\_module\_open@@Base+0x298>

3d7c: e1a0c00d mov ip, sp

3d80: e08f3003 add r3, pc, r3

3d84: e58400a0 str r0, [r4, #160] ; 0xa0

3d88: e58d3034 str r3, [sp, #52] ; 0x34

3d8c: e8b6000f ldm r6!, {r0, r1, r2, r3}

3d90: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d94: e8b6000f ldm r6!, {r0, r1, r2, r3}

3d98: e8ac000f stmia ip!, {r0, r1, r2, r3}

3d9c: e8b6000f ldm r6!, {r0, r1, r2, r3}

3da0: e8ac000f stmia ip!, {r0, r1, r2, r3}

3da4: e5963000 ldr r3, [r6]

3da8: e58c3000 str r3, [ip]

3dac: e898000f ldm r8, {r0, r1, r2, r3}

3db0: ebfffd29 bl 325c <mcrypt\_free@@Base+0x2f8>

3db4: e59f3170 ldr r3, [pc, #368] ; 3f2c <mcrypt\_module\_open@@Base+0x29c>

3db8: e284e054 add lr, r4, #84 ; 0x54

3dbc: e08f3003 add r3, pc, r3

3dc0: e1a0c00d mov ip, sp

3dc4: e5840094 str r0, [r4, #148] ; 0x94

3dc8: e58d3034 str r3, [sp, #52] ; 0x34

3dcc: e8be000f ldm lr!, {r0, r1, r2, r3}

3dd0: e8ac000f stmia ip!, {r0, r1, r2, r3}

3dd4: e8be000f ldm lr!, {r0, r1, r2, r3}

3dd8: e8ac000f stmia ip!, {r0, r1, r2, r3}

3ddc: e8be000f ldm lr!, {r0, r1, r2, r3}

3de0: e8ac000f stmia ip!, {r0, r1, r2, r3}

3de4: e5963000 ldr r3, [r6]

3de8: e58c3000 str r3, [ip]

3dec: e898000f ldm r8, {r0, r1, r2, r3}

3df0: ebfffd19 bl 325c <mcrypt\_free@@Base+0x2f8>

3df4: e59f3134 ldr r3, [pc, #308] ; 3f30 <mcrypt\_module\_open@@Base+0x2a0>

3df8: e1a0e007 mov lr, r7

3dfc: e08f3003 add r3, pc, r3

3e00: e1a0c00d mov ip, sp

3e04: e5840098 str r0, [r4, #152] ; 0x98

3e08: e58d3034 str r3, [sp, #52] ; 0x34

3e0c: e8be000f ldm lr!, {r0, r1, r2, r3}

3e10: e8ac000f stmia ip!, {r0, r1, r2, r3}

3e14: e8be000f ldm lr!, {r0, r1, r2, r3}

3e18: e8ac000f stmia ip!, {r0, r1, r2, r3}

3e1c: e8be000f ldm lr!, {r0, r1, r2, r3}

3e20: e8ac000f stmia ip!, {r0, r1, r2, r3}

3e24: e5953000 ldr r3, [r5]

3e28: e58c3000 str r3, [ip]

3e2c: e894000f ldm r4, {r0, r1, r2, r3}

3e30: ebfffd09 bl 325c <mcrypt\_free@@Base+0x2f8>

3e34: e594309c ldr r3, [r4, #156] ; 0x9c

3e38: e3530000 cmp r3, #0

3e3c: e58400a4 str r0, [r4, #164] ; 0xa4

3e40: 0a000017 beq 3ea4 <mcrypt\_module\_open@@Base+0x214>

3e44: e59430a0 ldr r3, [r4, #160] ; 0xa0

3e48: e3530000 cmp r3, #0

3e4c: 0a000014 beq 3ea4 <mcrypt\_module\_open@@Base+0x214>

3e50: e5943094 ldr r3, [r4, #148] ; 0x94

3e54: e3530000 cmp r3, #0

3e58: 0a000011 beq 3ea4 <mcrypt\_module\_open@@Base+0x214>

3e5c: e5945098 ldr r5, [r4, #152] ; 0x98

3e60: e16f0f10 clz r0, r0

3e64: e3550000 cmp r5, #0

3e68: e1a002a0 lsr r0, r0, #5

3e6c: 11a05000 movne r5, r0

3e70: 03a05001 moveq r5, #1

3e74: e3550000 cmp r5, #0

3e78: 1a000009 bne 3ea4 <mcrypt\_module\_open@@Base+0x214>

3e7c: e1a00004 mov r0, r4

3e80: ebfff945 bl 239c <mcrypt\_enc\_is\_block\_algorithm\_mode@plt>

3e84: e1a06000 mov r6, r0

3e88: e1a00004 mov r0, r4

3e8c: ebfff92a bl 233c <mcrypt\_enc\_is\_block\_algorithm@plt>

3e90: e1560000 cmp r6, r0

3e94: 1a000013 bne 3ee8 <mcrypt\_module\_open@@Base+0x258>

3e98: e1a00004 mov r0, r4

3e9c: e28dd03c add sp, sp, #60 ; 0x3c

3ea0: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

3ea4: e8b7000f ldm r7!, {r0, r1, r2, r3}

3ea8: e1a0c00d mov ip, sp

3eac: e8ac000f stmia ip!, {r0, r1, r2, r3}

3eb0: e8b7000f ldm r7!, {r0, r1, r2, r3}

3eb4: e8ac000f stmia ip!, {r0, r1, r2, r3}

3eb8: e8b7000f ldm r7!, {r0, r1, r2, r3}

3ebc: e8ac000f stmia ip!, {r0, r1, r2, r3}

3ec0: e5973000 ldr r3, [r7]

3ec4: e58c3000 str r3, [ip]

3ec8: e894000f ldm r4, {r0, r1, r2, r3}

3ecc: ebfffcdd bl 3248 <mcrypt\_free@@Base+0x2e4>

3ed0: e1a00004 mov r0, r4

3ed4: ebfff8e8 bl 227c <free@plt>

3ed8: e3a04000 mov r4, #0

3edc: e1a00004 mov r0, r4

3ee0: e28dd03c add sp, sp, #60 ; 0x3c

3ee4: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

3ee8: e1a00004 mov r0, r4

3eec: ebfff90f bl 2330 <mcrypt\_module\_close@plt>

3ef0: e1a04005 mov r4, r5

3ef4: eaffffe7 b 3e98 <mcrypt\_module\_open@@Base+0x208>

3ef8: e284e010 add lr, r4, #16

3efc: e1a0c00d mov ip, sp

3f00: e8be000f ldm lr!, {r0, r1, r2, r3}

3f04: e8ac000f stmia ip!, {r0, r1, r2, r3}

3f08: e8be000f ldm lr!, {r0, r1, r2, r3}

3f0c: e8ac000f stmia ip!, {r0, r1, r2, r3}

3f10: e8be000f ldm lr!, {r0, r1, r2, r3}

3f14: e8ac000f stmia ip!, {r0, r1, r2, r3}

3f18: e59e3000 ldr r3, [lr]

3f1c: eaffffe8 b 3ec4 <mcrypt\_module\_open@@Base+0x234>

3f20: 0001a654 andeq sl, r1, r4, asr r6

3f24: 0001a628 andeq sl, r1, r8, lsr #12

3f28: 00018490 muleq r1, r0, r4

3f2c: 00018464 andeq r8, r1, r4, ror #8

3f30: 0001a5a0 andeq sl, r1, r0, lsr #11

00003f34 <mcrypt\_enc\_get\_iv\_size@@Base>:

3f34: e92d4010 push {r4, lr}

3f38: e1a04000 mov r4, r0

3f3c: ebfff916 bl 239c <mcrypt\_enc\_is\_block\_algorithm\_mode@plt>

3f40: e3500001 cmp r0, #1

3f44: e1a00004 mov r0, r4

3f48: 0a000001 beq 3f54 <mcrypt\_enc\_get\_iv\_size@@Base+0x20>

3f4c: e8bd4010 pop {r4, lr}

3f50: eafffd9a b 35c0 <mcrypt\_enc\_get\_block\_size@@Base+0x8>

3f54: e8bd4010 pop {r4, lr}

3f58: eafff8ee b 2318 <mcrypt\_enc\_get\_block\_size@plt>

00003f5c <mcrypt\_enc\_self\_test@@Base>:

3f5c: e59f3060 ldr r3, [pc, #96] ; 3fc4 <mcrypt\_enc\_self\_test@@Base+0x68>

3f60: e92d4010 push {r4, lr}

3f64: e280e010 add lr, r0, #16

3f68: e24dd038 sub sp, sp, #56 ; 0x38

3f6c: e08f3003 add r3, pc, r3

3f70: e58d3034 str r3, [sp, #52] ; 0x34

3f74: e1a04000 mov r4, r0

3f78: e8be000f ldm lr!, {r0, r1, r2, r3}

3f7c: e1a0c00d mov ip, sp

3f80: e8ac000f stmia ip!, {r0, r1, r2, r3}

3f84: e8be000f ldm lr!, {r0, r1, r2, r3}

3f88: e8ac000f stmia ip!, {r0, r1, r2, r3}

3f8c: e8be000f ldm lr!, {r0, r1, r2, r3}

3f90: e59ee000 ldr lr, [lr]

3f94: e8ac000f stmia ip!, {r0, r1, r2, r3}

3f98: e894000f ldm r4, {r0, r1, r2, r3}

3f9c: e58ce000 str lr, [ip]

3fa0: ebfffcad bl 325c <mcrypt\_free@@Base+0x2f8>

3fa4: e3500000 cmp r0, #0

3fa8: 0a000002 beq 3fb8 <mcrypt\_enc\_self\_test@@Base+0x5c>

3fac: e28dd038 add sp, sp, #56 ; 0x38

3fb0: e8bd4010 pop {r4, lr}

3fb4: e12fff10 bx r0

3fb8: e3e00000 mvn r0, #0

3fbc: e28dd038 add sp, sp, #56 ; 0x38

3fc0: e8bd8010 pop {r4, pc}

3fc4: 0001a4d8 ldrdeq sl, [r1], -r8

00003fc8 <mcrypt\_module\_self\_test@@Base>:

3fc8: e92d4070 push {r4, r5, r6, lr}

3fcc: e24dd080 sub sp, sp, #128 ; 0x80

3fd0: e28d503c add r5, sp, #60 ; 0x3c

3fd4: e1a03000 mov r3, r0

3fd8: e3a02000 mov r2, #0

3fdc: e1a00005 mov r0, r5

3fe0: ebfffcdb bl 3354 <mcrypt\_module\_close@@Base+0x90>

3fe4: e3500000 cmp r0, #0

3fe8: 0a000022 beq 4078 <mcrypt\_module\_self\_test@@Base+0xb0>

3fec: e28d404c add r4, sp, #76 ; 0x4c

3ff0: e1a0e004 mov lr, r4

3ff4: e59f6098 ldr r6, [pc, #152] ; 4094 <mcrypt\_module\_self\_test@@Base+0xcc>

3ff8: e8be000f ldm lr!, {r0, r1, r2, r3}

3ffc: e1a0c00d mov ip, sp

4000: e08f6006 add r6, pc, r6

4004: e58d6034 str r6, [sp, #52] ; 0x34

4008: e8ac000f stmia ip!, {r0, r1, r2, r3}

400c: e8be000f ldm lr!, {r0, r1, r2, r3}

4010: e8ac000f stmia ip!, {r0, r1, r2, r3}

4014: e8be000f ldm lr!, {r0, r1, r2, r3}

4018: e59ee000 ldr lr, [lr]

401c: e8ac000f stmia ip!, {r0, r1, r2, r3}

4020: e895000f ldm r5, {r0, r1, r2, r3}

4024: e58ce000 str lr, [ip]

4028: ebfffc8b bl 325c <mcrypt\_free@@Base+0x2f8>

402c: e3500000 cmp r0, #0

4030: 0a000012 beq 4080 <mcrypt\_module\_self\_test@@Base+0xb8>

4034: e12fff30 blx r0

4038: e1a0c004 mov ip, r4

403c: e1a0e00d mov lr, sp

4040: e1a06000 mov r6, r0

4044: e8bc000f ldm ip!, {r0, r1, r2, r3}

4048: e8ae000f stmia lr!, {r0, r1, r2, r3}

404c: e8bc000f ldm ip!, {r0, r1, r2, r3}

4050: e8ae000f stmia lr!, {r0, r1, r2, r3}

4054: e8bc000f ldm ip!, {r0, r1, r2, r3}

4058: e59cc000 ldr ip, [ip]

405c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4060: e895000f ldm r5, {r0, r1, r2, r3}

4064: e58ec000 str ip, [lr]

4068: ebfffc76 bl 3248 <mcrypt\_free@@Base+0x2e4>

406c: e1a00006 mov r0, r6

4070: e28dd080 add sp, sp, #128 ; 0x80

4074: e8bd8070 pop {r4, r5, r6, pc}

4078: e3e06000 mvn r6, #0

407c: eafffffa b 406c <mcrypt\_module\_self\_test@@Base+0xa4>

4080: e1a0c004 mov ip, r4

4084: e1a0e00d mov lr, sp

4088: e8bc000f ldm ip!, {r0, r1, r2, r3}

408c: e3e06000 mvn r6, #0

4090: eaffffec b 4048 <mcrypt\_module\_self\_test@@Base+0x80>

4094: 0001a444 andeq sl, r1, r4, asr #8

00004098 <mcrypt\_module\_algorithm\_version@@Base>:

4098: e92d4070 push {r4, r5, r6, lr}

409c: e24dd080 sub sp, sp, #128 ; 0x80

40a0: e28d503c add r5, sp, #60 ; 0x3c

40a4: e1a03000 mov r3, r0

40a8: e3a02000 mov r2, #0

40ac: e1a00005 mov r0, r5

40b0: ebfffca7 bl 3354 <mcrypt\_module\_close@@Base+0x90>

40b4: e3500000 cmp r0, #0

40b8: 0a000022 beq 4148 <mcrypt\_module\_algorithm\_version@@Base+0xb0>

40bc: e28d404c add r4, sp, #76 ; 0x4c

40c0: e1a0e004 mov lr, r4

40c4: e59f6098 ldr r6, [pc, #152] ; 4164 <mcrypt\_module\_algorithm\_version@@Base+0xcc>

40c8: e8be000f ldm lr!, {r0, r1, r2, r3}

40cc: e1a0c00d mov ip, sp

40d0: e08f6006 add r6, pc, r6

40d4: e58d6034 str r6, [sp, #52] ; 0x34

40d8: e8ac000f stmia ip!, {r0, r1, r2, r3}

40dc: e8be000f ldm lr!, {r0, r1, r2, r3}

40e0: e8ac000f stmia ip!, {r0, r1, r2, r3}

40e4: e8be000f ldm lr!, {r0, r1, r2, r3}

40e8: e59ee000 ldr lr, [lr]

40ec: e8ac000f stmia ip!, {r0, r1, r2, r3}

40f0: e895000f ldm r5, {r0, r1, r2, r3}

40f4: e58ce000 str lr, [ip]

40f8: ebfffc57 bl 325c <mcrypt\_free@@Base+0x2f8>

40fc: e3500000 cmp r0, #0

4100: 0a000012 beq 4150 <mcrypt\_module\_algorithm\_version@@Base+0xb8>

4104: e12fff30 blx r0

4108: e1a0c004 mov ip, r4

410c: e1a0e00d mov lr, sp

4110: e1a06000 mov r6, r0

4114: e8bc000f ldm ip!, {r0, r1, r2, r3}

4118: e8ae000f stmia lr!, {r0, r1, r2, r3}

411c: e8bc000f ldm ip!, {r0, r1, r2, r3}

4120: e8ae000f stmia lr!, {r0, r1, r2, r3}

4124: e8bc000f ldm ip!, {r0, r1, r2, r3}

4128: e59cc000 ldr ip, [ip]

412c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4130: e895000f ldm r5, {r0, r1, r2, r3}

4134: e58ec000 str ip, [lr]

4138: ebfffc42 bl 3248 <mcrypt\_free@@Base+0x2e4>

413c: e1a00006 mov r0, r6

4140: e28dd080 add sp, sp, #128 ; 0x80

4144: e8bd8070 pop {r4, r5, r6, pc}

4148: e3e06000 mvn r6, #0

414c: eafffffa b 413c <mcrypt\_module\_algorithm\_version@@Base+0xa4>

4150: e1a0c004 mov ip, r4

4154: e1a0e00d mov lr, sp

4158: e8bc000f ldm ip!, {r0, r1, r2, r3}

415c: e3e06000 mvn r6, #0

4160: eaffffec b 4118 <mcrypt\_module\_algorithm\_version@@Base+0x80>

4164: 0001a390 muleq r1, r0, r3

00004168 <mcrypt\_module\_mode\_version@@Base>:

4168: e92d4070 push {r4, r5, r6, lr}

416c: e24dd080 sub sp, sp, #128 ; 0x80

4170: e28d503c add r5, sp, #60 ; 0x3c

4174: e1a03000 mov r3, r0

4178: e3a02000 mov r2, #0

417c: e1a00005 mov r0, r5

4180: ebfffc73 bl 3354 <mcrypt\_module\_close@@Base+0x90>

4184: e3500000 cmp r0, #0

4188: 0a000022 beq 4218 <mcrypt\_module\_mode\_version@@Base+0xb0>

418c: e28d404c add r4, sp, #76 ; 0x4c

4190: e1a0e004 mov lr, r4

4194: e59f6098 ldr r6, [pc, #152] ; 4234 <mcrypt\_module\_mode\_version@@Base+0xcc>

4198: e8be000f ldm lr!, {r0, r1, r2, r3}

419c: e1a0c00d mov ip, sp

41a0: e08f6006 add r6, pc, r6

41a4: e58d6034 str r6, [sp, #52] ; 0x34

41a8: e8ac000f stmia ip!, {r0, r1, r2, r3}

41ac: e8be000f ldm lr!, {r0, r1, r2, r3}

41b0: e8ac000f stmia ip!, {r0, r1, r2, r3}

41b4: e8be000f ldm lr!, {r0, r1, r2, r3}

41b8: e59ee000 ldr lr, [lr]

41bc: e8ac000f stmia ip!, {r0, r1, r2, r3}

41c0: e895000f ldm r5, {r0, r1, r2, r3}

41c4: e58ce000 str lr, [ip]

41c8: ebfffc23 bl 325c <mcrypt\_free@@Base+0x2f8>

41cc: e3500000 cmp r0, #0

41d0: 0a000012 beq 4220 <mcrypt\_module\_mode\_version@@Base+0xb8>

41d4: e12fff30 blx r0

41d8: e1a0c004 mov ip, r4

41dc: e1a0e00d mov lr, sp

41e0: e1a06000 mov r6, r0

41e4: e8bc000f ldm ip!, {r0, r1, r2, r3}

41e8: e8ae000f stmia lr!, {r0, r1, r2, r3}

41ec: e8bc000f ldm ip!, {r0, r1, r2, r3}

41f0: e8ae000f stmia lr!, {r0, r1, r2, r3}

41f4: e8bc000f ldm ip!, {r0, r1, r2, r3}

41f8: e59cc000 ldr ip, [ip]

41fc: e8ae000f stmia lr!, {r0, r1, r2, r3}

4200: e895000f ldm r5, {r0, r1, r2, r3}

4204: e58ec000 str ip, [lr]

4208: ebfffc0e bl 3248 <mcrypt\_free@@Base+0x2e4>

420c: e1a00006 mov r0, r6

4210: e28dd080 add sp, sp, #128 ; 0x80

4214: e8bd8070 pop {r4, r5, r6, pc}

4218: e3e06000 mvn r6, #0

421c: eafffffa b 420c <mcrypt\_module\_mode\_version@@Base+0xa4>

4220: e1a0c004 mov ip, r4

4224: e1a0e00d mov lr, sp

4228: e8bc000f ldm ip!, {r0, r1, r2, r3}

422c: e3e06000 mvn r6, #0

4230: eaffffec b 41e8 <mcrypt\_module\_mode\_version@@Base+0x80>

4234: 00018120 andeq r8, r1, r0, lsr #2

00004238 <mcrypt\_module\_is\_block\_algorithm@@Base>:

4238: e92d4070 push {r4, r5, r6, lr}

423c: e24dd080 sub sp, sp, #128 ; 0x80

4240: e28d503c add r5, sp, #60 ; 0x3c

4244: e1a03000 mov r3, r0

4248: e3a02000 mov r2, #0

424c: e1a00005 mov r0, r5

4250: ebfffc3f bl 3354 <mcrypt\_module\_close@@Base+0x90>

4254: e3500000 cmp r0, #0

4258: 0a000022 beq 42e8 <mcrypt\_module\_is\_block\_algorithm@@Base+0xb0>

425c: e28d404c add r4, sp, #76 ; 0x4c

4260: e1a0e004 mov lr, r4

4264: e59f6098 ldr r6, [pc, #152] ; 4304 <mcrypt\_module\_is\_block\_algorithm@@Base+0xcc>

4268: e8be000f ldm lr!, {r0, r1, r2, r3}

426c: e1a0c00d mov ip, sp

4270: e08f6006 add r6, pc, r6

4274: e58d6034 str r6, [sp, #52] ; 0x34

4278: e8ac000f stmia ip!, {r0, r1, r2, r3}

427c: e8be000f ldm lr!, {r0, r1, r2, r3}

4280: e8ac000f stmia ip!, {r0, r1, r2, r3}

4284: e8be000f ldm lr!, {r0, r1, r2, r3}

4288: e59ee000 ldr lr, [lr]

428c: e8ac000f stmia ip!, {r0, r1, r2, r3}

4290: e895000f ldm r5, {r0, r1, r2, r3}

4294: e58ce000 str lr, [ip]

4298: ebfffbef bl 325c <mcrypt\_free@@Base+0x2f8>

429c: e3500000 cmp r0, #0

42a0: 0a000012 beq 42f0 <mcrypt\_module\_is\_block\_algorithm@@Base+0xb8>

42a4: e12fff30 blx r0

42a8: e1a0c004 mov ip, r4

42ac: e1a0e00d mov lr, sp

42b0: e1a06000 mov r6, r0

42b4: e8bc000f ldm ip!, {r0, r1, r2, r3}

42b8: e8ae000f stmia lr!, {r0, r1, r2, r3}

42bc: e8bc000f ldm ip!, {r0, r1, r2, r3}

42c0: e8ae000f stmia lr!, {r0, r1, r2, r3}

42c4: e8bc000f ldm ip!, {r0, r1, r2, r3}

42c8: e59cc000 ldr ip, [ip]

42cc: e8ae000f stmia lr!, {r0, r1, r2, r3}

42d0: e895000f ldm r5, {r0, r1, r2, r3}

42d4: e58ec000 str ip, [lr]

42d8: ebfffbda bl 3248 <mcrypt\_free@@Base+0x2e4>

42dc: e1a00006 mov r0, r6

42e0: e28dd080 add sp, sp, #128 ; 0x80

42e4: e8bd8070 pop {r4, r5, r6, pc}

42e8: e3e06000 mvn r6, #0

42ec: eafffffa b 42dc <mcrypt\_module\_is\_block\_algorithm@@Base+0xa4>

42f0: e1a0c004 mov ip, r4

42f4: e1a0e00d mov lr, sp

42f8: e8bc000f ldm ip!, {r0, r1, r2, r3}

42fc: e3e06000 mvn r6, #0

4300: eaffffec b 42b8 <mcrypt\_module\_is\_block\_algorithm@@Base+0x80>

4304: 0001a14c andeq sl, r1, ip, asr #2

00004308 <mcrypt\_module\_is\_block\_algorithm\_mode@@Base>:

4308: e92d4070 push {r4, r5, r6, lr}

430c: e24dd080 sub sp, sp, #128 ; 0x80

4310: e28d503c add r5, sp, #60 ; 0x3c

4314: e1a03000 mov r3, r0

4318: e3a02000 mov r2, #0

431c: e1a00005 mov r0, r5

4320: ebfffc0b bl 3354 <mcrypt\_module\_close@@Base+0x90>

4324: e3500000 cmp r0, #0

4328: 0a000022 beq 43b8 <mcrypt\_module\_is\_block\_algorithm\_mode@@Base+0xb0>

432c: e28d404c add r4, sp, #76 ; 0x4c

4330: e1a0e004 mov lr, r4

4334: e59f6098 ldr r6, [pc, #152] ; 43d4 <mcrypt\_module\_is\_block\_algorithm\_mode@@Base+0xcc>

4338: e8be000f ldm lr!, {r0, r1, r2, r3}

433c: e1a0c00d mov ip, sp

4340: e08f6006 add r6, pc, r6

4344: e58d6034 str r6, [sp, #52] ; 0x34

4348: e8ac000f stmia ip!, {r0, r1, r2, r3}

434c: e8be000f ldm lr!, {r0, r1, r2, r3}

4350: e8ac000f stmia ip!, {r0, r1, r2, r3}

4354: e8be000f ldm lr!, {r0, r1, r2, r3}

4358: e59ee000 ldr lr, [lr]

435c: e8ac000f stmia ip!, {r0, r1, r2, r3}

4360: e895000f ldm r5, {r0, r1, r2, r3}

4364: e58ce000 str lr, [ip]

4368: ebfffbbb bl 325c <mcrypt\_free@@Base+0x2f8>

436c: e3500000 cmp r0, #0

4370: 0a000012 beq 43c0 <mcrypt\_module\_is\_block\_algorithm\_mode@@Base+0xb8>

4374: e12fff30 blx r0

4378: e1a0c004 mov ip, r4

437c: e1a0e00d mov lr, sp

4380: e1a06000 mov r6, r0

4384: e8bc000f ldm ip!, {r0, r1, r2, r3}

4388: e8ae000f stmia lr!, {r0, r1, r2, r3}

438c: e8bc000f ldm ip!, {r0, r1, r2, r3}

4390: e8ae000f stmia lr!, {r0, r1, r2, r3}

4394: e8bc000f ldm ip!, {r0, r1, r2, r3}

4398: e59cc000 ldr ip, [ip]

439c: e8ae000f stmia lr!, {r0, r1, r2, r3}

43a0: e895000f ldm r5, {r0, r1, r2, r3}

43a4: e58ec000 str ip, [lr]

43a8: ebfffba6 bl 3248 <mcrypt\_free@@Base+0x2e4>

43ac: e1a00006 mov r0, r6

43b0: e28dd080 add sp, sp, #128 ; 0x80

43b4: e8bd8070 pop {r4, r5, r6, pc}

43b8: e3e06000 mvn r6, #0

43bc: eafffffa b 43ac <mcrypt\_module\_is\_block\_algorithm\_mode@@Base+0xa4>

43c0: e1a0c004 mov ip, r4

43c4: e1a0e00d mov lr, sp

43c8: e8bc000f ldm ip!, {r0, r1, r2, r3}

43cc: e3e06000 mvn r6, #0

43d0: eaffffec b 4388 <mcrypt\_module\_is\_block\_algorithm\_mode@@Base+0x80>

43d4: 00017f1c andeq r7, r1, ip, lsl pc

000043d8 <mcrypt\_module\_is\_block\_mode@@Base>:

43d8: e92d4070 push {r4, r5, r6, lr}

43dc: e24dd080 sub sp, sp, #128 ; 0x80

43e0: e28d503c add r5, sp, #60 ; 0x3c

43e4: e1a03000 mov r3, r0

43e8: e3a02000 mov r2, #0

43ec: e1a00005 mov r0, r5

43f0: ebfffbd7 bl 3354 <mcrypt\_module\_close@@Base+0x90>

43f4: e3500000 cmp r0, #0

43f8: 0a000022 beq 4488 <mcrypt\_module\_is\_block\_mode@@Base+0xb0>

43fc: e28d404c add r4, sp, #76 ; 0x4c

4400: e1a0e004 mov lr, r4

4404: e59f6098 ldr r6, [pc, #152] ; 44a4 <mcrypt\_module\_is\_block\_mode@@Base+0xcc>

4408: e8be000f ldm lr!, {r0, r1, r2, r3}

440c: e1a0c00d mov ip, sp

4410: e08f6006 add r6, pc, r6

4414: e58d6034 str r6, [sp, #52] ; 0x34

4418: e8ac000f stmia ip!, {r0, r1, r2, r3}

441c: e8be000f ldm lr!, {r0, r1, r2, r3}

4420: e8ac000f stmia ip!, {r0, r1, r2, r3}

4424: e8be000f ldm lr!, {r0, r1, r2, r3}

4428: e59ee000 ldr lr, [lr]

442c: e8ac000f stmia ip!, {r0, r1, r2, r3}

4430: e895000f ldm r5, {r0, r1, r2, r3}

4434: e58ce000 str lr, [ip]

4438: ebfffb87 bl 325c <mcrypt\_free@@Base+0x2f8>

443c: e3500000 cmp r0, #0

4440: 0a000012 beq 4490 <mcrypt\_module\_is\_block\_mode@@Base+0xb8>

4444: e12fff30 blx r0

4448: e1a0c004 mov ip, r4

444c: e1a0e00d mov lr, sp

4450: e1a06000 mov r6, r0

4454: e8bc000f ldm ip!, {r0, r1, r2, r3}

4458: e8ae000f stmia lr!, {r0, r1, r2, r3}

445c: e8bc000f ldm ip!, {r0, r1, r2, r3}

4460: e8ae000f stmia lr!, {r0, r1, r2, r3}

4464: e8bc000f ldm ip!, {r0, r1, r2, r3}

4468: e59cc000 ldr ip, [ip]

446c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4470: e895000f ldm r5, {r0, r1, r2, r3}

4474: e58ec000 str ip, [lr]

4478: ebfffb72 bl 3248 <mcrypt\_free@@Base+0x2e4>

447c: e1a00006 mov r0, r6

4480: e28dd080 add sp, sp, #128 ; 0x80

4484: e8bd8070 pop {r4, r5, r6, pc}

4488: e3e06000 mvn r6, #0

448c: eafffffa b 447c <mcrypt\_module\_is\_block\_mode@@Base+0xa4>

4490: e1a0c004 mov ip, r4

4494: e1a0e00d mov lr, sp

4498: e8bc000f ldm ip!, {r0, r1, r2, r3}

449c: e3e06000 mvn r6, #0

44a0: eaffffec b 4458 <mcrypt\_module\_is\_block\_mode@@Base+0x80>

44a4: 00017e34 andeq r7, r1, r4, lsr lr

000044a8 <mcrypt\_module\_get\_algo\_block\_size@@Base>:

44a8: e92d4070 push {r4, r5, r6, lr}

44ac: e24dd080 sub sp, sp, #128 ; 0x80

44b0: e28d503c add r5, sp, #60 ; 0x3c

44b4: e1a03000 mov r3, r0

44b8: e3a02000 mov r2, #0

44bc: e1a00005 mov r0, r5

44c0: ebfffba3 bl 3354 <mcrypt\_module\_close@@Base+0x90>

44c4: e3500000 cmp r0, #0

44c8: 0a000022 beq 4558 <mcrypt\_module\_get\_algo\_block\_size@@Base+0xb0>

44cc: e28d404c add r4, sp, #76 ; 0x4c

44d0: e1a0e004 mov lr, r4

44d4: e59f6098 ldr r6, [pc, #152] ; 4574 <mcrypt\_module\_get\_algo\_block\_size@@Base+0xcc>

44d8: e8be000f ldm lr!, {r0, r1, r2, r3}

44dc: e1a0c00d mov ip, sp

44e0: e08f6006 add r6, pc, r6

44e4: e58d6034 str r6, [sp, #52] ; 0x34

44e8: e8ac000f stmia ip!, {r0, r1, r2, r3}

44ec: e8be000f ldm lr!, {r0, r1, r2, r3}

44f0: e8ac000f stmia ip!, {r0, r1, r2, r3}

44f4: e8be000f ldm lr!, {r0, r1, r2, r3}

44f8: e59ee000 ldr lr, [lr]

44fc: e8ac000f stmia ip!, {r0, r1, r2, r3}

4500: e895000f ldm r5, {r0, r1, r2, r3}

4504: e58ce000 str lr, [ip]

4508: ebfffb53 bl 325c <mcrypt\_free@@Base+0x2f8>

450c: e3500000 cmp r0, #0

4510: 0a000012 beq 4560 <mcrypt\_module\_get\_algo\_block\_size@@Base+0xb8>

4514: e12fff30 blx r0

4518: e1a0c004 mov ip, r4

451c: e1a0e00d mov lr, sp

4520: e1a06000 mov r6, r0

4524: e8bc000f ldm ip!, {r0, r1, r2, r3}

4528: e8ae000f stmia lr!, {r0, r1, r2, r3}

452c: e8bc000f ldm ip!, {r0, r1, r2, r3}

4530: e8ae000f stmia lr!, {r0, r1, r2, r3}

4534: e8bc000f ldm ip!, {r0, r1, r2, r3}

4538: e59cc000 ldr ip, [ip]

453c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4540: e895000f ldm r5, {r0, r1, r2, r3}

4544: e58ec000 str ip, [lr]

4548: ebfffb3e bl 3248 <mcrypt\_free@@Base+0x2e4>

454c: e1a00006 mov r0, r6

4550: e28dd080 add sp, sp, #128 ; 0x80

4554: e8bd8070 pop {r4, r5, r6, pc}

4558: e3e06000 mvn r6, #0

455c: eafffffa b 454c <mcrypt\_module\_get\_algo\_block\_size@@Base+0xa4>

4560: e1a0c004 mov ip, r4

4564: e1a0e00d mov lr, sp

4568: e8bc000f ldm ip!, {r0, r1, r2, r3}

456c: e3e06000 mvn r6, #0

4570: eaffffec b 4528 <mcrypt\_module\_get\_algo\_block\_size@@Base+0x80>

4574: 00019ebc ; <UNDEFINED> instruction: 0x00019ebc

00004578 <mcrypt\_module\_get\_algo\_key\_size@@Base>:

4578: e92d4070 push {r4, r5, r6, lr}

457c: e24dd080 sub sp, sp, #128 ; 0x80

4580: e28d503c add r5, sp, #60 ; 0x3c

4584: e1a03000 mov r3, r0

4588: e3a02000 mov r2, #0

458c: e1a00005 mov r0, r5

4590: ebfffb6f bl 3354 <mcrypt\_module\_close@@Base+0x90>

4594: e3500000 cmp r0, #0

4598: 0a000022 beq 4628 <mcrypt\_module\_get\_algo\_key\_size@@Base+0xb0>

459c: e28d404c add r4, sp, #76 ; 0x4c

45a0: e1a0e004 mov lr, r4

45a4: e59f6098 ldr r6, [pc, #152] ; 4644 <mcrypt\_module\_get\_algo\_key\_size@@Base+0xcc>

45a8: e8be000f ldm lr!, {r0, r1, r2, r3}

45ac: e1a0c00d mov ip, sp

45b0: e08f6006 add r6, pc, r6

45b4: e58d6034 str r6, [sp, #52] ; 0x34

45b8: e8ac000f stmia ip!, {r0, r1, r2, r3}

45bc: e8be000f ldm lr!, {r0, r1, r2, r3}

45c0: e8ac000f stmia ip!, {r0, r1, r2, r3}

45c4: e8be000f ldm lr!, {r0, r1, r2, r3}

45c8: e59ee000 ldr lr, [lr]

45cc: e8ac000f stmia ip!, {r0, r1, r2, r3}

45d0: e895000f ldm r5, {r0, r1, r2, r3}

45d4: e58ce000 str lr, [ip]

45d8: ebfffb1f bl 325c <mcrypt\_free@@Base+0x2f8>

45dc: e3500000 cmp r0, #0

45e0: 0a000012 beq 4630 <mcrypt\_module\_get\_algo\_key\_size@@Base+0xb8>

45e4: e12fff30 blx r0

45e8: e1a0c004 mov ip, r4

45ec: e1a0e00d mov lr, sp

45f0: e1a06000 mov r6, r0

45f4: e8bc000f ldm ip!, {r0, r1, r2, r3}

45f8: e8ae000f stmia lr!, {r0, r1, r2, r3}

45fc: e8bc000f ldm ip!, {r0, r1, r2, r3}

4600: e8ae000f stmia lr!, {r0, r1, r2, r3}

4604: e8bc000f ldm ip!, {r0, r1, r2, r3}

4608: e59cc000 ldr ip, [ip]

460c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4610: e895000f ldm r5, {r0, r1, r2, r3}

4614: e58ec000 str ip, [lr]

4618: ebfffb0a bl 3248 <mcrypt\_free@@Base+0x2e4>

461c: e1a00006 mov r0, r6

4620: e28dd080 add sp, sp, #128 ; 0x80

4624: e8bd8070 pop {r4, r5, r6, pc}

4628: e3e06000 mvn r6, #0

462c: eafffffa b 461c <mcrypt\_module\_get\_algo\_key\_size@@Base+0xa4>

4630: e1a0c004 mov ip, r4

4634: e1a0e00d mov lr, sp

4638: e8bc000f ldm ip!, {r0, r1, r2, r3}

463c: e3e06000 mvn r6, #0

4640: eaffffec b 45f8 <mcrypt\_module\_get\_algo\_key\_size@@Base+0x80>

4644: 00019e28 andeq r9, r1, r8, lsr #28

00004648 <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base>:

4648: e92d41f0 push {r4, r5, r6, r7, r8, lr}

464c: e24dd080 sub sp, sp, #128 ; 0x80

4650: e28d503c add r5, sp, #60 ; 0x3c

4654: e1a06002 mov r6, r2

4658: e1a03000 mov r3, r0

465c: e3a02000 mov r2, #0

4660: e1a00005 mov r0, r5

4664: ebfffb3a bl 3354 <mcrypt\_module\_close@@Base+0x90>

4668: e2507000 subs r7, r0, #0

466c: 05867000 streq r7, [r6]

4670: 0a000029 beq 471c <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0xd4>

4674: e28d404c add r4, sp, #76 ; 0x4c

4678: e1a0e004 mov lr, r4

467c: e59f7104 ldr r7, [pc, #260] ; 4788 <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0x140>

4680: e8be000f ldm lr!, {r0, r1, r2, r3}

4684: e1a0c00d mov ip, sp

4688: e08f7007 add r7, pc, r7

468c: e58d7034 str r7, [sp, #52] ; 0x34

4690: e8ac000f stmia ip!, {r0, r1, r2, r3}

4694: e8be000f ldm lr!, {r0, r1, r2, r3}

4698: e8ac000f stmia ip!, {r0, r1, r2, r3}

469c: e8be000f ldm lr!, {r0, r1, r2, r3}

46a0: e59ee000 ldr lr, [lr]

46a4: e8ac000f stmia ip!, {r0, r1, r2, r3}

46a8: e895000f ldm r5, {r0, r1, r2, r3}

46ac: e58ce000 str lr, [ip]

46b0: ebfffae9 bl 325c <mcrypt\_free@@Base+0x2f8>

46b4: e2508000 subs r8, r0, #0

46b8: 0a000023 beq 474c <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0x104>

46bc: e1a00006 mov r0, r6

46c0: e12fff38 blx r8

46c4: e5962000 ldr r2, [r6]

46c8: e2923000 adds r3, r2, #0

46cc: 13a03001 movne r3, #1

46d0: e3500000 cmp r0, #0

46d4: 03a03000 moveq r3, #0

46d8: e3530000 cmp r3, #0

46dc: e1a08000 mov r8, r0

46e0: 01a07003 moveq r7, r3

46e4: 05863000 streq r3, [r6]

46e8: 1a00000e bne 4728 <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0xe0>

46ec: e1a0c004 mov ip, r4

46f0: e1a0e00d mov lr, sp

46f4: e8bc000f ldm ip!, {r0, r1, r2, r3}

46f8: e8ae000f stmia lr!, {r0, r1, r2, r3}

46fc: e8bc000f ldm ip!, {r0, r1, r2, r3}

4700: e8ae000f stmia lr!, {r0, r1, r2, r3}

4704: e8bc000f ldm ip!, {r0, r1, r2, r3}

4708: e59cc000 ldr ip, [ip]

470c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4710: e895000f ldm r5, {r0, r1, r2, r3}

4714: e58ec000 str ip, [lr]

4718: ebfffaca bl 3248 <mcrypt\_free@@Base+0x2e4>

471c: e1a00007 mov r0, r7

4720: e28dd080 add sp, sp, #128 ; 0x80

4724: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4728: e1a00102 lsl r0, r2, #2

472c: ebfff6f0 bl 22f4 <malloc@plt>

4730: e2507000 subs r7, r0, #0

4734: 0affffec beq 46ec <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0xa4>

4738: e5962000 ldr r2, [r6]

473c: e1a01008 mov r1, r8

4740: e1a02102 lsl r2, r2, #2

4744: ebfff6cf bl 2288 <memcpy@plt>

4748: eaffffe7 b 46ec <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0xa4>

474c: e1a0c004 mov ip, r4

4750: e1a0e00d mov lr, sp

4754: e8bc000f ldm ip!, {r0, r1, r2, r3}

4758: e1a07008 mov r7, r8

475c: e8ae000f stmia lr!, {r0, r1, r2, r3}

4760: e8bc000f ldm ip!, {r0, r1, r2, r3}

4764: e8ae000f stmia lr!, {r0, r1, r2, r3}

4768: e8bc000f ldm ip!, {r0, r1, r2, r3}

476c: e59cc000 ldr ip, [ip]

4770: e8ae000f stmia lr!, {r0, r1, r2, r3}

4774: e895000f ldm r5, {r0, r1, r2, r3}

4778: e58ec000 str ip, [lr]

477c: ebfffab1 bl 3248 <mcrypt\_free@@Base+0x2e4>

4780: e5868000 str r8, [r6]

4784: eaffffe4 b 471c <mcrypt\_module\_get\_algo\_supported\_key\_sizes@@Base+0xd4>

4788: 00019d70 andeq r9, r1, r0, ror sp

0000478c <mcrypt\_module\_support\_dynamic@@Base>:

478c: e3a00000 mov r0, #0

4790: e12fff1e bx lr

00004794 <mcrypt\_mutex\_register@@Base>:

4794: e3a00000 mov r0, #0

4798: e12fff1e bx lr

479c: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

47a0: e3a01001 mov r1, #1

47a4: e59d5020 ldr r5, [sp, #32]

47a8: e1a06000 mov r6, r0

47ac: e5805008 str r5, [r0, #8]

47b0: e1a00005 mov r0, r5

47b4: e1a09003 mov r9, r3

47b8: ebfff69d bl 2234 <calloc@plt>

47bc: e1a07000 mov r7, r0

47c0: e5860000 str r0, [r6]

47c4: e1a00005 mov r0, r5

47c8: ebfff6c9 bl 22f4 <malloc@plt>

47cc: e3570000 cmp r7, #0

47d0: 03a04001 moveq r4, #1

47d4: e16f2f10 clz r2, r0

47d8: e1a022a2 lsr r2, r2, #5

47dc: 11a04002 movne r4, r2

47e0: e3540000 cmp r4, #0

47e4: e5860004 str r0, [r6, #4]

47e8: 1a000009 bne 4814 <mcrypt\_mutex\_register@@Base+0x80>

47ec: e3590000 cmp r9, #0

47f0: 0a000005 beq 480c <mcrypt\_mutex\_register@@Base+0x78>

47f4: e1a02005 mov r2, r5

47f8: e1a01009 mov r1, r9

47fc: e1a00007 mov r0, r7

4800: ebfff6a0 bl 2288 <memcpy@plt>

4804: e1a00004 mov r0, r4

4808: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

480c: e1a00009 mov r0, r9

4810: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

4814: e1a08000 mov r8, r0

4818: e1a00007 mov r0, r7

481c: ebfff696 bl 227c <free@plt>

4820: e1a00008 mov r0, r8

4824: ebfff694 bl 227c <free@plt>

4828: e3e00000 mvn r0, #0

482c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

4830: e92d4070 push {r4, r5, r6, lr}

4834: e1a04000 mov r4, r0

4838: e1a05001 mov r5, r1

483c: e1a06002 mov r6, r2

4840: e5900000 ldr r0, [r0]

4844: ebfff68f bl 2288 <memcpy@plt>

4848: e1a02006 mov r2, r6

484c: e1a01005 mov r1, r5

4850: e5940004 ldr r0, [r4, #4]

4854: ebfff68b bl 2288 <memcpy@plt>

4858: e3a00000 mov r0, #0

485c: e8bd8070 pop {r4, r5, r6, pc}

4860: e92d4010 push {r4, lr}

4864: e5923000 ldr r3, [r2]

4868: e590e008 ldr lr, [r0, #8]

486c: e153000e cmp r3, lr

4870: ba000007 blt 4894 <mcrypt\_mutex\_register@@Base+0x100>

4874: e1a0c000 mov ip, r0

4878: e582e000 str lr, [r2]

487c: e1a00001 mov r0, r1

4880: e1a0200e mov r2, lr

4884: e59c1000 ldr r1, [ip]

4888: ebfff67e bl 2288 <memcpy@plt>

488c: e3a00000 mov r0, #0

4890: e8bd8010 pop {r4, pc}

4894: e582e000 str lr, [r2]

4898: e3e00000 mvn r0, #0

489c: e8bd8010 pop {r4, pc}

48a0: e92d4010 push {r4, lr}

48a4: e1a04000 mov r4, r0

48a8: e5900000 ldr r0, [r0]

48ac: ebfff672 bl 227c <free@plt>

48b0: e5940004 ldr r0, [r4, #4]

48b4: e8bd4010 pop {r4, lr}

48b8: eafff66f b 227c <free@plt>

48bc: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

48c0: e24dd00c sub sp, sp, #12

48c4: e1a07000 mov r7, r0

48c8: e1a0b001 mov fp, r1

48cc: e1a00002 mov r0, r2

48d0: e1a01003 mov r1, r3

48d4: e58d2004 str r2, [sp, #4]

48d8: e1a06003 mov r6, r3

48dc: eb005c87 bl 1bb00 <mcrypt\_mutex\_register@@Base+0x1736c>

48e0: e250a000 subs sl, r0, #0

48e4: da000027 ble 4988 <mcrypt\_mutex\_register@@Base+0x1f4>

48e8: e1a08126 lsr r8, r6, #2

48ec: e3a05000 mov r5, #0

48f0: e1a09108 lsl r9, r8, #2

48f4: e2493004 sub r3, r9, #4

48f8: e1a09005 mov r9, r5

48fc: e58d3000 str r3, [sp]

4900: e3c54003 bic r4, r5, #3

4904: e3580000 cmp r8, #0

4908: e08b4004 add r4, fp, r4

490c: 0a00000a beq 493c <mcrypt\_mutex\_register@@Base+0x1a8>

4910: e5970000 ldr r0, [r7]

4914: e59d3000 ldr r3, [sp]

4918: e2442004 sub r2, r4, #4

491c: e2400004 sub r0, r0, #4

4920: e084c003 add ip, r4, r3

4924: e5b23004 ldr r3, [r2, #4]!

4928: e5b01004 ldr r1, [r0, #4]!

492c: e15c0002 cmp ip, r2

4930: e0233001 eor r3, r3, r1

4934: e5823000 str r3, [r2]

4938: 1afffff9 bne 4924 <mcrypt\_mutex\_register@@Base+0x190>

493c: e1a01004 mov r1, r4

4940: e59d0030 ldr r0, [sp, #48] ; 0x30

4944: e59d3034 ldr r3, [sp, #52] ; 0x34

4948: e12fff33 blx r3

494c: e2899001 add r9, r9, #1

4950: e1a02006 mov r2, r6

4954: e1a01004 mov r1, r4

4958: e5970000 ldr r0, [r7]

495c: ebfff649 bl 2288 <memcpy@plt>

4960: e159000a cmp r9, sl

4964: e0855006 add r5, r5, r6

4968: 1affffe4 bne 4900 <mcrypt\_mutex\_register@@Base+0x16c>

496c: e3a00000 mov r0, #0

4970: e59d3004 ldr r3, [sp, #4]

4974: e3530000 cmp r3, #0

4978: 03a00000 moveq r0, #0

497c: e2600000 rsb r0, r0, #0

4980: e28dd00c add sp, sp, #12

4984: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

4988: e3a00001 mov r0, #1

498c: eafffff7 b 4970 <mcrypt\_mutex\_register@@Base+0x1dc>

4990: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

4994: e24dd00c sub sp, sp, #12

4998: e1a07000 mov r7, r0

499c: e1a0b001 mov fp, r1

49a0: e1a00002 mov r0, r2

49a4: e1a01003 mov r1, r3

49a8: e58d2004 str r2, [sp, #4]

49ac: e1a04003 mov r4, r3

49b0: eb005c52 bl 1bb00 <mcrypt\_mutex\_register@@Base+0x1736c>

49b4: e250a000 subs sl, r0, #0

49b8: da00002a ble 4a68 <mcrypt\_mutex\_register@@Base+0x2d4>

49bc: e1a08124 lsr r8, r4, #2

49c0: e3a05000 mov r5, #0

49c4: e1a09108 lsl r9, r8, #2

49c8: e1a06005 mov r6, r5

49cc: e2493004 sub r3, r9, #4

49d0: e58d3000 str r3, [sp]

49d4: e3c53003 bic r3, r5, #3

49d8: e08b9003 add r9, fp, r3

49dc: e1a01009 mov r1, r9

49e0: e1a02004 mov r2, r4

49e4: e5970004 ldr r0, [r7, #4]

49e8: ebfff626 bl 2288 <memcpy@plt>

49ec: e59d0030 ldr r0, [sp, #48] ; 0x30

49f0: e1a01009 mov r1, r9

49f4: e59d3038 ldr r3, [sp, #56] ; 0x38

49f8: e12fff33 blx r3

49fc: e3580000 cmp r8, #0

4a00: e5970000 ldr r0, [r7]

4a04: 0a000009 beq 4a30 <mcrypt\_mutex\_register@@Base+0x29c>

4a08: e59d3000 ldr r3, [sp]

4a0c: e2492004 sub r2, r9, #4

4a10: e240c004 sub ip, r0, #4

4a14: e0893003 add r3, r9, r3

4a18: e5b21004 ldr r1, [r2, #4]!

4a1c: e5bce004 ldr lr, [ip, #4]!

4a20: e1530002 cmp r3, r2

4a24: e021100e eor r1, r1, lr

4a28: e5821000 str r1, [r2]

4a2c: 1afffff9 bne 4a18 <mcrypt\_mutex\_register@@Base+0x284>

4a30: e2866001 add r6, r6, #1

4a34: e1a02004 mov r2, r4

4a38: e5971004 ldr r1, [r7, #4]

4a3c: ebfff611 bl 2288 <memcpy@plt>

4a40: e156000a cmp r6, sl

4a44: e0855004 add r5, r5, r4

4a48: 1affffe1 bne 49d4 <mcrypt\_mutex\_register@@Base+0x240>

4a4c: e3a00000 mov r0, #0

4a50: e59d3004 ldr r3, [sp, #4]

4a54: e3530000 cmp r3, #0

4a58: 03a00000 moveq r0, #0

4a5c: e2600000 rsb r0, r0, #0

4a60: e28dd00c add sp, sp, #12

4a64: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

4a68: e3a00001 mov r0, #1

4a6c: eafffff7 b 4a50 <mcrypt\_mutex\_register@@Base+0x2bc>

4a70: e3a00001 mov r0, #1

4a74: e12fff1e bx lr

4a78: e3a00001 mov r0, #1

4a7c: e12fff1e bx lr

4a80: e3a00001 mov r0, #1

4a84: e12fff1e bx lr

4a88: e59f0004 ldr r0, [pc, #4] ; 4a94 <mcrypt\_mutex\_register@@Base+0x300>

4a8c: e08f0000 add r0, pc, r0

4a90: e12fff1e bx lr

4a94: 00019b90 muleq r1, r0, fp

4a98: e3a0000c mov r0, #12

4a9c: e12fff1e bx lr

4aa0: e59f0000 ldr r0, [pc] ; 4aa8 <mcrypt\_mutex\_register@@Base+0x314>

4aa4: e12fff1e bx lr

4aa8: 01315731 teqeq r1, r1, lsr r7

4aac: e92d41f0 push {r4, r5, r6, r7, r8, lr}

4ab0: e3a05000 mov r5, #0

4ab4: e59d6018 ldr r6, [sp, #24]

4ab8: e1a04000 mov r4, r0

4abc: e3a01001 mov r1, #1

4ac0: e9800060 stmib r0, {r5, r6}

4ac4: e1a00006 mov r0, r6

4ac8: e1a08003 mov r8, r3

4acc: ebfff5d8 bl 2234 <calloc@plt>

4ad0: e1500005 cmp r0, r5

4ad4: e1a07000 mov r7, r0

4ad8: e5840000 str r0, [r4]

4adc: 0a00000e beq 4b1c <mcrypt\_mutex\_register@@Base+0x388>

4ae0: e1a00006 mov r0, r6

4ae4: ebfff602 bl 22f4 <malloc@plt>

4ae8: e1500005 cmp r0, r5

4aec: e5840004 str r0, [r4, #4]

4af0: 0a000009 beq 4b1c <mcrypt\_mutex\_register@@Base+0x388>

4af4: e1580005 cmp r8, r5

4af8: 0a000005 beq 4b14 <mcrypt\_mutex\_register@@Base+0x380>

4afc: e1a02006 mov r2, r6

4b00: e1a01008 mov r1, r8

4b04: e1a00007 mov r0, r7

4b08: ebfff5de bl 2288 <memcpy@plt>

4b0c: e1a00005 mov r0, r5

4b10: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4b14: e1a00008 mov r0, r8

4b18: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4b1c: e1a00007 mov r0, r7

4b20: ebfff5d5 bl 227c <free@plt>

4b24: e3e00000 mvn r0, #0

4b28: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4b2c: e92d4070 push {r4, r5, r6, lr}

4b30: e1a04000 mov r4, r0

4b34: e1a05001 mov r5, r1

4b38: e1a06002 mov r6, r2

4b3c: e5900004 ldr r0, [r0, #4]

4b40: ebfff5d0 bl 2288 <memcpy@plt>

4b44: e1a02006 mov r2, r6

4b48: e1a01005 mov r1, r5

4b4c: e5940000 ldr r0, [r4]

4b50: ebfff5cc bl 2288 <memcpy@plt>

4b54: e3a00000 mov r0, #0

4b58: e8bd8070 pop {r4, r5, r6, pc}

4b5c: e92d4010 push {r4, lr}

4b60: e5923000 ldr r3, [r2]

4b64: e590e008 ldr lr, [r0, #8]

4b68: e153000e cmp r3, lr

4b6c: ba000007 blt 4b90 <mcrypt\_mutex\_register@@Base+0x3fc>

4b70: e1a0c000 mov ip, r0

4b74: e582e000 str lr, [r2]

4b78: e1a00001 mov r0, r1

4b7c: e1a0200e mov r2, lr

4b80: e59c1000 ldr r1, [ip]

4b84: ebfff5bf bl 2288 <memcpy@plt>

4b88: e3a00000 mov r0, #0

4b8c: e8bd8010 pop {r4, pc}

4b90: e582e000 str lr, [r2]

4b94: e3e00000 mvn r0, #0

4b98: e8bd8010 pop {r4, pc}

4b9c: e92d4010 push {r4, lr}

4ba0: e1a04000 mov r4, r0

4ba4: e5900000 ldr r0, [r0]

4ba8: ebfff5b3 bl 227c <free@plt>

4bac: e5940004 ldr r0, [r4, #4]

4bb0: e8bd4010 pop {r4, lr}

4bb4: eafff5b0 b 227c <free@plt>

4bb8: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

4bbc: e3520000 cmp r2, #0

4bc0: e59d8024 ldr r8, [sp, #36] ; 0x24

4bc4: da000020 ble 4c4c <mcrypt\_mutex\_register@@Base+0x4b8>

4bc8: e2427001 sub r7, r2, #1

4bcc: e1a09003 mov r9, r3

4bd0: e1a04000 mov r4, r0

4bd4: e0817007 add r7, r1, r7

4bd8: e2416001 sub r6, r1, #1

4bdc: e2435001 sub r5, r3, #1

4be0: e1a02009 mov r2, r9

4be4: e5941000 ldr r1, [r4]

4be8: e5940004 ldr r0, [r4, #4]

4bec: ebfff5a5 bl 2288 <memcpy@plt>

4bf0: e5941004 ldr r1, [r4, #4]

4bf4: e59d0020 ldr r0, [sp, #32]

4bf8: e12fff38 blx r8

4bfc: e5943004 ldr r3, [r4, #4]

4c00: e5f62001 ldrb r2, [r6, #1]!

4c04: e3590001 cmp r9, #1

4c08: e5d33000 ldrb r3, [r3]

4c0c: e0233002 eor r3, r3, r2

4c10: e5c63000 strb r3, [r6]

4c14: da000008 ble 4c3c <mcrypt\_mutex\_register@@Base+0x4a8>

4c18: e3a02000 mov r2, #0

4c1c: e5941000 ldr r1, [r4]

4c20: e0813002 add r3, r1, r2

4c24: e5d3c001 ldrb ip, [r3, #1]

4c28: e7c1c002 strb ip, [r1, r2]

4c2c: e2822001 add r2, r2, #1

4c30: e1550002 cmp r5, r2

4c34: 1afffff8 bne 4c1c <mcrypt\_mutex\_register@@Base+0x488>

4c38: e5d63000 ldrb r3, [r6]

4c3c: e5942000 ldr r2, [r4]

4c40: e1560007 cmp r6, r7

4c44: e7c23005 strb r3, [r2, r5]

4c48: 1affffe4 bne 4be0 <mcrypt\_mutex\_register@@Base+0x44c>

4c4c: e3a00000 mov r0, #0

4c50: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

4c54: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

4c58: e3520000 cmp r2, #0

4c5c: e59d8024 ldr r8, [sp, #36] ; 0x24

4c60: da00001f ble 4ce4 <mcrypt\_mutex\_register@@Base+0x550>

4c64: e2427001 sub r7, r2, #1

4c68: e1a09003 mov r9, r3

4c6c: e1a04000 mov r4, r0

4c70: e0817007 add r7, r1, r7

4c74: e2416001 sub r6, r1, #1

4c78: e2435001 sub r5, r3, #1

4c7c: e1a02009 mov r2, r9

4c80: e5941000 ldr r1, [r4]

4c84: e5940004 ldr r0, [r4, #4]

4c88: ebfff57e bl 2288 <memcpy@plt>

4c8c: e5941004 ldr r1, [r4, #4]

4c90: e59d0020 ldr r0, [sp, #32]

4c94: e12fff38 blx r8

4c98: e3590001 cmp r9, #1

4c9c: c3a02000 movgt r2, #0

4ca0: da000006 ble 4cc0 <mcrypt\_mutex\_register@@Base+0x52c>

4ca4: e5941000 ldr r1, [r4]

4ca8: e0813002 add r3, r1, r2

4cac: e5d3c001 ldrb ip, [r3, #1]

4cb0: e7c1c002 strb ip, [r1, r2]

4cb4: e2822001 add r2, r2, #1

4cb8: e1520005 cmp r2, r5

4cbc: 1afffff8 bne 4ca4 <mcrypt\_mutex\_register@@Base+0x510>

4cc0: e5f63001 ldrb r3, [r6, #1]!

4cc4: e5942000 ldr r2, [r4]

4cc8: e1560007 cmp r6, r7

4ccc: e7c23005 strb r3, [r2, r5]

4cd0: e5942004 ldr r2, [r4, #4]

4cd4: e5d22000 ldrb r2, [r2]

4cd8: e0233002 eor r3, r3, r2

4cdc: e5c63000 strb r3, [r6]

4ce0: 1affffe5 bne 4c7c <mcrypt\_mutex\_register@@Base+0x4e8>

4ce4: e3a00000 mov r0, #0

4ce8: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

4cec: e3a00001 mov r0, #1

4cf0: e12fff1e bx lr

4cf4: e3a00000 mov r0, #0

4cf8: e12fff1e bx lr

4cfc: e3a00001 mov r0, #1

4d00: e12fff1e bx lr

4d04: e59f0004 ldr r0, [pc, #4] ; 4d10 <mcrypt\_mutex\_register@@Base+0x57c>

4d08: e08f0000 add r0, pc, r0

4d0c: e12fff1e bx lr

4d10: 00019918 andeq r9, r1, r8, lsl r9

4d14: e3a0000c mov r0, #12

4d18: e12fff1e bx lr

4d1c: e59f0000 ldr r0, [pc] ; 4d24 <mcrypt\_mutex\_register@@Base+0x590>

4d20: e12fff1e bx lr

4d24: 01317c56 teqeq r1, r6, asr ip

4d28: e92d41f0 push {r4, r5, r6, r7, r8, lr}

4d2c: e3a05000 mov r5, #0

4d30: e59d6018 ldr r6, [sp, #24]

4d34: e1a04000 mov r4, r0

4d38: e5805000 str r5, [r0]

4d3c: e5805008 str r5, [r0, #8]

4d40: e580600c str r6, [r0, #12]

4d44: e1a01006 mov r1, r6

4d48: e3a00001 mov r0, #1

4d4c: e1a08003 mov r8, r3

4d50: ebfff537 bl 2234 <calloc@plt>

4d54: e1500005 cmp r0, r5

4d58: e1a07000 mov r7, r0

4d5c: e5840004 str r0, [r4, #4]

4d60: 0a000012 beq 4db0 <mcrypt\_mutex\_register@@Base+0x61c>

4d64: e1a01006 mov r1, r6

4d68: e3a00001 mov r0, #1

4d6c: ebfff530 bl 2234 <calloc@plt>

4d70: e1500005 cmp r0, r5

4d74: e5840000 str r0, [r4]

4d78: 0a00000c beq 4db0 <mcrypt\_mutex\_register@@Base+0x61c>

4d7c: e1580005 cmp r8, r5

4d80: 0a000008 beq 4da8 <mcrypt\_mutex\_register@@Base+0x614>

4d84: e1a02006 mov r2, r6

4d88: e1a01008 mov r1, r8

4d8c: ebfff53d bl 2288 <memcpy@plt>

4d90: e1a02006 mov r2, r6

4d94: e1a01008 mov r1, r8

4d98: e1a00007 mov r0, r7

4d9c: ebfff539 bl 2288 <memcpy@plt>

4da0: e1a00005 mov r0, r5

4da4: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4da8: e1a00008 mov r0, r8

4dac: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4db0: e1a00007 mov r0, r7

4db4: ebfff530 bl 227c <free@plt>

4db8: e3e00000 mvn r0, #0

4dbc: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

4dc0: e92d4070 push {r4, r5, r6, lr}

4dc4: e1a04001 mov r4, r1

4dc8: e1a05000 mov r5, r0

4dcc: e4d43001 ldrb r3, [r4], #1

4dd0: e2426001 sub r6, r2, #1

4dd4: e1a02006 mov r2, r6

4dd8: e5803008 str r3, [r0, #8]

4ddc: e1a01004 mov r1, r4

4de0: e5900004 ldr r0, [r0, #4]

4de4: ebfff527 bl 2288 <memcpy@plt>

4de8: e1a02006 mov r2, r6

4dec: e1a01004 mov r1, r4

4df0: e5950000 ldr r0, [r5]

4df4: ebfff523 bl 2288 <memcpy@plt>

4df8: e3a00000 mov r0, #0

4dfc: e8bd8070 pop {r4, r5, r6, pc}

4e00: e590300c ldr r3, [r0, #12]

4e04: e592c000 ldr ip, [r2]

4e08: e153000c cmp r3, ip

4e0c: e283c001 add ip, r3, #1

4e10: aa00000a bge 4e40 <mcrypt\_mutex\_register@@Base+0x6ac>

4e14: e1a03000 mov r3, r0

4e18: e92d4010 push {r4, lr}

4e1c: e1a00001 mov r0, r1

4e20: e582c000 str ip, [r2]

4e24: e5932008 ldr r2, [r3, #8]

4e28: e4c02001 strb r2, [r0], #1

4e2c: e593200c ldr r2, [r3, #12]

4e30: e5931004 ldr r1, [r3, #4]

4e34: ebfff513 bl 2288 <memcpy@plt>

4e38: e3a00000 mov r0, #0

4e3c: e8bd8010 pop {r4, pc}

4e40: e582c000 str ip, [r2]

4e44: e3e00000 mvn r0, #0

4e48: e12fff1e bx lr

4e4c: e92d4010 push {r4, lr}

4e50: e1a04000 mov r4, r0

4e54: e5900004 ldr r0, [r0, #4]

4e58: ebfff507 bl 227c <free@plt>

4e5c: e5940000 ldr r0, [r4]

4e60: e8bd4010 pop {r4, lr}

4e64: eafff504 b 227c <free@plt>

4e68: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

4e6c: e24dd01c sub sp, sp, #28

4e70: e1a04000 mov r4, r0

4e74: e58d1010 str r1, [sp, #16]

4e78: e1a05001 mov r5, r1

4e7c: e1a00002 mov r0, r2

4e80: e1a01003 mov r1, r3

4e84: e1a07003 mov r7, r3

4e88: eb005ba4 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

4e8c: e2503000 subs r3, r0, #0

4e90: e58d1014 str r1, [sp, #20]

4e94: e58d3004 str r3, [sp, #4]

4e98: da00004d ble 4fd4 <mcrypt\_mutex\_register@@Base+0x840>

4e9c: e3a08000 mov r8, #0

4ea0: e2473002 sub r3, r7, #2

4ea4: e1a0a008 mov sl, r8

4ea8: e2479001 sub r9, r7, #1

4eac: e2456001 sub r6, r5, #1

4eb0: e58d3008 str r3, [sp, #8]

4eb4: e2673001 rsb r3, r7, #1

4eb8: e58d300c str r3, [sp, #12]

4ebc: e5942008 ldr r2, [r4, #8]

4ec0: e286b001 add fp, r6, #1

4ec4: e3520000 cmp r2, #0

4ec8: e5940000 ldr r0, [r4]

4ecc: 0a000046 beq 4fec <mcrypt\_mutex\_register@@Base+0x858>

4ed0: e0475002 sub r5, r7, r2

4ed4: e3550000 cmp r5, #0

4ed8: da00000a ble 4f08 <mcrypt\_mutex\_register@@Base+0x774>

4edc: e2422001 sub r2, r2, #1

4ee0: e245c001 sub ip, r5, #1

4ee4: e0800002 add r0, r0, r2

4ee8: e08cc00b add ip, ip, fp

4eec: e1a02006 mov r2, r6

4ef0: e5f23001 ldrb r3, [r2, #1]!

4ef4: e5f01001 ldrb r1, [r0, #1]!

4ef8: e152000c cmp r2, ip

4efc: e0233001 eor r3, r3, r1

4f00: e5c23000 strb r3, [r2]

4f04: 1afffff9 bne 4ef0 <mcrypt\_mutex\_register@@Base+0x75c>

4f08: e3590000 cmp r9, #0

4f0c: e5941004 ldr r1, [r4, #4]

4f10: ba000011 blt 4f5c <mcrypt\_mutex\_register@@Base+0x7c8>

4f14: e7d12009 ldrb r2, [r1, r9]

4f18: e081c009 add ip, r1, r9

4f1c: e35200ff cmp r2, #255 ; 0xff

4f20: 1a000059 bne 508c <mcrypt\_mutex\_register@@Base+0x8f8>

4f24: e59d3008 ldr r3, [sp, #8]

4f28: e28c0001 add r0, ip, #1

4f2c: e0811003 add r1, r1, r3

4f30: e59d300c ldr r3, [sp, #12]

4f34: e08ce003 add lr, ip, r3

4f38: ea000002 b 4f48 <mcrypt\_mutex\_register@@Base+0x7b4>

4f3c: e4512001 ldrb r2, [r1], #-1

4f40: e35200ff cmp r2, #255 ; 0xff

4f44: 1a000050 bne 508c <mcrypt\_mutex\_register@@Base+0x8f8>

4f48: e560a001 strb sl, [r0, #-1]!

4f4c: e150000e cmp r0, lr

4f50: e1a0c001 mov ip, r1

4f54: 1afffff8 bne 4f3c <mcrypt\_mutex\_register@@Base+0x7a8>

4f58: e5941004 ldr r1, [r4, #4]

4f5c: e1a02007 mov r2, r7

4f60: e5940000 ldr r0, [r4]

4f64: ebfff4c7 bl 2288 <memcpy@plt>

4f68: e5941000 ldr r1, [r4]

4f6c: e59d0040 ldr r0, [sp, #64] ; 0x40

4f70: e59d3044 ldr r3, [sp, #68] ; 0x44

4f74: e12fff33 blx r3

4f78: e5941008 ldr r1, [r4, #8]

4f7c: e5942000 ldr r2, [r4]

4f80: e3510000 cmp r1, #0

4f84: da00000a ble 4fb4 <mcrypt\_mutex\_register@@Base+0x820>

4f88: e2455001 sub r5, r5, #1

4f8c: e0821001 add r1, r2, r1

4f90: e08b3005 add r3, fp, r5

4f94: e2411001 sub r1, r1, #1

4f98: e2420001 sub r0, r2, #1

4f9c: e5f0c001 ldrb ip, [r0, #1]!

4fa0: e5f32001 ldrb r2, [r3, #1]!

4fa4: e1500001 cmp r0, r1

4fa8: e022200c eor r2, r2, ip

4fac: e5c32000 strb r2, [r3]

4fb0: 1afffff9 bne 4f9c <mcrypt\_mutex\_register@@Base+0x808>

4fb4: e0876006 add r6, r7, r6

4fb8: e59d3004 ldr r3, [sp, #4]

4fbc: e2888001 add r8, r8, #1

4fc0: e1530008 cmp r3, r8

4fc4: 1affffbc bne 4ebc <mcrypt\_mutex\_register@@Base+0x728>

4fc8: e59d2010 ldr r2, [sp, #16]

4fcc: e0232397 mla r3, r7, r3, r2

4fd0: e58d3010 str r3, [sp, #16]

4fd4: e59d3014 ldr r3, [sp, #20]

4fd8: e3530000 cmp r3, #0

4fdc: ca00002d bgt 5098 <mcrypt\_mutex\_register@@Base+0x904>

4fe0: e3a00000 mov r0, #0

4fe4: e28dd01c add sp, sp, #28

4fe8: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

4fec: e1a02007 mov r2, r7

4ff0: e5941004 ldr r1, [r4, #4]

4ff4: ebfff4a3 bl 2288 <memcpy@plt>

4ff8: e5941000 ldr r1, [r4]

4ffc: e59d0040 ldr r0, [sp, #64] ; 0x40

5000: e59d3044 ldr r3, [sp, #68] ; 0x44

5004: e12fff33 blx r3

5008: e3570000 cmp r7, #0

500c: e5941000 ldr r1, [r4]

5010: da00006c ble 51c8 <mcrypt\_mutex\_register@@Base+0xa34>

5014: e1a03006 mov r3, r6

5018: e2411001 sub r1, r1, #1

501c: e0876006 add r6, r7, r6

5020: e5f32001 ldrb r2, [r3, #1]!

5024: e5f10001 ldrb r0, [r1, #1]!

5028: e1530006 cmp r3, r6

502c: e0222000 eor r2, r2, r0

5030: e5c32000 strb r2, [r3]

5034: 1afffff9 bne 5020 <mcrypt\_mutex\_register@@Base+0x88c>

5038: e5942004 ldr r2, [r4, #4]

503c: e082c009 add ip, r2, r9

5040: e7d21009 ldrb r1, [r2, r9]

5044: e35100ff cmp r1, #255 ; 0xff

5048: 1a000066 bne 51e8 <mcrypt\_mutex\_register@@Base+0xa54>

504c: e59d3008 ldr r3, [sp, #8]

5050: e0822003 add r2, r2, r3

5054: e28c3001 add r3, ip, #1

5058: e2431003 sub r1, r3, #3

505c: e047000c sub r0, r7, ip

5060: e1700001 cmn r0, r1

5064: e543a001 strb sl, [r3, #-1]

5068: e1a0e002 mov lr, r2

506c: e2433001 sub r3, r3, #1

5070: 4affffd0 bmi 4fb8 <mcrypt\_mutex\_register@@Base+0x824>

5074: e4521001 ldrb r1, [r2], #-1

5078: e35100ff cmp r1, #255 ; 0xff

507c: 0afffff5 beq 5058 <mcrypt\_mutex\_register@@Base+0x8c4>

5080: e2811001 add r1, r1, #1

5084: e5ce1000 strb r1, [lr]

5088: eaffffca b 4fb8 <mcrypt\_mutex\_register@@Base+0x824>

508c: e2822001 add r2, r2, #1

5090: e5cc2000 strb r2, [ip]

5094: eaffffaf b 4f58 <mcrypt\_mutex\_register@@Base+0x7c4>

5098: e1570003 cmp r7, r3

509c: e5940000 ldr r0, [r4]

50a0: e5943008 ldr r3, [r4, #8]

50a4: 0a00006a beq 5254 <mcrypt\_mutex\_register@@Base+0xac0>

50a8: e3530000 cmp r3, #0

50ac: 0a000051 beq 51f8 <mcrypt\_mutex\_register@@Base+0xa64>

50b0: e59d2014 ldr r2, [sp, #20]

50b4: e0476003 sub r6, r7, r3

50b8: e1560002 cmp r6, r2

50bc: a1a06002 movge r6, r2

50c0: e3560000 cmp r6, #0

50c4: da0000c1 ble 53d0 <mcrypt\_mutex\_register@@Base+0xc3c>

50c8: e2433001 sub r3, r3, #1

50cc: e0800003 add r0, r0, r3

50d0: e59d3010 ldr r3, [sp, #16]

50d4: e2465001 sub r5, r6, #1

50d8: e083c005 add ip, r3, r5

50dc: e2433001 sub r3, r3, #1

50e0: e5f32001 ldrb r2, [r3, #1]!

50e4: e5f01001 ldrb r1, [r0, #1]!

50e8: e153000c cmp r3, ip

50ec: e0222001 eor r2, r2, r1

50f0: e5c32000 strb r2, [r3]

50f4: 1afffff9 bne 50e0 <mcrypt\_mutex\_register@@Base+0x94c>

50f8: e5943008 ldr r3, [r4, #8]

50fc: e59d2014 ldr r2, [sp, #20]

5100: e0833006 add r3, r3, r6

5104: e1520006 cmp r2, r6

5108: e5843008 str r3, [r4, #8]

510c: daffffb3 ble 4fe0 <mcrypt\_mutex\_register@@Base+0x84c>

5110: e257c001 subs ip, r7, #1

5114: e5941004 ldr r1, [r4, #4]

5118: 4a000012 bmi 5168 <mcrypt\_mutex\_register@@Base+0x9d4>

511c: e7d1000c ldrb r0, [r1, ip]

5120: e081c00c add ip, r1, ip

5124: e35000ff cmp r0, #255 ; 0xff

5128: 1a000080 bne 5330 <mcrypt\_mutex\_register@@Base+0xb9c>

512c: e2672001 rsb r2, r7, #1

5130: e2473002 sub r3, r7, #2

5134: e0813003 add r3, r1, r3

5138: e3a0e000 mov lr, #0

513c: e08c1002 add r1, ip, r2

5140: e28c2001 add r2, ip, #1

5144: ea000002 b 5154 <mcrypt\_mutex\_register@@Base+0x9c0>

5148: e4530001 ldrb r0, [r3], #-1

514c: e35000ff cmp r0, #255 ; 0xff

5150: 1a000076 bne 5330 <mcrypt\_mutex\_register@@Base+0xb9c>

5154: e562e001 strb lr, [r2, #-1]!

5158: e1510002 cmp r1, r2

515c: e1a0c003 mov ip, r3

5160: 1afffff8 bne 5148 <mcrypt\_mutex\_register@@Base+0x9b4>

5164: e5941004 ldr r1, [r4, #4]

5168: e1a02007 mov r2, r7

516c: e5940000 ldr r0, [r4]

5170: ebfff444 bl 2288 <memcpy@plt>

5174: e59d0040 ldr r0, [sp, #64] ; 0x40

5178: e59d3044 ldr r3, [sp, #68] ; 0x44

517c: e5941000 ldr r1, [r4]

5180: e12fff33 blx r3

5184: e59d3014 ldr r3, [sp, #20]

5188: e5942000 ldr r2, [r4]

518c: e0436006 sub r6, r3, r6

5190: e59d3010 ldr r3, [sp, #16]

5194: e2422001 sub r2, r2, #1

5198: e0835005 add r5, r3, r5

519c: e0820006 add r0, r2, r6

51a0: e5f21001 ldrb r1, [r2, #1]!

51a4: e5f53001 ldrb r3, [r5, #1]!

51a8: e1500002 cmp r0, r2

51ac: e0233001 eor r3, r3, r1

51b0: e5c53000 strb r3, [r5]

51b4: 1afffff9 bne 51a0 <mcrypt\_mutex\_register@@Base+0xa0c>

51b8: e3a00000 mov r0, #0

51bc: e5846008 str r6, [r4, #8]

51c0: e28dd01c add sp, sp, #28

51c4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

51c8: e3590000 cmp r9, #0

51cc: baffff78 blt 4fb4 <mcrypt\_mutex\_register@@Base+0x820>

51d0: e5942004 ldr r2, [r4, #4]

51d4: e0876006 add r6, r7, r6

51d8: e082c009 add ip, r2, r9

51dc: e7d21009 ldrb r1, [r2, r9]

51e0: e35100ff cmp r1, #255 ; 0xff

51e4: 0affff98 beq 504c <mcrypt\_mutex\_register@@Base+0x8b8>

51e8: e1a0e00c mov lr, ip

51ec: e2811001 add r1, r1, #1

51f0: e5ce1000 strb r1, [lr]

51f4: eaffff6f b 4fb8 <mcrypt\_mutex\_register@@Base+0x824>

51f8: e1a02007 mov r2, r7

51fc: e5941004 ldr r1, [r4, #4]

5200: ebfff420 bl 2288 <memcpy@plt>

5204: e5941000 ldr r1, [r4]

5208: e59d3044 ldr r3, [sp, #68] ; 0x44

520c: e59d0040 ldr r0, [sp, #64] ; 0x40

5210: e12fff33 blx r3

5214: e59d3014 ldr r3, [sp, #20]

5218: e5941000 ldr r1, [r4]

521c: e243c001 sub ip, r3, #1

5220: e59d3010 ldr r3, [sp, #16]

5224: e2411001 sub r1, r1, #1

5228: e083c00c add ip, r3, ip

522c: e2433001 sub r3, r3, #1

5230: e5f32001 ldrb r2, [r3, #1]!

5234: e5f10001 ldrb r0, [r1, #1]!

5238: e153000c cmp r3, ip

523c: e0222000 eor r2, r2, r0

5240: e5c32000 strb r2, [r3]

5244: 1afffff9 bne 5230 <mcrypt\_mutex\_register@@Base+0xa9c>

5248: e59d3014 ldr r3, [sp, #20]

524c: e5843008 str r3, [r4, #8]

5250: eaffff62 b 4fe0 <mcrypt\_mutex\_register@@Base+0x84c>

5254: e3530000 cmp r3, #0

5258: 0a000037 beq 533c <mcrypt\_mutex\_register@@Base+0xba8>

525c: e59d2014 ldr r2, [sp, #20]

5260: e0425003 sub r5, r2, r3

5264: e3550000 cmp r5, #0

5268: da00000b ble 529c <mcrypt\_mutex\_register@@Base+0xb08>

526c: e2433001 sub r3, r3, #1

5270: e0800003 add r0, r0, r3

5274: e59d3010 ldr r3, [sp, #16]

5278: e245c001 sub ip, r5, #1

527c: e083c00c add ip, r3, ip

5280: e2433001 sub r3, r3, #1

5284: e5f32001 ldrb r2, [r3, #1]!

5288: e5f01001 ldrb r1, [r0, #1]!

528c: e15c0003 cmp ip, r3

5290: e0222001 eor r2, r2, r1

5294: e5c32000 strb r2, [r3]

5298: 1afffff9 bne 5284 <mcrypt\_mutex\_register@@Base+0xaf0>

529c: e59d3014 ldr r3, [sp, #20]

52a0: e5940004 ldr r0, [r4, #4]

52a4: e2432001 sub r2, r3, #1

52a8: e0802002 add r2, r0, r2

52ac: e3a0c000 mov ip, #0

52b0: e1a03002 mov r3, r2

52b4: e4531001 ldrb r1, [r3], #-1

52b8: e35100ff cmp r1, #255 ; 0xff

52bc: 1a000040 bne 53c4 <mcrypt\_mutex\_register@@Base+0xc30>

52c0: e1500002 cmp r0, r2

52c4: e5c3c001 strb ip, [r3, #1]

52c8: e1a02003 mov r2, r3

52cc: 1afffff7 bne 52b0 <mcrypt\_mutex\_register@@Base+0xb1c>

52d0: e59d2014 ldr r2, [sp, #20]

52d4: e1c400d0 ldrd r0, [r4]

52d8: ebfff3ea bl 2288 <memcpy@plt>

52dc: e5941000 ldr r1, [r4]

52e0: e59d3044 ldr r3, [sp, #68] ; 0x44

52e4: e59d0040 ldr r0, [sp, #64] ; 0x40

52e8: e12fff33 blx r3

52ec: e5943008 ldr r3, [r4, #8]

52f0: e5941000 ldr r1, [r4]

52f4: e3530000 cmp r3, #0

52f8: daffff38 ble 4fe0 <mcrypt\_mutex\_register@@Base+0x84c>

52fc: e59d0010 ldr r0, [sp, #16]

5300: e0813003 add r3, r1, r3

5304: e2452001 sub r2, r5, #1

5308: e0802002 add r2, r0, r2

530c: e243c001 sub ip, r3, #1

5310: e2411001 sub r1, r1, #1

5314: e5f10001 ldrb r0, [r1, #1]!

5318: e5f23001 ldrb r3, [r2, #1]!

531c: e15c0001 cmp ip, r1

5320: e0233000 eor r3, r3, r0

5324: e5c23000 strb r3, [r2]

5328: 1afffff9 bne 5314 <mcrypt\_mutex\_register@@Base+0xb80>

532c: eaffff2b b 4fe0 <mcrypt\_mutex\_register@@Base+0x84c>

5330: e2800001 add r0, r0, #1

5334: e5cc0000 strb r0, [ip]

5338: eaffff89 b 5164 <mcrypt\_mutex\_register@@Base+0x9d0>

533c: e1a02007 mov r2, r7

5340: e5941004 ldr r1, [r4, #4]

5344: ebfff3cf bl 2288 <memcpy@plt>

5348: e5941000 ldr r1, [r4]

534c: e59d3044 ldr r3, [sp, #68] ; 0x44

5350: e59d0040 ldr r0, [sp, #64] ; 0x40

5354: e12fff33 blx r3

5358: e59d3010 ldr r3, [sp, #16]

535c: e5941000 ldr r1, [r4]

5360: e2477001 sub r7, r7, #1

5364: e083c007 add ip, r3, r7

5368: e2411001 sub r1, r1, #1

536c: e2433001 sub r3, r3, #1

5370: e5f32001 ldrb r2, [r3, #1]!

5374: e5f10001 ldrb r0, [r1, #1]!

5378: e15c0003 cmp ip, r3

537c: e0222000 eor r2, r2, r0

5380: e5c32000 strb r2, [r3]

5384: 1afffff9 bne 5370 <mcrypt\_mutex\_register@@Base+0xbdc>

5388: e5940004 ldr r0, [r4, #4]

538c: e3a0c000 mov ip, #0

5390: e0802007 add r2, r0, r7

5394: ea000003 b 53a8 <mcrypt\_mutex\_register@@Base+0xc14>

5398: e1500002 cmp r0, r2

539c: e5c3c001 strb ip, [r3, #1]

53a0: e1a02003 mov r2, r3

53a4: 0affff0d beq 4fe0 <mcrypt\_mutex\_register@@Base+0x84c>

53a8: e1a03002 mov r3, r2

53ac: e4531001 ldrb r1, [r3], #-1

53b0: e35100ff cmp r1, #255 ; 0xff

53b4: 0afffff7 beq 5398 <mcrypt\_mutex\_register@@Base+0xc04>

53b8: e2811001 add r1, r1, #1

53bc: e5c21000 strb r1, [r2]

53c0: eaffff06 b 4fe0 <mcrypt\_mutex\_register@@Base+0x84c>

53c4: e2811001 add r1, r1, #1

53c8: e5c21000 strb r1, [r2]

53cc: eaffffbf b 52d0 <mcrypt\_mutex\_register@@Base+0xb3c>

53d0: e0863003 add r3, r6, r3

53d4: e5843008 str r3, [r4, #8]

53d8: e2465001 sub r5, r6, #1

53dc: eaffff4b b 5110 <mcrypt\_mutex\_register@@Base+0x97c>

53e0: eafffea0 b 4e68 <mcrypt\_mutex\_register@@Base+0x6d4>

53e4: e3a00001 mov r0, #1

53e8: e12fff1e bx lr

53ec: e3a00000 mov r0, #0

53f0: e12fff1e bx lr

53f4: e3a00001 mov r0, #1

53f8: e12fff1e bx lr

53fc: e59f0004 ldr r0, [pc, #4] ; 5408 <mcrypt\_mutex\_register@@Base+0xc74>

5400: e08f0000 add r0, pc, r0

5404: e12fff1e bx lr

5408: 00019224 andeq r9, r1, r4, lsr #4

540c: e3a00010 mov r0, #16

5410: e12fff1e bx lr

5414: e59f0000 ldr r0, [pc] ; 541c <mcrypt\_mutex\_register@@Base+0xc88>

5418: e12fff1e bx lr

541c: 01317c53 teqeq r1, r3, asr ip

5420: e3a00000 mov r0, #0

5424: e12fff1e bx lr

5428: e3e00000 mvn r0, #0

542c: e12fff1e bx lr

5430: e3e00000 mvn r0, #0

5434: e12fff1e bx lr

5438: e3a00000 mov r0, #0

543c: e12fff1e bx lr

5440: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

5444: e1a00002 mov r0, r2

5448: e1a04001 mov r4, r1

544c: e1a01003 mov r1, r3

5450: e1a09002 mov r9, r2

5454: e1a06003 mov r6, r3

5458: eb0059a8 bl 1bb00 <mcrypt\_mutex\_register@@Base+0x1736c>

545c: e59d8024 ldr r8, [sp, #36] ; 0x24

5460: e2507000 subs r7, r0, #0

5464: da00000c ble 549c <mcrypt\_mutex\_register@@Base+0xd08>

5468: e3a05000 mov r5, #0

546c: e1a01004 mov r1, r4

5470: e2855001 add r5, r5, #1

5474: e59d0020 ldr r0, [sp, #32]

5478: e12fff38 blx r8

547c: e1550007 cmp r5, r7

5480: e0844006 add r4, r4, r6

5484: 1afffff8 bne 546c <mcrypt\_mutex\_register@@Base+0xcd8>

5488: e3a00000 mov r0, #0

548c: e3590000 cmp r9, #0

5490: 03a00000 moveq r0, #0

5494: e2600000 rsb r0, r0, #0

5498: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

549c: e3a00001 mov r0, #1

54a0: eafffff9 b 548c <mcrypt\_mutex\_register@@Base+0xcf8>

54a4: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

54a8: e1a00002 mov r0, r2

54ac: e1a04001 mov r4, r1

54b0: e1a01003 mov r1, r3

54b4: e1a09002 mov r9, r2

54b8: e1a06003 mov r6, r3

54bc: eb00598f bl 1bb00 <mcrypt\_mutex\_register@@Base+0x1736c>

54c0: e59d8028 ldr r8, [sp, #40] ; 0x28

54c4: e2507000 subs r7, r0, #0

54c8: da00000c ble 5500 <mcrypt\_mutex\_register@@Base+0xd6c>

54cc: e3a05000 mov r5, #0

54d0: e1a01004 mov r1, r4

54d4: e2855001 add r5, r5, #1

54d8: e59d0020 ldr r0, [sp, #32]

54dc: e12fff38 blx r8

54e0: e1550007 cmp r5, r7

54e4: e0844006 add r4, r4, r6

54e8: 1afffff8 bne 54d0 <mcrypt\_mutex\_register@@Base+0xd3c>

54ec: e3a00000 mov r0, #0

54f0: e3590000 cmp r9, #0

54f4: 03a00000 moveq r0, #0

54f8: e2600000 rsb r0, r0, #0

54fc: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

5500: e3a00001 mov r0, #1

5504: eafffff9 b 54f0 <mcrypt\_mutex\_register@@Base+0xd5c>

5508: e3a00000 mov r0, #0

550c: e12fff1e bx lr

5510: e3a00001 mov r0, #1

5514: e12fff1e bx lr

5518: e3a00001 mov r0, #1

551c: e12fff1e bx lr

5520: e59f0004 ldr r0, [pc, #4] ; 552c <mcrypt\_mutex\_register@@Base+0xd98>

5524: e08f0000 add r0, pc, r0

5528: e12fff1e bx lr

552c: 00019104 andeq r9, r1, r4, lsl #2

5530: e3a00000 mov r0, #0

5534: e12fff1e bx lr

5538: e59f0000 ldr r0, [pc] ; 5540 <mcrypt\_mutex\_register@@Base+0xdac>

553c: e12fff1e bx lr

5540: 01315731 teqeq r1, r1, lsr r7

5544: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

5548: e3a05000 mov r5, #0

554c: e59d6020 ldr r6, [sp, #32]

5550: e1a04000 mov r4, r0

5554: e5805004 str r5, [r0, #4]

5558: e5805008 str r5, [r0, #8]

555c: e580600c str r6, [r0, #12]

5560: e3a01001 mov r1, #1

5564: e1a00006 mov r0, r6

5568: e1a08003 mov r8, r3

556c: ebfff330 bl 2234 <calloc@plt>

5570: e1500005 cmp r0, r5

5574: e1a07000 mov r7, r0

5578: e5840000 str r0, [r4]

557c: 0a000014 beq 55d4 <mcrypt\_mutex\_register@@Base+0xe40>

5580: e3a01001 mov r1, #1

5584: e1a00006 mov r0, r6

5588: ebfff329 bl 2234 <calloc@plt>

558c: e1500005 cmp r0, r5

5590: e1a09000 mov r9, r0

5594: e5840004 str r0, [r4, #4]

5598: 0a00000d beq 55d4 <mcrypt\_mutex\_register@@Base+0xe40>

559c: e1580005 cmp r8, r5

55a0: 0a000009 beq 55cc <mcrypt\_mutex\_register@@Base+0xe38>

55a4: e1a02006 mov r2, r6

55a8: e1a01008 mov r1, r8

55ac: e1a00007 mov r0, r7

55b0: ebfff334 bl 2288 <memcpy@plt>

55b4: e1a02006 mov r2, r6

55b8: e1a01008 mov r1, r8

55bc: e1a00009 mov r0, r9

55c0: ebfff330 bl 2288 <memcpy@plt>

55c4: e1a00005 mov r0, r5

55c8: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

55cc: e1a00008 mov r0, r8

55d0: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

55d4: e1a00007 mov r0, r7

55d8: ebfff327 bl 227c <free@plt>

55dc: e3e00000 mvn r0, #0

55e0: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

55e4: e92d4070 push {r4, r5, r6, lr}

55e8: e1a04001 mov r4, r1

55ec: e1a05000 mov r5, r0

55f0: e4d43001 ldrb r3, [r4], #1

55f4: e2426001 sub r6, r2, #1

55f8: e1a02006 mov r2, r6

55fc: e5803008 str r3, [r0, #8]

5600: e1a01004 mov r1, r4

5604: e5900000 ldr r0, [r0]

5608: ebfff31e bl 2288 <memcpy@plt>

560c: e1a02006 mov r2, r6

5610: e1a01004 mov r1, r4

5614: e5950004 ldr r0, [r5, #4]

5618: ebfff31a bl 2288 <memcpy@plt>

561c: e3a00000 mov r0, #0

5620: e8bd8070 pop {r4, r5, r6, pc}

5624: e590300c ldr r3, [r0, #12]

5628: e592c000 ldr ip, [r2]

562c: e153000c cmp r3, ip

5630: e283c001 add ip, r3, #1

5634: aa00000a bge 5664 <mcrypt\_mutex\_register@@Base+0xed0>

5638: e1a03000 mov r3, r0

563c: e92d4010 push {r4, lr}

5640: e1a00001 mov r0, r1

5644: e582c000 str ip, [r2]

5648: e5932008 ldr r2, [r3, #8]

564c: e4c02001 strb r2, [r0], #1

5650: e593200c ldr r2, [r3, #12]

5654: e5931004 ldr r1, [r3, #4]

5658: ebfff30a bl 2288 <memcpy@plt>

565c: e3a00000 mov r0, #0

5660: e8bd8010 pop {r4, pc}

5664: e582c000 str ip, [r2]

5668: e3e00000 mvn r0, #0

566c: e12fff1e bx lr

5670: e92d4010 push {r4, lr}

5674: e1a04000 mov r4, r0

5678: e5900000 ldr r0, [r0]

567c: ebfff2fe bl 227c <free@plt>

5680: e5940004 ldr r0, [r4, #4]

5684: e8bd4010 pop {r4, lr}

5688: eafff2fb b 227c <free@plt>

568c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

5690: e24dd00c sub sp, sp, #12

5694: e1a04000 mov r4, r0

5698: e58d1000 str r1, [sp]

569c: e1a05001 mov r5, r1

56a0: e1a00002 mov r0, r2

56a4: e1a01003 mov r1, r3

56a8: e1a06003 mov r6, r3

56ac: eb00599b bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

56b0: e59d9034 ldr r9, [sp, #52] ; 0x34

56b4: e250a000 subs sl, r0, #0

56b8: e58d1004 str r1, [sp, #4]

56bc: da000036 ble 579c <mcrypt\_mutex\_register@@Base+0x1008>

56c0: e2455001 sub r5, r5, #1

56c4: e3a07000 mov r7, #0

56c8: e5943008 ldr r3, [r4, #8]

56cc: e285b001 add fp, r5, #1

56d0: e3530000 cmp r3, #0

56d4: e5940000 ldr r0, [r4]

56d8: 0a000035 beq 57b4 <mcrypt\_mutex\_register@@Base+0x1020>

56dc: e0468003 sub r8, r6, r3

56e0: e3580000 cmp r8, #0

56e4: da00000b ble 5718 <mcrypt\_mutex\_register@@Base+0xf84>

56e8: e2433001 sub r3, r3, #1

56ec: e2481001 sub r1, r8, #1

56f0: e0800003 add r0, r0, r3

56f4: e081100b add r1, r1, fp

56f8: e1a03005 mov r3, r5

56fc: e5f32001 ldrb r2, [r3, #1]!

5700: e5f0c001 ldrb ip, [r0, #1]!

5704: e1530001 cmp r3, r1

5708: e022200c eor r2, r2, ip

570c: e5c32000 strb r2, [r3]

5710: 1afffff9 bne 56fc <mcrypt\_mutex\_register@@Base+0xf68>

5714: e5940000 ldr r0, [r4]

5718: e1a02006 mov r2, r6

571c: e5941004 ldr r1, [r4, #4]

5720: ebfff2d8 bl 2288 <memcpy@plt>

5724: e5941000 ldr r1, [r4]

5728: e59d0030 ldr r0, [sp, #48] ; 0x30

572c: e12fff39 blx r9

5730: e5942008 ldr r2, [r4, #8]

5734: e5941000 ldr r1, [r4]

5738: e3520000 cmp r2, #0

573c: da00000b ble 5770 <mcrypt\_mutex\_register@@Base+0xfdc>

5740: e0812002 add r2, r1, r2

5744: e2483001 sub r3, r8, #1

5748: e2422001 sub r2, r2, #1

574c: e2411001 sub r1, r1, #1

5750: e08b3003 add r3, fp, r3

5754: e5f1c001 ldrb ip, [r1, #1]!

5758: e5f30001 ldrb r0, [r3, #1]!

575c: e1510002 cmp r1, r2

5760: e020000c eor r0, r0, ip

5764: e5c30000 strb r0, [r3]

5768: 1afffff9 bne 5754 <mcrypt\_mutex\_register@@Base+0xfc0>

576c: e5942008 ldr r2, [r4, #8]

5770: e5940004 ldr r0, [r4, #4]

5774: e1a0100b mov r1, fp

5778: e0800008 add r0, r0, r8

577c: ebfff2c1 bl 2288 <memcpy@plt>

5780: e0865005 add r5, r6, r5

5784: e2877001 add r7, r7, #1

5788: e157000a cmp r7, sl

578c: 1affffcd bne 56c8 <mcrypt\_mutex\_register@@Base+0xf34>

5790: e59d3000 ldr r3, [sp]

5794: e0233796 mla r3, r6, r7, r3

5798: e58d3000 str r3, [sp]

579c: e59d3004 ldr r3, [sp, #4]

57a0: e3530000 cmp r3, #0

57a4: ca000019 bgt 5810 <mcrypt\_mutex\_register@@Base+0x107c>

57a8: e3a00000 mov r0, #0

57ac: e28dd00c add sp, sp, #12

57b0: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

57b4: e1a02006 mov r2, r6

57b8: e5941004 ldr r1, [r4, #4]

57bc: ebfff2b1 bl 2288 <memcpy@plt>

57c0: e5941000 ldr r1, [r4]

57c4: e59d0030 ldr r0, [sp, #48] ; 0x30

57c8: e12fff39 blx r9

57cc: e3560000 cmp r6, #0

57d0: e5941000 ldr r1, [r4]

57d4: c1a03005 movgt r3, r5

57d8: c2411001 subgt r1, r1, #1

57dc: c0865003 addgt r5, r6, r3

57e0: da000045 ble 58fc <mcrypt\_mutex\_register@@Base+0x1168>

57e4: e5f32001 ldrb r2, [r3, #1]!

57e8: e5f10001 ldrb r0, [r1, #1]!

57ec: e1530005 cmp r3, r5

57f0: e0222000 eor r2, r2, r0

57f4: e5c32000 strb r2, [r3]

57f8: 1afffff9 bne 57e4 <mcrypt\_mutex\_register@@Base+0x1050>

57fc: e1a0100b mov r1, fp

5800: e1a02006 mov r2, r6

5804: e5940004 ldr r0, [r4, #4]

5808: ebfff29e bl 2288 <memcpy@plt>

580c: eaffffdc b 5784 <mcrypt\_mutex\_register@@Base+0xff0>

5810: e1560003 cmp r6, r3

5814: e5940000 ldr r0, [r4]

5818: e5943008 ldr r3, [r4, #8]

581c: 0a000055 beq 5978 <mcrypt\_mutex\_register@@Base+0x11e4>

5820: e3530000 cmp r3, #0

5824: 0a00003a beq 5914 <mcrypt\_mutex\_register@@Base+0x1180>

5828: e59d2004 ldr r2, [sp, #4]

582c: e0467003 sub r7, r6, r3

5830: e1570002 cmp r7, r2

5834: a1a07002 movge r7, r2

5838: e3570000 cmp r7, #0

583c: da000092 ble 5a8c <mcrypt\_mutex\_register@@Base+0x12f8>

5840: e2433001 sub r3, r3, #1

5844: e0800003 add r0, r0, r3

5848: e59d3000 ldr r3, [sp]

584c: e2475001 sub r5, r7, #1

5850: e083c005 add ip, r3, r5

5854: e2433001 sub r3, r3, #1

5858: e5f32001 ldrb r2, [r3, #1]!

585c: e5f01001 ldrb r1, [r0, #1]!

5860: e15c0003 cmp ip, r3

5864: e0222001 eor r2, r2, r1

5868: e5c32000 strb r2, [r3]

586c: 1afffff9 bne 5858 <mcrypt\_mutex\_register@@Base+0x10c4>

5870: e9940009 ldmib r4, {r0, r3}

5874: e1a02007 mov r2, r7

5878: e59d1000 ldr r1, [sp]

587c: e0800003 add r0, r0, r3

5880: ebfff280 bl 2288 <memcpy@plt>

5884: e5943008 ldr r3, [r4, #8]

5888: e59d2004 ldr r2, [sp, #4]

588c: e0833007 add r3, r3, r7

5890: e1520007 cmp r2, r7

5894: e5843008 str r3, [r4, #8]

5898: daffffc2 ble 57a8 <mcrypt\_mutex\_register@@Base+0x1014>

589c: e1a02006 mov r2, r6

58a0: e1c400d0 ldrd r0, [r4]

58a4: ebfff277 bl 2288 <memcpy@plt>

58a8: e5941000 ldr r1, [r4]

58ac: e59d0030 ldr r0, [sp, #48] ; 0x30

58b0: e12fff39 blx r9

58b4: e59d3004 ldr r3, [sp, #4]

58b8: e5941004 ldr r1, [r4, #4]

58bc: e0432007 sub r2, r3, r7

58c0: e59d3000 ldr r3, [sp]

58c4: e2411001 sub r1, r1, #1

58c8: e0835005 add r5, r3, r5

58cc: e081c002 add ip, r1, r2

58d0: e5f10001 ldrb r0, [r1, #1]!

58d4: e5f53001 ldrb r3, [r5, #1]!

58d8: e151000c cmp r1, ip

58dc: e0233000 eor r3, r3, r0

58e0: e5c53000 strb r3, [r5]

58e4: 1afffff9 bne 58d0 <mcrypt\_mutex\_register@@Base+0x113c>

58e8: e5842008 str r2, [r4, #8]

58ec: e59d1000 ldr r1, [sp]

58f0: e5940004 ldr r0, [r4, #4]

58f4: ebfff263 bl 2288 <memcpy@plt>

58f8: eaffffaa b 57a8 <mcrypt\_mutex\_register@@Base+0x1014>

58fc: e1a0100b mov r1, fp

5900: e1a02006 mov r2, r6

5904: e5940004 ldr r0, [r4, #4]

5908: e0865005 add r5, r6, r5

590c: ebfff25d bl 2288 <memcpy@plt>

5910: eaffff9b b 5784 <mcrypt\_mutex\_register@@Base+0xff0>

5914: e1a02006 mov r2, r6

5918: e5941004 ldr r1, [r4, #4]

591c: ebfff259 bl 2288 <memcpy@plt>

5920: e5941000 ldr r1, [r4]

5924: e59d0030 ldr r0, [sp, #48] ; 0x30

5928: e12fff39 blx r9

592c: e59d3004 ldr r3, [sp, #4]

5930: e5941000 ldr r1, [r4]

5934: e243c001 sub ip, r3, #1

5938: e59d3000 ldr r3, [sp]

593c: e2411001 sub r1, r1, #1

5940: e083c00c add ip, r3, ip

5944: e2433001 sub r3, r3, #1

5948: e5f32001 ldrb r2, [r3, #1]!

594c: e5f10001 ldrb r0, [r1, #1]!

5950: e15c0003 cmp ip, r3

5954: e0222000 eor r2, r2, r0

5958: e5c32000 strb r2, [r3]

595c: 1afffff9 bne 5948 <mcrypt\_mutex\_register@@Base+0x11b4>

5960: e89d0022 ldm sp, {r1, r5}

5964: e5940004 ldr r0, [r4, #4]

5968: e1a02005 mov r2, r5

596c: ebfff245 bl 2288 <memcpy@plt>

5970: e5845008 str r5, [r4, #8]

5974: eaffff8b b 57a8 <mcrypt\_mutex\_register@@Base+0x1014>

5978: e3530000 cmp r3, #0

597c: 0a00002b beq 5a30 <mcrypt\_mutex\_register@@Base+0x129c>

5980: e0465003 sub r5, r6, r3

5984: e3550000 cmp r5, #0

5988: da00000c ble 59c0 <mcrypt\_mutex\_register@@Base+0x122c>

598c: e2433001 sub r3, r3, #1

5990: e0800003 add r0, r0, r3

5994: e59d3000 ldr r3, [sp]

5998: e245c001 sub ip, r5, #1

599c: e083c00c add ip, r3, ip

59a0: e2433001 sub r3, r3, #1

59a4: e5f32001 ldrb r2, [r3, #1]!

59a8: e5f01001 ldrb r1, [r0, #1]!

59ac: e15c0003 cmp ip, r3

59b0: e0222001 eor r2, r2, r1

59b4: e5c32000 strb r2, [r3]

59b8: 1afffff9 bne 59a4 <mcrypt\_mutex\_register@@Base+0x1210>

59bc: e5940000 ldr r0, [r4]

59c0: e1a02006 mov r2, r6

59c4: e5941004 ldr r1, [r4, #4]

59c8: ebfff22e bl 2288 <memcpy@plt>

59cc: e5941000 ldr r1, [r4]

59d0: e59d0030 ldr r0, [sp, #48] ; 0x30

59d4: e12fff39 blx r9

59d8: e5942008 ldr r2, [r4, #8]

59dc: e5941000 ldr r1, [r4]

59e0: e3520000 cmp r2, #0

59e4: da00000c ble 5a1c <mcrypt\_mutex\_register@@Base+0x1288>

59e8: e59d3000 ldr r3, [sp]

59ec: e081c002 add ip, r1, r2

59f0: e2452001 sub r2, r5, #1

59f4: e24cc001 sub ip, ip, #1

59f8: e2411001 sub r1, r1, #1

59fc: e0832002 add r2, r3, r2

5a00: e5f10001 ldrb r0, [r1, #1]!

5a04: e5f23001 ldrb r3, [r2, #1]!

5a08: e15c0001 cmp ip, r1

5a0c: e0233000 eor r3, r3, r0

5a10: e5c23000 strb r3, [r2]

5a14: 1afffff9 bne 5a00 <mcrypt\_mutex\_register@@Base+0x126c>

5a18: e5942008 ldr r2, [r4, #8]

5a1c: e5940004 ldr r0, [r4, #4]

5a20: e59d1000 ldr r1, [sp]

5a24: e0800005 add r0, r0, r5

5a28: ebfff216 bl 2288 <memcpy@plt>

5a2c: eaffff5d b 57a8 <mcrypt\_mutex\_register@@Base+0x1014>

5a30: e1a02006 mov r2, r6

5a34: e5941004 ldr r1, [r4, #4]

5a38: ebfff212 bl 2288 <memcpy@plt>

5a3c: e5941000 ldr r1, [r4]

5a40: e59d0030 ldr r0, [sp, #48] ; 0x30

5a44: e12fff39 blx r9

5a48: e59d3000 ldr r3, [sp]

5a4c: e5941000 ldr r1, [r4]

5a50: e246c001 sub ip, r6, #1

5a54: e083c00c add ip, r3, ip

5a58: e2411001 sub r1, r1, #1

5a5c: e2433001 sub r3, r3, #1

5a60: e5f32001 ldrb r2, [r3, #1]!

5a64: e5f10001 ldrb r0, [r1, #1]!

5a68: e15c0003 cmp ip, r3

5a6c: e0222000 eor r2, r2, r0

5a70: e5c32000 strb r2, [r3]

5a74: 1afffff9 bne 5a60 <mcrypt\_mutex\_register@@Base+0x12cc>

5a78: e1a02006 mov r2, r6

5a7c: e59d1000 ldr r1, [sp]

5a80: e5940004 ldr r0, [r4, #4]

5a84: ebfff1ff bl 2288 <memcpy@plt>

5a88: eaffff46 b 57a8 <mcrypt\_mutex\_register@@Base+0x1014>

5a8c: e5940004 ldr r0, [r4, #4]

5a90: e1a02007 mov r2, r7

5a94: e0800003 add r0, r0, r3

5a98: e59d1000 ldr r1, [sp]

5a9c: ebfff1f9 bl 2288 <memcpy@plt>

5aa0: e5943008 ldr r3, [r4, #8]

5aa4: e2475001 sub r5, r7, #1

5aa8: e0833007 add r3, r3, r7

5aac: e5843008 str r3, [r4, #8]

5ab0: eaffff79 b 589c <mcrypt\_mutex\_register@@Base+0x1108>

5ab4: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

5ab8: e24dd00c sub sp, sp, #12

5abc: e1a04000 mov r4, r0

5ac0: e58d1000 str r1, [sp]

5ac4: e1a05001 mov r5, r1

5ac8: e1a00002 mov r0, r2

5acc: e1a01003 mov r1, r3

5ad0: e1a07003 mov r7, r3

5ad4: eb005891 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

5ad8: e59da034 ldr sl, [sp, #52] ; 0x34

5adc: e250b000 subs fp, r0, #0

5ae0: e58d1004 str r1, [sp, #4]

5ae4: da000035 ble 5bc0 <mcrypt\_mutex\_register@@Base+0x142c>

5ae8: e2456001 sub r6, r5, #1

5aec: e3a09000 mov r9, #0

5af0: e5943008 ldr r3, [r4, #8]

5af4: e2865001 add r5, r6, #1

5af8: e3530000 cmp r3, #0

5afc: e5940000 ldr r0, [r4]

5b00: 0a000034 beq 5bd8 <mcrypt\_mutex\_register@@Base+0x1444>

5b04: e0478003 sub r8, r7, r3

5b08: e3580000 cmp r8, #0

5b0c: da00000b ble 5b40 <mcrypt\_mutex\_register@@Base+0x13ac>

5b10: e2433001 sub r3, r3, #1

5b14: e2481001 sub r1, r8, #1

5b18: e0800003 add r0, r0, r3

5b1c: e0811005 add r1, r1, r5

5b20: e1a03006 mov r3, r6

5b24: e5f32001 ldrb r2, [r3, #1]!

5b28: e5f0c001 ldrb ip, [r0, #1]!

5b2c: e1530001 cmp r3, r1

5b30: e022200c eor r2, r2, ip

5b34: e5c32000 strb r2, [r3]

5b38: 1afffff9 bne 5b24 <mcrypt\_mutex\_register@@Base+0x1390>

5b3c: e5940000 ldr r0, [r4]

5b40: e1a02007 mov r2, r7

5b44: e5941004 ldr r1, [r4, #4]

5b48: ebfff1ce bl 2288 <memcpy@plt>

5b4c: e5941000 ldr r1, [r4]

5b50: e59d0030 ldr r0, [sp, #48] ; 0x30

5b54: e12fff3a blx sl

5b58: e9940005 ldmib r4, {r0, r2}

5b5c: e1a01005 mov r1, r5

5b60: e0800008 add r0, r0, r8

5b64: ebfff1c7 bl 2288 <memcpy@plt>

5b68: e5942008 ldr r2, [r4, #8]

5b6c: e5943000 ldr r3, [r4]

5b70: e3520000 cmp r2, #0

5b74: da00000a ble 5ba4 <mcrypt\_mutex\_register@@Base+0x1410>

5b78: e0832002 add r2, r3, r2

5b7c: e2481001 sub r1, r8, #1

5b80: e0851001 add r1, r5, r1

5b84: e2422001 sub r2, r2, #1

5b88: e2433001 sub r3, r3, #1

5b8c: e5f3c001 ldrb ip, [r3, #1]!

5b90: e5f10001 ldrb r0, [r1, #1]!

5b94: e1520003 cmp r2, r3

5b98: e020000c eor r0, r0, ip

5b9c: e5c10000 strb r0, [r1]

5ba0: 1afffff9 bne 5b8c <mcrypt\_mutex\_register@@Base+0x13f8>

5ba4: e0866007 add r6, r6, r7

5ba8: e2899001 add r9, r9, #1

5bac: e159000b cmp r9, fp

5bb0: 1affffce bne 5af0 <mcrypt\_mutex\_register@@Base+0x135c>

5bb4: e59d3000 ldr r3, [sp]

5bb8: e0233997 mla r3, r7, r9, r3

5bbc: e58d3000 str r3, [sp]

5bc0: e59d3004 ldr r3, [sp, #4]

5bc4: e3530000 cmp r3, #0

5bc8: ca000019 bgt 5c34 <mcrypt\_mutex\_register@@Base+0x14a0>

5bcc: e3a00000 mov r0, #0

5bd0: e28dd00c add sp, sp, #12

5bd4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

5bd8: e1a02007 mov r2, r7

5bdc: e5941004 ldr r1, [r4, #4]

5be0: ebfff1a8 bl 2288 <memcpy@plt>

5be4: e5941000 ldr r1, [r4]

5be8: e59d0030 ldr r0, [sp, #48] ; 0x30

5bec: e12fff3a blx sl

5bf0: e1a01005 mov r1, r5

5bf4: e1a02007 mov r2, r7

5bf8: e5940004 ldr r0, [r4, #4]

5bfc: ebfff1a1 bl 2288 <memcpy@plt>

5c00: e3570000 cmp r7, #0

5c04: e5941000 ldr r1, [r4]

5c08: c1a03006 movgt r3, r6

5c0c: c2411001 subgt r1, r1, #1

5c10: c0836007 addgt r6, r3, r7

5c14: daffffe2 ble 5ba4 <mcrypt\_mutex\_register@@Base+0x1410>

5c18: e5f32001 ldrb r2, [r3, #1]!

5c1c: e5f10001 ldrb r0, [r1, #1]!

5c20: e1530006 cmp r3, r6

5c24: e0222000 eor r2, r2, r0

5c28: e5c32000 strb r2, [r3]

5c2c: 1afffff9 bne 5c18 <mcrypt\_mutex\_register@@Base+0x1484>

5c30: eaffffdc b 5ba8 <mcrypt\_mutex\_register@@Base+0x1414>

5c34: e1570003 cmp r7, r3

5c38: e5940000 ldr r0, [r4]

5c3c: e5943008 ldr r3, [r4, #8]

5c40: 0a000051 beq 5d8c <mcrypt\_mutex\_register@@Base+0x15f8>

5c44: e3530000 cmp r3, #0

5c48: 0a000035 beq 5d24 <mcrypt\_mutex\_register@@Base+0x1590>

5c4c: e59d2004 ldr r2, [sp, #4]

5c50: e0476003 sub r6, r7, r3

5c54: e1560002 cmp r6, r2

5c58: a1a06002 movge r6, r2

5c5c: e3560000 cmp r6, #0

5c60: da00008d ble 5e9c <mcrypt\_mutex\_register@@Base+0x1708>

5c64: e2433001 sub r3, r3, #1

5c68: e0800003 add r0, r0, r3

5c6c: e59d3000 ldr r3, [sp]

5c70: e2465001 sub r5, r6, #1

5c74: e083c005 add ip, r3, r5

5c78: e2433001 sub r3, r3, #1

5c7c: e5f32001 ldrb r2, [r3, #1]!

5c80: e5f01001 ldrb r1, [r0, #1]!

5c84: e15c0003 cmp ip, r3

5c88: e0222001 eor r2, r2, r1

5c8c: e5c32000 strb r2, [r3]

5c90: 1afffff9 bne 5c7c <mcrypt\_mutex\_register@@Base+0x14e8>

5c94: e9940009 ldmib r4, {r0, r3}

5c98: e1a02006 mov r2, r6

5c9c: e59d1000 ldr r1, [sp]

5ca0: e0800003 add r0, r0, r3

5ca4: ebfff177 bl 2288 <memcpy@plt>

5ca8: e5943008 ldr r3, [r4, #8]

5cac: e59d2004 ldr r2, [sp, #4]

5cb0: e0833006 add r3, r3, r6

5cb4: e1520006 cmp r2, r6

5cb8: e5843008 str r3, [r4, #8]

5cbc: daffffc2 ble 5bcc <mcrypt\_mutex\_register@@Base+0x1438>

5cc0: e59d3004 ldr r3, [sp, #4]

5cc4: e1a02007 mov r2, r7

5cc8: e1c400d0 ldrd r0, [r4]

5ccc: e0436006 sub r6, r3, r6

5cd0: ebfff16c bl 2288 <memcpy@plt>

5cd4: e5941000 ldr r1, [r4]

5cd8: e59d0030 ldr r0, [sp, #48] ; 0x30

5cdc: e12fff3a blx sl

5ce0: e59d7000 ldr r7, [sp]

5ce4: e1a02006 mov r2, r6

5ce8: e1a01007 mov r1, r7

5cec: e5940004 ldr r0, [r4, #4]

5cf0: ebfff164 bl 2288 <memcpy@plt>

5cf4: e5942004 ldr r2, [r4, #4]

5cf8: e0875005 add r5, r7, r5

5cfc: e2422001 sub r2, r2, #1

5d00: e0820006 add r0, r2, r6

5d04: e5f21001 ldrb r1, [r2, #1]!

5d08: e5f53001 ldrb r3, [r5, #1]!

5d0c: e1520000 cmp r2, r0

5d10: e0233001 eor r3, r3, r1

5d14: e5c53000 strb r3, [r5]

5d18: 1afffff9 bne 5d04 <mcrypt\_mutex\_register@@Base+0x1570>

5d1c: e5846008 str r6, [r4, #8]

5d20: eaffffa9 b 5bcc <mcrypt\_mutex\_register@@Base+0x1438>

5d24: e1a02007 mov r2, r7

5d28: e5941004 ldr r1, [r4, #4]

5d2c: ebfff155 bl 2288 <memcpy@plt>

5d30: e5941000 ldr r1, [r4]

5d34: e59d0030 ldr r0, [sp, #48] ; 0x30

5d38: e12fff3a blx sl

5d3c: e59d5000 ldr r5, [sp]

5d40: e59d6004 ldr r6, [sp, #4]

5d44: e1a01005 mov r1, r5

5d48: e1a02006 mov r2, r6

5d4c: e5940004 ldr r0, [r4, #4]

5d50: ebfff14c bl 2288 <memcpy@plt>

5d54: e5942000 ldr r2, [r4]

5d58: e2460001 sub r0, r6, #1

5d5c: e2422001 sub r2, r2, #1

5d60: e0850000 add r0, r5, r0

5d64: e2459001 sub r9, r5, #1

5d68: e5f93001 ldrb r3, [r9, #1]!

5d6c: e5f21001 ldrb r1, [r2, #1]!

5d70: e1500009 cmp r0, r9

5d74: e0233001 eor r3, r3, r1

5d78: e5c93000 strb r3, [r9]

5d7c: 1afffff9 bne 5d68 <mcrypt\_mutex\_register@@Base+0x15d4>

5d80: e59d3004 ldr r3, [sp, #4]

5d84: e5843008 str r3, [r4, #8]

5d88: eaffff8f b 5bcc <mcrypt\_mutex\_register@@Base+0x1438>

5d8c: e3530000 cmp r3, #0

5d90: 0a00002a beq 5e40 <mcrypt\_mutex\_register@@Base+0x16ac>

5d94: e0475003 sub r5, r7, r3

5d98: e3550000 cmp r5, #0

5d9c: da00000c ble 5dd4 <mcrypt\_mutex\_register@@Base+0x1640>

5da0: e2433001 sub r3, r3, #1

5da4: e0800003 add r0, r0, r3

5da8: e59d3000 ldr r3, [sp]

5dac: e245c001 sub ip, r5, #1

5db0: e083c00c add ip, r3, ip

5db4: e2433001 sub r3, r3, #1

5db8: e5f32001 ldrb r2, [r3, #1]!

5dbc: e5f01001 ldrb r1, [r0, #1]!

5dc0: e15c0003 cmp ip, r3

5dc4: e0222001 eor r2, r2, r1

5dc8: e5c32000 strb r2, [r3]

5dcc: 1afffff9 bne 5db8 <mcrypt\_mutex\_register@@Base+0x1624>

5dd0: e5940000 ldr r0, [r4]

5dd4: e1a02007 mov r2, r7

5dd8: e5941004 ldr r1, [r4, #4]

5ddc: ebfff129 bl 2288 <memcpy@plt>

5de0: e5941000 ldr r1, [r4]

5de4: e59d0030 ldr r0, [sp, #48] ; 0x30

5de8: e12fff3a blx sl

5dec: e9940005 ldmib r4, {r0, r2}

5df0: e59d6000 ldr r6, [sp]

5df4: e0800005 add r0, r0, r5

5df8: e1a01006 mov r1, r6

5dfc: ebfff121 bl 2288 <memcpy@plt>

5e00: e5943008 ldr r3, [r4, #8]

5e04: e5942000 ldr r2, [r4]

5e08: e3530000 cmp r3, #0

5e0c: daffff6e ble 5bcc <mcrypt\_mutex\_register@@Base+0x1438>

5e10: e0823003 add r3, r2, r3

5e14: e2455001 sub r5, r5, #1

5e18: e0869005 add r9, r6, r5

5e1c: e2430001 sub r0, r3, #1

5e20: e2422001 sub r2, r2, #1

5e24: e5f21001 ldrb r1, [r2, #1]!

5e28: e5f93001 ldrb r3, [r9, #1]!

5e2c: e1500002 cmp r0, r2

5e30: e0233001 eor r3, r3, r1

5e34: e5c93000 strb r3, [r9]

5e38: 1afffff9 bne 5e24 <mcrypt\_mutex\_register@@Base+0x1690>

5e3c: eaffff62 b 5bcc <mcrypt\_mutex\_register@@Base+0x1438>

5e40: e1a02007 mov r2, r7

5e44: e5941004 ldr r1, [r4, #4]

5e48: ebfff10e bl 2288 <memcpy@plt>

5e4c: e5941000 ldr r1, [r4]

5e50: e59d0030 ldr r0, [sp, #48] ; 0x30

5e54: e12fff3a blx sl

5e58: e59d5000 ldr r5, [sp]

5e5c: e1a02007 mov r2, r7

5e60: e1a01005 mov r1, r5

5e64: e5940004 ldr r0, [r4, #4]

5e68: ebfff106 bl 2288 <memcpy@plt>

5e6c: e5942000 ldr r2, [r4]

5e70: e2470001 sub r0, r7, #1

5e74: e2422001 sub r2, r2, #1

5e78: e0850000 add r0, r5, r0

5e7c: e2459001 sub r9, r5, #1

5e80: e5f93001 ldrb r3, [r9, #1]!

5e84: e5f21001 ldrb r1, [r2, #1]!

5e88: e1500009 cmp r0, r9

5e8c: e0233001 eor r3, r3, r1

5e90: e5c93000 strb r3, [r9]

5e94: 1afffff9 bne 5e80 <mcrypt\_mutex\_register@@Base+0x16ec>

5e98: eaffff4b b 5bcc <mcrypt\_mutex\_register@@Base+0x1438>

5e9c: e5940004 ldr r0, [r4, #4]

5ea0: e1a02006 mov r2, r6

5ea4: e0800003 add r0, r0, r3

5ea8: e59d1000 ldr r1, [sp]

5eac: ebfff0f5 bl 2288 <memcpy@plt>

5eb0: e5943008 ldr r3, [r4, #8]

5eb4: e2465001 sub r5, r6, #1

5eb8: e0833006 add r3, r3, r6

5ebc: e5843008 str r3, [r4, #8]

5ec0: eaffff7e b 5cc0 <mcrypt\_mutex\_register@@Base+0x152c>

5ec4: e3a00001 mov r0, #1

5ec8: e12fff1e bx lr

5ecc: e3a00000 mov r0, #0

5ed0: e12fff1e bx lr

5ed4: e3a00001 mov r0, #1

5ed8: e12fff1e bx lr

5edc: e59f0004 ldr r0, [pc, #4] ; 5ee8 <mcrypt\_mutex\_register@@Base+0x1754>

5ee0: e08f0000 add r0, pc, r0

5ee4: e12fff1e bx lr

5ee8: 0001874c andeq r8, r1, ip, asr #14

5eec: e3a00010 mov r0, #16

5ef0: e12fff1e bx lr

5ef4: e59f0000 ldr r0, [pc] ; 5efc <mcrypt\_mutex\_register@@Base+0x1768>

5ef8: e12fff1e bx lr

5efc: 01317c53 teqeq r1, r3, asr ip

5f00: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

5f04: e3a05000 mov r5, #0

5f08: e59d6020 ldr r6, [sp, #32]

5f0c: e1a04000 mov r4, r0

5f10: e5805004 str r5, [r0, #4]

5f14: e5805008 str r5, [r0, #8]

5f18: e580600c str r6, [r0, #12]

5f1c: e3a01001 mov r1, #1

5f20: e1a00006 mov r0, r6

5f24: e1a08003 mov r8, r3

5f28: ebfff0c1 bl 2234 <calloc@plt>

5f2c: e1500005 cmp r0, r5

5f30: e1a07000 mov r7, r0

5f34: e5840000 str r0, [r4]

5f38: 0a000014 beq 5f90 <mcrypt\_mutex\_register@@Base+0x17fc>

5f3c: e3a01001 mov r1, #1

5f40: e1a00006 mov r0, r6

5f44: ebfff0ba bl 2234 <calloc@plt>

5f48: e1500005 cmp r0, r5

5f4c: e1a09000 mov r9, r0

5f50: e5840004 str r0, [r4, #4]

5f54: 0a00000d beq 5f90 <mcrypt\_mutex\_register@@Base+0x17fc>

5f58: e1580005 cmp r8, r5

5f5c: 0a000009 beq 5f88 <mcrypt\_mutex\_register@@Base+0x17f4>

5f60: e1a02006 mov r2, r6

5f64: e1a01008 mov r1, r8

5f68: e1a00007 mov r0, r7

5f6c: ebfff0c5 bl 2288 <memcpy@plt>

5f70: e1a02006 mov r2, r6

5f74: e1a01008 mov r1, r8

5f78: e1a00009 mov r0, r9

5f7c: ebfff0c1 bl 2288 <memcpy@plt>

5f80: e1a00005 mov r0, r5

5f84: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

5f88: e1a00008 mov r0, r8

5f8c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

5f90: e1a00007 mov r0, r7

5f94: ebfff0b8 bl 227c <free@plt>

5f98: e3e00000 mvn r0, #0

5f9c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

5fa0: e92d4070 push {r4, r5, r6, lr}

5fa4: e1a04001 mov r4, r1

5fa8: e1a05000 mov r5, r0

5fac: e4d43001 ldrb r3, [r4], #1

5fb0: e2426001 sub r6, r2, #1

5fb4: e1a02006 mov r2, r6

5fb8: e5803008 str r3, [r0, #8]

5fbc: e1a01004 mov r1, r4

5fc0: e5900000 ldr r0, [r0]

5fc4: ebfff0af bl 2288 <memcpy@plt>

5fc8: e1a02006 mov r2, r6

5fcc: e1a01004 mov r1, r4

5fd0: e5950004 ldr r0, [r5, #4]

5fd4: ebfff0ab bl 2288 <memcpy@plt>

5fd8: e3a00000 mov r0, #0

5fdc: e8bd8070 pop {r4, r5, r6, pc}

5fe0: e590300c ldr r3, [r0, #12]

5fe4: e592c000 ldr ip, [r2]

5fe8: e153000c cmp r3, ip

5fec: e283c001 add ip, r3, #1

5ff0: aa00000a bge 6020 <mcrypt\_mutex\_register@@Base+0x188c>

5ff4: e1a03000 mov r3, r0

5ff8: e92d4010 push {r4, lr}

5ffc: e1a00001 mov r0, r1

6000: e582c000 str ip, [r2]

6004: e5932008 ldr r2, [r3, #8]

6008: e4c02001 strb r2, [r0], #1

600c: e593200c ldr r2, [r3, #12]

6010: e5931004 ldr r1, [r3, #4]

6014: ebfff09b bl 2288 <memcpy@plt>

6018: e3a00000 mov r0, #0

601c: e8bd8010 pop {r4, pc}

6020: e582c000 str ip, [r2]

6024: e3e00000 mvn r0, #0

6028: e12fff1e bx lr

602c: e92d4010 push {r4, lr}

6030: e1a04000 mov r4, r0

6034: e5900004 ldr r0, [r0, #4]

6038: ebfff08f bl 227c <free@plt>

603c: e5940000 ldr r0, [r4]

6040: e8bd4010 pop {r4, lr}

6044: eafff08c b 227c <free@plt>

6048: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

604c: e24dd00c sub sp, sp, #12

6050: e1a04000 mov r4, r0

6054: e58d1000 str r1, [sp]

6058: e1a05001 mov r5, r1

605c: e1a00002 mov r0, r2

6060: e1a01003 mov r1, r3

6064: e1a07003 mov r7, r3

6068: eb00572c bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

606c: e59d9034 ldr r9, [sp, #52] ; 0x34

6070: e250a000 subs sl, r0, #0

6074: e58d1004 str r1, [sp, #4]

6078: da000035 ble 6154 <mcrypt\_mutex\_register@@Base+0x19c0>

607c: e2456001 sub r6, r5, #1

6080: e3a08000 mov r8, #0

6084: e5943008 ldr r3, [r4, #8]

6088: e2865001 add r5, r6, #1

608c: e3530000 cmp r3, #0

6090: e5940000 ldr r0, [r4]

6094: 0a000034 beq 616c <mcrypt\_mutex\_register@@Base+0x19d8>

6098: e047b003 sub fp, r7, r3

609c: e35b0000 cmp fp, #0

60a0: da00000b ble 60d4 <mcrypt\_mutex\_register@@Base+0x1940>

60a4: e2433001 sub r3, r3, #1

60a8: e24b1001 sub r1, fp, #1

60ac: e0800003 add r0, r0, r3

60b0: e0811005 add r1, r1, r5

60b4: e1a03006 mov r3, r6

60b8: e5f32001 ldrb r2, [r3, #1]!

60bc: e5f0c001 ldrb ip, [r0, #1]!

60c0: e1530001 cmp r3, r1

60c4: e022200c eor r2, r2, ip

60c8: e5c32000 strb r2, [r3]

60cc: 1afffff9 bne 60b8 <mcrypt\_mutex\_register@@Base+0x1924>

60d0: e5940000 ldr r0, [r4]

60d4: e1a02007 mov r2, r7

60d8: e5941004 ldr r1, [r4, #4]

60dc: ebfff069 bl 2288 <memcpy@plt>

60e0: e5941000 ldr r1, [r4]

60e4: e59d0030 ldr r0, [sp, #48] ; 0x30

60e8: e12fff39 blx r9

60ec: e1a02007 mov r2, r7

60f0: e5941000 ldr r1, [r4]

60f4: e5940004 ldr r0, [r4, #4]

60f8: ebfff062 bl 2288 <memcpy@plt>

60fc: e5941008 ldr r1, [r4, #8]

6100: e5942000 ldr r2, [r4]

6104: e3510000 cmp r1, #0

6108: da00000a ble 6138 <mcrypt\_mutex\_register@@Base+0x19a4>

610c: e0821001 add r1, r2, r1

6110: e24b3001 sub r3, fp, #1

6114: e0853003 add r3, r5, r3

6118: e2411001 sub r1, r1, #1

611c: e2422001 sub r2, r2, #1

6120: e5f2c001 ldrb ip, [r2, #1]!

6124: e5f30001 ldrb r0, [r3, #1]!

6128: e1520001 cmp r2, r1

612c: e020000c eor r0, r0, ip

6130: e5c30000 strb r0, [r3]

6134: 1afffff9 bne 6120 <mcrypt\_mutex\_register@@Base+0x198c>

6138: e0866007 add r6, r6, r7

613c: e2888001 add r8, r8, #1

6140: e158000a cmp r8, sl

6144: 1affffce bne 6084 <mcrypt\_mutex\_register@@Base+0x18f0>

6148: e59d3000 ldr r3, [sp]

614c: e0233897 mla r3, r7, r8, r3

6150: e58d3000 str r3, [sp]

6154: e59d3004 ldr r3, [sp, #4]

6158: e3530000 cmp r3, #0

615c: ca000019 bgt 61c8 <mcrypt\_mutex\_register@@Base+0x1a34>

6160: e3a00000 mov r0, #0

6164: e28dd00c add sp, sp, #12

6168: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

616c: e1a02007 mov r2, r7

6170: e5941004 ldr r1, [r4, #4]

6174: ebfff043 bl 2288 <memcpy@plt>

6178: e5941000 ldr r1, [r4]

617c: e59d0030 ldr r0, [sp, #48] ; 0x30

6180: e12fff39 blx r9

6184: e5941000 ldr r1, [r4]

6188: e1a02007 mov r2, r7

618c: e5940004 ldr r0, [r4, #4]

6190: ebfff03c bl 2288 <memcpy@plt>

6194: e3570000 cmp r7, #0

6198: e5941000 ldr r1, [r4]

619c: c1a03006 movgt r3, r6

61a0: c2411001 subgt r1, r1, #1

61a4: c0836007 addgt r6, r3, r7

61a8: daffffe2 ble 6138 <mcrypt\_mutex\_register@@Base+0x19a4>

61ac: e5f32001 ldrb r2, [r3, #1]!

61b0: e5f10001 ldrb r0, [r1, #1]!

61b4: e1530006 cmp r3, r6

61b8: e0222000 eor r2, r2, r0

61bc: e5c32000 strb r2, [r3]

61c0: 1afffff9 bne 61ac <mcrypt\_mutex\_register@@Base+0x1a18>

61c4: eaffffdc b 613c <mcrypt\_mutex\_register@@Base+0x19a8>

61c8: e5940000 ldr r0, [r4]

61cc: e1570003 cmp r7, r3

61d0: e1a02000 mov r2, r0

61d4: e5943008 ldr r3, [r4, #8]

61d8: 0a00004d beq 6314 <mcrypt\_mutex\_register@@Base+0x1b80>

61dc: e3530000 cmp r3, #0

61e0: 0a000031 beq 62ac <mcrypt\_mutex\_register@@Base+0x1b18>

61e4: e59d2004 ldr r2, [sp, #4]

61e8: e0475003 sub r5, r7, r3

61ec: e1550002 cmp r5, r2

61f0: a1a05002 movge r5, r2

61f4: e3550000 cmp r5, #0

61f8: da00008a ble 6428 <mcrypt\_mutex\_register@@Base+0x1c94>

61fc: e2433001 sub r3, r3, #1

6200: e0800003 add r0, r0, r3

6204: e59d3000 ldr r3, [sp]

6208: e2456001 sub r6, r5, #1

620c: e083c006 add ip, r3, r6

6210: e2433001 sub r3, r3, #1

6214: e5f32001 ldrb r2, [r3, #1]!

6218: e5f01001 ldrb r1, [r0, #1]!

621c: e15c0003 cmp ip, r3

6220: e0222001 eor r2, r2, r1

6224: e5c32000 strb r2, [r3]

6228: 1afffff9 bne 6214 <mcrypt\_mutex\_register@@Base+0x1a80>

622c: e5943008 ldr r3, [r4, #8]

6230: e59d2004 ldr r2, [sp, #4]

6234: e0833005 add r3, r3, r5

6238: e1520005 cmp r2, r5

623c: e5843008 str r3, [r4, #8]

6240: daffffc6 ble 6160 <mcrypt\_mutex\_register@@Base+0x19cc>

6244: e5940000 ldr r0, [r4]

6248: e1a02007 mov r2, r7

624c: e5941004 ldr r1, [r4, #4]

6250: ebfff00c bl 2288 <memcpy@plt>

6254: e5941000 ldr r1, [r4]

6258: e59d0030 ldr r0, [sp, #48] ; 0x30

625c: e12fff39 blx r9

6260: e1a02007 mov r2, r7

6264: e5941000 ldr r1, [r4]

6268: e5940004 ldr r0, [r4, #4]

626c: ebfff005 bl 2288 <memcpy@plt>

6270: e59d3004 ldr r3, [sp, #4]

6274: e5942004 ldr r2, [r4, #4]

6278: e0435005 sub r5, r3, r5

627c: e59d3000 ldr r3, [sp]

6280: e2422001 sub r2, r2, #1

6284: e0836006 add r6, r3, r6

6288: e0820005 add r0, r2, r5

628c: e5f21001 ldrb r1, [r2, #1]!

6290: e5f63001 ldrb r3, [r6, #1]!

6294: e1500002 cmp r0, r2

6298: e0233001 eor r3, r3, r1

629c: e5c63000 strb r3, [r6]

62a0: 1afffff9 bne 628c <mcrypt\_mutex\_register@@Base+0x1af8>

62a4: e5845008 str r5, [r4, #8]

62a8: eaffffac b 6160 <mcrypt\_mutex\_register@@Base+0x19cc>

62ac: e1a02007 mov r2, r7

62b0: e5941004 ldr r1, [r4, #4]

62b4: ebffeff3 bl 2288 <memcpy@plt>

62b8: e5941000 ldr r1, [r4]

62bc: e59d0030 ldr r0, [sp, #48] ; 0x30

62c0: e12fff39 blx r9

62c4: e1a02007 mov r2, r7

62c8: e5941000 ldr r1, [r4]

62cc: e5940004 ldr r0, [r4, #4]

62d0: ebffefec bl 2288 <memcpy@plt>

62d4: e59d3004 ldr r3, [sp, #4]

62d8: e5942000 ldr r2, [r4]

62dc: e2430001 sub r0, r3, #1

62e0: e59d3000 ldr r3, [sp]

62e4: e2422001 sub r2, r2, #1

62e8: e0830000 add r0, r3, r0

62ec: e243b001 sub fp, r3, #1

62f0: e5fb3001 ldrb r3, [fp, #1]!

62f4: e5f21001 ldrb r1, [r2, #1]!

62f8: e150000b cmp r0, fp

62fc: e0233001 eor r3, r3, r1

6300: e5cb3000 strb r3, [fp]

6304: 1afffff9 bne 62f0 <mcrypt\_mutex\_register@@Base+0x1b5c>

6308: e59d3004 ldr r3, [sp, #4]

630c: e5843008 str r3, [r4, #8]

6310: eaffff92 b 6160 <mcrypt\_mutex\_register@@Base+0x19cc>

6314: e3530000 cmp r3, #0

6318: 0a00002b beq 63cc <mcrypt\_mutex\_register@@Base+0x1c38>

631c: e0475003 sub r5, r7, r3

6320: e3550000 cmp r5, #0

6324: da00000c ble 635c <mcrypt\_mutex\_register@@Base+0x1bc8>

6328: e59d2000 ldr r2, [sp]

632c: e2433001 sub r3, r3, #1

6330: e245c001 sub ip, r5, #1

6334: e082c00c add ip, r2, ip

6338: e0803003 add r3, r0, r3

633c: e2422001 sub r2, r2, #1

6340: e5f21001 ldrb r1, [r2, #1]!

6344: e5f30001 ldrb r0, [r3, #1]!

6348: e15c0002 cmp ip, r2

634c: e0211000 eor r1, r1, r0

6350: e5c21000 strb r1, [r2]

6354: 1afffff9 bne 6340 <mcrypt\_mutex\_register@@Base+0x1bac>

6358: e5942000 ldr r2, [r4]

635c: e1a00002 mov r0, r2

6360: e5941004 ldr r1, [r4, #4]

6364: e1a02007 mov r2, r7

6368: ebffefc6 bl 2288 <memcpy@plt>

636c: e5941000 ldr r1, [r4]

6370: e59d0030 ldr r0, [sp, #48] ; 0x30

6374: e12fff39 blx r9

6378: e1a02007 mov r2, r7

637c: e5941000 ldr r1, [r4]

6380: e5940004 ldr r0, [r4, #4]

6384: ebffefbf bl 2288 <memcpy@plt>

6388: e5943008 ldr r3, [r4, #8]

638c: e5942000 ldr r2, [r4]

6390: e3530000 cmp r3, #0

6394: daffff71 ble 6160 <mcrypt\_mutex\_register@@Base+0x19cc>

6398: e59d1000 ldr r1, [sp]

639c: e0823003 add r3, r2, r3

63a0: e2455001 sub r5, r5, #1

63a4: e081b005 add fp, r1, r5

63a8: e2430001 sub r0, r3, #1

63ac: e2422001 sub r2, r2, #1

63b0: e5f21001 ldrb r1, [r2, #1]!

63b4: e5fb3001 ldrb r3, [fp, #1]!

63b8: e1500002 cmp r0, r2

63bc: e0233001 eor r3, r3, r1

63c0: e5cb3000 strb r3, [fp]

63c4: 1afffff9 bne 63b0 <mcrypt\_mutex\_register@@Base+0x1c1c>

63c8: eaffff64 b 6160 <mcrypt\_mutex\_register@@Base+0x19cc>

63cc: e1a02007 mov r2, r7

63d0: e5941004 ldr r1, [r4, #4]

63d4: ebffefab bl 2288 <memcpy@plt>

63d8: e5941000 ldr r1, [r4]

63dc: e59d0030 ldr r0, [sp, #48] ; 0x30

63e0: e12fff39 blx r9

63e4: e1a02007 mov r2, r7

63e8: e5941000 ldr r1, [r4]

63ec: e5940004 ldr r0, [r4, #4]

63f0: ebffefa4 bl 2288 <memcpy@plt>

63f4: e59d3000 ldr r3, [sp]

63f8: e5942000 ldr r2, [r4]

63fc: e2470001 sub r0, r7, #1

6400: e2422001 sub r2, r2, #1

6404: e0830000 add r0, r3, r0

6408: e243b001 sub fp, r3, #1

640c: e5fb3001 ldrb r3, [fp, #1]!

6410: e5f21001 ldrb r1, [r2, #1]!

6414: e150000b cmp r0, fp

6418: e0233001 eor r3, r3, r1

641c: e5cb3000 strb r3, [fp]

6420: 1afffff9 bne 640c <mcrypt\_mutex\_register@@Base+0x1c78>

6424: eaffff4d b 6160 <mcrypt\_mutex\_register@@Base+0x19cc>

6428: e0853003 add r3, r5, r3

642c: e5843008 str r3, [r4, #8]

6430: e2456001 sub r6, r5, #1

6434: eaffff83 b 6248 <mcrypt\_mutex\_register@@Base+0x1ab4>

6438: eaffff02 b 6048 <mcrypt\_mutex\_register@@Base+0x18b4>

643c: e3a00001 mov r0, #1

6440: e12fff1e bx lr

6444: e3a00000 mov r0, #0

6448: e12fff1e bx lr

644c: e3a00001 mov r0, #1

6450: e12fff1e bx lr

6454: e59f0004 ldr r0, [pc, #4] ; 6460 <mcrypt\_mutex\_register@@Base+0x1ccc>

6458: e08f0000 add r0, pc, r0

645c: e12fff1e bx lr

6460: 000181dc ldrdeq r8, [r1], -ip

6464: e3a00010 mov r0, #16

6468: e12fff1e bx lr

646c: e59f0000 ldr r0, [pc] ; 6474 <mcrypt\_mutex\_register@@Base+0x1ce0>

6470: e12fff1e bx lr

6474: 01317c53 teqeq r1, r3, asr ip

6478: e92d41f0 push {r4, r5, r6, r7, r8, lr}

647c: e3a05000 mov r5, #0

6480: e59d6018 ldr r6, [sp, #24]

6484: e1a04000 mov r4, r0

6488: e3a01001 mov r1, #1

648c: e9800060 stmib r0, {r5, r6}

6490: e1a00006 mov r0, r6

6494: e1a08003 mov r8, r3

6498: ebffef65 bl 2234 <calloc@plt>

649c: e1500005 cmp r0, r5

64a0: e1a07000 mov r7, r0

64a4: e5840000 str r0, [r4]

64a8: 0a00000e beq 64e8 <mcrypt\_mutex\_register@@Base+0x1d54>

64ac: e1a00006 mov r0, r6

64b0: ebffef8f bl 22f4 <malloc@plt>

64b4: e1500005 cmp r0, r5

64b8: e5840004 str r0, [r4, #4]

64bc: 0a000009 beq 64e8 <mcrypt\_mutex\_register@@Base+0x1d54>

64c0: e1580005 cmp r8, r5

64c4: 0a000005 beq 64e0 <mcrypt\_mutex\_register@@Base+0x1d4c>

64c8: e1a02006 mov r2, r6

64cc: e1a01008 mov r1, r8

64d0: e1a00007 mov r0, r7

64d4: ebffef6b bl 2288 <memcpy@plt>

64d8: e1a00005 mov r0, r5

64dc: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

64e0: e1a00008 mov r0, r8

64e4: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

64e8: e1a00007 mov r0, r7

64ec: ebffef62 bl 227c <free@plt>

64f0: e3e00000 mvn r0, #0

64f4: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

64f8: e92d4010 push {r4, lr}

64fc: e5923000 ldr r3, [r2]

6500: e590e008 ldr lr, [r0, #8]

6504: e153000e cmp r3, lr

6508: ba000007 blt 652c <mcrypt\_mutex\_register@@Base+0x1d98>

650c: e1a0c000 mov ip, r0

6510: e582e000 str lr, [r2]

6514: e1a00001 mov r0, r1

6518: e1a0200e mov r2, lr

651c: e59c1000 ldr r1, [ip]

6520: ebffef58 bl 2288 <memcpy@plt>

6524: e3a00000 mov r0, #0

6528: e8bd8010 pop {r4, pc}

652c: e582e000 str lr, [r2]

6530: e3e00000 mvn r0, #0

6534: e8bd8010 pop {r4, pc}

6538: e92d4070 push {r4, r5, r6, lr}

653c: e1a04000 mov r4, r0

6540: e1a05001 mov r5, r1

6544: e1a06002 mov r6, r2

6548: e5900004 ldr r0, [r0, #4]

654c: ebffef4d bl 2288 <memcpy@plt>

6550: e1a02006 mov r2, r6

6554: e1a01005 mov r1, r5

6558: e5940000 ldr r0, [r4]

655c: ebffef49 bl 2288 <memcpy@plt>

6560: e3a00000 mov r0, #0

6564: e8bd8070 pop {r4, r5, r6, pc}

6568: e92d4010 push {r4, lr}

656c: e1a04000 mov r4, r0

6570: e5900000 ldr r0, [r0]

6574: ebffef40 bl 227c <free@plt>

6578: e5940004 ldr r0, [r4, #4]

657c: e8bd4010 pop {r4, lr}

6580: eaffef3d b 227c <free@plt>

6584: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

6588: e3520000 cmp r2, #0

658c: e59d8024 ldr r8, [sp, #36] ; 0x24

6590: da000021 ble 661c <mcrypt\_mutex\_register@@Base+0x1e88>

6594: e2427001 sub r7, r2, #1

6598: e1a09003 mov r9, r3

659c: e1a04000 mov r4, r0

65a0: e0817007 add r7, r1, r7

65a4: e2416001 sub r6, r1, #1

65a8: e2435001 sub r5, r3, #1

65ac: e1a02009 mov r2, r9

65b0: e5941000 ldr r1, [r4]

65b4: e5940004 ldr r0, [r4, #4]

65b8: ebffef32 bl 2288 <memcpy@plt>

65bc: e5941004 ldr r1, [r4, #4]

65c0: e59d0020 ldr r0, [sp, #32]

65c4: e12fff38 blx r8

65c8: e5943004 ldr r3, [r4, #4]

65cc: e5f62001 ldrb r2, [r6, #1]!

65d0: e3590001 cmp r9, #1

65d4: e5d33000 ldrb r3, [r3]

65d8: e0233002 eor r3, r3, r2

65dc: e5c63000 strb r3, [r6]

65e0: c3a02000 movgt r2, #0

65e4: da000006 ble 6604 <mcrypt\_mutex\_register@@Base+0x1e70>

65e8: e5941000 ldr r1, [r4]

65ec: e0813002 add r3, r1, r2

65f0: e5d3c001 ldrb ip, [r3, #1]

65f4: e7c1c002 strb ip, [r1, r2]

65f8: e2822001 add r2, r2, #1

65fc: e1520005 cmp r2, r5

6600: 1afffff8 bne 65e8 <mcrypt\_mutex\_register@@Base+0x1e54>

6604: e5942004 ldr r2, [r4, #4]

6608: e5943000 ldr r3, [r4]

660c: e1560007 cmp r6, r7

6610: e5d22000 ldrb r2, [r2]

6614: e7c32005 strb r2, [r3, r5]

6618: 1affffe3 bne 65ac <mcrypt\_mutex\_register@@Base+0x1e18>

661c: e3a00000 mov r0, #0

6620: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

6624: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

6628: e3520000 cmp r2, #0

662c: e59d8024 ldr r8, [sp, #36] ; 0x24

6630: da000021 ble 66bc <mcrypt\_mutex\_register@@Base+0x1f28>

6634: e2427001 sub r7, r2, #1

6638: e1a09003 mov r9, r3

663c: e1a04000 mov r4, r0

6640: e0817007 add r7, r1, r7

6644: e2416001 sub r6, r1, #1

6648: e2435001 sub r5, r3, #1

664c: e1a02009 mov r2, r9

6650: e5941000 ldr r1, [r4]

6654: e5940004 ldr r0, [r4, #4]

6658: ebffef0a bl 2288 <memcpy@plt>

665c: e5941004 ldr r1, [r4, #4]

6660: e59d0020 ldr r0, [sp, #32]

6664: e12fff38 blx r8

6668: e3590001 cmp r9, #1

666c: c3a02000 movgt r2, #0

6670: da000006 ble 6690 <mcrypt\_mutex\_register@@Base+0x1efc>

6674: e5941000 ldr r1, [r4]

6678: e0813002 add r3, r1, r2

667c: e5d3c001 ldrb ip, [r3, #1]

6680: e7c1c002 strb ip, [r1, r2]

6684: e2822001 add r2, r2, #1

6688: e1520005 cmp r2, r5

668c: 1afffff8 bne 6674 <mcrypt\_mutex\_register@@Base+0x1ee0>

6690: e5942004 ldr r2, [r4, #4]

6694: e5943000 ldr r3, [r4]

6698: e5d22000 ldrb r2, [r2]

669c: e7c32005 strb r2, [r3, r5]

66a0: e5943004 ldr r3, [r4, #4]

66a4: e5f62001 ldrb r2, [r6, #1]!

66a8: e5d33000 ldrb r3, [r3]

66ac: e1560007 cmp r6, r7

66b0: e0233002 eor r3, r3, r2

66b4: e5c63000 strb r3, [r6]

66b8: 1affffe3 bne 664c <mcrypt\_mutex\_register@@Base+0x1eb8>

66bc: e3a00000 mov r0, #0

66c0: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

66c4: e3a00000 mov r0, #0

66c8: e12fff1e bx lr

66cc: e3a00001 mov r0, #1

66d0: e12fff1e bx lr

66d4: e3a00001 mov r0, #1

66d8: e12fff1e bx lr

66dc: e59f0004 ldr r0, [pc, #4] ; 66e8 <mcrypt\_mutex\_register@@Base+0x1f54>

66e0: e08f0000 add r0, pc, r0

66e4: e12fff1e bx lr

66e8: 00017f5c andeq r7, r1, ip, asr pc

66ec: e3a0000c mov r0, #12

66f0: e12fff1e bx lr

66f4: e59f0000 ldr r0, [pc] ; 66fc <mcrypt\_mutex\_register@@Base+0x1f68>

66f8: e12fff1e bx lr

66fc: 01315546 teqeq r1, r6, asr #10

6700: e3a00000 mov r0, #0

6704: e12fff1e bx lr

6708: e3e00000 mvn r0, #0

670c: e12fff1e bx lr

6710: e3e00000 mvn r0, #0

6714: e12fff1e bx lr

6718: e3a00000 mov r0, #0

671c: e12fff1e bx lr

6720: e92d4010 push {r4, lr}

6724: e59d0008 ldr r0, [sp, #8]

6728: e59d300c ldr r3, [sp, #12]

672c: e12fff33 blx r3

6730: e3a00000 mov r0, #0

6734: e8bd8010 pop {r4, pc}

6738: e92d4010 push {r4, lr}

673c: e59d0008 ldr r0, [sp, #8]

6740: e59d3010 ldr r3, [sp, #16]

6744: e12fff33 blx r3

6748: e3a00000 mov r0, #0

674c: e8bd8010 pop {r4, pc}

6750: e3a00001 mov r0, #1

6754: e12fff1e bx lr

6758: e3a00000 mov r0, #0

675c: e12fff1e bx lr

6760: e3a00000 mov r0, #0

6764: e12fff1e bx lr

6768: e59f0004 ldr r0, [pc, #4] ; 6774 <mcrypt\_mutex\_register@@Base+0x1fe0>

676c: e08f0000 add r0, pc, r0

6770: e12fff1e bx lr

6774: 00017ed4 ldrdeq r7, [r1], -r4

6778: e3a00000 mov r0, #0

677c: e12fff1e bx lr

6780: e59f0000 ldr r0, [pc] ; 6788 <mcrypt\_mutex\_register@@Base+0x1ff4>

6784: e12fff1e bx lr

6788: 01315731 teqeq r1, r1, lsr r7

678c: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

6790: e5914004 ldr r4, [r1, #4]

6794: e5902000 ldr r2, [r0]

6798: e590c040 ldr ip, [r0, #64] ; 0x40

679c: e6bf4f34 rev r4, r4

67a0: e0842002 add r2, r4, r2

67a4: e26cc020 rsb ip, ip, #32

67a8: e59f35c0 ldr r3, [pc, #1472] ; 6d70 <mcrypt\_mutex\_register@@Base+0x25dc>

67ac: e1a02c72 ror r2, r2, ip

67b0: e08f3003 add r3, pc, r3

67b4: e1a0e822 lsr lr, r2, #16

67b8: e1a06c22 lsr r6, r2, #24

67bc: e1a0c322 lsr ip, r2, #6

67c0: e6efe07e uxtb lr, lr

67c4: e0836106 add r6, r3, r6, lsl #2

67c8: e20ccfff and ip, ip, #1020 ; 0x3fc

67cc: e6ef2072 uxtb r2, r2

67d0: e083c00c add ip, r3, ip

67d4: e5966400 ldr r6, [r6, #1024] ; 0x400

67d8: e793e10e ldr lr, [r3, lr, lsl #2]

67dc: e0832102 add r2, r3, r2, lsl #2

67e0: e59cc800 ldr ip, [ip, #2048] ; 0x800

67e4: e5922c00 ldr r2, [r2, #3072] ; 0xc00

67e8: e5915000 ldr r5, [r1]

67ec: e02ee006 eor lr, lr, r6

67f0: e04ee00c sub lr, lr, ip

67f4: e08ee002 add lr, lr, r2

67f8: e6bf5f35 rev r5, r5

67fc: e5902044 ldr r2, [r0, #68] ; 0x44

6800: e590c004 ldr ip, [r0, #4]

6804: e02ee005 eor lr, lr, r5

6808: e02cc00e eor ip, ip, lr

680c: e2625020 rsb r5, r2, #32

6810: e5902008 ldr r2, [r0, #8]

6814: e1a0c57c ror ip, ip, r5

6818: e1a0532c lsr r5, ip, #6

681c: e1a07c2c lsr r7, ip, #24

6820: e6ef607c uxtb r6, ip

6824: e2055fff and r5, r5, #1020 ; 0x3fc

6828: e1a0c82c lsr ip, ip, #16

682c: e0835005 add r5, r3, r5

6830: e0837107 add r7, r3, r7, lsl #2

6834: e0836106 add r6, r3, r6, lsl #2

6838: e6efc07c uxtb ip, ip

683c: e5959800 ldr r9, [r5, #2048] ; 0x800

6840: e5977400 ldr r7, [r7, #1024] ; 0x400

6844: e5965c00 ldr r5, [r6, #3072] ; 0xc00

6848: e793810c ldr r8, [r3, ip, lsl #2]

684c: e0899007 add r9, r9, r7

6850: e0244005 eor r4, r4, r5

6854: e590c048 ldr ip, [r0, #72] ; 0x48

6858: e0499008 sub r9, r9, r8

685c: e0299004 eor r9, r9, r4

6860: e0422009 sub r2, r2, r9

6864: e26c4020 rsb r4, ip, #32

6868: e590c00c ldr ip, [r0, #12]

686c: e1a02472 ror r2, r2, r4

6870: e1a06c22 lsr r6, r2, #24

6874: e1a05822 lsr r5, r2, #16

6878: e1a04322 lsr r4, r2, #6

687c: e6ef5075 uxtb r5, r5

6880: e0836106 add r6, r3, r6, lsl #2

6884: e2044fff and r4, r4, #1020 ; 0x3fc

6888: e6ef2072 uxtb r2, r2

688c: e0834004 add r4, r3, r4

6890: e5966400 ldr r6, [r6, #1024] ; 0x400

6894: e7937105 ldr r7, [r3, r5, lsl #2]

6898: e0832102 add r2, r3, r2, lsl #2

689c: e5944800 ldr r4, [r4, #2048] ; 0x800

68a0: e5922c00 ldr r2, [r2, #3072] ; 0xc00

68a4: e0877006 add r7, r7, r6

68a8: e0277004 eor r7, r7, r4

68ac: e0477002 sub r7, r7, r2

68b0: e590404c ldr r4, [r0, #76] ; 0x4c

68b4: e02ee007 eor lr, lr, r7

68b8: e08ec00c add ip, lr, ip

68bc: e2644020 rsb r4, r4, #32

68c0: e5902010 ldr r2, [r0, #16]

68c4: e1a0c47c ror ip, ip, r4

68c8: e1a04c2c lsr r4, ip, #24

68cc: e1a0682c lsr r6, ip, #16

68d0: e1a0532c lsr r5, ip, #6

68d4: e6ef6076 uxtb r6, r6

68d8: e0834104 add r4, r3, r4, lsl #2

68dc: e2055fff and r5, r5, #1020 ; 0x3fc

68e0: e6efc07c uxtb ip, ip

68e4: e0835005 add r5, r3, r5

68e8: e5944400 ldr r4, [r4, #1024] ; 0x400

68ec: e7938106 ldr r8, [r3, r6, lsl #2]

68f0: e083c10c add ip, r3, ip, lsl #2

68f4: e5955800 ldr r5, [r5, #2048] ; 0x800

68f8: e0288004 eor r8, r8, r4

68fc: e59c4c00 ldr r4, [ip, #3072] ; 0xc00

6900: e0488005 sub r8, r8, r5

6904: e0888004 add r8, r8, r4

6908: e590c050 ldr ip, [r0, #80] ; 0x50

690c: e0284009 eor r4, r8, r9

6910: e0222004 eor r2, r2, r4

6914: e26cc020 rsb ip, ip, #32

6918: e1a02c72 ror r2, r2, ip

691c: e1a0c322 lsr ip, r2, #6

6920: e1a06c22 lsr r6, r2, #24

6924: e6ef5072 uxtb r5, r2

6928: e20ccfff and ip, ip, #1020 ; 0x3fc

692c: e1a02822 lsr r2, r2, #16

6930: e083c00c add ip, r3, ip

6934: e0836106 add r6, r3, r6, lsl #2

6938: e0835105 add r5, r3, r5, lsl #2

693c: e6ef2072 uxtb r2, r2

6940: e59c7800 ldr r7, [ip, #2048] ; 0x800

6944: e5966400 ldr r6, [r6, #1024] ; 0x400

6948: e595cc00 ldr ip, [r5, #3072] ; 0xc00

694c: e7935102 ldr r5, [r3, r2, lsl #2]

6950: e0877006 add r7, r7, r6

6954: e0477005 sub r7, r7, r5

6958: e02ee00c eor lr, lr, ip

695c: e5905054 ldr r5, [r0, #84] ; 0x54

6960: e590c014 ldr ip, [r0, #20]

6964: e02ee007 eor lr, lr, r7

6968: e04cc00e sub ip, ip, lr

696c: e2655020 rsb r5, r5, #32

6970: e5902018 ldr r2, [r0, #24]

6974: e1a0c57c ror ip, ip, r5

6978: e1a07c2c lsr r7, ip, #24

697c: e1a0682c lsr r6, ip, #16

6980: e1a0532c lsr r5, ip, #6

6984: e6ef6076 uxtb r6, r6

6988: e0837107 add r7, r3, r7, lsl #2

698c: e2055fff and r5, r5, #1020 ; 0x3fc

6990: e6efc07c uxtb ip, ip

6994: e0835005 add r5, r3, r5

6998: e5977400 ldr r7, [r7, #1024] ; 0x400

699c: e7938106 ldr r8, [r3, r6, lsl #2]

69a0: e083c10c add ip, r3, ip, lsl #2

69a4: e5955800 ldr r5, [r5, #2048] ; 0x800

69a8: e59c6c00 ldr r6, [ip, #3072] ; 0xc00

69ac: e0888007 add r8, r8, r7

69b0: e0288005 eor r8, r8, r5

69b4: e0488006 sub r8, r8, r6

69b8: e590c058 ldr ip, [r0, #88] ; 0x58

69bc: e0288004 eor r8, r8, r4

69c0: e0882002 add r2, r8, r2

69c4: e26cc020 rsb ip, ip, #32

69c8: e590401c ldr r4, [r0, #28]

69cc: e1a02c72 ror r2, r2, ip

69d0: e1a05c22 lsr r5, r2, #24

69d4: e1a06822 lsr r6, r2, #16

69d8: e1a0c322 lsr ip, r2, #6

69dc: e0835105 add r5, r3, r5, lsl #2

69e0: e6ef6076 uxtb r6, r6

69e4: e20ccfff and ip, ip, #1020 ; 0x3fc

69e8: e6ef2072 uxtb r2, r2

69ec: e083c00c add ip, r3, ip

69f0: e5957400 ldr r7, [r5, #1024] ; 0x400

69f4: e0832102 add r2, r3, r2, lsl #2

69f8: e7935106 ldr r5, [r3, r6, lsl #2]

69fc: e59cc800 ldr ip, [ip, #2048] ; 0x800

6a00: e0255007 eor r5, r5, r7

6a04: e5927c00 ldr r7, [r2, #3072] ; 0xc00

6a08: e045500c sub r5, r5, ip

6a0c: e0855007 add r5, r5, r7

6a10: e590205c ldr r2, [r0, #92] ; 0x5c

6a14: e025700e eor r7, r5, lr

6a18: e0244007 eor r4, r4, r7

6a1c: e2622020 rsb r2, r2, #32

6a20: e590e020 ldr lr, [r0, #32]

6a24: e1a04274 ror r4, r4, r2

6a28: e1a02324 lsr r2, r4, #6

6a2c: e1a05c24 lsr r5, r4, #24

6a30: e6efc074 uxtb ip, r4

6a34: e2022fff and r2, r2, #1020 ; 0x3fc

6a38: e1a04824 lsr r4, r4, #16

6a3c: e0832002 add r2, r3, r2

6a40: e0835105 add r5, r3, r5, lsl #2

6a44: e083c10c add ip, r3, ip, lsl #2

6a48: e6ef4074 uxtb r4, r4

6a4c: e5926800 ldr r6, [r2, #2048] ; 0x800

6a50: e5955400 ldr r5, [r5, #1024] ; 0x400

6a54: e59c2c00 ldr r2, [ip, #3072] ; 0xc00

6a58: e793c104 ldr ip, [r3, r4, lsl #2]

6a5c: e0864005 add r4, r6, r5

6a60: e0288002 eor r8, r8, r2

6a64: e044400c sub r4, r4, ip

6a68: e5902060 ldr r2, [r0, #96] ; 0x60

6a6c: e0246008 eor r6, r4, r8

6a70: e04ee006 sub lr, lr, r6

6a74: e2622020 rsb r2, r2, #32

6a78: e5904024 ldr r4, [r0, #36] ; 0x24

6a7c: e1a0e27e ror lr, lr, r2

6a80: e1a05c2e lsr r5, lr, #24

6a84: e1a0c82e lsr ip, lr, #16

6a88: e1a0232e lsr r2, lr, #6

6a8c: e0835105 add r5, r3, r5, lsl #2

6a90: e6efc07c uxtb ip, ip

6a94: e2022fff and r2, r2, #1020 ; 0x3fc

6a98: e6efe07e uxtb lr, lr

6a9c: e0832002 add r2, r3, r2

6aa0: e5958400 ldr r8, [r5, #1024] ; 0x400

6aa4: e083e10e add lr, r3, lr, lsl #2

6aa8: e793510c ldr r5, [r3, ip, lsl #2]

6aac: e592c800 ldr ip, [r2, #2048] ; 0x800

6ab0: e0855008 add r5, r5, r8

6ab4: e59e2c00 ldr r2, [lr, #3072] ; 0xc00

6ab8: e025500c eor r5, r5, ip

6abc: e0455002 sub r5, r5, r2

6ac0: e5902064 ldr r2, [r0, #100] ; 0x64

6ac4: e0255007 eor r5, r5, r7

6ac8: e0854004 add r4, r5, r4

6acc: e2622020 rsb r2, r2, #32

6ad0: e590e028 ldr lr, [r0, #40] ; 0x28

6ad4: e1a04274 ror r4, r4, r2

6ad8: e1a0cc24 lsr ip, r4, #24

6adc: e1a07824 lsr r7, r4, #16

6ae0: e1a02324 lsr r2, r4, #6

6ae4: e083c10c add ip, r3, ip, lsl #2

6ae8: e6ef7077 uxtb r7, r7

6aec: e2022fff and r2, r2, #1020 ; 0x3fc

6af0: e6ef4074 uxtb r4, r4

6af4: e0832002 add r2, r3, r2

6af8: e59c8400 ldr r8, [ip, #1024] ; 0x400

6afc: e0834104 add r4, r3, r4, lsl #2

6b00: e793c107 ldr ip, [r3, r7, lsl #2]

6b04: e5922800 ldr r2, [r2, #2048] ; 0x800

6b08: e5944c00 ldr r4, [r4, #3072] ; 0xc00

6b0c: e02cc008 eor ip, ip, r8

6b10: e04cc002 sub ip, ip, r2

6b14: e08cc004 add ip, ip, r4

6b18: e5902068 ldr r2, [r0, #104] ; 0x68

6b1c: e02c4006 eor r4, ip, r6

6b20: e2622020 rsb r2, r2, #32

6b24: e02ee004 eor lr, lr, r4

6b28: e1a0e27e ror lr, lr, r2

6b2c: e6ef207e uxtb r2, lr

6b30: e1a0c32e lsr ip, lr, #6

6b34: e1a06c2e lsr r6, lr, #24

6b38: e20ccfff and ip, ip, #1020 ; 0x3fc

6b3c: e1a0e82e lsr lr, lr, #16

6b40: e0832102 add r2, r3, r2, lsl #2

6b44: e083c00c add ip, r3, ip

6b48: e0836106 add r6, r3, r6, lsl #2

6b4c: e6efe07e uxtb lr, lr

6b50: e5927c00 ldr r7, [r2, #3072] ; 0xc00

6b54: e5966400 ldr r6, [r6, #1024] ; 0x400

6b58: e59c2800 ldr r2, [ip, #2048] ; 0x800

6b5c: e793c10e ldr ip, [r3, lr, lsl #2]

6b60: e0822006 add r2, r2, r6

6b64: e0255007 eor r5, r5, r7

6b68: e042200c sub r2, r2, ip

6b6c: e590e02c ldr lr, [r0, #44] ; 0x2c

6b70: e590c06c ldr ip, [r0, #108] ; 0x6c

6b74: e0222005 eor r2, r2, r5

6b78: e26cc020 rsb ip, ip, #32

6b7c: e04ee002 sub lr, lr, r2

6b80: e5905080 ldr r5, [r0, #128] ; 0x80

6b84: e1a0ec7e ror lr, lr, ip

6b88: e355000c cmp r5, #12

6b8c: e1a0cc2e lsr ip, lr, #24

6b90: e1a0682e lsr r6, lr, #16

6b94: e1a0532e lsr r5, lr, #6

6b98: e083c10c add ip, r3, ip, lsl #2

6b9c: e6ef6076 uxtb r6, r6

6ba0: e2055fff and r5, r5, #1020 ; 0x3fc

6ba4: e6efe07e uxtb lr, lr

6ba8: e0835005 add r5, r3, r5

6bac: e59c7400 ldr r7, [ip, #1024] ; 0x400

6bb0: e083e10e add lr, r3, lr, lsl #2

6bb4: e793c106 ldr ip, [r3, r6, lsl #2]

6bb8: e5955800 ldr r5, [r5, #2048] ; 0x800

6bbc: e08cc007 add ip, ip, r7

6bc0: e59eec00 ldr lr, [lr, #3072] ; 0xc00

6bc4: e02cc005 eor ip, ip, r5

6bc8: e04cc00e sub ip, ip, lr

6bcc: e02cc004 eor ip, ip, r4

6bd0: 9a000057 bls 6d34 <mcrypt\_mutex\_register@@Base+0x25a0>

6bd4: e590e030 ldr lr, [r0, #48] ; 0x30

6bd8: e5905070 ldr r5, [r0, #112] ; 0x70

6bdc: e08ce00e add lr, ip, lr

6be0: e2655020 rsb r5, r5, #32

6be4: e5904034 ldr r4, [r0, #52] ; 0x34

6be8: e1a0e57e ror lr, lr, r5

6bec: e1a0582e lsr r5, lr, #16

6bf0: e1a07c2e lsr r7, lr, #24

6bf4: e1a0632e lsr r6, lr, #6

6bf8: e0837107 add r7, r3, r7, lsl #2

6bfc: e6ef5075 uxtb r5, r5

6c00: e2066fff and r6, r6, #1020 ; 0x3fc

6c04: e6efe07e uxtb lr, lr

6c08: e0836006 add r6, r3, r6

6c0c: e083e10e add lr, r3, lr, lsl #2

6c10: e7935105 ldr r5, [r3, r5, lsl #2]

6c14: e5978400 ldr r8, [r7, #1024] ; 0x400

6c18: e5967800 ldr r7, [r6, #2048] ; 0x800

6c1c: e0255008 eor r5, r5, r8

6c20: e59e6c00 ldr r6, [lr, #3072] ; 0xc00

6c24: e045e007 sub lr, r5, r7

6c28: e08ee006 add lr, lr, r6

6c2c: e5905074 ldr r5, [r0, #116] ; 0x74

6c30: e022200e eor r2, r2, lr

6c34: e0244002 eor r4, r4, r2

6c38: e2655020 rsb r5, r5, #32

6c3c: e590e038 ldr lr, [r0, #56] ; 0x38

6c40: e1a04574 ror r4, r4, r5

6c44: e1a05324 lsr r5, r4, #6

6c48: e1a07c24 lsr r7, r4, #24

6c4c: e1a06824 lsr r6, r4, #16

6c50: e2055fff and r5, r5, #1020 ; 0x3fc

6c54: e6ef4074 uxtb r4, r4

6c58: e0835005 add r5, r3, r5

6c5c: e0837107 add r7, r3, r7, lsl #2

6c60: e0834104 add r4, r3, r4, lsl #2

6c64: e6ef6076 uxtb r6, r6

6c68: e5977400 ldr r7, [r7, #1024] ; 0x400

6c6c: e5955800 ldr r5, [r5, #2048] ; 0x800

6c70: e7936106 ldr r6, [r3, r6, lsl #2]

6c74: e5944c00 ldr r4, [r4, #3072] ; 0xc00

6c78: e0855007 add r5, r5, r7

6c7c: e024400c eor r4, r4, ip

6c80: e0455006 sub r5, r5, r6

6c84: e590c078 ldr ip, [r0, #120] ; 0x78

6c88: e0255004 eor r5, r5, r4

6c8c: e26cc020 rsb ip, ip, #32

6c90: e04ee005 sub lr, lr, r5

6c94: e590403c ldr r4, [r0, #60] ; 0x3c

6c98: e1a0ec7e ror lr, lr, ip

6c9c: e590007c ldr r0, [r0, #124] ; 0x7c

6ca0: e1a0c82e lsr ip, lr, #16

6ca4: e1a07c2e lsr r7, lr, #24

6ca8: e1a0632e lsr r6, lr, #6

6cac: e0837107 add r7, r3, r7, lsl #2

6cb0: e6efc07c uxtb ip, ip

6cb4: e2066fff and r6, r6, #1020 ; 0x3fc

6cb8: e6efe07e uxtb lr, lr

6cbc: e0836006 add r6, r3, r6

6cc0: e793c10c ldr ip, [r3, ip, lsl #2]

6cc4: e083e10e add lr, r3, lr, lsl #2

6cc8: e5977400 ldr r7, [r7, #1024] ; 0x400

6ccc: e5966800 ldr r6, [r6, #2048] ; 0x800

6cd0: e59eec00 ldr lr, [lr, #3072] ; 0xc00

6cd4: e08cc007 add ip, ip, r7

6cd8: e02cc006 eor ip, ip, r6

6cdc: e04cc00e sub ip, ip, lr

6ce0: e022200c eor r2, r2, ip

6ce4: e0824004 add r4, r2, r4

6ce8: e2600020 rsb r0, r0, #32

6cec: e1a00074 ror r0, r4, r0

6cf0: e1a0c820 lsr ip, r0, #16

6cf4: e1a04c20 lsr r4, r0, #24

6cf8: e1a0e320 lsr lr, r0, #6

6cfc: e0834104 add r4, r3, r4, lsl #2

6d00: e6ef0070 uxtb r0, r0

6d04: e6efc07c uxtb ip, ip

6d08: e20eefff and lr, lr, #1020 ; 0x3fc

6d0c: e083e00e add lr, r3, lr

6d10: e793c10c ldr ip, [r3, ip, lsl #2]

6d14: e5944400 ldr r4, [r4, #1024] ; 0x400

6d18: e0833100 add r3, r3, r0, lsl #2

6d1c: e59e0800 ldr r0, [lr, #2048] ; 0x800

6d20: e02cc004 eor ip, ip, r4

6d24: e5933c00 ldr r3, [r3, #3072] ; 0xc00

6d28: e04cc000 sub ip, ip, r0

6d2c: e08cc003 add ip, ip, r3

6d30: e02cc005 eor ip, ip, r5

6d34: e1a03c2c lsr r3, ip, #24

6d38: e1a00c22 lsr r0, r2, #24

6d3c: e1a0482c lsr r4, ip, #16

6d40: e1a0e42c lsr lr, ip, #8

6d44: e5c12007 strb r2, [r1, #7]

6d48: e5c13000 strb r3, [r1]

6d4c: e1a03822 lsr r3, r2, #16

6d50: e1a02422 lsr r2, r2, #8

6d54: e5c1c003 strb ip, [r1, #3]

6d58: e5c14001 strb r4, [r1, #1]

6d5c: e5c1e002 strb lr, [r1, #2]

6d60: e5c10004 strb r0, [r1, #4]

6d64: e5c13005 strb r3, [r1, #5]

6d68: e5c12006 strb r2, [r1, #6]

6d6c: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

6d70: 00017e98 muleq r1, r8, lr

6d74: e5903080 ldr r3, [r0, #128] ; 0x80

6d78: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

6d7c: e353000c cmp r3, #12

6d80: e591a000 ldr sl, [r1]

6d84: e591b004 ldr fp, [r1, #4]

6d88: e6bf6f3a rev r6, sl

6d8c: e6bfbf3b rev fp, fp

6d90: 9a000059 bls 6efc <mcrypt\_mutex\_register@@Base+0x2768>

6d94: e590203c ldr r2, [r0, #60] ; 0x3c

6d98: e590c07c ldr ip, [r0, #124] ; 0x7c

6d9c: e08b2002 add r2, fp, r2

6da0: e26cc020 rsb ip, ip, #32

6da4: e59f35b4 ldr r3, [pc, #1460] ; 7360 <mcrypt\_mutex\_register@@Base+0x2bcc>

6da8: e1a02c72 ror r2, r2, ip

6dac: e08f3003 add r3, pc, r3

6db0: e1a05822 lsr r5, r2, #16

6db4: e1a04c22 lsr r4, r2, #24

6db8: e1a0e322 lsr lr, r2, #6

6dbc: e6ef5075 uxtb r5, r5

6dc0: e0834104 add r4, r3, r4, lsl #2

6dc4: e20eefff and lr, lr, #1020 ; 0x3fc

6dc8: e6ef2072 uxtb r2, r2

6dcc: e083e00e add lr, r3, lr

6dd0: e793a105 ldr sl, [r3, r5, lsl #2]

6dd4: e5944400 ldr r4, [r4, #1024] ; 0x400

6dd8: e0832102 add r2, r3, r2, lsl #2

6ddc: e59ee800 ldr lr, [lr, #2048] ; 0x800

6de0: e02aa004 eor sl, sl, r4

6de4: e5922c00 ldr r2, [r2, #3072] ; 0xc00

6de8: e04aa00e sub sl, sl, lr

6dec: e08aa002 add sl, sl, r2

6df0: e590c038 ldr ip, [r0, #56] ; 0x38

6df4: e590e078 ldr lr, [r0, #120] ; 0x78

6df8: e02aa006 eor sl, sl, r6

6dfc: e04cc00a sub ip, ip, sl

6e00: e26ee020 rsb lr, lr, #32

6e04: e5902034 ldr r2, [r0, #52] ; 0x34

6e08: e1a0ce7c ror ip, ip, lr

6e0c: e1a05c2c lsr r5, ip, #24

6e10: e1a0482c lsr r4, ip, #16

6e14: e1a0e32c lsr lr, ip, #6

6e18: e0835105 add r5, r3, r5, lsl #2

6e1c: e6ef4074 uxtb r4, r4

6e20: e20eefff and lr, lr, #1020 ; 0x3fc

6e24: e6efc07c uxtb ip, ip

6e28: e083e00e add lr, r3, lr

6e2c: e5956400 ldr r6, [r5, #1024] ; 0x400

6e30: e083c10c add ip, r3, ip, lsl #2

6e34: e7935104 ldr r5, [r3, r4, lsl #2]

6e38: e59ee800 ldr lr, [lr, #2048] ; 0x800

6e3c: e0855006 add r5, r5, r6

6e40: e59ccc00 ldr ip, [ip, #3072] ; 0xc00

6e44: e025500e eor r5, r5, lr

6e48: e045500c sub r5, r5, ip

6e4c: e590e074 ldr lr, [r0, #116] ; 0x74

6e50: e02bb005 eor fp, fp, r5

6e54: e022200b eor r2, r2, fp

6e58: e26ee020 rsb lr, lr, #32

6e5c: e590c030 ldr ip, [r0, #48] ; 0x30

6e60: e1a02e72 ror r2, r2, lr

6e64: e1a0e322 lsr lr, r2, #6

6e68: e1a05c22 lsr r5, r2, #24

6e6c: e6ef4072 uxtb r4, r2

6e70: e20eefff and lr, lr, #1020 ; 0x3fc

6e74: e1a02822 lsr r2, r2, #16

6e78: e083e00e add lr, r3, lr

6e7c: e0835105 add r5, r3, r5, lsl #2

6e80: e0834104 add r4, r3, r4, lsl #2

6e84: e6ef2072 uxtb r2, r2

6e88: e59e6800 ldr r6, [lr, #2048] ; 0x800

6e8c: e5955400 ldr r5, [r5, #1024] ; 0x400

6e90: e793e102 ldr lr, [r3, r2, lsl #2]

6e94: e5944c00 ldr r4, [r4, #3072] ; 0xc00

6e98: e0862005 add r2, r6, r5

6e9c: e042200e sub r2, r2, lr

6ea0: e02aa004 eor sl, sl, r4

6ea4: e590e070 ldr lr, [r0, #112] ; 0x70

6ea8: e022600a eor r6, r2, sl

6eac: e086200c add r2, r6, ip

6eb0: e26ec020 rsb ip, lr, #32

6eb4: e1a02c72 ror r2, r2, ip

6eb8: e1a04c22 lsr r4, r2, #24

6ebc: e1a0e822 lsr lr, r2, #16

6ec0: e1a0c322 lsr ip, r2, #6

6ec4: e0834104 add r4, r3, r4, lsl #2

6ec8: e6ef2072 uxtb r2, r2

6ecc: e6efe07e uxtb lr, lr

6ed0: e20ccfff and ip, ip, #1020 ; 0x3fc

6ed4: e083c00c add ip, r3, ip

6ed8: e793510e ldr r5, [r3, lr, lsl #2]

6edc: e5944400 ldr r4, [r4, #1024] ; 0x400

6ee0: e0833102 add r3, r3, r2, lsl #2

6ee4: e59c2800 ldr r2, [ip, #2048] ; 0x800

6ee8: e0255004 eor r5, r5, r4

6eec: e5939c00 ldr r9, [r3, #3072] ; 0xc00

6ef0: e0455002 sub r5, r5, r2

6ef4: e0855009 add r5, r5, r9

6ef8: e02bb005 eor fp, fp, r5

6efc: e590c02c ldr ip, [r0, #44] ; 0x2c

6f00: e590206c ldr r2, [r0, #108] ; 0x6c

6f04: e04cc00b sub ip, ip, fp

6f08: e2622020 rsb r2, r2, #32

6f0c: e59f3450 ldr r3, [pc, #1104] ; 7364 <mcrypt\_mutex\_register@@Base+0x2bd0>

6f10: e1a0c27c ror ip, ip, r2

6f14: e08f3003 add r3, pc, r3

6f18: e1a0282c lsr r2, ip, #16

6f1c: e1a05c2c lsr r5, ip, #24

6f20: e1a0432c lsr r4, ip, #6

6f24: e0835105 add r5, r3, r5, lsl #2

6f28: e6ef2072 uxtb r2, r2

6f2c: e2044fff and r4, r4, #1020 ; 0x3fc

6f30: e6efc07c uxtb ip, ip

6f34: e0834004 add r4, r3, r4

6f38: e595a400 ldr sl, [r5, #1024] ; 0x400

6f3c: e7932102 ldr r2, [r3, r2, lsl #2]

6f40: e083c10c add ip, r3, ip, lsl #2

6f44: e5944800 ldr r4, [r4, #2048] ; 0x800

6f48: e082200a add r2, r2, sl

6f4c: e59cac00 ldr sl, [ip, #3072] ; 0xc00

6f50: e0222004 eor r2, r2, r4

6f54: e590c068 ldr ip, [r0, #104] ; 0x68

6f58: e590e028 ldr lr, [r0, #40] ; 0x28

6f5c: e042200a sub r2, r2, sl

6f60: e022a006 eor sl, r2, r6

6f64: e02ee00a eor lr, lr, sl

6f68: e26c2020 rsb r2, ip, #32

6f6c: e590c024 ldr ip, [r0, #36] ; 0x24

6f70: e1a0e27e ror lr, lr, r2

6f74: e1a0232e lsr r2, lr, #6

6f78: e1a05c2e lsr r5, lr, #24

6f7c: e1a0482e lsr r4, lr, #16

6f80: e2022fff and r2, r2, #1020 ; 0x3fc

6f84: e6efe07e uxtb lr, lr

6f88: e0832002 add r2, r3, r2

6f8c: e0835105 add r5, r3, r5, lsl #2

6f90: e083e10e add lr, r3, lr, lsl #2

6f94: e6ef4074 uxtb r4, r4

6f98: e5929800 ldr r9, [r2, #2048] ; 0x800

6f9c: e5952400 ldr r2, [r5, #1024] ; 0x400

6fa0: e7934104 ldr r4, [r3, r4, lsl #2]

6fa4: e59e5c00 ldr r5, [lr, #3072] ; 0xc00

6fa8: e0899002 add r9, r9, r2

6fac: e0499004 sub r9, r9, r4

6fb0: e5902064 ldr r2, [r0, #100] ; 0x64

6fb4: e02bb005 eor fp, fp, r5

6fb8: e029500b eor r5, r9, fp

6fbc: e085c00c add ip, r5, ip

6fc0: e2622020 rsb r2, r2, #32

6fc4: e590e020 ldr lr, [r0, #32]

6fc8: e1a0c27c ror ip, ip, r2

6fcc: e1a02c2c lsr r2, ip, #24

6fd0: e1a0682c lsr r6, ip, #16

6fd4: e1a0432c lsr r4, ip, #6

6fd8: e0832102 add r2, r3, r2, lsl #2

6fdc: e6ef6076 uxtb r6, r6

6fe0: e2044fff and r4, r4, #1020 ; 0x3fc

6fe4: e6efc07c uxtb ip, ip

6fe8: e0834004 add r4, r3, r4

6fec: e5927400 ldr r7, [r2, #1024] ; 0x400

6ff0: e083c10c add ip, r3, ip, lsl #2

6ff4: e7932106 ldr r2, [r3, r6, lsl #2]

6ff8: e5944800 ldr r4, [r4, #2048] ; 0x800

6ffc: e59c8c00 ldr r8, [ip, #3072] ; 0xc00

7000: e0222007 eor r2, r2, r7

7004: e0422004 sub r2, r2, r4

7008: e0822008 add r2, r2, r8

700c: e5904060 ldr r4, [r0, #96] ; 0x60

7010: e022200a eor r2, r2, sl

7014: e04ee002 sub lr, lr, r2

7018: e2644020 rsb r4, r4, #32

701c: e590c01c ldr ip, [r0, #28]

7020: e1a0e47e ror lr, lr, r4

7024: e1a04c2e lsr r4, lr, #24

7028: e1a0782e lsr r7, lr, #16

702c: e1a0632e lsr r6, lr, #6

7030: e0834104 add r4, r3, r4, lsl #2

7034: e6ef7077 uxtb r7, r7

7038: e2066fff and r6, r6, #1020 ; 0x3fc

703c: e6efe07e uxtb lr, lr

7040: e0836006 add r6, r3, r6

7044: e5949400 ldr r9, [r4, #1024] ; 0x400

7048: e083e10e add lr, r3, lr, lsl #2

704c: e7934107 ldr r4, [r3, r7, lsl #2]

7050: e5966800 ldr r6, [r6, #2048] ; 0x800

7054: e0844009 add r4, r4, r9

7058: e59e9c00 ldr r9, [lr, #3072] ; 0xc00

705c: e0244006 eor r4, r4, r6

7060: e590e05c ldr lr, [r0, #92] ; 0x5c

7064: e0444009 sub r4, r4, r9

7068: e0249005 eor r9, r4, r5

706c: e26ee020 rsb lr, lr, #32

7070: e02cc009 eor ip, ip, r9

7074: e5905018 ldr r5, [r0, #24]

7078: e1a0ce7c ror ip, ip, lr

707c: e1a0e32c lsr lr, ip, #6

7080: e1a06c2c lsr r6, ip, #24

7084: e6ef407c uxtb r4, ip

7088: e20eefff and lr, lr, #1020 ; 0x3fc

708c: e1a0c82c lsr ip, ip, #16

7090: e083e00e add lr, r3, lr

7094: e0836106 add r6, r3, r6, lsl #2

7098: e0834104 add r4, r3, r4, lsl #2

709c: e6efc07c uxtb ip, ip

70a0: e59e7800 ldr r7, [lr, #2048] ; 0x800

70a4: e5966400 ldr r6, [r6, #1024] ; 0x400

70a8: e793810c ldr r8, [r3, ip, lsl #2]

70ac: e594ec00 ldr lr, [r4, #3072] ; 0xc00

70b0: e0877006 add r7, r7, r6

70b4: e590c058 ldr ip, [r0, #88] ; 0x58

70b8: e022200e eor r2, r2, lr

70bc: e0477008 sub r7, r7, r8

70c0: e0278002 eor r8, r7, r2

70c4: e0885005 add r5, r8, r5

70c8: e26c2020 rsb r2, ip, #32

70cc: e1a05275 ror r5, r5, r2

70d0: e1a0cc25 lsr ip, r5, #24

70d4: e1a0e825 lsr lr, r5, #16

70d8: e1a02325 lsr r2, r5, #6

70dc: e083c10c add ip, r3, ip, lsl #2

70e0: e6efe07e uxtb lr, lr

70e4: e2022fff and r2, r2, #1020 ; 0x3fc

70e8: e6ef5075 uxtb r5, r5

70ec: e0832002 add r2, r3, r2

70f0: e793410e ldr r4, [r3, lr, lsl #2]

70f4: e0835105 add r5, r3, r5, lsl #2

70f8: e59cc400 ldr ip, [ip, #1024] ; 0x400

70fc: e5922800 ldr r2, [r2, #2048] ; 0x800

7100: e5956c00 ldr r6, [r5, #3072] ; 0xc00

7104: e024400c eor r4, r4, ip

7108: e0444002 sub r4, r4, r2

710c: e5905014 ldr r5, [r0, #20]

7110: e5902054 ldr r2, [r0, #84] ; 0x54

7114: e0844006 add r4, r4, r6

7118: e0244009 eor r4, r4, r9

711c: e0455004 sub r5, r5, r4

7120: e2622020 rsb r2, r2, #32

7124: e590e010 ldr lr, [r0, #16]

7128: e1a05275 ror r5, r5, r2

712c: e1a0cc25 lsr ip, r5, #24

7130: e1a06825 lsr r6, r5, #16

7134: e1a02325 lsr r2, r5, #6

7138: e083c10c add ip, r3, ip, lsl #2

713c: e6ef6076 uxtb r6, r6

7140: e2022fff and r2, r2, #1020 ; 0x3fc

7144: e6ef5075 uxtb r5, r5

7148: e0832002 add r2, r3, r2

714c: e59c7400 ldr r7, [ip, #1024] ; 0x400

7150: e0835105 add r5, r3, r5, lsl #2

7154: e793c106 ldr ip, [r3, r6, lsl #2]

7158: e5922800 ldr r2, [r2, #2048] ; 0x800

715c: e08cc007 add ip, ip, r7

7160: e5957c00 ldr r7, [r5, #3072] ; 0xc00

7164: e02cc002 eor ip, ip, r2

7168: e04cc007 sub ip, ip, r7

716c: e5902050 ldr r2, [r0, #80] ; 0x50

7170: e02c7008 eor r7, ip, r8

7174: e02ee007 eor lr, lr, r7

7178: e2622020 rsb r2, r2, #32

717c: e590500c ldr r5, [r0, #12]

7180: e1a0e27e ror lr, lr, r2

7184: e1a0232e lsr r2, lr, #6

7188: e1a08c2e lsr r8, lr, #24

718c: e6efc07e uxtb ip, lr

7190: e2022fff and r2, r2, #1020 ; 0x3fc

7194: e1a0e82e lsr lr, lr, #16

7198: e0832002 add r2, r3, r2

719c: e0838108 add r8, r3, r8, lsl #2

71a0: e083c10c add ip, r3, ip, lsl #2

71a4: e6efe07e uxtb lr, lr

71a8: e5926800 ldr r6, [r2, #2048] ; 0x800

71ac: e5988400 ldr r8, [r8, #1024] ; 0x400

71b0: e793210e ldr r2, [r3, lr, lsl #2]

71b4: e59ccc00 ldr ip, [ip, #3072] ; 0xc00

71b8: e086e008 add lr, r6, r8

71bc: e024400c eor r4, r4, ip

71c0: e04ee002 sub lr, lr, r2

71c4: e590204c ldr r2, [r0, #76] ; 0x4c

71c8: e02e6004 eor r6, lr, r4

71cc: e0865005 add r5, r6, r5

71d0: e2622020 rsb r2, r2, #32

71d4: e5904008 ldr r4, [r0, #8]

71d8: e1a05275 ror r5, r5, r2

71dc: e1a0cc25 lsr ip, r5, #24

71e0: e1a0e825 lsr lr, r5, #16

71e4: e1a02325 lsr r2, r5, #6

71e8: e083c10c add ip, r3, ip, lsl #2

71ec: e6efe07e uxtb lr, lr

71f0: e2022fff and r2, r2, #1020 ; 0x3fc

71f4: e6ef5075 uxtb r5, r5

71f8: e0832002 add r2, r3, r2

71fc: e59c8400 ldr r8, [ip, #1024] ; 0x400

7200: e0835105 add r5, r3, r5, lsl #2

7204: e793c10e ldr ip, [r3, lr, lsl #2]

7208: e592e800 ldr lr, [r2, #2048] ; 0x800

720c: e02cc008 eor ip, ip, r8

7210: e5952c00 ldr r2, [r5, #3072] ; 0xc00

7214: e04cc00e sub ip, ip, lr

7218: e08cc002 add ip, ip, r2

721c: e590e048 ldr lr, [r0, #72] ; 0x48

7220: e02cc007 eor ip, ip, r7

7224: e044400c sub r4, r4, ip

7228: e26ee020 rsb lr, lr, #32

722c: e5902004 ldr r2, [r0, #4]

7230: e1a04e74 ror r4, r4, lr

7234: e1a0ec24 lsr lr, r4, #24

7238: e1a07824 lsr r7, r4, #16

723c: e1a05324 lsr r5, r4, #6

7240: e083e10e add lr, r3, lr, lsl #2

7244: e6ef7077 uxtb r7, r7

7248: e2055fff and r5, r5, #1020 ; 0x3fc

724c: e6ef4074 uxtb r4, r4

7250: e0835005 add r5, r3, r5

7254: e59e8400 ldr r8, [lr, #1024] ; 0x400

7258: e0834104 add r4, r3, r4, lsl #2

725c: e793e107 ldr lr, [r3, r7, lsl #2]

7260: e5955800 ldr r5, [r5, #2048] ; 0x800

7264: e5944c00 ldr r4, [r4, #3072] ; 0xc00

7268: e08ee008 add lr, lr, r8

726c: e02ee005 eor lr, lr, r5

7270: e04ee004 sub lr, lr, r4

7274: e5904044 ldr r4, [r0, #68] ; 0x44

7278: e02ee006 eor lr, lr, r6

727c: e022200e eor r2, r2, lr

7280: e2644020 rsb r4, r4, #32

7284: e1a02472 ror r2, r2, r4

7288: e6ef6072 uxtb r6, r2

728c: e1a04322 lsr r4, r2, #6

7290: e1a05c22 lsr r5, r2, #24

7294: e2044fff and r4, r4, #1020 ; 0x3fc

7298: e1a02822 lsr r2, r2, #16

729c: e0835105 add r5, r3, r5, lsl #2

72a0: e0836106 add r6, r3, r6, lsl #2

72a4: e0834004 add r4, r3, r4

72a8: e6ef2072 uxtb r2, r2

72ac: e5966c00 ldr r6, [r6, #3072] ; 0xc00

72b0: e5944800 ldr r4, [r4, #2048] ; 0x800

72b4: e5957400 ldr r7, [r5, #1024] ; 0x400

72b8: e7935102 ldr r5, [r3, r2, lsl #2]

72bc: e0842007 add r2, r4, r7

72c0: e02cc006 eor ip, ip, r6

72c4: e5904000 ldr r4, [r0]

72c8: e0422005 sub r2, r2, r5

72cc: e5900040 ldr r0, [r0, #64] ; 0x40

72d0: e022200c eor r2, r2, ip

72d4: e0824004 add r4, r2, r4

72d8: e2600020 rsb r0, r0, #32

72dc: e1a05c22 lsr r5, r2, #24

72e0: e1a00074 ror r0, r4, r0

72e4: e5c12007 strb r2, [r1, #7]

72e8: e1a07c20 lsr r7, r0, #24

72ec: e1a06820 lsr r6, r0, #16

72f0: e1a04320 lsr r4, r0, #6

72f4: e6efc070 uxtb ip, r0

72f8: e0837107 add r7, r3, r7, lsl #2

72fc: e6ef6076 uxtb r6, r6

7300: e2044fff and r4, r4, #1020 ; 0x3fc

7304: e0834004 add r4, r3, r4

7308: e7930106 ldr r0, [r3, r6, lsl #2]

730c: e5977400 ldr r7, [r7, #1024] ; 0x400

7310: e083310c add r3, r3, ip, lsl #2

7314: e5944800 ldr r4, [r4, #2048] ; 0x800

7318: e593cc00 ldr ip, [r3, #3072] ; 0xc00

731c: e0200007 eor r0, r0, r7

7320: e0403004 sub r3, r0, r4

7324: e083300c add r3, r3, ip

7328: e023300e eor r3, r3, lr

732c: e1a00822 lsr r0, r2, #16

7330: e1a04c23 lsr r4, r3, #24

7334: e1a0e823 lsr lr, r3, #16

7338: e1a0c423 lsr ip, r3, #8

733c: e1a02422 lsr r2, r2, #8

7340: e5c13003 strb r3, [r1, #3]

7344: e5c14000 strb r4, [r1]

7348: e5c1e001 strb lr, [r1, #1]

734c: e5c1c002 strb ip, [r1, #2]

7350: e5c15004 strb r5, [r1, #4]

7354: e5c10005 strb r0, [r1, #5]

7358: e5c12006 strb r2, [r1, #6]

735c: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

7360: 0001789c muleq r1, ip, r8

7364: 00017734 andeq r7, r1, r4, lsr r7

7368: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

736c: e24dd05c sub sp, sp, #92 ; 0x5c

7370: e352000b cmp r2, #11

7374: e28d5048 add r5, sp, #72 ; 0x48

7378: e3a04003 mov r4, #3

737c: e3a09000 mov r9, #0

7380: 33a0600c movcc r6, #12

7384: 23a06010 movcs r6, #16

7388: e5806080 str r6, [r0, #128] ; 0x80

738c: e2446003 sub r6, r4, #3

7390: e1560002 cmp r6, r2

7394: e2448002 sub r8, r4, #2

7398: 35d16000 ldrbcc r6, [r1]

739c: e4859004 str r9, [r5], #4

73a0: e2447001 sub r7, r4, #1

73a4: e2811004 add r1, r1, #4

73a8: 31a06c06 lslcc r6, r6, #24

73ac: 35056004 strcc r6, [r5, #-4]

73b0: e1580002 cmp r8, r2

73b4: 35518003 ldrbcc r8, [r1, #-3]

73b8: 35156004 ldrcc r6, [r5, #-4]

73bc: 31866808 orrcc r6, r6, r8, lsl #16

73c0: 35056004 strcc r6, [r5, #-4]

73c4: e1570002 cmp r7, r2

73c8: 35517002 ldrbcc r7, [r1, #-2]

73cc: 35156004 ldrcc r6, [r5, #-4]

73d0: 31866407 orrcc r6, r6, r7, lsl #8

73d4: 35056004 strcc r6, [r5, #-4]

73d8: e1540002 cmp r4, r2

73dc: e2844004 add r4, r4, #4

73e0: 35517001 ldrbcc r7, [r1, #-1]

73e4: 35156004 ldrcc r6, [r5, #-4]

73e8: 31866007 orrcc r6, r6, r7

73ec: 35056004 strcc r6, [r5, #-4]

73f0: e3540013 cmp r4, #19

73f4: 1affffe4 bne 738c <mcrypt\_mutex\_register@@Base+0x2bf8>

73f8: e59f48b4 ldr r4, [pc, #2228] ; 7cb4 <mcrypt\_mutex\_register@@Base+0x3520>

73fc: e59d2048 ldr r2, [sp, #72] ; 0x48

7400: e08f4004 add r4, pc, r4

7404: e58d4034 str r4, [sp, #52] ; 0x34

7408: e59f48a8 ldr r4, [pc, #2216] ; 7cb8 <mcrypt\_mutex\_register@@Base+0x3524>

740c: e58d201c str r2, [sp, #28]

7410: e08f4004 add r4, pc, r4

7414: e58d4020 str r4, [sp, #32]

7418: e59f489c ldr r4, [pc, #2204] ; 7cbc <mcrypt\_mutex\_register@@Base+0x3528>

741c: e59d204c ldr r2, [sp, #76] ; 0x4c

7420: e08f4004 add r4, pc, r4

7424: e58d4038 str r4, [sp, #56] ; 0x38

7428: e59f4890 ldr r4, [pc, #2192] ; 7cc0 <mcrypt\_mutex\_register@@Base+0x352c>

742c: e58d2010 str r2, [sp, #16]

7430: e08f4004 add r4, pc, r4

7434: e59d2050 ldr r2, [sp, #80] ; 0x50

7438: e58d403c str r4, [sp, #60] ; 0x3c

743c: e59f4880 ldr r4, [pc, #2176] ; 7cc4 <mcrypt\_mutex\_register@@Base+0x3530>

7440: e58d2018 str r2, [sp, #24]

7444: e59d2054 ldr r2, [sp, #84] ; 0x54

7448: e08f4004 add r4, pc, r4

744c: e58d2014 str r2, [sp, #20]

7450: e59f1870 ldr r1, [pc, #2160] ; 7cc8 <mcrypt\_mutex\_register@@Base+0x3534>

7454: e59f2870 ldr r2, [pc, #2160] ; 7ccc <mcrypt\_mutex\_register@@Base+0x3538>

7458: e58d4040 str r4, [sp, #64] ; 0x40

745c: e59f486c ldr r4, [pc, #2156] ; 7cd0 <mcrypt\_mutex\_register@@Base+0x353c>

7460: e08f1001 add r1, pc, r1

7464: e08f4004 add r4, pc, r4

7468: e08f2002 add r2, pc, r2

746c: e58d4044 str r4, [sp, #68] ; 0x44

7470: e3a04000 mov r4, #0

7474: e58d4004 str r4, [sp, #4]

7478: e59d4004 ldr r4, [sp, #4]

747c: e2144004 ands r4, r4, #4

7480: 0a0001a7 beq 7b24 <mcrypt\_mutex\_register@@Base+0x3390>

7484: e3540004 cmp r4, #4

7488: 1a000062 bne 7618 <mcrypt\_mutex\_register@@Base+0x2e84>

748c: e59d802c ldr r8, [sp, #44] ; 0x2c

7490: e59d9030 ldr r9, [sp, #48] ; 0x30

7494: e6efc078 uxtb ip, r8

7498: e1a03c28 lsr r3, r8, #24

749c: e082c10c add ip, r2, ip, lsl #2

74a0: e0823103 add r3, r2, r3, lsl #2

74a4: e1a05c29 lsr r5, r9, #24

74a8: e1a04728 lsr r4, r8, #14

74ac: e51ccff8 ldr ip, [ip, #-4088] ; 0xfffff008

74b0: e5133bf8 ldr r3, [r3, #-3064] ; 0xfffff408

74b4: e1a0e328 lsr lr, r8, #6

74b8: e0825105 add r5, r2, r5, lsl #2

74bc: e2044fff and r4, r4, #1020 ; 0x3fc

74c0: e023300c eor r3, r3, ip

74c4: e0824004 add r4, r2, r4

74c8: e59dc028 ldr ip, [sp, #40] ; 0x28

74cc: e20eefff and lr, lr, #1020 ; 0x3fc

74d0: e5155bf8 ldr r5, [r5, #-3064] ; 0xfffff408

74d4: e082e00e add lr, r2, lr

74d8: e023300c eor r3, r3, ip

74dc: e51447f8 ldr r4, [r4, #-2040] ; 0xfffff808

74e0: e0233005 eor r3, r3, r5

74e4: e51ee3f8 ldr lr, [lr, #-1016] ; 0xfffffc08

74e8: e0233004 eor r3, r3, r4

74ec: e023c00e eor ip, r3, lr

74f0: e1a03329 lsr r3, r9, #6

74f4: e1a06c2c lsr r6, ip, #24

74f8: e2033fff and r3, r3, #1020 ; 0x3fc

74fc: e0823003 add r3, r2, r3

7500: e6ef507c uxtb r5, ip

7504: e0826106 add r6, r2, r6, lsl #2

7508: e1a0432c lsr r4, ip, #6

750c: e51333f8 ldr r3, [r3, #-1016] ; 0xfffffc08

7510: e0825105 add r5, r2, r5, lsl #2

7514: e2044fff and r4, r4, #1020 ; 0x3fc

7518: e1a0e72c lsr lr, ip, #14

751c: e51667f8 ldr r6, [r6, #-2040] ; 0xfffff808

7520: e0824004 add r4, r2, r4

7524: e20eefff and lr, lr, #1020 ; 0x3fc

7528: e0233009 eor r3, r3, r9

752c: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

7530: e082e00e add lr, r2, lr

7534: e0233006 eor r3, r3, r6

7538: e5144ff8 ldr r4, [r4, #-4088] ; 0xfffff008

753c: e0233005 eor r3, r3, r5

7540: e51eebf8 ldr lr, [lr, #-3064] ; 0xfffff408

7544: e0233004 eor r3, r3, r4

7548: e023300e eor r3, r3, lr

754c: e1a0e729 lsr lr, r9, #14

7550: e6ef7073 uxtb r7, r3

7554: e20eefff and lr, lr, #1020 ; 0x3fc

7558: e082e00e add lr, r2, lr

755c: e1a06c23 lsr r6, r3, #24

7560: e0827107 add r7, r2, r7, lsl #2

7564: e1a05323 lsr r5, r3, #6

7568: e51ee7f8 ldr lr, [lr, #-2040] ; 0xfffff808

756c: e0826106 add r6, r2, r6, lsl #2

7570: e2055fff and r5, r5, #1020 ; 0x3fc

7574: e1a04723 lsr r4, r3, #14

7578: e51777f8 ldr r7, [r7, #-2040] ; 0xfffff808

757c: e0825005 add r5, r2, r5

7580: e2044fff and r4, r4, #1020 ; 0x3fc

7584: e02ee008 eor lr, lr, r8

7588: e51663f8 ldr r6, [r6, #-1016] ; 0xfffffc08

758c: e0824004 add r4, r2, r4

7590: e02ee007 eor lr, lr, r7

7594: e5155ff8 ldr r5, [r5, #-4088] ; 0xfffff008

7598: e02ee006 eor lr, lr, r6

759c: e5144bf8 ldr r4, [r4, #-3064] ; 0xfffff408

75a0: e02ee005 eor lr, lr, r5

75a4: e02ee004 eor lr, lr, r4

75a8: e6ef4079 uxtb r4, r9

75ac: e6ef807e uxtb r8, lr

75b0: e0824104 add r4, r2, r4, lsl #2

75b4: e1a07c2e lsr r7, lr, #24

75b8: e0828108 add r8, r2, r8, lsl #2

75bc: e1a0632e lsr r6, lr, #6

75c0: e5144ff8 ldr r4, [r4, #-4088] ; 0xfffff008

75c4: e0827107 add r7, r2, r7, lsl #2

75c8: e2066fff and r6, r6, #1020 ; 0x3fc

75cc: e1a0572e lsr r5, lr, #14

75d0: e59d9024 ldr r9, [sp, #36] ; 0x24

75d4: e5188bf8 ldr r8, [r8, #-3064] ; 0xfffff408

75d8: e0826006 add r6, r2, r6

75dc: e2055fff and r5, r5, #1020 ; 0x3fc

75e0: e0244009 eor r4, r4, r9

75e4: e51773f8 ldr r7, [r7, #-1016] ; 0xfffffc08

75e8: e0825005 add r5, r2, r5

75ec: e0244008 eor r4, r4, r8

75f0: e51667f8 ldr r6, [r6, #-2040] ; 0xfffff808

75f4: e0244007 eor r4, r4, r7

75f8: e5155ff8 ldr r5, [r5, #-4088] ; 0xfffff008

75fc: e0244006 eor r4, r4, r6

7600: e0244005 eor r4, r4, r5

7604: e58d4008 str r4, [sp, #8]

7608: e58d4014 str r4, [sp, #20]

760c: e58de018 str lr, [sp, #24]

7610: e58d3010 str r3, [sp, #16]

7614: e58dc01c str ip, [sp, #28]

7618: e59d4004 ldr r4, [sp, #4]

761c: e204400c and r4, r4, #12

7620: e58d400c str r4, [sp, #12]

7624: e354000c cmp r4, #12

7628: 908ff104 addls pc, pc, r4, lsl #2

762c: ea000057 b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7630: ea000067 b 77d4 <mcrypt\_mutex\_register@@Base+0x3040>

7634: ea000055 b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7638: ea000054 b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

763c: ea000053 b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7640: ea000007 b 7664 <mcrypt\_mutex\_register@@Base+0x2ed0>

7644: ea000051 b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7648: ea000050 b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

764c: ea00004f b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7650: ea000003 b 7664 <mcrypt\_mutex\_register@@Base+0x2ed0>

7654: ea00004d b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7658: ea00004c b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

765c: ea00004b b 7790 <mcrypt\_mutex\_register@@Base+0x2ffc>

7660: ea00005b b 77d4 <mcrypt\_mutex\_register@@Base+0x3040>

7664: e59d4008 ldr r4, [sp, #8]

7668: e6ef607c uxtb r6, ip

766c: e5dd5008 ldrb r5, [sp, #8]

7670: e1a07c24 lsr r7, r4, #24

7674: e59d4034 ldr r4, [sp, #52] ; 0x34

7678: e1a09c2c lsr r9, ip, #24

767c: e0846106 add r6, r4, r6, lsl #2

7680: e0847107 add r7, r4, r7, lsl #2

7684: e516a7f8 ldr sl, [r6, #-2040] ; 0xfffff808

7688: e5177bf8 ldr r7, [r7, #-3064] ; 0xfffff408

768c: e0849109 add r9, r4, r9, lsl #2

7690: e027700a eor r7, r7, sl

7694: e1a0a32c lsr sl, ip, #6

7698: e0845105 add r5, r4, r5, lsl #2

769c: e6ef8073 uxtb r8, r3

76a0: e20aafff and sl, sl, #1020 ; 0x3fc

76a4: e084a00a add sl, r4, sl

76a8: e0848108 add r8, r4, r8, lsl #2

76ac: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

76b0: e1a06c2e lsr r6, lr, #24

76b4: e5199ff8 ldr r9, [r9, #-4088] ; 0xfffff008

76b8: e51aaff8 ldr sl, [sl, #-4088] ; 0xfffff008

76bc: e518b7f8 ldr fp, [r8, #-2040] ; 0xfffff808

76c0: e0299005 eor r9, r9, r5

76c4: e0846106 add r6, r4, r6, lsl #2

76c8: e1a08c23 lsr r8, r3, #24

76cc: e6ef507e uxtb r5, lr

76d0: e027700a eor r7, r7, sl

76d4: e59da008 ldr sl, [sp, #8]

76d8: e5166bf8 ldr r6, [r6, #-3064] ; 0xfffff408

76dc: e0848108 add r8, r4, r8, lsl #2

76e0: e0845105 add r5, r4, r5, lsl #2

76e4: e5188ff8 ldr r8, [r8, #-4088] ; 0xfffff008

76e8: e026600b eor r6, r6, fp

76ec: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

76f0: e1a0b72a lsr fp, sl, #14

76f4: e1a0a72c lsr sl, ip, #14

76f8: e20aafff and sl, sl, #1020 ; 0x3fc

76fc: e0255008 eor r5, r5, r8

7700: e084a00a add sl, r4, sl

7704: e1a08323 lsr r8, r3, #6

7708: e2088fff and r8, r8, #1020 ; 0x3fc

770c: e51aa7f8 ldr sl, [sl, #-2040] ; 0xfffff808

7710: e0848008 add r8, r4, r8

7714: e029900a eor r9, r9, sl

7718: e518aff8 ldr sl, [r8, #-4088] ; 0xfffff008

771c: e1a08723 lsr r8, r3, #14

7720: e2088fff and r8, r8, #1020 ; 0x3fc

7724: e0848008 add r8, r4, r8

7728: e026600a eor r6, r6, sl

772c: e518a7f8 ldr sl, [r8, #-2040] ; 0xfffff808

7730: e59d8008 ldr r8, [sp, #8]

7734: e20bbfff and fp, fp, #1020 ; 0x3fc

7738: e084b00b add fp, r4, fp

773c: e1a08328 lsr r8, r8, #6

7740: e2088fff and r8, r8, #1020 ; 0x3fc

7744: e0848008 add r8, r4, r8

7748: e025500a eor r5, r5, sl

774c: e518abf8 ldr sl, [r8, #-3064] ; 0xfffff408

7750: e1a0872e lsr r8, lr, #14

7754: e2088fff and r8, r8, #1020 ; 0x3fc

7758: e51bb3f8 ldr fp, [fp, #-1016] ; 0xfffffc08

775c: e0848008 add r8, r4, r8

7760: e027700b eor r7, r7, fp

7764: e518b3f8 ldr fp, [r8, #-1016] ; 0xfffffc08

7768: e1a0832e lsr r8, lr, #6

776c: e2088fff and r8, r8, #1020 ; 0x3fc

7770: e0848008 add r8, r4, r8

7774: e029900a eor r9, r9, sl

7778: e5188bf8 ldr r8, [r8, #-3064] ; 0xfffff408

777c: e026600b eor r6, r6, fp

7780: e0255008 eor r5, r5, r8

7784: e8800280 stm r0, {r7, r9}

7788: e5806008 str r6, [r0, #8]

778c: e580500c str r5, [r0, #12]

7790: e59d400c ldr r4, [sp, #12]

7794: e354000c cmp r4, #12

7798: 908ff104 addls pc, pc, r4, lsl #2

779c: ea00006b b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77a0: ea0000a2 b 7a30 <mcrypt\_mutex\_register@@Base+0x329c>

77a4: ea000069 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77a8: ea000068 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77ac: ea000067 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77b0: ea0000be b 7ab0 <mcrypt\_mutex\_register@@Base+0x331c>

77b4: ea000065 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77b8: ea000064 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77bc: ea000063 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77c0: ea000079 b 79ac <mcrypt\_mutex\_register@@Base+0x3218>

77c4: ea000061 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77c8: ea000060 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77cc: ea00005f b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

77d0: ea000043 b 78e4 <mcrypt\_mutex\_register@@Base+0x3150>

77d4: e59d4008 ldr r4, [sp, #8]

77d8: e59db020 ldr fp, [sp, #32]

77dc: e1a06c2e lsr r6, lr, #24

77e0: e1a08c24 lsr r8, r4, #24

77e4: e08b6106 add r6, fp, r6, lsl #2

77e8: e6ef907e uxtb r9, lr

77ec: e1a05c23 lsr r5, r3, #24

77f0: e6ef7073 uxtb r7, r3

77f4: e516a7f8 ldr sl, [r6, #-2040] ; 0xfffff808

77f8: e08b9109 add r9, fp, r9, lsl #2

77fc: e08b5105 add r5, fp, r5, lsl #2

7800: e08b8108 add r8, fp, r8, lsl #2

7804: e6ef607c uxtb r6, ip

7808: e08b7107 add r7, fp, r7, lsl #2

780c: e08b6106 add r6, fp, r6, lsl #2

7810: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

7814: e518b7f8 ldr fp, [r8, #-2040] ; 0xfffff808

7818: e5199ff8 ldr r9, [r9, #-4088] ; 0xfffff008

781c: e5dd8008 ldrb r8, [sp, #8]

7820: e5177bf8 ldr r7, [r7, #-3064] ; 0xfffff408

7824: e59d4020 ldr r4, [sp, #32]

7828: e0299005 eor r9, r9, r5

782c: e1a05c2c lsr r5, ip, #24

7830: e027700a eor r7, r7, sl

7834: e1a0a72e lsr sl, lr, #14

7838: e20aafff and sl, sl, #1020 ; 0x3fc

783c: e0848108 add r8, r4, r8, lsl #2

7840: e0845105 add r5, r4, r5, lsl #2

7844: e084a00a add sl, r4, sl

7848: e5188ff8 ldr r8, [r8, #-4088] ; 0xfffff008

784c: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

7850: e51aaff8 ldr sl, [sl, #-4088] ; 0xfffff008

7854: e0255008 eor r5, r5, r8

7858: e59d8008 ldr r8, [sp, #8]

785c: e027700a eor r7, r7, sl

7860: e1a0a32e lsr sl, lr, #6

7864: e20aafff and sl, sl, #1020 ; 0x3fc

7868: e084a00a add sl, r4, sl

786c: e1a08728 lsr r8, r8, #14

7870: e2088fff and r8, r8, #1020 ; 0x3fc

7874: e51aa7f8 ldr sl, [sl, #-2040] ; 0xfffff808

7878: e0848008 add r8, r4, r8

787c: e029900a eor r9, r9, sl

7880: e518aff8 ldr sl, [r8, #-4088] ; 0xfffff008

7884: e59d8008 ldr r8, [sp, #8]

7888: e5166bf8 ldr r6, [r6, #-3064] ; 0xfffff408

788c: e1a08328 lsr r8, r8, #6

7890: e2088fff and r8, r8, #1020 ; 0x3fc

7894: e0848008 add r8, r4, r8

7898: e026600b eor r6, r6, fp

789c: e026600a eor r6, r6, sl

78a0: e518a7f8 ldr sl, [r8, #-2040] ; 0xfffff808

78a4: e1a08723 lsr r8, r3, #14

78a8: e2088fff and r8, r8, #1020 ; 0x3fc

78ac: e0848008 add r8, r4, r8

78b0: e1a0b323 lsr fp, r3, #6

78b4: e20bbfff and fp, fp, #1020 ; 0x3fc

78b8: e084b00b add fp, r4, fp

78bc: e025500a eor r5, r5, sl

78c0: e518abf8 ldr sl, [r8, #-3064] ; 0xfffff408

78c4: e1a0832c lsr r8, ip, #6

78c8: e2088fff and r8, r8, #1020 ; 0x3fc

78cc: e51bb3f8 ldr fp, [fp, #-1016] ; 0xfffffc08

78d0: e0848008 add r8, r4, r8

78d4: e027700b eor r7, r7, fp

78d8: e518b3f8 ldr fp, [r8, #-1016] ; 0xfffffc08

78dc: e1a0872c lsr r8, ip, #14

78e0: eaffffa1 b 776c <mcrypt\_mutex\_register@@Base+0x2fd8>

78e4: e59d4014 ldr r4, [sp, #20]

78e8: e59d5018 ldr r5, [sp, #24]

78ec: e59d8038 ldr r8, [sp, #56] ; 0x38

78f0: e1a04724 lsr r4, r4, #14

78f4: e5dd701c ldrb r7, [sp, #28]

78f8: e5dd6010 ldrb r6, [sp, #16]

78fc: e2044fff and r4, r4, #1020 ; 0x3fc

7900: e0884004 add r4, r8, r4

7904: e1a05c25 lsr r5, r5, #24

7908: e0887107 add r7, r8, r7, lsl #2

790c: e0886106 add r6, r8, r6, lsl #2

7910: e0885105 add r5, r8, r5, lsl #2

7914: e51443f8 ldr r4, [r4, #-1016] ; 0xfffffc08

7918: e590800c ldr r8, [r0, #12]

791c: e51777f8 ldr r7, [r7, #-2040] ; 0xfffff808

7920: e0244008 eor r4, r4, r8

7924: e580400c str r4, [r0, #12]

7928: e8900110 ldm r0, {r4, r8}

792c: e5166ff8 ldr r6, [r6, #-4088] ; 0xfffff008

7930: e5155bf8 ldr r5, [r5, #-3064] ; 0xfffff408

7934: e0277004 eor r7, r7, r4

7938: e5904008 ldr r4, [r0, #8]

793c: e0266008 eor r6, r6, r8

7940: e0255004 eor r5, r5, r4

7944: e5807000 str r7, [r0]

7948: e5806004 str r6, [r0, #4]

794c: e5805008 str r5, [r0, #8]

7950: e59d4004 ldr r4, [sp, #4]

7954: e354000f cmp r4, #15

7958: 9a00000a bls 7988 <mcrypt\_mutex\_register@@Base+0x31f4>

795c: e8900050 ldm r0, {r4, r6}

7960: e5905008 ldr r5, [r0, #8]

7964: e204401f and r4, r4, #31

7968: e5804000 str r4, [r0]

796c: e590400c ldr r4, [r0, #12]

7970: e206601f and r6, r6, #31

7974: e205501f and r5, r5, #31

7978: e204401f and r4, r4, #31

797c: e5806004 str r6, [r0, #4]

7980: e5805008 str r5, [r0, #8]

7984: e580400c str r4, [r0, #12]

7988: e59d4004 ldr r4, [sp, #4]

798c: e2800010 add r0, r0, #16

7990: e2844004 add r4, r4, #4

7994: e3540020 cmp r4, #32

7998: e58d4004 str r4, [sp, #4]

799c: 1afffeb5 bne 7478 <mcrypt\_mutex\_register@@Base+0x2ce4>

79a0: e3a00000 mov r0, #0

79a4: e28dd05c add sp, sp, #92 ; 0x5c

79a8: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

79ac: e59d4028 ldr r4, [sp, #40] ; 0x28

79b0: e59d602c ldr r6, [sp, #44] ; 0x2c

79b4: e59d803c ldr r8, [sp, #60] ; 0x3c

79b8: e1a05724 lsr r5, r4, #14

79bc: e1a07326 lsr r7, r6, #6

79c0: e59d4030 ldr r4, [sp, #48] ; 0x30

79c4: e59d6024 ldr r6, [sp, #36] ; 0x24

79c8: e2055fff and r5, r5, #1020 ; 0x3fc

79cc: e0885005 add r5, r8, r5

79d0: e1a04324 lsr r4, r4, #6

79d4: e1a06c26 lsr r6, r6, #24

79d8: e2044fff and r4, r4, #1020 ; 0x3fc

79dc: e2077fff and r7, r7, #1020 ; 0x3fc

79e0: e0884004 add r4, r8, r4

79e4: e0887007 add r7, r8, r7

79e8: e0886106 add r6, r8, r6, lsl #2

79ec: e51557f8 ldr r5, [r5, #-2040] ; 0xfffff808

79f0: e5908000 ldr r8, [r0]

79f4: e5144bf8 ldr r4, [r4, #-3064] ; 0xfffff408

79f8: e0255008 eor r5, r5, r8

79fc: e5805000 str r5, [r0]

7a00: e5905008 ldr r5, [r0, #8]

7a04: e590800c ldr r8, [r0, #12]

7a08: e0244005 eor r4, r4, r5

7a0c: e51773f8 ldr r7, [r7, #-1016] ; 0xfffffc08

7a10: e5905004 ldr r5, [r0, #4]

7a14: e5166ff8 ldr r6, [r6, #-4088] ; 0xfffff008

7a18: e0277008 eor r7, r7, r8

7a1c: e0266005 eor r6, r6, r5

7a20: e5804008 str r4, [r0, #8]

7a24: e580700c str r7, [r0, #12]

7a28: e5806004 str r6, [r0, #4]

7a2c: eaffffc7 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

7a30: e59d4030 ldr r4, [sp, #48] ; 0x30

7a34: e59d6028 ldr r6, [sp, #40] ; 0x28

7a38: e59d8044 ldr r8, [sp, #68] ; 0x44

7a3c: e1a05324 lsr r5, r4, #6

7a40: e1a07726 lsr r7, r6, #14

7a44: e59d402c ldr r4, [sp, #44] ; 0x2c

7a48: e59d6024 ldr r6, [sp, #36] ; 0x24

7a4c: e2055fff and r5, r5, #1020 ; 0x3fc

7a50: e0885005 add r5, r8, r5

7a54: e1a04324 lsr r4, r4, #6

7a58: e1a06c26 lsr r6, r6, #24

7a5c: e2044fff and r4, r4, #1020 ; 0x3fc

7a60: e2077fff and r7, r7, #1020 ; 0x3fc

7a64: e0884004 add r4, r8, r4

7a68: e0887007 add r7, r8, r7

7a6c: e0886106 add r6, r8, r6, lsl #2

7a70: e51557f8 ldr r5, [r5, #-2040] ; 0xfffff808

7a74: e5908000 ldr r8, [r0]

7a78: e5144ff8 ldr r4, [r4, #-4088] ; 0xfffff008

7a7c: e0255008 eor r5, r5, r8

7a80: e5805000 str r5, [r0]

7a84: e9900120 ldmib r0, {r5, r8}

7a88: e5177bf8 ldr r7, [r7, #-3064] ; 0xfffff408

7a8c: e51663f8 ldr r6, [r6, #-1016] ; 0xfffffc08

7a90: e0244005 eor r4, r4, r5

7a94: e590500c ldr r5, [r0, #12]

7a98: e0277008 eor r7, r7, r8

7a9c: e0266005 eor r6, r6, r5

7aa0: e5804004 str r4, [r0, #4]

7aa4: e5807008 str r7, [r0, #8]

7aa8: e580600c str r6, [r0, #12]

7aac: eaffffa7 b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

7ab0: e59d5018 ldr r5, [sp, #24]

7ab4: e59d4014 ldr r4, [sp, #20]

7ab8: e59d8040 ldr r8, [sp, #64] ; 0x40

7abc: e1a07c25 lsr r7, r5, #24

7ac0: e1a04724 lsr r4, r4, #14

7ac4: e5dd601c ldrb r6, [sp, #28]

7ac8: e5dd5010 ldrb r5, [sp, #16]

7acc: e2044fff and r4, r4, #1020 ; 0x3fc

7ad0: e0884004 add r4, r8, r4

7ad4: e0887107 add r7, r8, r7, lsl #2

7ad8: e0886106 add r6, r8, r6, lsl #2

7adc: e0885105 add r5, r8, r5, lsl #2

7ae0: e5144ff8 ldr r4, [r4, #-4088] ; 0xfffff008

7ae4: e5908004 ldr r8, [r0, #4]

7ae8: e51777f8 ldr r7, [r7, #-2040] ; 0xfffff808

7aec: e0244008 eor r4, r4, r8

7af0: e5804004 str r4, [r0, #4]

7af4: e5904000 ldr r4, [r0]

7af8: e5908008 ldr r8, [r0, #8]

7afc: e0277004 eor r7, r7, r4

7b00: e5166bf8 ldr r6, [r6, #-3064] ; 0xfffff408

7b04: e590400c ldr r4, [r0, #12]

7b08: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

7b0c: e0266008 eor r6, r6, r8

7b10: e0255004 eor r5, r5, r4

7b14: e5807000 str r7, [r0]

7b18: e5806008 str r6, [r0, #8]

7b1c: e580500c str r5, [r0, #12]

7b20: eaffff8a b 7950 <mcrypt\_mutex\_register@@Base+0x31bc>

7b24: e59d8014 ldr r8, [sp, #20]

7b28: e59d9018 ldr r9, [sp, #24]

7b2c: e6efc078 uxtb ip, r8

7b30: e1a03c28 lsr r3, r8, #24

7b34: e081c10c add ip, r1, ip, lsl #2

7b38: e0813103 add r3, r1, r3, lsl #2

7b3c: e1a05c29 lsr r5, r9, #24

7b40: e1a04728 lsr r4, r8, #14

7b44: e51ccff8 ldr ip, [ip, #-4088] ; 0xfffff008

7b48: e5133bf8 ldr r3, [r3, #-3064] ; 0xfffff408

7b4c: e1a0e328 lsr lr, r8, #6

7b50: e0815105 add r5, r1, r5, lsl #2

7b54: e2044fff and r4, r4, #1020 ; 0x3fc

7b58: e023300c eor r3, r3, ip

7b5c: e0814004 add r4, r1, r4

7b60: e59dc01c ldr ip, [sp, #28]

7b64: e20eefff and lr, lr, #1020 ; 0x3fc

7b68: e5155bf8 ldr r5, [r5, #-3064] ; 0xfffff408

7b6c: e081e00e add lr, r1, lr

7b70: e023300c eor r3, r3, ip

7b74: e51447f8 ldr r4, [r4, #-2040] ; 0xfffff808

7b78: e0233005 eor r3, r3, r5

7b7c: e51ee3f8 ldr lr, [lr, #-1016] ; 0xfffffc08

7b80: e0233004 eor r3, r3, r4

7b84: e023c00e eor ip, r3, lr

7b88: e1a03329 lsr r3, r9, #6

7b8c: e1a06c2c lsr r6, ip, #24

7b90: e2033fff and r3, r3, #1020 ; 0x3fc

7b94: e0813003 add r3, r1, r3

7b98: e6ef507c uxtb r5, ip

7b9c: e0816106 add r6, r1, r6, lsl #2

7ba0: e1a0432c lsr r4, ip, #6

7ba4: e51333f8 ldr r3, [r3, #-1016] ; 0xfffffc08

7ba8: e0815105 add r5, r1, r5, lsl #2

7bac: e2044fff and r4, r4, #1020 ; 0x3fc

7bb0: e1a0e72c lsr lr, ip, #14

7bb4: e51667f8 ldr r6, [r6, #-2040] ; 0xfffff808

7bb8: e0814004 add r4, r1, r4

7bbc: e20eefff and lr, lr, #1020 ; 0x3fc

7bc0: e0233009 eor r3, r3, r9

7bc4: e51553f8 ldr r5, [r5, #-1016] ; 0xfffffc08

7bc8: e081e00e add lr, r1, lr

7bcc: e0233006 eor r3, r3, r6

7bd0: e5144ff8 ldr r4, [r4, #-4088] ; 0xfffff008

7bd4: e0233005 eor r3, r3, r5

7bd8: e51eebf8 ldr lr, [lr, #-3064] ; 0xfffff408

7bdc: e0233004 eor r3, r3, r4

7be0: e023300e eor r3, r3, lr

7be4: e1a0e729 lsr lr, r9, #14

7be8: e6ef7073 uxtb r7, r3

7bec: e20eefff and lr, lr, #1020 ; 0x3fc

7bf0: e081e00e add lr, r1, lr

7bf4: e1a06c23 lsr r6, r3, #24

7bf8: e0817107 add r7, r1, r7, lsl #2

7bfc: e1a05323 lsr r5, r3, #6

7c00: e51ee7f8 ldr lr, [lr, #-2040] ; 0xfffff808

7c04: e0816106 add r6, r1, r6, lsl #2

7c08: e2055fff and r5, r5, #1020 ; 0x3fc

7c0c: e1a04723 lsr r4, r3, #14

7c10: e51777f8 ldr r7, [r7, #-2040] ; 0xfffff808

7c14: e0815005 add r5, r1, r5

7c18: e2044fff and r4, r4, #1020 ; 0x3fc

7c1c: e02ee008 eor lr, lr, r8

7c20: e51663f8 ldr r6, [r6, #-1016] ; 0xfffffc08

7c24: e0814004 add r4, r1, r4

7c28: e02ee007 eor lr, lr, r7

7c2c: e5155ff8 ldr r5, [r5, #-4088] ; 0xfffff008

7c30: e02ee006 eor lr, lr, r6

7c34: e5144bf8 ldr r4, [r4, #-3064] ; 0xfffff408

7c38: e02ee005 eor lr, lr, r5

7c3c: e02ee004 eor lr, lr, r4

7c40: e6ef4079 uxtb r4, r9

7c44: e6ef807e uxtb r8, lr

7c48: e0814104 add r4, r1, r4, lsl #2

7c4c: e1a07c2e lsr r7, lr, #24

7c50: e0818108 add r8, r1, r8, lsl #2

7c54: e1a0632e lsr r6, lr, #6

7c58: e5144ff8 ldr r4, [r4, #-4088] ; 0xfffff008

7c5c: e0817107 add r7, r1, r7, lsl #2

7c60: e2066fff and r6, r6, #1020 ; 0x3fc

7c64: e1a0572e lsr r5, lr, #14

7c68: e59d9010 ldr r9, [sp, #16]

7c6c: e5188bf8 ldr r8, [r8, #-3064] ; 0xfffff408

7c70: e0816006 add r6, r1, r6

7c74: e2055fff and r5, r5, #1020 ; 0x3fc

7c78: e0244009 eor r4, r4, r9

7c7c: e51773f8 ldr r7, [r7, #-1016] ; 0xfffffc08

7c80: e0815005 add r5, r1, r5

7c84: e0244008 eor r4, r4, r8

7c88: e51667f8 ldr r6, [r6, #-2040] ; 0xfffff808

7c8c: e0244007 eor r4, r4, r7

7c90: e5155ff8 ldr r5, [r5, #-4088] ; 0xfffff008

7c94: e0244006 eor r4, r4, r6

7c98: e0244005 eor r4, r4, r5

7c9c: e58d4008 str r4, [sp, #8]

7ca0: e58d4024 str r4, [sp, #36] ; 0x24

7ca4: e58de028 str lr, [sp, #40] ; 0x28

7ca8: e58d302c str r3, [sp, #44] ; 0x2c

7cac: e58dc030 str ip, [sp, #48] ; 0x30

7cb0: eafffe58 b 7618 <mcrypt\_mutex\_register@@Base+0x2e84>

7cb4: 00019240 andeq r9, r1, r0, asr #4

7cb8: 00019230 andeq r9, r1, r0, lsr r2

7cbc: 00019220 andeq r9, r1, r0, lsr #4

7cc0: 00019210 andeq r9, r1, r0, lsl r2

7cc4: 000191f8 strdeq r9, [r1], -r8

7cc8: 000191e0 andeq r9, r1, r0, ror #3

7ccc: 000191d8 ldrdeq r9, [r1], -r8

7cd0: 000191dc ldrdeq r9, [r1], -ip

7cd4: e3a00084 mov r0, #132 ; 0x84

7cd8: e12fff1e bx lr

7cdc: e3a00008 mov r0, #8

7ce0: e12fff1e bx lr

7ce4: e3a00001 mov r0, #1

7ce8: e12fff1e bx lr

7cec: e3a00010 mov r0, #16

7cf0: e12fff1e bx lr

7cf4: e59f3010 ldr r3, [pc, #16] ; 7d0c <mcrypt\_mutex\_register@@Base+0x3578>

7cf8: e3a02003 mov r2, #3

7cfc: e08f3003 add r3, pc, r3

7d00: e5802000 str r2, [r0]

7d04: e2830008 add r0, r3, #8

7d08: e12fff1e bx lr

7d0c: 00018944 andeq r8, r1, r4, asr #18

7d10: e59f0004 ldr r0, [pc, #4] ; 7d1c <mcrypt\_mutex\_register@@Base+0x3588>

7d14: e08f0000 add r0, pc, r0

7d18: e12fff1e bx lr

7d1c: 00018940 andeq r8, r1, r0, asr #18

7d20: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

7d24: e24dd0e8 sub sp, sp, #232 ; 0xe8

7d28: ebffffeb bl 7cdc <mcrypt\_mutex\_register@@Base+0x3548>

7d2c: e1a06000 mov r6, r0

7d30: ebffffed bl 7cec <mcrypt\_mutex\_register@@Base+0x3558>

7d34: e1a01000 mov r1, r0

7d38: e3a00001 mov r0, #1

7d3c: ebffe93c bl 2234 <calloc@plt>

7d40: e2507000 subs r7, r0, #0

7d44: 0a000064 beq 7edc <mcrypt\_mutex\_register@@Base+0x3748>

7d48: e2475001 sub r5, r7, #1

7d4c: e3a0400a mov r4, #10

7d50: e2678001 rsb r8, r7, #1

7d54: ea000004 b 7d6c <mcrypt\_mutex\_register@@Base+0x35d8>

7d58: e2742000 rsbs r2, r4, #0

7d5c: e2844002 add r4, r4, #2

7d60: e6ef2072 uxtb r2, r2

7d64: 52623000 rsbpl r3, r2, #0

7d68: e5e53001 strb r3, [r5, #1]!

7d6c: ebffffde bl 7cec <mcrypt\_mutex\_register@@Base+0x3558>

7d70: e0882005 add r2, r8, r5

7d74: e6ef3074 uxtb r3, r4

7d78: e1500002 cmp r0, r2

7d7c: cafffff5 bgt 7d58 <mcrypt\_mutex\_register@@Base+0x35c4>

7d80: e3560000 cmp r6, #0

7d84: da000039 ble 7e70 <mcrypt\_mutex\_register@@Base+0x36dc>

7d88: e1a0400d mov r4, sp

7d8c: e1a02004 mov r2, r4

7d90: e3a03000 mov r3, #0

7d94: e4c23001 strb r3, [r2], #1

7d98: e2833001 add r3, r3, #1

7d9c: e1560003 cmp r6, r3

7da0: 1afffffb bne 7d94 <mcrypt\_mutex\_register@@Base+0x3600>

7da4: ebffffca bl 7cd4 <mcrypt\_mutex\_register@@Base+0x3540>

7da8: ebffe951 bl 22f4 <malloc@plt>

7dac: e2509000 subs r9, r0, #0

7db0: 0a000049 beq 7edc <mcrypt\_mutex\_register@@Base+0x3748>

7db4: e28d8010 add r8, sp, #16

7db8: e1a01004 mov r1, r4

7dbc: e1a02006 mov r2, r6

7dc0: e1a00008 mov r0, r8

7dc4: ebffe92f bl 2288 <memcpy@plt>

7dc8: ebffffc7 bl 7cec <mcrypt\_mutex\_register@@Base+0x3558>

7dcc: e1a01007 mov r1, r7

7dd0: e28da020 add sl, sp, #32

7dd4: e0886006 add r6, r8, r6

7dd8: e1a05008 mov r5, r8

7ddc: e1a0400a mov r4, sl

7de0: e1a02000 mov r2, r0

7de4: e1a00009 mov r0, r9

7de8: ebfffd5e bl 7368 <mcrypt\_mutex\_register@@Base+0x2bd4>

7dec: e1a00007 mov r0, r7

7df0: ebffe921 bl 227c <free@plt>

7df4: e1a01008 mov r1, r8

7df8: e1a00009 mov r0, r9

7dfc: ebfffa62 bl 678c <mcrypt\_mutex\_register@@Base+0x1ff8>

7e00: e59f70f0 ldr r7, [pc, #240] ; 7ef8 <mcrypt\_mutex\_register@@Base+0x3764>

7e04: e08f7007 add r7, pc, r7

7e08: e4d52001 ldrb r2, [r5], #1

7e0c: e1a00004 mov r0, r4

7e10: e1a01007 mov r1, r7

7e14: ebffe954 bl 236c <sprintf@plt>

7e18: e1550006 cmp r5, r6

7e1c: e2844002 add r4, r4, #2

7e20: 1afffff8 bne 7e08 <mcrypt\_mutex\_register@@Base+0x3674>

7e24: e59f40d0 ldr r4, [pc, #208] ; 7efc <mcrypt\_mutex\_register@@Base+0x3768>

7e28: e1a0000a mov r0, sl

7e2c: e08f4004 add r4, pc, r4

7e30: e1a01004 mov r1, r4

7e34: ebffe904 bl 224c <strcmp@plt>

7e38: e3500000 cmp r0, #0

7e3c: 1a00001c bne 7eb4 <mcrypt\_mutex\_register@@Base+0x3720>

7e40: e1a01008 mov r1, r8

7e44: e1a00009 mov r0, r9

7e48: ebfffbc9 bl 6d74 <mcrypt\_mutex\_register@@Base+0x25e0>

7e4c: e1a00009 mov r0, r9

7e50: ebffe909 bl 227c <free@plt>

7e54: e1a00008 mov r0, r8

7e58: e1a0100d mov r1, sp

7e5c: ebffe8fa bl 224c <strcmp@plt>

7e60: e3500000 cmp r0, #0

7e64: 1a00001e bne 7ee4 <mcrypt\_mutex\_register@@Base+0x3750>

7e68: e28dd0e8 add sp, sp, #232 ; 0xe8

7e6c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

7e70: ebffff97 bl 7cd4 <mcrypt\_mutex\_register@@Base+0x3540>

7e74: ebffe91e bl 22f4 <malloc@plt>

7e78: e2509000 subs r9, r0, #0

7e7c: 0a000016 beq 7edc <mcrypt\_mutex\_register@@Base+0x3748>

7e80: ebffff99 bl 7cec <mcrypt\_mutex\_register@@Base+0x3558>

7e84: e1a01007 mov r1, r7

7e88: e28d8010 add r8, sp, #16

7e8c: e28da020 add sl, sp, #32

7e90: e1a02000 mov r2, r0

7e94: e1a00009 mov r0, r9

7e98: ebfffd32 bl 7368 <mcrypt\_mutex\_register@@Base+0x2bd4>

7e9c: e1a00007 mov r0, r7

7ea0: ebffe8f5 bl 227c <free@plt>

7ea4: e1a01008 mov r1, r8

7ea8: e1a00009 mov r0, r9

7eac: ebfffa36 bl 678c <mcrypt\_mutex\_register@@Base+0x1ff8>

7eb0: eaffffdb b 7e24 <mcrypt\_mutex\_register@@Base+0x3690>

7eb4: e59f0044 ldr r0, [pc, #68] ; 7f00 <mcrypt\_mutex\_register@@Base+0x376c>

7eb8: e08f0000 add r0, pc, r0

7ebc: ebffe909 bl 22e8 <puts@plt>

7ec0: e59f003c ldr r0, [pc, #60] ; 7f04 <mcrypt\_mutex\_register@@Base+0x3770>

7ec4: e1a0200a mov r2, sl

7ec8: e1a01004 mov r1, r4

7ecc: e08f0000 add r0, pc, r0

7ed0: ebffe8e3 bl 2264 <printf@plt>

7ed4: e1a00009 mov r0, r9

7ed8: ebffe8e7 bl 227c <free@plt>

7edc: e3e00000 mvn r0, #0

7ee0: eaffffe0 b 7e68 <mcrypt\_mutex\_register@@Base+0x36d4>

7ee4: e59f001c ldr r0, [pc, #28] ; 7f08 <mcrypt\_mutex\_register@@Base+0x3774>

7ee8: e08f0000 add r0, pc, r0

7eec: ebffe8fd bl 22e8 <puts@plt>

7ef0: e3e00000 mvn r0, #0

7ef4: eaffffdb b 7e68 <mcrypt\_mutex\_register@@Base+0x36d4>

7ef8: 0001885c andeq r8, r1, ip, asr r8

7efc: 0001883c andeq r8, r1, ip, lsr r8

7f00: 000187c4 andeq r8, r1, r4, asr #15

7f04: 000187c8 andeq r8, r1, r8, asr #15

7f08: 000187c4 andeq r8, r1, r4, asr #15

7f0c: e59f0000 ldr r0, [pc] ; 7f14 <mcrypt\_mutex\_register@@Base+0x3780>

7f10: e12fff1e bx lr

7f14: 01315731 teqeq r1, r1, lsr r7

7f18: e59f20e4 ldr r2, [pc, #228] ; 8004 <mcrypt\_mutex\_register@@Base+0x3870>

7f1c: e08f2002 add r2, pc, r2

7f20: e5923000 ldr r3, [r2]

7f24: e3530000 cmp r3, #0

7f28: 112fff1e bxne lr

7f2c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

7f30: e1a00002 mov r0, r2

7f34: e24dd00c sub sp, sp, #12

7f38: e3a03001 mov r3, #1

7f3c: e59fe0c4 ldr lr, [pc, #196] ; 8008 <mcrypt\_mutex\_register@@Base+0x3874>

7f40: e2827c01 add r7, r2, #256 ; 0x100

7f44: e2826c02 add r6, r2, #512 ; 0x200

7f48: e2825c03 add r5, r2, #768 ; 0x300

7f4c: e4803004 str r3, [r0], #4

7f50: e3a01004 mov r1, #4

7f54: e58d0004 str r0, [sp, #4]

7f58: e28000ff add r0, r0, #255 ; 0xff

7f5c: e58d0000 str r0, [sp]

7f60: e08fe00e add lr, pc, lr

7f64: e2877003 add r7, r7, #3

7f68: e2866003 add r6, r6, #3

7f6c: e2855003 add r5, r5, #3

7f70: e2822003 add r2, r2, #3

7f74: e1a0b001 mov fp, r1

7f78: e3a0a00d mov sl, #13

7f7c: e3a09006 mov r9, #6

7f80: e3a08007 mov r8, #7

7f84: e3a0c005 mov ip, #5

7f88: e3a0000e mov r0, #14

7f8c: ea000008 b 7fb4 <mcrypt\_mutex\_register@@Base+0x3820>

7f90: e5d30010 ldrb r0, [r3, #16]

7f94: e5d1c020 ldrb ip, [r1, #32]

7f98: e5d38030 ldrb r8, [r3, #48] ; 0x30

7f9c: e5d19040 ldrb r9, [r1, #64] ; 0x40

7fa0: e5d1a060 ldrb sl, [r1, #96] ; 0x60

7fa4: e5d31050 ldrb r1, [r3, #80] ; 0x50

7fa8: e5d33070 ldrb r3, [r3, #112] ; 0x70

7fac: e7deb242 ldrb fp, [lr, r2, asr #4]

7fb0: e1a02004 mov r2, r4

7fb4: e183a20a orr sl, r3, sl, lsl #4

7fb8: e59d3000 ldr r3, [sp]

7fbc: e2824001 add r4, r2, #1

7fc0: e1530004 cmp r3, r4

7fc4: e59d3004 ldr r3, [sp, #4]

7fc8: e188c20c orr ip, r8, ip, lsl #4

7fcc: e180020b orr r0, r0, fp, lsl #4

7fd0: e2828002 add r8, r2, #2

7fd4: e5c20001 strb r0, [r2, #1]

7fd8: e0482003 sub r2, r8, r3

7fdc: e1811209 orr r1, r1, r9, lsl #4

7fe0: e202300f and r3, r2, #15

7fe4: e5e61001 strb r1, [r6, #1]!

7fe8: e5e7c001 strb ip, [r7, #1]!

7fec: e5e5a001 strb sl, [r5, #1]!

7ff0: e08e3003 add r3, lr, r3

7ff4: e08e1242 add r1, lr, r2, asr #4

7ff8: 1affffe4 bne 7f90 <mcrypt\_mutex\_register@@Base+0x37fc>

7ffc: e28dd00c add sp, sp, #12

8000: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

8004: 0002d5fc strdeq sp, [r2], -ip

8008: 00018760 andeq r8, r1, r0, ror #14

800c: e92d41f0 push {r4, r5, r6, r7, r8, lr}

8010: e1a05000 mov r5, r0

8014: e3a04000 mov r4, #0

8018: e1a06001 mov r6, r1

801c: e1a07002 mov r7, r2

8020: ebffffbc bl 7f18 <mcrypt\_mutex\_register@@Base+0x3784>

8024: e1a02007 mov r2, r7

8028: e1a01006 mov r1, r6

802c: e5854000 str r4, [r5]

8030: e5854004 str r4, [r5, #4]

8034: e5854008 str r4, [r5, #8]

8038: e585400c str r4, [r5, #12]

803c: e5854010 str r4, [r5, #16]

8040: e5854014 str r4, [r5, #20]

8044: e5854018 str r4, [r5, #24]

8048: e585401c str r4, [r5, #28]

804c: e1a00005 mov r0, r5

8050: ebffe886 bl 2270 <memmove@plt>

8054: e1a00004 mov r0, r4

8058: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

805c: e5912000 ldr r2, [r1]

8060: e5903000 ldr r3, [r0]

8064: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

8068: e6bf5f32 rev r5, r2

806c: e24dd00c sub sp, sp, #12

8070: e0832005 add r2, r3, r5

8074: e58d3000 str r3, [sp]

8078: e59f37d4 ldr r3, [pc, #2004] ; 8854 <mcrypt\_mutex\_register@@Base+0x40c0>

807c: e1a0e822 lsr lr, r2, #16

8080: e08f3003 add r3, pc, r3

8084: e1a0c422 lsr ip, r2, #8

8088: e6e3e07e uxtab lr, r3, lr

808c: e0834c22 add r4, r3, r2, lsr #24

8090: e6e32072 uxtab r2, r3, r2

8094: e6e3c07c uxtab ip, r3, ip

8098: e5d44004 ldrb r4, [r4, #4]

809c: e5de8104 ldrb r8, [lr, #260] ; 0x104

80a0: e5d22304 ldrb r2, [r2, #772] ; 0x304

80a4: e5dce204 ldrb lr, [ip, #516] ; 0x204

80a8: e591c004 ldr ip, [r1, #4]

80ac: e1822c04 orr r2, r2, r4, lsl #24

80b0: e1828808 orr r8, r2, r8, lsl #16

80b4: e188240e orr r2, r8, lr, lsl #8

80b8: e6bf8f3c rev r8, ip

80bc: e590c004 ldr ip, [r0, #4]

80c0: e0288ae2 eor r8, r8, r2, ror #21

80c4: e08c2008 add r2, ip, r8

80c8: e58dc004 str ip, [sp, #4]

80cc: e1a04822 lsr r4, r2, #16

80d0: e1a0e422 lsr lr, r2, #8

80d4: e0836c22 add r6, r3, r2, lsr #24

80d8: e6e34074 uxtab r4, r3, r4

80dc: e6e32072 uxtab r2, r3, r2

80e0: e6e3e07e uxtab lr, r3, lr

80e4: e5d67004 ldrb r7, [r6, #4]

80e8: e5d46104 ldrb r6, [r4, #260] ; 0x104

80ec: e5d24304 ldrb r4, [r2, #772] ; 0x304

80f0: e5de2204 ldrb r2, [lr, #516] ; 0x204

80f4: e590c008 ldr ip, [r0, #8]

80f8: e1844c07 orr r4, r4, r7, lsl #24

80fc: e1844806 orr r4, r4, r6, lsl #16

8100: e1842402 orr r2, r4, r2, lsl #8

8104: e590e00c ldr lr, [r0, #12]

8108: e0252ae2 eor r2, r5, r2, ror #21

810c: e08c6002 add r6, ip, r2

8110: e5904010 ldr r4, [r0, #16]

8114: e1a09826 lsr r9, r6, #16

8118: e1a07426 lsr r7, r6, #8

811c: e083ac26 add sl, r3, r6, lsr #24

8120: e6e39079 uxtab r9, r3, r9

8124: e6e36076 uxtab r6, r3, r6

8128: e6e37077 uxtab r7, r3, r7

812c: e5daa004 ldrb sl, [sl, #4]

8130: e5d66304 ldrb r6, [r6, #772] ; 0x304

8134: e5d99104 ldrb r9, [r9, #260] ; 0x104

8138: e5d77204 ldrb r7, [r7, #516] ; 0x204

813c: e1866c0a orr r6, r6, sl, lsl #24

8140: e5905014 ldr r5, [r0, #20]

8144: e1869809 orr r9, r6, r9, lsl #16

8148: e1897407 orr r7, r9, r7, lsl #8

814c: e0288ae7 eor r8, r8, r7, ror #21

8150: e08e6008 add r6, lr, r8

8154: e1a09826 lsr r9, r6, #16

8158: e1a07426 lsr r7, r6, #8

815c: e083ac26 add sl, r3, r6, lsr #24

8160: e6e39079 uxtab r9, r3, r9

8164: e6e36076 uxtab r6, r3, r6

8168: e6e37077 uxtab r7, r3, r7

816c: e5daa004 ldrb sl, [sl, #4]

8170: e5d66304 ldrb r6, [r6, #772] ; 0x304

8174: e5d99104 ldrb r9, [r9, #260] ; 0x104

8178: e5d77204 ldrb r7, [r7, #516] ; 0x204

817c: e1866c0a orr r6, r6, sl, lsl #24

8180: e1869809 orr r9, r6, r9, lsl #16

8184: e1897407 orr r7, r9, r7, lsl #8

8188: e0222ae7 eor r2, r2, r7, ror #21

818c: e0846002 add r6, r4, r2

8190: e1a09826 lsr r9, r6, #16

8194: e1a07426 lsr r7, r6, #8

8198: e083ac26 add sl, r3, r6, lsr #24

819c: e6e39079 uxtab r9, r3, r9

81a0: e6e36076 uxtab r6, r3, r6

81a4: e6e37077 uxtab r7, r3, r7

81a8: e5daa004 ldrb sl, [sl, #4]

81ac: e5d66304 ldrb r6, [r6, #772] ; 0x304

81b0: e5d99104 ldrb r9, [r9, #260] ; 0x104

81b4: e5d77204 ldrb r7, [r7, #516] ; 0x204

81b8: e1866c0a orr r6, r6, sl, lsl #24

81bc: e1869809 orr r9, r6, r9, lsl #16

81c0: e1897407 orr r7, r9, r7, lsl #8

81c4: e0288ae7 eor r8, r8, r7, ror #21

81c8: e0857008 add r7, r5, r8

81cc: e1a0a827 lsr sl, r7, #16

81d0: e1a09427 lsr r9, r7, #8

81d4: e0836c27 add r6, r3, r7, lsr #24

81d8: e6e3a07a uxtab sl, r3, sl

81dc: e6e37077 uxtab r7, r3, r7

81e0: e6e39079 uxtab r9, r3, r9

81e4: e5d66004 ldrb r6, [r6, #4]

81e8: e5d77304 ldrb r7, [r7, #772] ; 0x304

81ec: e5daa104 ldrb sl, [sl, #260] ; 0x104

81f0: e5d99204 ldrb r9, [r9, #516] ; 0x204

81f4: e1877c06 orr r7, r7, r6, lsl #24

81f8: e5906018 ldr r6, [r0, #24]

81fc: e187780a orr r7, r7, sl, lsl #16

8200: e1879409 orr r9, r7, r9, lsl #8

8204: e590701c ldr r7, [r0, #28]

8208: e0222ae9 eor r2, r2, r9, ror #21

820c: e0869002 add r9, r6, r2

8210: e1a00829 lsr r0, r9, #16

8214: e083bc29 add fp, r3, r9, lsr #24

8218: e6e3a079 uxtab sl, r3, r9

821c: e1a09429 lsr r9, r9, #8

8220: e6e30070 uxtab r0, r3, r0

8224: e6e39079 uxtab r9, r3, r9

8228: e5dbb004 ldrb fp, [fp, #4]

822c: e5daa304 ldrb sl, [sl, #772] ; 0x304

8230: e5d00104 ldrb r0, [r0, #260] ; 0x104

8234: e5d99204 ldrb r9, [r9, #516] ; 0x204

8238: e18aac0b orr sl, sl, fp, lsl #24

823c: e18a0800 orr r0, sl, r0, lsl #16

8240: e1800409 orr r0, r0, r9, lsl #8

8244: e0280ae0 eor r0, r8, r0, ror #21

8248: e0878000 add r8, r7, r0

824c: e1a0a828 lsr sl, r8, #16

8250: e083bc28 add fp, r3, r8, lsr #24

8254: e6e39078 uxtab r9, r3, r8

8258: e1a08428 lsr r8, r8, #8

825c: e6e3a07a uxtab sl, r3, sl

8260: e6e38078 uxtab r8, r3, r8

8264: e5dbb004 ldrb fp, [fp, #4]

8268: e5daa104 ldrb sl, [sl, #260] ; 0x104

826c: e5d99304 ldrb r9, [r9, #772] ; 0x304

8270: e5d88204 ldrb r8, [r8, #516] ; 0x204

8274: e1899c0b orr r9, r9, fp, lsl #24

8278: e189980a orr r9, r9, sl, lsl #16

827c: e1898408 orr r8, r9, r8, lsl #8

8280: e0222ae8 eor r2, r2, r8, ror #21

8284: e59d8000 ldr r8, [sp]

8288: e0888002 add r8, r8, r2

828c: e1a0a828 lsr sl, r8, #16

8290: e083bc28 add fp, r3, r8, lsr #24

8294: e6e39078 uxtab r9, r3, r8

8298: e1a08428 lsr r8, r8, #8

829c: e6e3a07a uxtab sl, r3, sl

82a0: e6e38078 uxtab r8, r3, r8

82a4: e5dbb004 ldrb fp, [fp, #4]

82a8: e5daa104 ldrb sl, [sl, #260] ; 0x104

82ac: e5d99304 ldrb r9, [r9, #772] ; 0x304

82b0: e5d88204 ldrb r8, [r8, #516] ; 0x204

82b4: e1899c0b orr r9, r9, fp, lsl #24

82b8: e189980a orr r9, r9, sl, lsl #16

82bc: e1898408 orr r8, r9, r8, lsl #8

82c0: e59d9004 ldr r9, [sp, #4]

82c4: e0200ae8 eor r0, r0, r8, ror #21

82c8: e0898000 add r8, r9, r0

82cc: e1a0a828 lsr sl, r8, #16

82d0: e083bc28 add fp, r3, r8, lsr #24

82d4: e6e39078 uxtab r9, r3, r8

82d8: e1a08428 lsr r8, r8, #8

82dc: e6e3a07a uxtab sl, r3, sl

82e0: e6e38078 uxtab r8, r3, r8

82e4: e5dbb004 ldrb fp, [fp, #4]

82e8: e5daa104 ldrb sl, [sl, #260] ; 0x104

82ec: e5d99304 ldrb r9, [r9, #772] ; 0x304

82f0: e5d88204 ldrb r8, [r8, #516] ; 0x204

82f4: e1899c0b orr r9, r9, fp, lsl #24

82f8: e189980a orr r9, r9, sl, lsl #16

82fc: e1898408 orr r8, r9, r8, lsl #8

8300: e0222ae8 eor r2, r2, r8, ror #21

8304: e08c8002 add r8, ip, r2

8308: e1a0a828 lsr sl, r8, #16

830c: e083bc28 add fp, r3, r8, lsr #24

8310: e6e39078 uxtab r9, r3, r8

8314: e1a08428 lsr r8, r8, #8

8318: e6e3a07a uxtab sl, r3, sl

831c: e6e38078 uxtab r8, r3, r8

8320: e5dbb004 ldrb fp, [fp, #4]

8324: e5daa104 ldrb sl, [sl, #260] ; 0x104

8328: e5d99304 ldrb r9, [r9, #772] ; 0x304

832c: e5d88204 ldrb r8, [r8, #516] ; 0x204

8330: e1899c0b orr r9, r9, fp, lsl #24

8334: e189980a orr r9, r9, sl, lsl #16

8338: e1898408 orr r8, r9, r8, lsl #8

833c: e0200ae8 eor r0, r0, r8, ror #21

8340: e08e8000 add r8, lr, r0

8344: e1a0a828 lsr sl, r8, #16

8348: e083bc28 add fp, r3, r8, lsr #24

834c: e6e39078 uxtab r9, r3, r8

8350: e1a08428 lsr r8, r8, #8

8354: e6e3a07a uxtab sl, r3, sl

8358: e6e38078 uxtab r8, r3, r8

835c: e5dbb004 ldrb fp, [fp, #4]

8360: e5daa104 ldrb sl, [sl, #260] ; 0x104

8364: e5d99304 ldrb r9, [r9, #772] ; 0x304

8368: e5d88204 ldrb r8, [r8, #516] ; 0x204

836c: e1899c0b orr r9, r9, fp, lsl #24

8370: e189980a orr r9, r9, sl, lsl #16

8374: e1898408 orr r8, r9, r8, lsl #8

8378: e0222ae8 eor r2, r2, r8, ror #21

837c: e0848002 add r8, r4, r2

8380: e1a0a828 lsr sl, r8, #16

8384: e083bc28 add fp, r3, r8, lsr #24

8388: e6e39078 uxtab r9, r3, r8

838c: e1a08428 lsr r8, r8, #8

8390: e6e3a07a uxtab sl, r3, sl

8394: e6e38078 uxtab r8, r3, r8

8398: e5dbb004 ldrb fp, [fp, #4]

839c: e5d99304 ldrb r9, [r9, #772] ; 0x304

83a0: e5daa104 ldrb sl, [sl, #260] ; 0x104

83a4: e5d88204 ldrb r8, [r8, #516] ; 0x204

83a8: e1899c0b orr r9, r9, fp, lsl #24

83ac: e189980a orr r9, r9, sl, lsl #16

83b0: e1898408 orr r8, r9, r8, lsl #8

83b4: e0200ae8 eor r0, r0, r8, ror #21

83b8: e0858000 add r8, r5, r0

83bc: e1a0a828 lsr sl, r8, #16

83c0: e083bc28 add fp, r3, r8, lsr #24

83c4: e6e39078 uxtab r9, r3, r8

83c8: e1a08428 lsr r8, r8, #8

83cc: e6e3a07a uxtab sl, r3, sl

83d0: e6e38078 uxtab r8, r3, r8

83d4: e5dbb004 ldrb fp, [fp, #4]

83d8: e5daa104 ldrb sl, [sl, #260] ; 0x104

83dc: e5d99304 ldrb r9, [r9, #772] ; 0x304

83e0: e5d88204 ldrb r8, [r8, #516] ; 0x204

83e4: e1899c0b orr r9, r9, fp, lsl #24

83e8: e189980a orr r9, r9, sl, lsl #16

83ec: e1898408 orr r8, r9, r8, lsl #8

83f0: e0222ae8 eor r2, r2, r8, ror #21

83f4: e0868002 add r8, r6, r2

83f8: e1a0a828 lsr sl, r8, #16

83fc: e083bc28 add fp, r3, r8, lsr #24

8400: e6e39078 uxtab r9, r3, r8

8404: e1a08428 lsr r8, r8, #8

8408: e6e3a07a uxtab sl, r3, sl

840c: e6e38078 uxtab r8, r3, r8

8410: e5dbb004 ldrb fp, [fp, #4]

8414: e5daa104 ldrb sl, [sl, #260] ; 0x104

8418: e5d99304 ldrb r9, [r9, #772] ; 0x304

841c: e5d88204 ldrb r8, [r8, #516] ; 0x204

8420: e1899c0b orr r9, r9, fp, lsl #24

8424: e189980a orr r9, r9, sl, lsl #16

8428: e1898408 orr r8, r9, r8, lsl #8

842c: e0200ae8 eor r0, r0, r8, ror #21

8430: e0878000 add r8, r7, r0

8434: e1a0a828 lsr sl, r8, #16

8438: e083bc28 add fp, r3, r8, lsr #24

843c: e6e39078 uxtab r9, r3, r8

8440: e1a08428 lsr r8, r8, #8

8444: e6e3a07a uxtab sl, r3, sl

8448: e6e38078 uxtab r8, r3, r8

844c: e5dbb004 ldrb fp, [fp, #4]

8450: e5daa104 ldrb sl, [sl, #260] ; 0x104

8454: e5d99304 ldrb r9, [r9, #772] ; 0x304

8458: e5d88204 ldrb r8, [r8, #516] ; 0x204

845c: e1899c0b orr r9, r9, fp, lsl #24

8460: e189980a orr r9, r9, sl, lsl #16

8464: e1898408 orr r8, r9, r8, lsl #8

8468: e0222ae8 eor r2, r2, r8, ror #21

846c: e59d8000 ldr r8, [sp]

8470: e0888002 add r8, r8, r2

8474: e1a0a828 lsr sl, r8, #16

8478: e083bc28 add fp, r3, r8, lsr #24

847c: e6e39078 uxtab r9, r3, r8

8480: e1a08428 lsr r8, r8, #8

8484: e6e3a07a uxtab sl, r3, sl

8488: e6e38078 uxtab r8, r3, r8

848c: e5dbb004 ldrb fp, [fp, #4]

8490: e5daa104 ldrb sl, [sl, #260] ; 0x104

8494: e5d99304 ldrb r9, [r9, #772] ; 0x304

8498: e5d88204 ldrb r8, [r8, #516] ; 0x204

849c: e1899c0b orr r9, r9, fp, lsl #24

84a0: e189980a orr r9, r9, sl, lsl #16

84a4: e1898408 orr r8, r9, r8, lsl #8

84a8: e59d9004 ldr r9, [sp, #4]

84ac: e0200ae8 eor r0, r0, r8, ror #21

84b0: e0898000 add r8, r9, r0

84b4: e1a0a828 lsr sl, r8, #16

84b8: e083bc28 add fp, r3, r8, lsr #24

84bc: e6e39078 uxtab r9, r3, r8

84c0: e1a08428 lsr r8, r8, #8

84c4: e6e3a07a uxtab sl, r3, sl

84c8: e6e38078 uxtab r8, r3, r8

84cc: e5dbb004 ldrb fp, [fp, #4]

84d0: e5daa104 ldrb sl, [sl, #260] ; 0x104

84d4: e5d99304 ldrb r9, [r9, #772] ; 0x304

84d8: e5d88204 ldrb r8, [r8, #516] ; 0x204

84dc: e1899c0b orr r9, r9, fp, lsl #24

84e0: e189980a orr r9, r9, sl, lsl #16

84e4: e1898408 orr r8, r9, r8, lsl #8

84e8: e0222ae8 eor r2, r2, r8, ror #21

84ec: e08c8002 add r8, ip, r2

84f0: e1a0a828 lsr sl, r8, #16

84f4: e083bc28 add fp, r3, r8, lsr #24

84f8: e6e39078 uxtab r9, r3, r8

84fc: e1a08428 lsr r8, r8, #8

8500: e6e3a07a uxtab sl, r3, sl

8504: e6e38078 uxtab r8, r3, r8

8508: e5dbb004 ldrb fp, [fp, #4]

850c: e5daa104 ldrb sl, [sl, #260] ; 0x104

8510: e5d99304 ldrb r9, [r9, #772] ; 0x304

8514: e5d88204 ldrb r8, [r8, #516] ; 0x204

8518: e1899c0b orr r9, r9, fp, lsl #24

851c: e189980a orr r9, r9, sl, lsl #16

8520: e1898408 orr r8, r9, r8, lsl #8

8524: e0200ae8 eor r0, r0, r8, ror #21

8528: e08e8000 add r8, lr, r0

852c: e1a0a828 lsr sl, r8, #16

8530: e083bc28 add fp, r3, r8, lsr #24

8534: e6e39078 uxtab r9, r3, r8

8538: e1a08428 lsr r8, r8, #8

853c: e6e3a07a uxtab sl, r3, sl

8540: e6e38078 uxtab r8, r3, r8

8544: e5dbb004 ldrb fp, [fp, #4]

8548: e5daa104 ldrb sl, [sl, #260] ; 0x104

854c: e5d99304 ldrb r9, [r9, #772] ; 0x304

8550: e5d88204 ldrb r8, [r8, #516] ; 0x204

8554: e1899c0b orr r9, r9, fp, lsl #24

8558: e189980a orr r9, r9, sl, lsl #16

855c: e1898408 orr r8, r9, r8, lsl #8

8560: e0222ae8 eor r2, r2, r8, ror #21

8564: e0848002 add r8, r4, r2

8568: e1a0a828 lsr sl, r8, #16

856c: e083bc28 add fp, r3, r8, lsr #24

8570: e6e39078 uxtab r9, r3, r8

8574: e1a08428 lsr r8, r8, #8

8578: e6e3a07a uxtab sl, r3, sl

857c: e6e38078 uxtab r8, r3, r8

8580: e5dbb004 ldrb fp, [fp, #4]

8584: e5daa104 ldrb sl, [sl, #260] ; 0x104

8588: e5d99304 ldrb r9, [r9, #772] ; 0x304

858c: e5d88204 ldrb r8, [r8, #516] ; 0x204

8590: e1899c0b orr r9, r9, fp, lsl #24

8594: e189980a orr r9, r9, sl, lsl #16

8598: e1898408 orr r8, r9, r8, lsl #8

859c: e0200ae8 eor r0, r0, r8, ror #21

85a0: e0858000 add r8, r5, r0

85a4: e1a0a828 lsr sl, r8, #16

85a8: e083bc28 add fp, r3, r8, lsr #24

85ac: e6e39078 uxtab r9, r3, r8

85b0: e1a08428 lsr r8, r8, #8

85b4: e6e3a07a uxtab sl, r3, sl

85b8: e6e38078 uxtab r8, r3, r8

85bc: e5dbb004 ldrb fp, [fp, #4]

85c0: e5daa104 ldrb sl, [sl, #260] ; 0x104

85c4: e5d99304 ldrb r9, [r9, #772] ; 0x304

85c8: e5d88204 ldrb r8, [r8, #516] ; 0x204

85cc: e1899c0b orr r9, r9, fp, lsl #24

85d0: e189980a orr r9, r9, sl, lsl #16

85d4: e1898408 orr r8, r9, r8, lsl #8

85d8: e0222ae8 eor r2, r2, r8, ror #21

85dc: e0868002 add r8, r6, r2

85e0: e1a0a828 lsr sl, r8, #16

85e4: e083bc28 add fp, r3, r8, lsr #24

85e8: e6e39078 uxtab r9, r3, r8

85ec: e1a08428 lsr r8, r8, #8

85f0: e6e3a07a uxtab sl, r3, sl

85f4: e6e38078 uxtab r8, r3, r8

85f8: e5dbb004 ldrb fp, [fp, #4]

85fc: e5daa104 ldrb sl, [sl, #260] ; 0x104

8600: e5d99304 ldrb r9, [r9, #772] ; 0x304

8604: e5d88204 ldrb r8, [r8, #516] ; 0x204

8608: e1899c0b orr r9, r9, fp, lsl #24

860c: e189980a orr r9, r9, sl, lsl #16

8610: e1898408 orr r8, r9, r8, lsl #8

8614: e0200ae8 eor r0, r0, r8, ror #21

8618: e0879000 add r9, r7, r0

861c: e1a0a829 lsr sl, r9, #16

8620: e083bc29 add fp, r3, r9, lsr #24

8624: e6e38079 uxtab r8, r3, r9

8628: e1a09429 lsr r9, r9, #8

862c: e6e3a07a uxtab sl, r3, sl

8630: e6e39079 uxtab r9, r3, r9

8634: e5dbb004 ldrb fp, [fp, #4]

8638: e5daa104 ldrb sl, [sl, #260] ; 0x104

863c: e5d88304 ldrb r8, [r8, #772] ; 0x304

8640: e5d99204 ldrb r9, [r9, #516] ; 0x204

8644: e1888c0b orr r8, r8, fp, lsl #24

8648: e188880a orr r8, r8, sl, lsl #16

864c: e1889409 orr r9, r8, r9, lsl #8

8650: e0222ae9 eor r2, r2, r9, ror #21

8654: e0877002 add r7, r7, r2

8658: e1a09827 lsr r9, r7, #16

865c: e1a08427 lsr r8, r7, #8

8660: e083ac27 add sl, r3, r7, lsr #24

8664: e6e39079 uxtab r9, r3, r9

8668: e6e37077 uxtab r7, r3, r7

866c: e6e38078 uxtab r8, r3, r8

8670: e5daa004 ldrb sl, [sl, #4]

8674: e5d77304 ldrb r7, [r7, #772] ; 0x304

8678: e5d99104 ldrb r9, [r9, #260] ; 0x104

867c: e5d88204 ldrb r8, [r8, #516] ; 0x204

8680: e187ac0a orr sl, r7, sl, lsl #24

8684: e18a9809 orr r9, sl, r9, lsl #16

8688: e1898408 orr r8, r9, r8, lsl #8

868c: e0200ae8 eor r0, r0, r8, ror #21

8690: e0866000 add r6, r6, r0

8694: e1a08826 lsr r8, r6, #16

8698: e1a07426 lsr r7, r6, #8

869c: e0839c26 add r9, r3, r6, lsr #24

86a0: e6e38078 uxtab r8, r3, r8

86a4: e6e36076 uxtab r6, r3, r6

86a8: e6e37077 uxtab r7, r3, r7

86ac: e5d9a004 ldrb sl, [r9, #4]

86b0: e5d66304 ldrb r6, [r6, #772] ; 0x304

86b4: e5d88104 ldrb r8, [r8, #260] ; 0x104

86b8: e5d77204 ldrb r7, [r7, #516] ; 0x204

86bc: e1869c0a orr r9, r6, sl, lsl #24

86c0: e1899808 orr r9, r9, r8, lsl #16

86c4: e1899407 orr r9, r9, r7, lsl #8

86c8: e0222ae9 eor r2, r2, r9, ror #21

86cc: e0855002 add r5, r5, r2

86d0: e1a06825 lsr r6, r5, #16

86d4: e1a09425 lsr r9, r5, #8

86d8: e0837c25 add r7, r3, r5, lsr #24

86dc: e6e36076 uxtab r6, r3, r6

86e0: e6e35075 uxtab r5, r3, r5

86e4: e6e39079 uxtab r9, r3, r9

86e8: e5d77004 ldrb r7, [r7, #4]

86ec: e5d58304 ldrb r8, [r5, #772] ; 0x304

86f0: e5d66104 ldrb r6, [r6, #260] ; 0x104

86f4: e5d95204 ldrb r5, [r9, #516] ; 0x204

86f8: e1888c07 orr r8, r8, r7, lsl #24

86fc: e1888806 orr r8, r8, r6, lsl #16

8700: e1888405 orr r8, r8, r5, lsl #8

8704: e0200ae8 eor r0, r0, r8, ror #21

8708: e0844000 add r4, r4, r0

870c: e1a06824 lsr r6, r4, #16

8710: e1a05424 lsr r5, r4, #8

8714: e0837c24 add r7, r3, r4, lsr #24

8718: e6e36076 uxtab r6, r3, r6

871c: e6e34074 uxtab r4, r3, r4

8720: e6e35075 uxtab r5, r3, r5

8724: e5d77004 ldrb r7, [r7, #4]

8728: e5d66104 ldrb r6, [r6, #260] ; 0x104

872c: e5d49304 ldrb r9, [r4, #772] ; 0x304

8730: e5d54204 ldrb r4, [r5, #516] ; 0x204

8734: e1899c07 orr r9, r9, r7, lsl #24

8738: e1899806 orr r9, r9, r6, lsl #16

873c: e1899404 orr r9, r9, r4, lsl #8

8740: e0222ae9 eor r2, r2, r9, ror #21

8744: e08ee002 add lr, lr, r2

8748: e59d9004 ldr r9, [sp, #4]

874c: e1a0582e lsr r5, lr, #16

8750: e1a0442e lsr r4, lr, #8

8754: e0836c2e add r6, r3, lr, lsr #24

8758: e6e35075 uxtab r5, r3, r5

875c: e6e3e07e uxtab lr, r3, lr

8760: e6e34074 uxtab r4, r3, r4

8764: e5d66004 ldrb r6, [r6, #4]

8768: e5de8304 ldrb r8, [lr, #772] ; 0x304

876c: e5d55104 ldrb r5, [r5, #260] ; 0x104

8770: e5d4e204 ldrb lr, [r4, #516] ; 0x204

8774: e1888c06 orr r8, r8, r6, lsl #24

8778: e1888805 orr r8, r8, r5, lsl #16

877c: e188840e orr r8, r8, lr, lsl #8

8780: e0200ae8 eor r0, r0, r8, ror #21

8784: e08cc000 add ip, ip, r0

8788: e59d8000 ldr r8, [sp]

878c: e1a0482c lsr r4, ip, #16

8790: e1a0e42c lsr lr, ip, #8

8794: e0835c2c add r5, r3, ip, lsr #24

8798: e6e34074 uxtab r4, r3, r4

879c: e6e3c07c uxtab ip, r3, ip

87a0: e6e3e07e uxtab lr, r3, lr

87a4: e5d55004 ldrb r5, [r5, #4]

87a8: e5d44104 ldrb r4, [r4, #260] ; 0x104

87ac: e5dcc304 ldrb ip, [ip, #772] ; 0x304

87b0: e5dee204 ldrb lr, [lr, #516] ; 0x204

87b4: e18ccc05 orr ip, ip, r5, lsl #24

87b8: e18cc804 orr ip, ip, r4, lsl #16

87bc: e18cc40e orr ip, ip, lr, lsl #8

87c0: e0222aec eor r2, r2, ip, ror #21

87c4: e089c002 add ip, r9, r2

87c8: e1a0482c lsr r4, ip, #16

87cc: e1a0e42c lsr lr, ip, #8

87d0: e0835c2c add r5, r3, ip, lsr #24

87d4: e6e34074 uxtab r4, r3, r4

87d8: e6e3c07c uxtab ip, r3, ip

87dc: e6e3e07e uxtab lr, r3, lr

87e0: e5d55004 ldrb r5, [r5, #4]

87e4: e5d44104 ldrb r4, [r4, #260] ; 0x104

87e8: e5dcc304 ldrb ip, [ip, #772] ; 0x304

87ec: e5dee204 ldrb lr, [lr, #516] ; 0x204

87f0: e18ccc05 orr ip, ip, r5, lsl #24

87f4: e18cc804 orr ip, ip, r4, lsl #16

87f8: e18cc40e orr ip, ip, lr, lsl #8

87fc: e0200aec eor r0, r0, ip, ror #21

8800: e088c000 add ip, r8, r0

8804: e6bf0f30 rev r0, r0

8808: e1a0482c lsr r4, ip, #16

880c: e1a0e42c lsr lr, ip, #8

8810: e0835c2c add r5, r3, ip, lsr #24

8814: e6e34074 uxtab r4, r3, r4

8818: e6e3c07c uxtab ip, r3, ip

881c: e6e3e07e uxtab lr, r3, lr

8820: e5d55004 ldrb r5, [r5, #4]

8824: e5dc3304 ldrb r3, [ip, #772] ; 0x304

8828: e5d44104 ldrb r4, [r4, #260] ; 0x104

882c: e5dec204 ldrb ip, [lr, #516] ; 0x204

8830: e1833c05 orr r3, r3, r5, lsl #24

8834: e5810000 str r0, [r1]

8838: e1833804 orr r3, r3, r4, lsl #16

883c: e183340c orr r3, r3, ip, lsl #8

8840: e0222ae3 eor r2, r2, r3, ror #21

8844: e6bf2f32 rev r2, r2

8848: e5812004 str r2, [r1, #4]

884c: e28dd00c add sp, sp, #12

8850: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

8854: 0002d498 muleq r2, r8, r4

8858: e5912000 ldr r2, [r1]

885c: e5903000 ldr r3, [r0]

8860: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

8864: e6bf5f32 rev r5, r2

8868: e24dd00c sub sp, sp, #12

886c: e0832005 add r2, r3, r5

8870: e58d3000 str r3, [sp]

8874: e59f37d4 ldr r3, [pc, #2004] ; 9050 <mcrypt\_mutex\_register@@Base+0x48bc>

8878: e1a0e822 lsr lr, r2, #16

887c: e08f3003 add r3, pc, r3

8880: e1a0c422 lsr ip, r2, #8

8884: e6e3e07e uxtab lr, r3, lr

8888: e0834c22 add r4, r3, r2, lsr #24

888c: e6e32072 uxtab r2, r3, r2

8890: e6e3c07c uxtab ip, r3, ip

8894: e5d44004 ldrb r4, [r4, #4]

8898: e5de8104 ldrb r8, [lr, #260] ; 0x104

889c: e5d22304 ldrb r2, [r2, #772] ; 0x304

88a0: e5dce204 ldrb lr, [ip, #516] ; 0x204

88a4: e591c004 ldr ip, [r1, #4]

88a8: e1822c04 orr r2, r2, r4, lsl #24

88ac: e1828808 orr r8, r2, r8, lsl #16

88b0: e188240e orr r2, r8, lr, lsl #8

88b4: e6bf8f3c rev r8, ip

88b8: e590c004 ldr ip, [r0, #4]

88bc: e0288ae2 eor r8, r8, r2, ror #21

88c0: e08c2008 add r2, ip, r8

88c4: e58dc004 str ip, [sp, #4]

88c8: e1a04822 lsr r4, r2, #16

88cc: e1a0e422 lsr lr, r2, #8

88d0: e0836c22 add r6, r3, r2, lsr #24

88d4: e6e34074 uxtab r4, r3, r4

88d8: e6e32072 uxtab r2, r3, r2

88dc: e6e3e07e uxtab lr, r3, lr

88e0: e5d67004 ldrb r7, [r6, #4]

88e4: e5d46104 ldrb r6, [r4, #260] ; 0x104

88e8: e5d24304 ldrb r4, [r2, #772] ; 0x304

88ec: e5de2204 ldrb r2, [lr, #516] ; 0x204

88f0: e590c008 ldr ip, [r0, #8]

88f4: e1844c07 orr r4, r4, r7, lsl #24

88f8: e1844806 orr r4, r4, r6, lsl #16

88fc: e1842402 orr r2, r4, r2, lsl #8

8900: e590e00c ldr lr, [r0, #12]

8904: e0252ae2 eor r2, r5, r2, ror #21

8908: e08c6002 add r6, ip, r2

890c: e5904010 ldr r4, [r0, #16]

8910: e1a09826 lsr r9, r6, #16

8914: e1a07426 lsr r7, r6, #8

8918: e083ac26 add sl, r3, r6, lsr #24

891c: e6e39079 uxtab r9, r3, r9

8920: e6e36076 uxtab r6, r3, r6

8924: e6e37077 uxtab r7, r3, r7

8928: e5daa004 ldrb sl, [sl, #4]

892c: e5d66304 ldrb r6, [r6, #772] ; 0x304

8930: e5d99104 ldrb r9, [r9, #260] ; 0x104

8934: e5d77204 ldrb r7, [r7, #516] ; 0x204

8938: e1866c0a orr r6, r6, sl, lsl #24

893c: e5905014 ldr r5, [r0, #20]

8940: e1869809 orr r9, r6, r9, lsl #16

8944: e1897407 orr r7, r9, r7, lsl #8

8948: e0288ae7 eor r8, r8, r7, ror #21

894c: e08e6008 add r6, lr, r8

8950: e1a09826 lsr r9, r6, #16

8954: e1a07426 lsr r7, r6, #8

8958: e083ac26 add sl, r3, r6, lsr #24

895c: e6e39079 uxtab r9, r3, r9

8960: e6e36076 uxtab r6, r3, r6

8964: e6e37077 uxtab r7, r3, r7

8968: e5daa004 ldrb sl, [sl, #4]

896c: e5d66304 ldrb r6, [r6, #772] ; 0x304

8970: e5d99104 ldrb r9, [r9, #260] ; 0x104

8974: e5d77204 ldrb r7, [r7, #516] ; 0x204

8978: e1866c0a orr r6, r6, sl, lsl #24

897c: e1869809 orr r9, r6, r9, lsl #16

8980: e1897407 orr r7, r9, r7, lsl #8

8984: e0222ae7 eor r2, r2, r7, ror #21

8988: e0846002 add r6, r4, r2

898c: e1a09826 lsr r9, r6, #16

8990: e1a07426 lsr r7, r6, #8

8994: e083ac26 add sl, r3, r6, lsr #24

8998: e6e39079 uxtab r9, r3, r9

899c: e6e36076 uxtab r6, r3, r6

89a0: e6e37077 uxtab r7, r3, r7

89a4: e5daa004 ldrb sl, [sl, #4]

89a8: e5d66304 ldrb r6, [r6, #772] ; 0x304

89ac: e5d99104 ldrb r9, [r9, #260] ; 0x104

89b0: e5d77204 ldrb r7, [r7, #516] ; 0x204

89b4: e1866c0a orr r6, r6, sl, lsl #24

89b8: e1869809 orr r9, r6, r9, lsl #16

89bc: e1897407 orr r7, r9, r7, lsl #8

89c0: e0288ae7 eor r8, r8, r7, ror #21

89c4: e0857008 add r7, r5, r8

89c8: e1a0a827 lsr sl, r7, #16

89cc: e1a09427 lsr r9, r7, #8

89d0: e0836c27 add r6, r3, r7, lsr #24

89d4: e6e3a07a uxtab sl, r3, sl

89d8: e6e37077 uxtab r7, r3, r7

89dc: e6e39079 uxtab r9, r3, r9

89e0: e5d66004 ldrb r6, [r6, #4]

89e4: e5d77304 ldrb r7, [r7, #772] ; 0x304

89e8: e5daa104 ldrb sl, [sl, #260] ; 0x104

89ec: e5d99204 ldrb r9, [r9, #516] ; 0x204

89f0: e1877c06 orr r7, r7, r6, lsl #24

89f4: e5906018 ldr r6, [r0, #24]

89f8: e187780a orr r7, r7, sl, lsl #16

89fc: e1879409 orr r9, r7, r9, lsl #8

8a00: e590701c ldr r7, [r0, #28]

8a04: e0222ae9 eor r2, r2, r9, ror #21

8a08: e0869002 add r9, r6, r2

8a0c: e1a00829 lsr r0, r9, #16

8a10: e083bc29 add fp, r3, r9, lsr #24

8a14: e6e3a079 uxtab sl, r3, r9

8a18: e1a09429 lsr r9, r9, #8

8a1c: e6e30070 uxtab r0, r3, r0

8a20: e6e39079 uxtab r9, r3, r9

8a24: e5dbb004 ldrb fp, [fp, #4]

8a28: e5daa304 ldrb sl, [sl, #772] ; 0x304

8a2c: e5d00104 ldrb r0, [r0, #260] ; 0x104

8a30: e5d99204 ldrb r9, [r9, #516] ; 0x204

8a34: e18aac0b orr sl, sl, fp, lsl #24

8a38: e18a0800 orr r0, sl, r0, lsl #16

8a3c: e1800409 orr r0, r0, r9, lsl #8

8a40: e0280ae0 eor r0, r8, r0, ror #21

8a44: e0878000 add r8, r7, r0

8a48: e1a0a828 lsr sl, r8, #16

8a4c: e083bc28 add fp, r3, r8, lsr #24

8a50: e6e39078 uxtab r9, r3, r8

8a54: e1a08428 lsr r8, r8, #8

8a58: e6e3a07a uxtab sl, r3, sl

8a5c: e6e38078 uxtab r8, r3, r8

8a60: e5dbb004 ldrb fp, [fp, #4]

8a64: e5daa104 ldrb sl, [sl, #260] ; 0x104

8a68: e5d99304 ldrb r9, [r9, #772] ; 0x304

8a6c: e5d88204 ldrb r8, [r8, #516] ; 0x204

8a70: e1899c0b orr r9, r9, fp, lsl #24

8a74: e189980a orr r9, r9, sl, lsl #16

8a78: e1898408 orr r8, r9, r8, lsl #8

8a7c: e0222ae8 eor r2, r2, r8, ror #21

8a80: e0878002 add r8, r7, r2

8a84: e1a0a828 lsr sl, r8, #16

8a88: e083bc28 add fp, r3, r8, lsr #24

8a8c: e6e39078 uxtab r9, r3, r8

8a90: e1a08428 lsr r8, r8, #8

8a94: e6e3a07a uxtab sl, r3, sl

8a98: e6e38078 uxtab r8, r3, r8

8a9c: e5dbb004 ldrb fp, [fp, #4]

8aa0: e5daa104 ldrb sl, [sl, #260] ; 0x104

8aa4: e5d99304 ldrb r9, [r9, #772] ; 0x304

8aa8: e5d88204 ldrb r8, [r8, #516] ; 0x204

8aac: e1899c0b orr r9, r9, fp, lsl #24

8ab0: e189980a orr r9, r9, sl, lsl #16

8ab4: e1898408 orr r8, r9, r8, lsl #8

8ab8: e0200ae8 eor r0, r0, r8, ror #21

8abc: e0868000 add r8, r6, r0

8ac0: e1a0a828 lsr sl, r8, #16

8ac4: e083bc28 add fp, r3, r8, lsr #24

8ac8: e6e39078 uxtab r9, r3, r8

8acc: e1a08428 lsr r8, r8, #8

8ad0: e6e3a07a uxtab sl, r3, sl

8ad4: e6e38078 uxtab r8, r3, r8

8ad8: e5dbb004 ldrb fp, [fp, #4]

8adc: e5daa104 ldrb sl, [sl, #260] ; 0x104

8ae0: e5d99304 ldrb r9, [r9, #772] ; 0x304

8ae4: e5d88204 ldrb r8, [r8, #516] ; 0x204

8ae8: e1899c0b orr r9, r9, fp, lsl #24

8aec: e189980a orr r9, r9, sl, lsl #16

8af0: e1898408 orr r8, r9, r8, lsl #8

8af4: e0222ae8 eor r2, r2, r8, ror #21

8af8: e0858002 add r8, r5, r2

8afc: e1a0a828 lsr sl, r8, #16

8b00: e083bc28 add fp, r3, r8, lsr #24

8b04: e6e39078 uxtab r9, r3, r8

8b08: e1a08428 lsr r8, r8, #8

8b0c: e6e3a07a uxtab sl, r3, sl

8b10: e6e38078 uxtab r8, r3, r8

8b14: e5dbb004 ldrb fp, [fp, #4]

8b18: e5daa104 ldrb sl, [sl, #260] ; 0x104

8b1c: e5d99304 ldrb r9, [r9, #772] ; 0x304

8b20: e5d88204 ldrb r8, [r8, #516] ; 0x204

8b24: e1899c0b orr r9, r9, fp, lsl #24

8b28: e189980a orr r9, r9, sl, lsl #16

8b2c: e1898408 orr r8, r9, r8, lsl #8

8b30: e0200ae8 eor r0, r0, r8, ror #21

8b34: e0848000 add r8, r4, r0

8b38: e1a0a828 lsr sl, r8, #16

8b3c: e083bc28 add fp, r3, r8, lsr #24

8b40: e6e39078 uxtab r9, r3, r8

8b44: e1a08428 lsr r8, r8, #8

8b48: e6e3a07a uxtab sl, r3, sl

8b4c: e6e38078 uxtab r8, r3, r8

8b50: e5dbb004 ldrb fp, [fp, #4]

8b54: e5daa104 ldrb sl, [sl, #260] ; 0x104

8b58: e5d99304 ldrb r9, [r9, #772] ; 0x304

8b5c: e5d88204 ldrb r8, [r8, #516] ; 0x204

8b60: e1899c0b orr r9, r9, fp, lsl #24

8b64: e189980a orr r9, r9, sl, lsl #16

8b68: e1898408 orr r8, r9, r8, lsl #8

8b6c: e0222ae8 eor r2, r2, r8, ror #21

8b70: e08e8002 add r8, lr, r2

8b74: e1a0a828 lsr sl, r8, #16

8b78: e083bc28 add fp, r3, r8, lsr #24

8b7c: e6e39078 uxtab r9, r3, r8

8b80: e1a08428 lsr r8, r8, #8

8b84: e6e3a07a uxtab sl, r3, sl

8b88: e6e38078 uxtab r8, r3, r8

8b8c: e5dbb004 ldrb fp, [fp, #4]

8b90: e5daa104 ldrb sl, [sl, #260] ; 0x104

8b94: e5d99304 ldrb r9, [r9, #772] ; 0x304

8b98: e5d88204 ldrb r8, [r8, #516] ; 0x204

8b9c: e1899c0b orr r9, r9, fp, lsl #24

8ba0: e189980a orr r9, r9, sl, lsl #16

8ba4: e1898408 orr r8, r9, r8, lsl #8

8ba8: e0200ae8 eor r0, r0, r8, ror #21

8bac: e08c8000 add r8, ip, r0

8bb0: e1a0a828 lsr sl, r8, #16

8bb4: e083bc28 add fp, r3, r8, lsr #24

8bb8: e6e39078 uxtab r9, r3, r8

8bbc: e1a08428 lsr r8, r8, #8

8bc0: e6e3a07a uxtab sl, r3, sl

8bc4: e6e38078 uxtab r8, r3, r8

8bc8: e5dbb004 ldrb fp, [fp, #4]

8bcc: e5daa104 ldrb sl, [sl, #260] ; 0x104

8bd0: e5d99304 ldrb r9, [r9, #772] ; 0x304

8bd4: e5d88204 ldrb r8, [r8, #516] ; 0x204

8bd8: e1899c0b orr r9, r9, fp, lsl #24

8bdc: e189980a orr r9, r9, sl, lsl #16

8be0: e1898408 orr r8, r9, r8, lsl #8

8be4: e59d9004 ldr r9, [sp, #4]

8be8: e0222ae8 eor r2, r2, r8, ror #21

8bec: e0898002 add r8, r9, r2

8bf0: e1a0a828 lsr sl, r8, #16

8bf4: e083bc28 add fp, r3, r8, lsr #24

8bf8: e6e39078 uxtab r9, r3, r8

8bfc: e1a08428 lsr r8, r8, #8

8c00: e6e3a07a uxtab sl, r3, sl

8c04: e6e38078 uxtab r8, r3, r8

8c08: e5dbb004 ldrb fp, [fp, #4]

8c0c: e5daa104 ldrb sl, [sl, #260] ; 0x104

8c10: e5d99304 ldrb r9, [r9, #772] ; 0x304

8c14: e5d88204 ldrb r8, [r8, #516] ; 0x204

8c18: e1899c0b orr r9, r9, fp, lsl #24

8c1c: e189980a orr r9, r9, sl, lsl #16

8c20: e1898408 orr r8, r9, r8, lsl #8

8c24: e0200ae8 eor r0, r0, r8, ror #21

8c28: e59d8000 ldr r8, [sp]

8c2c: e0888000 add r8, r8, r0

8c30: e1a0a828 lsr sl, r8, #16

8c34: e083bc28 add fp, r3, r8, lsr #24

8c38: e6e39078 uxtab r9, r3, r8

8c3c: e1a08428 lsr r8, r8, #8

8c40: e6e3a07a uxtab sl, r3, sl

8c44: e6e38078 uxtab r8, r3, r8

8c48: e5dbb004 ldrb fp, [fp, #4]

8c4c: e5daa104 ldrb sl, [sl, #260] ; 0x104

8c50: e5d99304 ldrb r9, [r9, #772] ; 0x304

8c54: e5d88204 ldrb r8, [r8, #516] ; 0x204

8c58: e1899c0b orr r9, r9, fp, lsl #24

8c5c: e189980a orr r9, r9, sl, lsl #16

8c60: e1898408 orr r8, r9, r8, lsl #8

8c64: e0222ae8 eor r2, r2, r8, ror #21

8c68: e0878002 add r8, r7, r2

8c6c: e1a0a828 lsr sl, r8, #16

8c70: e083bc28 add fp, r3, r8, lsr #24

8c74: e6e39078 uxtab r9, r3, r8

8c78: e1a08428 lsr r8, r8, #8

8c7c: e6e3a07a uxtab sl, r3, sl

8c80: e6e38078 uxtab r8, r3, r8

8c84: e5dbb004 ldrb fp, [fp, #4]

8c88: e5daa104 ldrb sl, [sl, #260] ; 0x104

8c8c: e5d99304 ldrb r9, [r9, #772] ; 0x304

8c90: e5d88204 ldrb r8, [r8, #516] ; 0x204

8c94: e1899c0b orr r9, r9, fp, lsl #24

8c98: e189980a orr r9, r9, sl, lsl #16

8c9c: e1898408 orr r8, r9, r8, lsl #8

8ca0: e0200ae8 eor r0, r0, r8, ror #21

8ca4: e0868000 add r8, r6, r0

8ca8: e1a0a828 lsr sl, r8, #16

8cac: e083bc28 add fp, r3, r8, lsr #24

8cb0: e6e39078 uxtab r9, r3, r8

8cb4: e1a08428 lsr r8, r8, #8

8cb8: e6e3a07a uxtab sl, r3, sl

8cbc: e6e38078 uxtab r8, r3, r8

8cc0: e5dbb004 ldrb fp, [fp, #4]

8cc4: e5daa104 ldrb sl, [sl, #260] ; 0x104

8cc8: e5d99304 ldrb r9, [r9, #772] ; 0x304

8ccc: e5d88204 ldrb r8, [r8, #516] ; 0x204

8cd0: e1899c0b orr r9, r9, fp, lsl #24

8cd4: e189980a orr r9, r9, sl, lsl #16

8cd8: e1898408 orr r8, r9, r8, lsl #8

8cdc: e0222ae8 eor r2, r2, r8, ror #21

8ce0: e0858002 add r8, r5, r2

8ce4: e1a0a828 lsr sl, r8, #16

8ce8: e083bc28 add fp, r3, r8, lsr #24

8cec: e6e39078 uxtab r9, r3, r8

8cf0: e1a08428 lsr r8, r8, #8

8cf4: e6e3a07a uxtab sl, r3, sl

8cf8: e6e38078 uxtab r8, r3, r8

8cfc: e5dbb004 ldrb fp, [fp, #4]

8d00: e5daa104 ldrb sl, [sl, #260] ; 0x104

8d04: e5d99304 ldrb r9, [r9, #772] ; 0x304

8d08: e5d88204 ldrb r8, [r8, #516] ; 0x204

8d0c: e1899c0b orr r9, r9, fp, lsl #24

8d10: e189980a orr r9, r9, sl, lsl #16

8d14: e1898408 orr r8, r9, r8, lsl #8

8d18: e0200ae8 eor r0, r0, r8, ror #21

8d1c: e0848000 add r8, r4, r0

8d20: e1a0a828 lsr sl, r8, #16

8d24: e083bc28 add fp, r3, r8, lsr #24

8d28: e6e39078 uxtab r9, r3, r8

8d2c: e1a08428 lsr r8, r8, #8

8d30: e6e3a07a uxtab sl, r3, sl

8d34: e6e38078 uxtab r8, r3, r8

8d38: e5dbb004 ldrb fp, [fp, #4]

8d3c: e5daa104 ldrb sl, [sl, #260] ; 0x104

8d40: e5d99304 ldrb r9, [r9, #772] ; 0x304

8d44: e5d88204 ldrb r8, [r8, #516] ; 0x204

8d48: e1899c0b orr r9, r9, fp, lsl #24

8d4c: e189980a orr r9, r9, sl, lsl #16

8d50: e1898408 orr r8, r9, r8, lsl #8

8d54: e0222ae8 eor r2, r2, r8, ror #21

8d58: e08e8002 add r8, lr, r2

8d5c: e1a0a828 lsr sl, r8, #16

8d60: e083bc28 add fp, r3, r8, lsr #24

8d64: e6e39078 uxtab r9, r3, r8

8d68: e1a08428 lsr r8, r8, #8

8d6c: e6e3a07a uxtab sl, r3, sl

8d70: e6e38078 uxtab r8, r3, r8

8d74: e5dbb004 ldrb fp, [fp, #4]

8d78: e5d99304 ldrb r9, [r9, #772] ; 0x304

8d7c: e5daa104 ldrb sl, [sl, #260] ; 0x104

8d80: e5d88204 ldrb r8, [r8, #516] ; 0x204

8d84: e1899c0b orr r9, r9, fp, lsl #24

8d88: e189980a orr r9, r9, sl, lsl #16

8d8c: e1898408 orr r8, r9, r8, lsl #8

8d90: e0200ae8 eor r0, r0, r8, ror #21

8d94: e08c8000 add r8, ip, r0

8d98: e1a0a828 lsr sl, r8, #16

8d9c: e083bc28 add fp, r3, r8, lsr #24

8da0: e6e39078 uxtab r9, r3, r8

8da4: e1a08428 lsr r8, r8, #8

8da8: e6e3a07a uxtab sl, r3, sl

8dac: e6e38078 uxtab r8, r3, r8

8db0: e5dbb004 ldrb fp, [fp, #4]

8db4: e5daa104 ldrb sl, [sl, #260] ; 0x104

8db8: e5d99304 ldrb r9, [r9, #772] ; 0x304

8dbc: e5d88204 ldrb r8, [r8, #516] ; 0x204

8dc0: e1899c0b orr r9, r9, fp, lsl #24

8dc4: e189980a orr r9, r9, sl, lsl #16

8dc8: e1898408 orr r8, r9, r8, lsl #8

8dcc: e59d9004 ldr r9, [sp, #4]

8dd0: e0222ae8 eor r2, r2, r8, ror #21

8dd4: e0898002 add r8, r9, r2

8dd8: e1a0a828 lsr sl, r8, #16

8ddc: e083bc28 add fp, r3, r8, lsr #24

8de0: e6e39078 uxtab r9, r3, r8

8de4: e1a08428 lsr r8, r8, #8

8de8: e6e3a07a uxtab sl, r3, sl

8dec: e6e38078 uxtab r8, r3, r8

8df0: e5dbb004 ldrb fp, [fp, #4]

8df4: e5daa104 ldrb sl, [sl, #260] ; 0x104

8df8: e5d99304 ldrb r9, [r9, #772] ; 0x304

8dfc: e5d88204 ldrb r8, [r8, #516] ; 0x204

8e00: e1899c0b orr r9, r9, fp, lsl #24

8e04: e189980a orr r9, r9, sl, lsl #16

8e08: e1898408 orr r8, r9, r8, lsl #8

8e0c: e0200ae8 eor r0, r0, r8, ror #21

8e10: e59d8000 ldr r8, [sp]

8e14: e0889000 add r9, r8, r0

8e18: e1a0a829 lsr sl, r9, #16

8e1c: e083bc29 add fp, r3, r9, lsr #24

8e20: e6e38079 uxtab r8, r3, r9

8e24: e1a09429 lsr r9, r9, #8

8e28: e6e3a07a uxtab sl, r3, sl

8e2c: e6e39079 uxtab r9, r3, r9

8e30: e5dbb004 ldrb fp, [fp, #4]

8e34: e5daa104 ldrb sl, [sl, #260] ; 0x104

8e38: e5d88304 ldrb r8, [r8, #772] ; 0x304

8e3c: e5d99204 ldrb r9, [r9, #516] ; 0x204

8e40: e1888c0b orr r8, r8, fp, lsl #24

8e44: e188880a orr r8, r8, sl, lsl #16

8e48: e1889409 orr r9, r8, r9, lsl #8

8e4c: e0222ae9 eor r2, r2, r9, ror #21

8e50: e0877002 add r7, r7, r2

8e54: e1a09827 lsr r9, r7, #16

8e58: e1a08427 lsr r8, r7, #8

8e5c: e083ac27 add sl, r3, r7, lsr #24

8e60: e6e39079 uxtab r9, r3, r9

8e64: e6e37077 uxtab r7, r3, r7

8e68: e6e38078 uxtab r8, r3, r8

8e6c: e5daa004 ldrb sl, [sl, #4]

8e70: e5d77304 ldrb r7, [r7, #772] ; 0x304

8e74: e5d99104 ldrb r9, [r9, #260] ; 0x104

8e78: e5d88204 ldrb r8, [r8, #516] ; 0x204

8e7c: e187ac0a orr sl, r7, sl, lsl #24

8e80: e18a9809 orr r9, sl, r9, lsl #16

8e84: e1898408 orr r8, r9, r8, lsl #8

8e88: e0200ae8 eor r0, r0, r8, ror #21

8e8c: e0866000 add r6, r6, r0

8e90: e1a08826 lsr r8, r6, #16

8e94: e1a07426 lsr r7, r6, #8

8e98: e0839c26 add r9, r3, r6, lsr #24

8e9c: e6e38078 uxtab r8, r3, r8

8ea0: e6e36076 uxtab r6, r3, r6

8ea4: e6e37077 uxtab r7, r3, r7

8ea8: e5d9a004 ldrb sl, [r9, #4]

8eac: e5d66304 ldrb r6, [r6, #772] ; 0x304

8eb0: e5d88104 ldrb r8, [r8, #260] ; 0x104

8eb4: e5d77204 ldrb r7, [r7, #516] ; 0x204

8eb8: e1869c0a orr r9, r6, sl, lsl #24

8ebc: e1899808 orr r9, r9, r8, lsl #16

8ec0: e1899407 orr r9, r9, r7, lsl #8

8ec4: e0222ae9 eor r2, r2, r9, ror #21

8ec8: e0855002 add r5, r5, r2

8ecc: e1a06825 lsr r6, r5, #16

8ed0: e1a09425 lsr r9, r5, #8

8ed4: e0837c25 add r7, r3, r5, lsr #24

8ed8: e6e36076 uxtab r6, r3, r6

8edc: e6e35075 uxtab r5, r3, r5

8ee0: e6e39079 uxtab r9, r3, r9

8ee4: e5d77004 ldrb r7, [r7, #4]

8ee8: e5d58304 ldrb r8, [r5, #772] ; 0x304

8eec: e5d66104 ldrb r6, [r6, #260] ; 0x104

8ef0: e5d95204 ldrb r5, [r9, #516] ; 0x204

8ef4: e1888c07 orr r8, r8, r7, lsl #24

8ef8: e1888806 orr r8, r8, r6, lsl #16

8efc: e1888405 orr r8, r8, r5, lsl #8

8f00: e0200ae8 eor r0, r0, r8, ror #21

8f04: e0844000 add r4, r4, r0

8f08: e1a06824 lsr r6, r4, #16

8f0c: e1a05424 lsr r5, r4, #8

8f10: e0837c24 add r7, r3, r4, lsr #24

8f14: e6e36076 uxtab r6, r3, r6

8f18: e6e34074 uxtab r4, r3, r4

8f1c: e6e35075 uxtab r5, r3, r5

8f20: e5d77004 ldrb r7, [r7, #4]

8f24: e5d66104 ldrb r6, [r6, #260] ; 0x104

8f28: e5d49304 ldrb r9, [r4, #772] ; 0x304

8f2c: e5d54204 ldrb r4, [r5, #516] ; 0x204

8f30: e1899c07 orr r9, r9, r7, lsl #24

8f34: e1899806 orr r9, r9, r6, lsl #16

8f38: e1899404 orr r9, r9, r4, lsl #8

8f3c: e0222ae9 eor r2, r2, r9, ror #21

8f40: e08ee002 add lr, lr, r2

8f44: e59d9004 ldr r9, [sp, #4]

8f48: e1a0582e lsr r5, lr, #16

8f4c: e1a0442e lsr r4, lr, #8

8f50: e0836c2e add r6, r3, lr, lsr #24

8f54: e6e35075 uxtab r5, r3, r5

8f58: e6e3e07e uxtab lr, r3, lr

8f5c: e6e34074 uxtab r4, r3, r4

8f60: e5d66004 ldrb r6, [r6, #4]

8f64: e5de8304 ldrb r8, [lr, #772] ; 0x304

8f68: e5d55104 ldrb r5, [r5, #260] ; 0x104

8f6c: e5d4e204 ldrb lr, [r4, #516] ; 0x204

8f70: e1888c06 orr r8, r8, r6, lsl #24

8f74: e1888805 orr r8, r8, r5, lsl #16

8f78: e188840e orr r8, r8, lr, lsl #8

8f7c: e0200ae8 eor r0, r0, r8, ror #21

8f80: e08cc000 add ip, ip, r0

8f84: e59d8000 ldr r8, [sp]

8f88: e1a0482c lsr r4, ip, #16

8f8c: e1a0e42c lsr lr, ip, #8

8f90: e0835c2c add r5, r3, ip, lsr #24

8f94: e6e34074 uxtab r4, r3, r4

8f98: e6e3c07c uxtab ip, r3, ip

8f9c: e6e3e07e uxtab lr, r3, lr

8fa0: e5d55004 ldrb r5, [r5, #4]

8fa4: e5d44104 ldrb r4, [r4, #260] ; 0x104

8fa8: e5dcc304 ldrb ip, [ip, #772] ; 0x304

8fac: e5dee204 ldrb lr, [lr, #516] ; 0x204

8fb0: e18ccc05 orr ip, ip, r5, lsl #24

8fb4: e18cc804 orr ip, ip, r4, lsl #16

8fb8: e18cc40e orr ip, ip, lr, lsl #8

8fbc: e0222aec eor r2, r2, ip, ror #21

8fc0: e089c002 add ip, r9, r2

8fc4: e1a0482c lsr r4, ip, #16

8fc8: e1a0e42c lsr lr, ip, #8

8fcc: e0835c2c add r5, r3, ip, lsr #24

8fd0: e6e34074 uxtab r4, r3, r4

8fd4: e6e3c07c uxtab ip, r3, ip

8fd8: e6e3e07e uxtab lr, r3, lr

8fdc: e5d55004 ldrb r5, [r5, #4]

8fe0: e5d44104 ldrb r4, [r4, #260] ; 0x104

8fe4: e5dcc304 ldrb ip, [ip, #772] ; 0x304

8fe8: e5dee204 ldrb lr, [lr, #516] ; 0x204

8fec: e18ccc05 orr ip, ip, r5, lsl #24

8ff0: e18cc804 orr ip, ip, r4, lsl #16

8ff4: e18cc40e orr ip, ip, lr, lsl #8

8ff8: e0200aec eor r0, r0, ip, ror #21

8ffc: e088c000 add ip, r8, r0

9000: e6bf0f30 rev r0, r0

9004: e1a0482c lsr r4, ip, #16

9008: e1a0e42c lsr lr, ip, #8

900c: e0835c2c add r5, r3, ip, lsr #24

9010: e6e34074 uxtab r4, r3, r4

9014: e6e3c07c uxtab ip, r3, ip

9018: e6e3e07e uxtab lr, r3, lr

901c: e5d55004 ldrb r5, [r5, #4]

9020: e5dc3304 ldrb r3, [ip, #772] ; 0x304

9024: e5d44104 ldrb r4, [r4, #260] ; 0x104

9028: e5dec204 ldrb ip, [lr, #516] ; 0x204

902c: e1833c05 orr r3, r3, r5, lsl #24

9030: e5810000 str r0, [r1]

9034: e1833804 orr r3, r3, r4, lsl #16

9038: e183340c orr r3, r3, ip, lsl #8

903c: e0222ae3 eor r2, r2, r3, ror #21

9040: e6bf2f32 rev r2, r2

9044: e5812004 str r2, [r1, #4]

9048: e28dd00c add sp, sp, #12

904c: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

9050: 0002cc9c muleq r2, ip, ip

9054: e3a00020 mov r0, #32

9058: e12fff1e bx lr

905c: e3a00008 mov r0, #8

9060: e12fff1e bx lr

9064: e3a00001 mov r0, #1

9068: e12fff1e bx lr

906c: e3a00020 mov r0, #32

9070: e12fff1e bx lr

9074: e59f3010 ldr r3, [pc, #16] ; 908c <mcrypt\_mutex\_register@@Base+0x48f8>

9078: e3a02001 mov r2, #1

907c: e08f3003 add r3, pc, r3

9080: e5802000 str r2, [r0]

9084: e2830080 add r0, r3, #128 ; 0x80

9088: e12fff1e bx lr

908c: 00017644 andeq r7, r1, r4, asr #12

9090: e59f0004 ldr r0, [pc, #4] ; 909c <mcrypt\_mutex\_register@@Base+0x4908>

9094: e08f0000 add r0, pc, r0

9098: e12fff1e bx lr

909c: 000176b0 ; <UNDEFINED> instruction: 0x000176b0

90a0: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

90a4: e24dd0e8 sub sp, sp, #232 ; 0xe8

90a8: ebffffeb bl 905c <mcrypt\_mutex\_register@@Base+0x48c8>

90ac: e1a06000 mov r6, r0

90b0: ebffffed bl 906c <mcrypt\_mutex\_register@@Base+0x48d8>

90b4: e1a01000 mov r1, r0

90b8: e3a00001 mov r0, #1

90bc: ebffe45c bl 2234 <calloc@plt>

90c0: e2507000 subs r7, r0, #0

90c4: 0a00006f beq 9288 <mcrypt\_mutex\_register@@Base+0x4af4>

90c8: e2475001 sub r5, r7, #1

90cc: e3a0400a mov r4, #10

90d0: e2678001 rsb r8, r7, #1

90d4: ea000004 b 90ec <mcrypt\_mutex\_register@@Base+0x4958>

90d8: e2742000 rsbs r2, r4, #0

90dc: e2844002 add r4, r4, #2

90e0: e6ef2072 uxtb r2, r2

90e4: 52623000 rsbpl r3, r2, #0

90e8: e5e53001 strb r3, [r5, #1]!

90ec: ebffffde bl 906c <mcrypt\_mutex\_register@@Base+0x48d8>

90f0: e0882005 add r2, r8, r5

90f4: e6ef3074 uxtb r3, r4

90f8: e1500002 cmp r0, r2

90fc: cafffff5 bgt 90d8 <mcrypt\_mutex\_register@@Base+0x4944>

9100: e3560000 cmp r6, #0

9104: da000039 ble 91f0 <mcrypt\_mutex\_register@@Base+0x4a5c>

9108: e1a0400d mov r4, sp

910c: e1a02004 mov r2, r4

9110: e3a03000 mov r3, #0

9114: e4c23001 strb r3, [r2], #1

9118: e2833001 add r3, r3, #1

911c: e1560003 cmp r6, r3

9120: 1afffffb bne 9114 <mcrypt\_mutex\_register@@Base+0x4980>

9124: ebffffca bl 9054 <mcrypt\_mutex\_register@@Base+0x48c0>

9128: ebffe471 bl 22f4 <malloc@plt>

912c: e2509000 subs r9, r0, #0

9130: 0a000050 beq 9278 <mcrypt\_mutex\_register@@Base+0x4ae4>

9134: e28d8010 add r8, sp, #16

9138: e1a01004 mov r1, r4

913c: e1a02006 mov r2, r6

9140: e1a00008 mov r0, r8

9144: ebffe44f bl 2288 <memcpy@plt>

9148: ebffffc7 bl 906c <mcrypt\_mutex\_register@@Base+0x48d8>

914c: e1a01007 mov r1, r7

9150: e28da020 add sl, sp, #32

9154: e0886006 add r6, r8, r6

9158: e1a05008 mov r5, r8

915c: e1a0400a mov r4, sl

9160: e1a02000 mov r2, r0

9164: e1a00009 mov r0, r9

9168: ebfffba7 bl 800c <mcrypt\_mutex\_register@@Base+0x3878>

916c: e1a00007 mov r0, r7

9170: ebffe441 bl 227c <free@plt>

9174: e1a01008 mov r1, r8

9178: e1a00009 mov r0, r9

917c: ebfffbb6 bl 805c <mcrypt\_mutex\_register@@Base+0x38c8>

9180: e59f7108 ldr r7, [pc, #264] ; 9290 <mcrypt\_mutex\_register@@Base+0x4afc>

9184: e08f7007 add r7, pc, r7

9188: e4d52001 ldrb r2, [r5], #1

918c: e1a00004 mov r0, r4

9190: e1a01007 mov r1, r7

9194: ebffe474 bl 236c <sprintf@plt>

9198: e1560005 cmp r6, r5

919c: e2844002 add r4, r4, #2

91a0: 1afffff8 bne 9188 <mcrypt\_mutex\_register@@Base+0x49f4>

91a4: e59f40e8 ldr r4, [pc, #232] ; 9294 <mcrypt\_mutex\_register@@Base+0x4b00>

91a8: e1a0000a mov r0, sl

91ac: e08f4004 add r4, pc, r4

91b0: e1a01004 mov r1, r4

91b4: ebffe424 bl 224c <strcmp@plt>

91b8: e3500000 cmp r0, #0

91bc: 1a00001c bne 9234 <mcrypt\_mutex\_register@@Base+0x4aa0>

91c0: e1a01008 mov r1, r8

91c4: e1a00009 mov r0, r9

91c8: ebfffda2 bl 8858 <mcrypt\_mutex\_register@@Base+0x40c4>

91cc: e1a00009 mov r0, r9

91d0: ebffe429 bl 227c <free@plt>

91d4: e1a00008 mov r0, r8

91d8: e1a0100d mov r1, sp

91dc: ebffe41a bl 224c <strcmp@plt>

91e0: e3500000 cmp r0, #0

91e4: 1a00001e bne 9264 <mcrypt\_mutex\_register@@Base+0x4ad0>

91e8: e28dd0e8 add sp, sp, #232 ; 0xe8

91ec: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

91f0: ebffff97 bl 9054 <mcrypt\_mutex\_register@@Base+0x48c0>

91f4: ebffe43e bl 22f4 <malloc@plt>

91f8: e2509000 subs r9, r0, #0

91fc: 0a00001d beq 9278 <mcrypt\_mutex\_register@@Base+0x4ae4>

9200: ebffff99 bl 906c <mcrypt\_mutex\_register@@Base+0x48d8>

9204: e1a01007 mov r1, r7

9208: e28d8010 add r8, sp, #16

920c: e28da020 add sl, sp, #32

9210: e1a02000 mov r2, r0

9214: e1a00009 mov r0, r9

9218: ebfffb7b bl 800c <mcrypt\_mutex\_register@@Base+0x3878>

921c: e1a00007 mov r0, r7

9220: ebffe415 bl 227c <free@plt>

9224: e1a01008 mov r1, r8

9228: e1a00009 mov r0, r9

922c: ebfffb8a bl 805c <mcrypt\_mutex\_register@@Base+0x38c8>

9230: eaffffdb b 91a4 <mcrypt\_mutex\_register@@Base+0x4a10>

9234: e59f005c ldr r0, [pc, #92] ; 9298 <mcrypt\_mutex\_register@@Base+0x4b04>

9238: e08f0000 add r0, pc, r0

923c: ebffe429 bl 22e8 <puts@plt>

9240: e59f0054 ldr r0, [pc, #84] ; 929c <mcrypt\_mutex\_register@@Base+0x4b08>

9244: e1a0200a mov r2, sl

9248: e1a01004 mov r1, r4

924c: e08f0000 add r0, pc, r0

9250: ebffe403 bl 2264 <printf@plt>

9254: e1a00009 mov r0, r9

9258: ebffe407 bl 227c <free@plt>

925c: e3e00000 mvn r0, #0

9260: eaffffe0 b 91e8 <mcrypt\_mutex\_register@@Base+0x4a54>

9264: e59f0034 ldr r0, [pc, #52] ; 92a0 <mcrypt\_mutex\_register@@Base+0x4b0c>

9268: e08f0000 add r0, pc, r0

926c: ebffe41d bl 22e8 <puts@plt>

9270: e3e00000 mvn r0, #0

9274: eaffffdb b 91e8 <mcrypt\_mutex\_register@@Base+0x4a54>

9278: e1a00007 mov r0, r7

927c: ebffe3fe bl 227c <free@plt>

9280: e3e00000 mvn r0, #0

9284: eaffffd7 b 91e8 <mcrypt\_mutex\_register@@Base+0x4a54>

9288: e3e00000 mvn r0, #0

928c: eaffffd5 b 91e8 <mcrypt\_mutex\_register@@Base+0x4a54>

9290: 000174dc ldrdeq r7, [r1], -ip

9294: 000175a0 andeq r7, r1, r0, lsr #11

9298: 00017444 andeq r7, r1, r4, asr #8

929c: 00017448 andeq r7, r1, r8, asr #8

92a0: 00017444 andeq r7, r1, r4, asr #8

92a4: e59f0000 ldr r0, [pc] ; 92ac <mcrypt\_mutex\_register@@Base+0x4b18>

92a8: e12fff1e bx lr

92ac: 01315731 teqeq r1, r1, lsr r7

92b0: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

92b4: e21090ff ands r9, r0, #255 ; 0xff

92b8: 03a07000 moveq r7, #0

92bc: 13a07001 movne r7, #1

92c0: e21180ff ands r8, r1, #255 ; 0xff

92c4: 03a07000 moveq r7, #0

92c8: e1a04420 lsr r4, r0, #8

92cc: e1a0c820 lsr ip, r0, #16

92d0: e1a03c20 lsr r3, r0, #24

92d4: e1a0e421 lsr lr, r1, #8

92d8: e1a02821 lsr r2, r1, #16

92dc: e1a01c21 lsr r1, r1, #24

92e0: e3570000 cmp r7, #0

92e4: e6ef4074 uxtb r4, r4

92e8: e6efc07c uxtb ip, ip

92ec: e6ef5073 uxtb r5, r3

92f0: e6efe07e uxtb lr, lr

92f4: e6ef2072 uxtb r2, r2

92f8: e6ef6071 uxtb r6, r1

92fc: 01a00007 moveq r0, r7

9300: 0a00000c beq 9338 <mcrypt\_mutex\_register@@Base+0x4ba4>

9304: e59f70fc ldr r7, [pc, #252] ; 9408 <mcrypt\_mutex\_register@@Base+0x4c74>

9308: e59f00fc ldr r0, [pc, #252] ; 940c <mcrypt\_mutex\_register@@Base+0x4c78>

930c: e08f7007 add r7, pc, r7

9310: e0879009 add r9, r7, r9

9314: e0878008 add r8, r7, r8

9318: e5d99100 ldrb r9, [r9, #256] ; 0x100

931c: e5d88100 ldrb r8, [r8, #256] ; 0x100

9320: e0898008 add r8, r9, r8

9324: e0809890 umull r9, r0, r0, r8

9328: e1a003a0 lsr r0, r0, #7

932c: e0600400 rsb r0, r0, r0, lsl #8

9330: e0480000 sub r0, r8, r0

9334: e7d70000 ldrb r0, [r7, r0]

9338: e3540000 cmp r4, #0

933c: 135e0000 cmpne lr, #0

9340: 0a00000d beq 937c <mcrypt\_mutex\_register@@Base+0x4be8>

9344: e59f80c4 ldr r8, [pc, #196] ; 9410 <mcrypt\_mutex\_register@@Base+0x4c7c>

9348: e59f70bc ldr r7, [pc, #188] ; 940c <mcrypt\_mutex\_register@@Base+0x4c78>

934c: e08f8008 add r8, pc, r8

9350: e0884004 add r4, r8, r4

9354: e088e00e add lr, r8, lr

9358: e5d44100 ldrb r4, [r4, #256] ; 0x100

935c: e5dee100 ldrb lr, [lr, #256] ; 0x100

9360: e084400e add r4, r4, lr

9364: e08e7497 umull r7, lr, r7, r4

9368: e1a0e3ae lsr lr, lr, #7

936c: e06ee40e rsb lr, lr, lr, lsl #8

9370: e044e00e sub lr, r4, lr

9374: e7d8e00e ldrb lr, [r8, lr]

9378: e020000e eor r0, r0, lr

937c: e35c0000 cmp ip, #0

9380: 13520000 cmpne r2, #0

9384: 0a00000d beq 93c0 <mcrypt\_mutex\_register@@Base+0x4c2c>

9388: e59f4084 ldr r4, [pc, #132] ; 9414 <mcrypt\_mutex\_register@@Base+0x4c80>

938c: e59fe078 ldr lr, [pc, #120] ; 940c <mcrypt\_mutex\_register@@Base+0x4c78>

9390: e08f4004 add r4, pc, r4

9394: e084c00c add ip, r4, ip

9398: e0842002 add r2, r4, r2

939c: e5dcc100 ldrb ip, [ip, #256] ; 0x100

93a0: e5d22100 ldrb r2, [r2, #256] ; 0x100

93a4: e08cc002 add ip, ip, r2

93a8: e082ec9e umull lr, r2, lr, ip

93ac: e1a023a2 lsr r2, r2, #7

93b0: e0622402 rsb r2, r2, r2, lsl #8

93b4: e04c2002 sub r2, ip, r2

93b8: e7d42002 ldrb r2, [r4, r2]

93bc: e0200002 eor r0, r0, r2

93c0: e3560000 cmp r6, #0

93c4: 13550000 cmpne r5, #0

93c8: 08bd83f0 popeq {r4, r5, r6, r7, r8, r9, pc}

93cc: e59fc044 ldr ip, [pc, #68] ; 9418 <mcrypt\_mutex\_register@@Base+0x4c84>

93d0: e59f2034 ldr r2, [pc, #52] ; 940c <mcrypt\_mutex\_register@@Base+0x4c78>

93d4: e08fc00c add ip, pc, ip

93d8: e08c3003 add r3, ip, r3

93dc: e08c1001 add r1, ip, r1

93e0: e5d33100 ldrb r3, [r3, #256] ; 0x100

93e4: e5d11100 ldrb r1, [r1, #256] ; 0x100

93e8: e0831001 add r1, r3, r1

93ec: e0832192 umull r2, r3, r2, r1

93f0: e1a033a3 lsr r3, r3, #7

93f4: e0633403 rsb r3, r3, r3, lsl #8

93f8: e0413003 sub r3, r1, r3

93fc: e7dc3003 ldrb r3, [ip, r3]

9400: e0200003 eor r0, r0, r3

9404: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

9408: 0002c610 andeq ip, r2, r0, lsl r6

940c: 80808081 addhi r8, r0, r1, lsl #1

9410: 0002c5d0 ldrdeq ip, [r2], -r0

9414: 0002c58c andeq ip, r2, ip, lsl #11

9418: 0002c548 andeq ip, r2, r8, asr #10

941c: e59f36a0 ldr r3, [pc, #1696] ; 9ac4 <mcrypt\_mutex\_register@@Base+0x5330>

9420: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

9424: e08f3003 add r3, pc, r3

9428: e24dd04c sub sp, sp, #76 ; 0x4c

942c: e352000f cmp r2, #15

9430: e1cd01fc strd r0, [sp, #28]

9434: e5933200 ldr r3, [r3, #512] ; 0x200

9438: ca0000b4 bgt 9710 <mcrypt\_mutex\_register@@Base+0x4f7c>

943c: e3530000 cmp r3, #0

9440: 0a0000f8 beq 9828 <mcrypt\_mutex\_register@@Base+0x5094>

9444: e3a03004 mov r3, #4

9448: e1a0b003 mov fp, r3

944c: e5803004 str r3, [r0, #4]

9450: e5803000 str r3, [r0]

9454: e3a03028 mov r3, #40 ; 0x28

9458: e3a0200a mov r2, #10

945c: e58d3018 str r3, [sp, #24]

9460: e3a0302c mov r3, #44 ; 0x2c

9464: e58d300c str r3, [sp, #12]

9468: e59d401c ldr r4, [sp, #28]

946c: e59f3654 ldr r3, [pc, #1620] ; 9ac8 <mcrypt\_mutex\_register@@Base+0x5334>

9470: e59d1020 ldr r1, [sp, #32]

9474: e5843010 str r3, [r4, #16]

9478: e2433b7f sub r3, r3, #130048 ; 0x1fc00

947c: e2433c02 sub r3, r3, #512 ; 0x200

9480: e1a0010b lsl r0, fp, #2

9484: e59fe640 ldr lr, [pc, #1600] ; 9acc <mcrypt\_mutex\_register@@Base+0x5338>

9488: e5842008 str r2, [r4, #8]

948c: e5843024 str r3, [r4, #36] ; 0x24

9490: e59f2638 ldr r2, [pc, #1592] ; 9ad0 <mcrypt\_mutex\_register@@Base+0x533c>

9494: e5843028 str r3, [r4, #40] ; 0x28

9498: e584302c str r3, [r4, #44] ; 0x2c

949c: e081c000 add ip, r1, r0

94a0: e28d3028 add r3, sp, #40 ; 0x28

94a4: e584e00c str lr, [r4, #12]

94a8: e5842014 str r2, [r4, #20]

94ac: e4912004 ldr r2, [r1], #4

94b0: e151000c cmp r1, ip

94b4: e4832004 str r2, [r3], #4

94b8: 1afffffb bne 94ac <mcrypt\_mutex\_register@@Base+0x4d18>

94bc: e59d301c ldr r3, [sp, #28]

94c0: e28d2024 add r2, sp, #36 ; 0x24

94c4: e283303c add r3, r3, #60 ; 0x3c

94c8: e0830000 add r0, r3, r0

94cc: e58d3020 str r3, [sp, #32]

94d0: e5b21004 ldr r1, [r2, #4]!

94d4: e4831004 str r1, [r3], #4

94d8: e1500003 cmp r0, r3

94dc: 1afffffb bne 94d0 <mcrypt\_mutex\_register@@Base+0x4d3c>

94e0: e59d300c ldr r3, [sp, #12]

94e4: e15b0003 cmp fp, r3

94e8: aa000043 bge 95fc <mcrypt\_mutex\_register@@Base+0x4e68>

94ec: e59d201c ldr r2, [sp, #28]

94f0: e59f55dc ldr r5, [pc, #1500] ; 9ad4 <mcrypt\_mutex\_register@@Base+0x5340>

94f4: e1a04002 mov r4, r2

94f8: e06b3f0b rsb r3, fp, fp, lsl #30

94fc: e4946040 ldr r6, [r4], #64 ; 0x40

9500: e28bc010 add ip, fp, #16

9504: e59d800c ldr r8, [sp, #12]

9508: e08f5005 add r5, pc, r5

950c: e59f15c4 ldr r1, [pc, #1476] ; 9ad8 <mcrypt\_mutex\_register@@Base+0x5344>

9510: e285ab03 add sl, r5, #3072 ; 0xc00

9514: e0823103 add r3, r2, r3, lsl #2

9518: e1a07106 lsl r7, r6, #2

951c: e082c10c add ip, r2, ip, lsl #2

9520: e048000b sub r0, r8, fp

9524: e28be004 add lr, fp, #4

9528: e08f1001 add r1, pc, r1

952c: e28aa004 add sl, sl, #4

9530: e283303c add r3, r3, #60 ; 0x3c

9534: e58d1014 str r1, [sp, #20]

9538: e58d3008 str r3, [sp, #8]

953c: e58da004 str sl, [sp, #4]

9540: e51c1008 ldr r1, [ip, #-8]

9544: e59d3004 ldr r3, [sp, #4]

9548: e35b0006 cmp fp, #6

954c: e1a01461 ror r1, r1, #8

9550: e4939004 ldr r9, [r3], #4

9554: e1a0a821 lsr sl, r1, #16

9558: e6e52071 uxtab r2, r5, r1

955c: e58d3004 str r3, [sp, #4]

9560: e0853c21 add r3, r5, r1, lsr #24

9564: e1a01421 lsr r1, r1, #8

9568: e6e5a07a uxtab sl, r5, sl

956c: e6e51071 uxtab r1, r5, r1

9570: e5d33204 ldrb r3, [r3, #516] ; 0x204

9574: e5daa204 ldrb sl, [sl, #516] ; 0x204

9578: e5d22204 ldrb r2, [r2, #516] ; 0x204

957c: e5d11204 ldrb r1, [r1, #516] ; 0x204

9580: e1822c03 orr r2, r2, r3, lsl #24

9584: e5143004 ldr r3, [r4, #-4]

9588: e182280a orr r2, r2, sl, lsl #16

958c: e1822401 orr r2, r2, r1, lsl #8

9590: e0233009 eor r3, r3, r9

9594: e0233002 eor r3, r3, r2

9598: e50c3004 str r3, [ip, #-4]

959c: ca00006b bgt 9750 <mcrypt\_mutex\_register@@Base+0x4fbc>

95a0: e24e2003 sub r2, lr, #3

95a4: e1520008 cmp r2, r8

95a8: aa00000c bge 95e0 <mcrypt\_mutex\_register@@Base+0x4e4c>

95ac: e1a0900c mov r9, ip

95b0: e1a0a004 mov sl, r4

95b4: e3a01001 mov r1, #1

95b8: ea000002 b 95c8 <mcrypt\_mutex\_register@@Base+0x4e34>

95bc: e1510000 cmp r1, r0

95c0: 0a000006 beq 95e0 <mcrypt\_mutex\_register@@Base+0x4e4c>

95c4: e5193004 ldr r3, [r9, #-4]

95c8: e49a2004 ldr r2, [sl], #4

95cc: e2811001 add r1, r1, #1

95d0: e0233002 eor r3, r3, r2

95d4: e151000b cmp r1, fp

95d8: e4893004 str r3, [r9], #4

95dc: 1afffff6 bne 95bc <mcrypt\_mutex\_register@@Base+0x4e28>

95e0: e08ee006 add lr, lr, r6

95e4: e24e3004 sub r3, lr, #4

95e8: e1530008 cmp r3, r8

95ec: e0844007 add r4, r4, r7

95f0: e08cc007 add ip, ip, r7

95f4: e0400006 sub r0, r0, r6

95f8: baffffd0 blt 9540 <mcrypt\_mutex\_register@@Base+0x4dac>

95fc: e59d300c ldr r3, [sp, #12]

9600: e59d101c ldr r1, [sp, #28]

9604: e2832083 add r2, r3, #131 ; 0x83

9608: e59d3020 ldr r3, [sp, #32]

960c: e0812102 add r2, r1, r2, lsl #2

9610: e281004c add r0, r1, #76 ; 0x4c

9614: e4931004 ldr r1, [r3], #4

9618: e1500003 cmp r0, r3

961c: e4821004 str r1, [r2], #4

9620: 1afffffb bne 9614 <mcrypt\_mutex\_register@@Base+0x4e80>

9624: e59d3018 ldr r3, [sp, #24]

9628: e59d001c ldr r0, [sp, #28]

962c: e2432005 sub r2, r3, #5

9630: e0803083 add r3, r0, r3, lsl #1

9634: e1a02122 lsr r2, r2, #2

9638: e59fb49c ldr fp, [pc, #1180] ; 9adc <mcrypt\_mutex\_register@@Base+0x5348>

963c: e280106c add r1, r0, #108 ; 0x6c

9640: e1a03083 lsl r3, r3, #1

9644: e280705c add r7, r0, #92 ; 0x5c

9648: e0812202 add r2, r1, r2, lsl #4

964c: e2833f9a add r3, r3, #616 ; 0x268

9650: e58d2004 str r2, [sp, #4]

9654: e58d3008 str r3, [sp, #8]

9658: e59d3008 ldr r3, [sp, #8]

965c: e2476010 sub r6, r7, #16

9660: e043a007 sub sl, r3, r7

9664: e4965004 ldr r5, [r6], #4

9668: e1a0000b mov r0, fp

966c: e1a01005 mov r1, r5

9670: ebffff0e bl 92b0 <mcrypt\_mutex\_register@@Base+0x4b1c>

9674: e1a01005 mov r1, r5

9678: e1a09000 mov r9, r0

967c: e59f045c ldr r0, [pc, #1116] ; 9ae0 <mcrypt\_mutex\_register@@Base+0x534c>

9680: ebffff0a bl 92b0 <mcrypt\_mutex\_register@@Base+0x4b1c>

9684: e1a01005 mov r1, r5

9688: e1a04000 mov r4, r0

968c: e59f0450 ldr r0, [pc, #1104] ; 9ae4 <mcrypt\_mutex\_register@@Base+0x5350>

9690: ebffff06 bl 92b0 <mcrypt\_mutex\_register@@Base+0x4b1c>

9694: e1a01005 mov r1, r5

9698: e1a04804 lsl r4, r4, #16

969c: e1844c09 orr r4, r4, r9, lsl #24

96a0: e1a08000 mov r8, r0

96a4: e59f043c ldr r0, [pc, #1084] ; 9ae8 <mcrypt\_mutex\_register@@Base+0x5354>

96a8: ebffff00 bl 92b0 <mcrypt\_mutex\_register@@Base+0x4b1c>

96ac: e1570006 cmp r7, r6

96b0: e1840000 orr r0, r4, r0

96b4: e1800408 orr r0, r0, r8, lsl #8

96b8: e48a0004 str r0, [sl], #4

96bc: 1affffe8 bne 9664 <mcrypt\_mutex\_register@@Base+0x4ed0>

96c0: e59d3004 ldr r3, [sp, #4]

96c4: e2877010 add r7, r7, #16

96c8: e1530007 cmp r3, r7

96cc: 1affffe1 bne 9658 <mcrypt\_mutex\_register@@Base+0x4ec4>

96d0: e59d1018 ldr r1, [sp, #24]

96d4: e59dc00c ldr ip, [sp, #12]

96d8: e281208b add r2, r1, #139 ; 0x8b

96dc: e59d001c ldr r0, [sp, #28]

96e0: e042200c sub r2, r2, ip

96e4: e281300f add r3, r1, #15

96e8: e0802102 add r2, r0, r2, lsl #2

96ec: e0803103 add r3, r0, r3, lsl #2

96f0: e2811001 add r1, r1, #1

96f4: e4930004 ldr r0, [r3], #4

96f8: e151000c cmp r1, ip

96fc: e4820004 str r0, [r2], #4

9700: bafffffa blt 96f0 <mcrypt\_mutex\_register@@Base+0x4f5c>

9704: e3a00000 mov r0, #0

9708: e28dd04c add sp, sp, #76 ; 0x4c

970c: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

9710: e3530000 cmp r3, #0

9714: e1a0b142 asr fp, r2, #2

9718: 0a000043 beq 982c <mcrypt\_mutex\_register@@Base+0x5098>

971c: e59d201c ldr r2, [sp, #28]

9720: e3a03004 mov r3, #4

9724: e35b0004 cmp fp, #4

9728: e582b000 str fp, [r2]

972c: e5823004 str r3, [r2, #4]

9730: 0affff47 beq 9454 <mcrypt\_mutex\_register@@Base+0x4cc0>

9734: e28b3007 add r3, fp, #7

9738: e28b2006 add r2, fp, #6

973c: e1a03103 lsl r3, r3, #2

9740: e58d300c str r3, [sp, #12]

9744: e2433004 sub r3, r3, #4

9748: e58d3018 str r3, [sp, #24]

974c: eaffff45 b 9468 <mcrypt\_mutex\_register@@Base+0x4cd4>

9750: e59d9008 ldr r9, [sp, #8]

9754: e24e3003 sub r3, lr, #3

9758: e24c1004 sub r1, ip, #4

975c: e1530008 cmp r3, r8

9760: aa000006 bge 9780 <mcrypt\_mutex\_register@@Base+0x4fec>

9764: e7992103 ldr r2, [r9, r3, lsl #2]

9768: e591a000 ldr sl, [r1]

976c: e2833001 add r3, r3, #1

9770: e022200a eor r2, r2, sl

9774: e15e0003 cmp lr, r3

9778: e5a12004 str r2, [r1, #4]!

977c: 1afffff6 bne 975c <mcrypt\_mutex\_register@@Base+0x4fc8>

9780: e15e0008 cmp lr, r8

9784: aa000013 bge 97d8 <mcrypt\_mutex\_register@@Base+0x5044>

9788: e59c2008 ldr r2, [ip, #8]

978c: e594300c ldr r3, [r4, #12]

9790: e58d3010 str r3, [sp, #16]

9794: e59d3014 ldr r3, [sp, #20]

9798: e1a01822 lsr r1, r2, #16

979c: e0839c22 add r9, r3, r2, lsr #24

97a0: e6e3a072 uxtab sl, r3, r2

97a4: e1a02422 lsr r2, r2, #8

97a8: e6e31071 uxtab r1, r3, r1

97ac: e6e32072 uxtab r2, r3, r2

97b0: e5d99204 ldrb r9, [r9, #516] ; 0x204

97b4: e5da3204 ldrb r3, [sl, #516] ; 0x204

97b8: e5d11204 ldrb r1, [r1, #516] ; 0x204

97bc: e5d22204 ldrb r2, [r2, #516] ; 0x204

97c0: e1833c09 orr r3, r3, r9, lsl #24

97c4: e59da010 ldr sl, [sp, #16]

97c8: e1833801 orr r3, r3, r1, lsl #16

97cc: e1833402 orr r3, r3, r2, lsl #8

97d0: e02aa003 eor sl, sl, r3

97d4: e58ca00c str sl, [ip, #12]

97d8: e28e3001 add r3, lr, #1

97dc: e1530008 cmp r3, r8

97e0: aaffff7e bge 95e0 <mcrypt\_mutex\_register@@Base+0x4e4c>

97e4: e59c300c ldr r3, [ip, #12]

97e8: e5942010 ldr r2, [r4, #16]

97ec: e1a0100c mov r1, ip

97f0: e0233002 eor r3, r3, r2

97f4: e5a13010 str r3, [r1, #16]!

97f8: e2849014 add r9, r4, #20

97fc: e3a03006 mov r3, #6

9800: e1530000 cmp r3, r0

9804: e2833001 add r3, r3, #1

9808: 0affff74 beq 95e0 <mcrypt\_mutex\_register@@Base+0x4e4c>

980c: e4992004 ldr r2, [r9], #4

9810: e591a000 ldr sl, [r1]

9814: e153000b cmp r3, fp

9818: e022200a eor r2, r2, sl

981c: e5a12004 str r2, [r1, #4]!

9820: 1afffff6 bne 9800 <mcrypt\_mutex\_register@@Base+0x506c>

9824: eaffff6d b 95e0 <mcrypt\_mutex\_register@@Base+0x4e4c>

9828: e3a0b004 mov fp, #4

982c: e59f12b8 ldr r1, [pc, #696] ; 9aec <mcrypt\_mutex\_register@@Base+0x5358>

9830: e3a00001 mov r0, #1

9834: e08f1001 add r1, pc, r1

9838: e2813c01 add r3, r1, #256 ; 0x100

983c: e59fc2ac ldr ip, [pc, #684] ; 9af0 <mcrypt\_mutex\_register@@Base+0x535c>

9840: e3a02000 mov r2, #0

9844: e081e000 add lr, r1, r0

9848: e5c10103 strb r0, [r1, #259] ; 0x103

984c: e1c320b0 strh r2, [r3]

9850: e1c1c0b0 strh ip, [r1]

9854: e08e0000 add r0, lr, r0

9858: e28140ff add r4, r1, #255 ; 0xff

985c: e3a03003 mov r3, #3

9860: ea000002 b 9870 <mcrypt\_mutex\_register@@Base+0x50dc>

9864: e3130080 tst r3, #128 ; 0x80

9868: 13a0201b movne r2, #27

986c: 03a02000 moveq r2, #0

9870: e0222083 eor r2, r2, r3, lsl #1

9874: e0233002 eor r3, r3, r2

9878: e1a0c000 mov ip, r0

987c: e6ef3073 uxtb r3, r3

9880: e0812003 add r2, r1, r3

9884: e4c03001 strb r3, [r0], #1

9888: e15c0004 cmp ip, r4

988c: e040c00e sub ip, r0, lr

9890: e5c2c100 strb ip, [r2, #256] ; 0x100

9894: 1afffff2 bne 9864 <mcrypt\_mutex\_register@@Base+0x50d0>

9898: e2818c01 add r8, r1, #256 ; 0x100

989c: e1a04008 mov r4, r8

98a0: e2810f81 add r0, r1, #516 ; 0x204

98a4: e28880ff add r8, r8, #255 ; 0xff

98a8: e2649001 rsb r9, r4, #1

98ac: e2445c01 sub r5, r4, #256 ; 0x100

98b0: e1a06000 mov r6, r0

98b4: e3a02063 mov r2, #99 ; 0x63

98b8: e3a03000 mov r3, #0

98bc: e5c12204 strb r2, [r1, #516] ; 0x204

98c0: e5c13367 strb r3, [r1, #871] ; 0x367

98c4: e0891004 add r1, r9, r4

98c8: e5f43001 ldrb r3, [r4, #1]!

98cc: e26330ff rsb r3, r3, #255 ; 0xff

98d0: e1580004 cmp r8, r4

98d4: e7d5e003 ldrb lr, [r5, r3]

98d8: e1a0208e lsl r2, lr, #1

98dc: e18223ae orr r2, r2, lr, lsr #7

98e0: e6ef2072 uxtb r2, r2

98e4: e1a03082 lsl r3, r2, #1

98e8: e18333a2 orr r3, r3, r2, lsr #7

98ec: e6ef3073 uxtb r3, r3

98f0: e1a0c083 lsl ip, r3, #1

98f4: e18cc3a3 orr ip, ip, r3, lsr #7

98f8: e6efc07c uxtb ip, ip

98fc: e1a0708c lsl r7, ip, #1

9900: e18773ac orr r7, r7, ip, lsr #7

9904: e6af7077 sxtb r7, r7

9908: e02ee007 eor lr, lr, r7

990c: e022200e eor r2, r2, lr

9910: e0233002 eor r3, r3, r2

9914: e6ef3073 uxtb r3, r3

9918: e023300c eor r3, r3, ip

991c: e2233063 eor r3, r3, #99 ; 0x63

9920: e0852003 add r2, r5, r3

9924: e5e63001 strb r3, [r6, #1]!

9928: e5c21304 strb r1, [r2, #772] ; 0x304

992c: 1affffe4 bne 98c4 <mcrypt\_mutex\_register@@Base+0x5130>

9930: e285cb03 add ip, r5, #3072 ; 0xc00

9934: e3a03001 mov r3, #1

9938: e28c2004 add r2, ip, #4

993c: e5853c04 str r3, [r5, #3076] ; 0xc04

9940: e28cc078 add ip, ip, #120 ; 0x78

9944: e3a01000 mov r1, #0

9948: ea000003 b 995c <mcrypt\_mutex\_register@@Base+0x51c8>

994c: e3130080 tst r3, #128 ; 0x80

9950: 13a0101b movne r1, #27

9954: 03a01000 moveq r1, #0

9958: e5a23004 str r3, [r2, #4]!

995c: e1a03083 lsl r3, r3, #1

9960: e15c0002 cmp ip, r2

9964: e6ef3073 uxtb r3, r3

9968: e0233001 eor r3, r3, r1

996c: 1afffff6 bne 994c <mcrypt\_mutex\_register@@Base+0x51b8>

9970: e59f617c ldr r6, [pc, #380] ; 9af4 <mcrypt\_mutex\_register@@Base+0x5360>

9974: e59f417c ldr r4, [pc, #380] ; 9af8 <mcrypt\_mutex\_register@@Base+0x5364>

9978: e08f6006 add r6, pc, r6

997c: e286ec02 add lr, r6, #512 ; 0x200

9980: e5d6310b ldrb r3, [r6, #267] ; 0x10b

9984: e2867c03 add r7, r6, #768 ; 0x300

9988: e59f916c ldr r9, [pc, #364] ; 9afc <mcrypt\_mutex\_register@@Base+0x5368>

998c: e58d300c str r3, [sp, #12]

9990: e5d6310d ldrb r3, [r6, #269] ; 0x10d

9994: e2868b01 add r8, r6, #1024 ; 0x400

9998: e08f4004 add r4, pc, r4

999c: e58d3010 str r3, [sp, #16]

99a0: e5d63109 ldrb r3, [r6, #265] ; 0x109

99a4: e28ee003 add lr, lr, #3

99a8: e2877003 add r7, r7, #3

99ac: e58d3014 str r3, [sp, #20]

99b0: e5d6310e ldrb r3, [r6, #270] ; 0x10e

99b4: e2866b02 add r6, r6, #2048 ; 0x800

99b8: e58db024 str fp, [sp, #36] ; 0x24

99bc: e58d3018 str r3, [sp, #24]

99c0: e28030ff add r3, r0, #255 ; 0xff

99c4: e58d3004 str r3, [sp, #4]

99c8: e5fe1001 ldrb r1, [lr, #1]!

99cc: e5f70001 ldrb r0, [r7, #1]!

99d0: e3110080 tst r1, #128 ; 0x80

99d4: e1a02081 lsl r2, r1, #1

99d8: 13a0301b movne r3, #27

99dc: 03a03000 moveq r3, #0

99e0: e6ef2072 uxtb r2, r2

99e4: e0222003 eor r2, r2, r3

99e8: e1a03401 lsl r3, r1, #8

99ec: e021c002 eor ip, r1, r2

99f0: e1833801 orr r3, r3, r1, lsl #16

99f4: e1832002 orr r2, r3, r2

99f8: e1822c0c orr r2, r2, ip, lsl #24

99fc: e3500000 cmp r0, #0

9a00: e0845000 add r5, r4, r0

9a04: e1a0a000 mov sl, r0

9a08: e5a62004 str r2, [r6, #4]!

9a0c: 0a000022 beq 9a9c <mcrypt\_mutex\_register@@Base+0x5308>

9a10: e5d50100 ldrb r0, [r5, #256] ; 0x100

9a14: e59d3010 ldr r3, [sp, #16]

9a18: e080b003 add fp, r0, r3

9a1c: e59d300c ldr r3, [sp, #12]

9a20: e083a000 add sl, r3, r0

9a24: e0813b99 umull r3, r1, r9, fp

9a28: e0823a99 umull r3, r2, r9, sl

9a2c: e59d3018 ldr r3, [sp, #24]

9a30: e58d2008 str r2, [sp, #8]

9a34: e0805003 add r5, r0, r3

9a38: e59d3014 ldr r3, [sp, #20]

9a3c: e1a013a1 lsr r1, r1, #7

9a40: e0800003 add r0, r0, r3

9a44: e08c3599 umull r3, ip, r9, r5

9a48: e0832099 umull r2, r3, r9, r0

9a4c: e59d2008 ldr r2, [sp, #8]

9a50: e0611401 rsb r1, r1, r1, lsl #8

9a54: e04b1001 sub r1, fp, r1

9a58: e1a023a2 lsr r2, r2, #7

9a5c: e1a0c3ac lsr ip, ip, #7

9a60: e0622402 rsb r2, r2, r2, lsl #8

9a64: e1a033a3 lsr r3, r3, #7

9a68: e04a2002 sub r2, sl, r2

9a6c: e7d41001 ldrb r1, [r4, r1]

9a70: e06cc40c rsb ip, ip, ip, lsl #8

9a74: e0633403 rsb r3, r3, r3, lsl #8

9a78: e0403003 sub r3, r0, r3

9a7c: e7d42002 ldrb r2, [r4, r2]

9a80: e045c00c sub ip, r5, ip

9a84: e7d43003 ldrb r3, [r4, r3]

9a88: e7d4000c ldrb r0, [r4, ip]

9a8c: e1a01801 lsl r1, r1, #16

9a90: e1811c02 orr r1, r1, r2, lsl #24

9a94: e1811000 orr r1, r1, r0

9a98: e181a403 orr sl, r1, r3, lsl #8

9a9c: e59d3004 ldr r3, [sp, #4]

9aa0: e5a8a004 str sl, [r8, #4]!

9aa4: e153000e cmp r3, lr

9aa8: 1affffc6 bne 99c8 <mcrypt\_mutex\_register@@Base+0x5234>

9aac: e59f304c ldr r3, [pc, #76] ; 9b00 <mcrypt\_mutex\_register@@Base+0x536c>

9ab0: e3a02001 mov r2, #1

9ab4: e08f3003 add r3, pc, r3

9ab8: e59db024 ldr fp, [sp, #36] ; 0x24

9abc: e5832200 str r2, [r3, #512] ; 0x200

9ac0: eaffff15 b 971c <mcrypt\_mutex\_register@@Base+0x4f88>

9ac4: 0002c4f8 strdeq ip, [r2], -r8

9ac8: 00030003 andeq r0, r3, r3

9acc: 02030201 andeq r0, r3, #268435456 ; 0x10000000

9ad0: 02010001 andeq r0, r1, #1

9ad4: 0002c414 andeq ip, r2, r4, lsl r4

9ad8: 0002c3f4 strdeq ip, [r2], -r4

9adc: 0e090d0b cdpeq 13, 0, cr0, cr9, cr11, {0}

9ae0: 0b0e090d bleq 38bf1c <mcrypt\_mutex\_register@@Base+0x387788>

9ae4: 0d0b0e09 stceq 14, cr0, [fp, #-36] ; 0xffffffdc

9ae8: 090d0b0e stmdbeq sp, {r1, r2, r3, r8, r9, fp}

9aec: 0002c0e8 andeq ip, r2, r8, ror #1

9af0: 00000301 andeq r0, r0, r1, lsl #6

9af4: 0002bfa4 andeq fp, r2, r4, lsr #31

9af8: 0002bf84 andeq fp, r2, r4, lsl #31

9afc: 80808081 addhi r8, r0, r1, lsl #1

9b00: 0002be68 andeq fp, r2, r8, ror #28

9b04: e5903004 ldr r3, [r0, #4]

9b08: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

9b0c: e24dd05c sub sp, sp, #92 ; 0x5c

9b10: e3530000 cmp r3, #0

9b14: e58d1014 str r1, [sp, #20]

9b18: e58d3004 str r3, [sp, #4]

9b1c: da000086 ble 9d3c <mcrypt\_mutex\_register@@Base+0x55a8>

9b20: e1a03103 lsl r3, r3, #2

9b24: e1a02001 mov r2, r1

9b28: e0814003 add r4, r1, r3

9b2c: e280c03c add ip, r0, #60 ; 0x3c

9b30: e28d1014 add r1, sp, #20

9b34: e58d300c str r3, [sp, #12]

9b38: e4923004 ldr r3, [r2], #4

9b3c: e49ce004 ldr lr, [ip], #4

9b40: e1540002 cmp r4, r2

9b44: e023300e eor r3, r3, lr

9b48: e5a13004 str r3, [r1, #4]!

9b4c: 1afffff9 bne 9b38 <mcrypt\_mutex\_register@@Base+0x53a4>

9b50: e5903008 ldr r3, [r0, #8]

9b54: e3530001 cmp r3, #1

9b58: e58d3008 str r3, [sp, #8]

9b5c: da00007e ble 9d5c <mcrypt\_mutex\_register@@Base+0x55c8>

9b60: e59f1208 ldr r1, [pc, #520] ; 9d70 <mcrypt\_mutex\_register@@Base+0x55dc>

9b64: e59d3004 ldr r3, [sp, #4]

9b68: e28db038 add fp, sp, #56 ; 0x38

9b6c: e28d4018 add r4, sp, #24

9b70: e3a0a001 mov sl, #1

9b74: e08f1001 add r1, pc, r1

9b78: e58d3000 str r3, [sp]

9b7c: e58d0010 str r0, [sp, #16]

9b80: e59d3004 ldr r3, [sp, #4]

9b84: e3530000 cmp r3, #0

9b88: da000026 ble 9c28 <mcrypt\_mutex\_register@@Base+0x5494>

9b8c: e59d3000 ldr r3, [sp]

9b90: e59d2010 ldr r2, [sp, #16]

9b94: e1a0e004 mov lr, r4

9b98: e0825103 add r5, r2, r3, lsl #2

9b9c: e59d300c ldr r3, [sp, #12]

9ba0: e2855038 add r5, r5, #56 ; 0x38

9ba4: e24b6004 sub r6, fp, #4

9ba8: e0849003 add r9, r4, r3

9bac: e5d2700e ldrb r7, [r2, #14]

9bb0: e5d2c00c ldrb ip, [r2, #12]

9bb4: e5d2000d ldrb r0, [r2, #13]

9bb8: e49e3004 ldr r3, [lr], #4

9bbc: e7947107 ldr r7, [r4, r7, lsl #2]

9bc0: e084c10c add ip, r4, ip, lsl #2

9bc4: e0840100 add r0, r4, r0, lsl #2

9bc8: e5dcc001 ldrb ip, [ip, #1]

9bcc: e5d00002 ldrb r0, [r0, #2]

9bd0: e1a07c27 lsr r7, r7, #24

9bd4: e6ef3073 uxtb r3, r3

9bd8: e0817107 add r7, r1, r7, lsl #2

9bdc: e0813103 add r3, r1, r3, lsl #2

9be0: e081c10c add ip, r1, ip, lsl #2

9be4: e5977804 ldr r7, [r7, #2052] ; 0x804

9be8: e5933804 ldr r3, [r3, #2052] ; 0x804

9bec: e5b58004 ldr r8, [r5, #4]!

9bf0: e0810100 add r0, r1, r0, lsl #2

9bf4: e59cc804 ldr ip, [ip, #2052] ; 0x804

9bf8: e0233008 eor r3, r3, r8

9bfc: e5900804 ldr r0, [r0, #2052] ; 0x804

9c00: e0233467 eor r3, r3, r7, ror #8

9c04: e0233c6c eor r3, r3, ip, ror #24

9c08: e0233860 eor r3, r3, r0, ror #16

9c0c: e159000e cmp r9, lr

9c10: e5a63004 str r3, [r6, #4]!

9c14: e2822003 add r2, r2, #3

9c18: 1affffe3 bne 9bac <mcrypt\_mutex\_register@@Base+0x5418>

9c1c: e1cd20d0 ldrd r2, [sp]

9c20: e0833002 add r3, r3, r2

9c24: e58d3000 str r3, [sp]

9c28: e59d3008 ldr r3, [sp, #8]

9c2c: e28aa001 add sl, sl, #1

9c30: e153000a cmp r3, sl

9c34: 11a03004 movne r3, r4

9c38: 11a0400b movne r4, fp

9c3c: 11a0b003 movne fp, r3

9c40: 1affffce bne 9b80 <mcrypt\_mutex\_register@@Base+0x53ec>

9c44: e59d3004 ldr r3, [sp, #4]

9c48: e59d0010 ldr r0, [sp, #16]

9c4c: e3530000 cmp r3, #0

9c50: da000037 ble 9d34 <mcrypt\_mutex\_register@@Base+0x55a0>

9c54: e59d3000 ldr r3, [sp]

9c58: e59f1114 ldr r1, [pc, #276] ; 9d74 <mcrypt\_mutex\_register@@Base+0x55e0>

9c5c: e2444004 sub r4, r4, #4

9c60: e0805103 add r5, r0, r3, lsl #2

9c64: e59d300c ldr r3, [sp, #12]

9c68: e08f1001 add r1, pc, r1

9c6c: e2855038 add r5, r5, #56 ; 0x38

9c70: e08ba003 add sl, fp, r3

9c74: e1a06004 mov r6, r4

9c78: e1a0e00b mov lr, fp

9c7c: e1a02000 mov r2, r0

9c80: e5d2700c ldrb r7, [r2, #12]

9c84: e5d2800e ldrb r8, [r2, #14]

9c88: e5d2c00d ldrb ip, [r2, #13]

9c8c: e49e3004 ldr r3, [lr], #4

9c90: e08b7107 add r7, fp, r7, lsl #2

9c94: e79b8108 ldr r8, [fp, r8, lsl #2]

9c98: e08bc10c add ip, fp, ip, lsl #2

9c9c: e5d77001 ldrb r7, [r7, #1]

9ca0: e5dcc002 ldrb ip, [ip, #2]

9ca4: e6e13073 uxtab r3, r1, r3

9ca8: e0818c28 add r8, r1, r8, lsr #24

9cac: e0817007 add r7, r1, r7

9cb0: e5d88204 ldrb r8, [r8, #516] ; 0x204

9cb4: e5d33204 ldrb r3, [r3, #516] ; 0x204

9cb8: e081c00c add ip, r1, ip

9cbc: e5d77204 ldrb r7, [r7, #516] ; 0x204

9cc0: e5b59004 ldr r9, [r5, #4]!

9cc4: e5dcc204 ldrb ip, [ip, #516] ; 0x204

9cc8: e0233009 eor r3, r3, r9

9ccc: e0233468 eor r3, r3, r8, ror #8

9cd0: e0233c67 eor r3, r3, r7, ror #24

9cd4: e023386c eor r3, r3, ip, ror #16

9cd8: e15a000e cmp sl, lr

9cdc: e5a63004 str r3, [r6, #4]!

9ce0: e2822003 add r2, r2, #3

9ce4: 1affffe5 bne 9c80 <mcrypt\_mutex\_register@@Base+0x54ec>

9ce8: e3a02000 mov r2, #0

9cec: e59d1014 ldr r1, [sp, #20]

9cf0: e24bb004 sub fp, fp, #4

9cf4: e1a0c002 mov ip, r2

9cf8: e5b43004 ldr r3, [r4, #4]!

9cfc: e2822001 add r2, r2, #1

9d00: e5c13000 strb r3, [r1]

9d04: e1a05423 lsr r5, r3, #8

9d08: e1a0e823 lsr lr, r3, #16

9d0c: e1a03c23 lsr r3, r3, #24

9d10: e5c15001 strb r5, [r1, #1]

9d14: e5c1e002 strb lr, [r1, #2]

9d18: e5c13003 strb r3, [r1, #3]

9d1c: e5903004 ldr r3, [r0, #4]

9d20: e584c000 str ip, [r4]

9d24: e1530002 cmp r3, r2

9d28: e5abc004 str ip, [fp, #4]!

9d2c: e2811004 add r1, r1, #4

9d30: cafffff0 bgt 9cf8 <mcrypt\_mutex\_register@@Base+0x5564>

9d34: e28dd05c add sp, sp, #92 ; 0x5c

9d38: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

9d3c: e5903008 ldr r3, [r0, #8]

9d40: e3530001 cmp r3, #1

9d44: e58d3008 str r3, [sp, #8]

9d48: dafffff9 ble 9d34 <mcrypt\_mutex\_register@@Base+0x55a0>

9d4c: e59d3004 ldr r3, [sp, #4]

9d50: e1a03103 lsl r3, r3, #2

9d54: e58d300c str r3, [sp, #12]

9d58: eaffff80 b 9b60 <mcrypt\_mutex\_register@@Base+0x53cc>

9d5c: e59d3004 ldr r3, [sp, #4]

9d60: e28d4038 add r4, sp, #56 ; 0x38

9d64: e58d3000 str r3, [sp]

9d68: e28db018 add fp, sp, #24

9d6c: eaffffb8 b 9c54 <mcrypt\_mutex\_register@@Base+0x54c0>

9d70: 0002bda8 andeq fp, r2, r8, lsr #27

9d74: 0002bcb4 ; <UNDEFINED> instruction: 0x0002bcb4

9d78: e5903004 ldr r3, [r0, #4]

9d7c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

9d80: e24dd05c sub sp, sp, #92 ; 0x5c

9d84: e3530000 cmp r3, #0

9d88: e58d1014 str r1, [sp, #20]

9d8c: e58d3004 str r3, [sp, #4]

9d90: da000086 ble 9fb0 <mcrypt\_mutex\_register@@Base+0x581c>

9d94: e1a03103 lsl r3, r3, #2

9d98: e1a02001 mov r2, r1

9d9c: e0814003 add r4, r1, r3

9da0: e280cf87 add ip, r0, #540 ; 0x21c

9da4: e28d1014 add r1, sp, #20

9da8: e58d300c str r3, [sp, #12]

9dac: e4923004 ldr r3, [r2], #4

9db0: e49ce004 ldr lr, [ip], #4

9db4: e1540002 cmp r4, r2

9db8: e023300e eor r3, r3, lr

9dbc: e5a13004 str r3, [r1, #4]!

9dc0: 1afffff9 bne 9dac <mcrypt\_mutex\_register@@Base+0x5618>

9dc4: e5903008 ldr r3, [r0, #8]

9dc8: e3530001 cmp r3, #1

9dcc: e58d3008 str r3, [sp, #8]

9dd0: da00007e ble 9fd0 <mcrypt\_mutex\_register@@Base+0x583c>

9dd4: e59f1208 ldr r1, [pc, #520] ; 9fe4 <mcrypt\_mutex\_register@@Base+0x5850>

9dd8: e59d3004 ldr r3, [sp, #4]

9ddc: e28db038 add fp, sp, #56 ; 0x38

9de0: e28d4018 add r4, sp, #24

9de4: e3a0a001 mov sl, #1

9de8: e08f1001 add r1, pc, r1

9dec: e58d3000 str r3, [sp]

9df0: e58d0010 str r0, [sp, #16]

9df4: e59d3004 ldr r3, [sp, #4]

9df8: e3530000 cmp r3, #0

9dfc: da000026 ble 9e9c <mcrypt\_mutex\_register@@Base+0x5708>

9e00: e59d3000 ldr r3, [sp]

9e04: e59d2010 ldr r2, [sp, #16]

9e08: e1a0e004 mov lr, r4

9e0c: e0825103 add r5, r2, r3, lsl #2

9e10: e59d300c ldr r3, [sp, #12]

9e14: e2855f86 add r5, r5, #536 ; 0x218

9e18: e24b6004 sub r6, fp, #4

9e1c: e0849003 add r9, r4, r3

9e20: e5d27026 ldrb r7, [r2, #38] ; 0x26

9e24: e5d2c024 ldrb ip, [r2, #36] ; 0x24

9e28: e5d20025 ldrb r0, [r2, #37] ; 0x25

9e2c: e49e3004 ldr r3, [lr], #4

9e30: e7947107 ldr r7, [r4, r7, lsl #2]

9e34: e084c10c add ip, r4, ip, lsl #2

9e38: e0840100 add r0, r4, r0, lsl #2

9e3c: e5dcc001 ldrb ip, [ip, #1]

9e40: e5d00002 ldrb r0, [r0, #2]

9e44: e1a07c27 lsr r7, r7, #24

9e48: e6ef3073 uxtb r3, r3

9e4c: e0817107 add r7, r1, r7, lsl #2

9e50: e0813103 add r3, r1, r3, lsl #2

9e54: e081c10c add ip, r1, ip, lsl #2

9e58: e5977404 ldr r7, [r7, #1028] ; 0x404

9e5c: e5933404 ldr r3, [r3, #1028] ; 0x404

9e60: e5b58004 ldr r8, [r5, #4]!

9e64: e0810100 add r0, r1, r0, lsl #2

9e68: e59cc404 ldr ip, [ip, #1028] ; 0x404

9e6c: e0233008 eor r3, r3, r8

9e70: e5900404 ldr r0, [r0, #1028] ; 0x404

9e74: e0233467 eor r3, r3, r7, ror #8

9e78: e0233c6c eor r3, r3, ip, ror #24

9e7c: e0233860 eor r3, r3, r0, ror #16

9e80: e159000e cmp r9, lr

9e84: e5a63004 str r3, [r6, #4]!

9e88: e2822003 add r2, r2, #3

9e8c: 1affffe3 bne 9e20 <mcrypt\_mutex\_register@@Base+0x568c>

9e90: e1cd20d0 ldrd r2, [sp]

9e94: e0833002 add r3, r3, r2

9e98: e58d3000 str r3, [sp]

9e9c: e59d3008 ldr r3, [sp, #8]

9ea0: e28aa001 add sl, sl, #1

9ea4: e153000a cmp r3, sl

9ea8: 11a03004 movne r3, r4

9eac: 11a0400b movne r4, fp

9eb0: 11a0b003 movne fp, r3

9eb4: 1affffce bne 9df4 <mcrypt\_mutex\_register@@Base+0x5660>

9eb8: e59d3004 ldr r3, [sp, #4]

9ebc: e59d0010 ldr r0, [sp, #16]

9ec0: e3530000 cmp r3, #0

9ec4: da000037 ble 9fa8 <mcrypt\_mutex\_register@@Base+0x5814>

9ec8: e59d3000 ldr r3, [sp]

9ecc: e59f1114 ldr r1, [pc, #276] ; 9fe8 <mcrypt\_mutex\_register@@Base+0x5854>

9ed0: e2444004 sub r4, r4, #4

9ed4: e0805103 add r5, r0, r3, lsl #2

9ed8: e59d300c ldr r3, [sp, #12]

9edc: e08f1001 add r1, pc, r1

9ee0: e2855f86 add r5, r5, #536 ; 0x218

9ee4: e08ba003 add sl, fp, r3

9ee8: e1a06004 mov r6, r4

9eec: e1a0e00b mov lr, fp

9ef0: e1a02000 mov r2, r0

9ef4: e5d27024 ldrb r7, [r2, #36] ; 0x24

9ef8: e5d28026 ldrb r8, [r2, #38] ; 0x26

9efc: e5d2c025 ldrb ip, [r2, #37] ; 0x25

9f00: e49e3004 ldr r3, [lr], #4

9f04: e08b7107 add r7, fp, r7, lsl #2

9f08: e79b8108 ldr r8, [fp, r8, lsl #2]

9f0c: e08bc10c add ip, fp, ip, lsl #2

9f10: e5d77001 ldrb r7, [r7, #1]

9f14: e5dcc002 ldrb ip, [ip, #2]

9f18: e6e13073 uxtab r3, r1, r3

9f1c: e0818c28 add r8, r1, r8, lsr #24

9f20: e0817007 add r7, r1, r7

9f24: e5d88304 ldrb r8, [r8, #772] ; 0x304

9f28: e5d33304 ldrb r3, [r3, #772] ; 0x304

9f2c: e081c00c add ip, r1, ip

9f30: e5d77304 ldrb r7, [r7, #772] ; 0x304

9f34: e5b59004 ldr r9, [r5, #4]!

9f38: e5dcc304 ldrb ip, [ip, #772] ; 0x304

9f3c: e0233009 eor r3, r3, r9

9f40: e0233468 eor r3, r3, r8, ror #8

9f44: e0233c67 eor r3, r3, r7, ror #24

9f48: e023386c eor r3, r3, ip, ror #16

9f4c: e15a000e cmp sl, lr

9f50: e5a63004 str r3, [r6, #4]!

9f54: e2822003 add r2, r2, #3

9f58: 1affffe5 bne 9ef4 <mcrypt\_mutex\_register@@Base+0x5760>

9f5c: e3a02000 mov r2, #0

9f60: e59d1014 ldr r1, [sp, #20]

9f64: e24bb004 sub fp, fp, #4

9f68: e1a0c002 mov ip, r2

9f6c: e5b43004 ldr r3, [r4, #4]!

9f70: e2822001 add r2, r2, #1

9f74: e5c13000 strb r3, [r1]

9f78: e1a05423 lsr r5, r3, #8

9f7c: e1a0e823 lsr lr, r3, #16

9f80: e1a03c23 lsr r3, r3, #24

9f84: e5c15001 strb r5, [r1, #1]

9f88: e5c1e002 strb lr, [r1, #2]

9f8c: e5c13003 strb r3, [r1, #3]

9f90: e5903004 ldr r3, [r0, #4]

9f94: e584c000 str ip, [r4]

9f98: e1530002 cmp r3, r2

9f9c: e5abc004 str ip, [fp, #4]!

9fa0: e2811004 add r1, r1, #4

9fa4: cafffff0 bgt 9f6c <mcrypt\_mutex\_register@@Base+0x57d8>

9fa8: e28dd05c add sp, sp, #92 ; 0x5c

9fac: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

9fb0: e5903008 ldr r3, [r0, #8]

9fb4: e3530001 cmp r3, #1

9fb8: e58d3008 str r3, [sp, #8]

9fbc: dafffff9 ble 9fa8 <mcrypt\_mutex\_register@@Base+0x5814>

9fc0: e59d3004 ldr r3, [sp, #4]

9fc4: e1a03103 lsl r3, r3, #2

9fc8: e58d300c str r3, [sp, #12]

9fcc: eaffff80 b 9dd4 <mcrypt\_mutex\_register@@Base+0x5640>

9fd0: e59d3004 ldr r3, [sp, #4]

9fd4: e28d4038 add r4, sp, #56 ; 0x38

9fd8: e58d3000 str r3, [sp]

9fdc: e28db018 add fp, sp, #24

9fe0: eaffffb8 b 9ec8 <mcrypt\_mutex\_register@@Base+0x5734>

9fe4: 0002bb34 andeq fp, r2, r4, lsr fp

9fe8: 0002ba40 andeq fp, r2, r0, asr #20

9fec: e3a00fff mov r0, #1020 ; 0x3fc

9ff0: e12fff1e bx lr

9ff4: e3a00010 mov r0, #16

9ff8: e12fff1e bx lr

9ffc: e3a00001 mov r0, #1

a000: e12fff1e bx lr

a004: e3a00020 mov r0, #32

a008: e12fff1e bx lr

a00c: e3a03003 mov r3, #3

a010: e5803000 str r3, [r0]

a014: e59f0004 ldr r0, [pc, #4] ; a020 <mcrypt\_mutex\_register@@Base+0x588c>

a018: e08f0000 add r0, pc, r0

a01c: e12fff1e bx lr

a020: 00016748 andeq r6, r1, r8, asr #14

a024: e59f0004 ldr r0, [pc, #4] ; a030 <mcrypt\_mutex\_register@@Base+0x589c>

a028: e08f0000 add r0, pc, r0

a02c: e12fff1e bx lr

a030: 00016744 andeq r6, r1, r4, asr #14

a034: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

a038: e24ddf42 sub sp, sp, #264 ; 0x108

a03c: ebffffec bl 9ff4 <mcrypt\_mutex\_register@@Base+0x5860>

a040: e3a01010 mov r1, #16

a044: e1a07000 mov r7, r0

a048: e3a00001 mov r0, #1

a04c: ebffe078 bl 2234 <calloc@plt>

a050: e2505000 subs r5, r0, #0

a054: 0a00005d beq a1d0 <mcrypt\_mutex\_register@@Base+0x5a3c>

a058: e2453001 sub r3, r5, #1

a05c: e285100f add r1, r5, #15

a060: e3a02000 mov r2, #0

a064: e5e32001 strb r2, [r3, #1]!

a068: e1530001 cmp r3, r1

a06c: 1afffffc bne a064 <mcrypt\_mutex\_register@@Base+0x58d0>

a070: e3a03001 mov r3, #1

a074: e3570000 cmp r7, #0

a078: e5c53000 strb r3, [r5]

a07c: da000038 ble a164 <mcrypt\_mutex\_register@@Base+0x59d0>

a080: e1a0600d mov r6, sp

a084: e1a02006 mov r2, r6

a088: e3a03000 mov r3, #0

a08c: e4c23001 strb r3, [r2], #1

a090: e2833001 add r3, r3, #1

a094: e1570003 cmp r7, r3

a098: 1afffffb bne a08c <mcrypt\_mutex\_register@@Base+0x58f8>

a09c: ebffffd2 bl 9fec <mcrypt\_mutex\_register@@Base+0x5858>

a0a0: ebffe093 bl 22f4 <malloc@plt>

a0a4: e2509000 subs r9, r0, #0

a0a8: 0a00004f beq a1ec <mcrypt\_mutex\_register@@Base+0x5a58>

a0ac: e28d8020 add r8, sp, #32

a0b0: e1a01006 mov r1, r6

a0b4: e1a02007 mov r2, r7

a0b8: e1a00008 mov r0, r8

a0bc: ebffe071 bl 2288 <memcpy@plt>

a0c0: e1a01005 mov r1, r5

a0c4: e3a02010 mov r2, #16

a0c8: e1a00009 mov r0, r9

a0cc: ebfffcd2 bl 941c <mcrypt\_mutex\_register@@Base+0x4c88>

a0d0: e1a00005 mov r0, r5

a0d4: ebffe068 bl 227c <free@plt>

a0d8: e1a01008 mov r1, r8

a0dc: e1a00009 mov r0, r9

a0e0: ebfffe87 bl 9b04 <mcrypt\_mutex\_register@@Base+0x5370>

a0e4: e59f6110 ldr r6, [pc, #272] ; a1fc <mcrypt\_mutex\_register@@Base+0x5a68>

a0e8: e28da040 add sl, sp, #64 ; 0x40

a0ec: e0887007 add r7, r8, r7

a0f0: e1a05008 mov r5, r8

a0f4: e1a0400a mov r4, sl

a0f8: e08f6006 add r6, pc, r6

a0fc: e4d52001 ldrb r2, [r5], #1

a100: e1a00004 mov r0, r4

a104: e1a01006 mov r1, r6

a108: ebffe097 bl 236c <sprintf@plt>

a10c: e1570005 cmp r7, r5

a110: e2844002 add r4, r4, #2

a114: 1afffff8 bne a0fc <mcrypt\_mutex\_register@@Base+0x5968>

a118: e59f40e0 ldr r4, [pc, #224] ; a200 <mcrypt\_mutex\_register@@Base+0x5a6c>

a11c: e1a0000a mov r0, sl

a120: e08f4004 add r4, pc, r4

a124: e1a01004 mov r1, r4

a128: ebffe047 bl 224c <strcmp@plt>

a12c: e3500000 cmp r0, #0

a130: 1a00001a bne a1a0 <mcrypt\_mutex\_register@@Base+0x5a0c>

a134: e1a01008 mov r1, r8

a138: e1a00009 mov r0, r9

a13c: ebffff0d bl 9d78 <mcrypt\_mutex\_register@@Base+0x55e4>

a140: e1a00009 mov r0, r9

a144: ebffe04c bl 227c <free@plt>

a148: e1a00008 mov r0, r8

a14c: e1a0100d mov r1, sp

a150: ebffe03d bl 224c <strcmp@plt>

a154: e3500000 cmp r0, #0

a158: 1a00001e bne a1d8 <mcrypt\_mutex\_register@@Base+0x5a44>

a15c: e28ddf42 add sp, sp, #264 ; 0x108

a160: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

a164: ebffffa0 bl 9fec <mcrypt\_mutex\_register@@Base+0x5858>

a168: ebffe061 bl 22f4 <malloc@plt>

a16c: e2509000 subs r9, r0, #0

a170: 0a00001d beq a1ec <mcrypt\_mutex\_register@@Base+0x5a58>

a174: e3a02010 mov r2, #16

a178: e1a01005 mov r1, r5

a17c: ebfffca6 bl 941c <mcrypt\_mutex\_register@@Base+0x4c88>

a180: e28d8020 add r8, sp, #32

a184: e1a00005 mov r0, r5

a188: ebffe03b bl 227c <free@plt>

a18c: e1a01008 mov r1, r8

a190: e1a00009 mov r0, r9

a194: ebfffe5a bl 9b04 <mcrypt\_mutex\_register@@Base+0x5370>

a198: e28da040 add sl, sp, #64 ; 0x40

a19c: eaffffdd b a118 <mcrypt\_mutex\_register@@Base+0x5984>

a1a0: e59f005c ldr r0, [pc, #92] ; a204 <mcrypt\_mutex\_register@@Base+0x5a70>

a1a4: e08f0000 add r0, pc, r0

a1a8: ebffe04e bl 22e8 <puts@plt>

a1ac: e59f0054 ldr r0, [pc, #84] ; a208 <mcrypt\_mutex\_register@@Base+0x5a74>

a1b0: e1a0200a mov r2, sl

a1b4: e1a01004 mov r1, r4

a1b8: e08f0000 add r0, pc, r0

a1bc: ebffe028 bl 2264 <printf@plt>

a1c0: e1a00009 mov r0, r9

a1c4: ebffe02c bl 227c <free@plt>

a1c8: e3e00000 mvn r0, #0

a1cc: eaffffe2 b a15c <mcrypt\_mutex\_register@@Base+0x59c8>

a1d0: e3e00000 mvn r0, #0

a1d4: eaffffe0 b a15c <mcrypt\_mutex\_register@@Base+0x59c8>

a1d8: e59f002c ldr r0, [pc, #44] ; a20c <mcrypt\_mutex\_register@@Base+0x5a78>

a1dc: e08f0000 add r0, pc, r0

a1e0: ebffe040 bl 22e8 <puts@plt>

a1e4: e3e00000 mvn r0, #0

a1e8: eaffffdb b a15c <mcrypt\_mutex\_register@@Base+0x59c8>

a1ec: e1a00005 mov r0, r5

a1f0: ebffe021 bl 227c <free@plt>

a1f4: e3e00000 mvn r0, #0

a1f8: eaffffd7 b a15c <mcrypt\_mutex\_register@@Base+0x59c8>

a1fc: 00016568 andeq r6, r1, r8, ror #10

a200: 0001665c andeq r6, r1, ip, asr r6

a204: 000164d8 ldrdeq r6, [r1], -r8

a208: 000164dc ldrdeq r6, [r1], -ip

a20c: 000164d0 ldrdeq r6, [r1], -r0

a210: e59f0000 ldr r0, [pc] ; a218 <mcrypt\_mutex\_register@@Base+0x5a84>

a214: e12fff1e bx lr

a218: 01315731 teqeq r1, r1, lsr r7

a21c: e59f308c ldr r3, [pc, #140] ; a2b0 <mcrypt\_mutex\_register@@Base+0x5b1c>

a220: e59fc08c ldr ip, [pc, #140] ; a2b4 <mcrypt\_mutex\_register@@Base+0x5b20>

a224: e59f208c ldr r2, [pc, #140] ; a2b8 <mcrypt\_mutex\_register@@Base+0x5b24>

a228: e08f3003 add r3, pc, r3

a22c: e92d40f0 push {r4, r5, r6, r7, lr}

a230: e1a00200 lsl r0, r0, #4

a234: e793600c ldr r6, [r3, ip]

a238: e7935002 ldr r5, [r3, r2]

a23c: e59f4078 ldr r4, [pc, #120] ; a2bc <mcrypt\_mutex\_register@@Base+0x5b28>

a240: e1a02221 lsr r2, r1, #4

a244: e59fc074 ldr ip, [pc, #116] ; a2c0 <mcrypt\_mutex\_register@@Base+0x5b2c>

a248: e201100f and r1, r1, #15

a24c: e7d57002 ldrb r7, [r5, r2]

a250: e7d6e001 ldrb lr, [r6, r1]

a254: e7934004 ldr r4, [r3, r4]

a258: e793c00c ldr ip, [r3, ip]

a25c: e02ee007 eor lr, lr, r7

a260: e08cc000 add ip, ip, r0

a264: e0211002 eor r1, r1, r2

a268: e0844000 add r4, r4, r0

a26c: e7dc2001 ldrb r2, [ip, r1]

a270: e7d4400e ldrb r4, [r4, lr]

a274: e59fe048 ldr lr, [pc, #72] ; a2c4 <mcrypt\_mutex\_register@@Base+0x5b30>

a278: e59f1048 ldr r1, [pc, #72] ; a2c8 <mcrypt\_mutex\_register@@Base+0x5b34>

a27c: e7d55002 ldrb r5, [r5, r2]

a280: e7d6c004 ldrb ip, [r6, r4]

a284: e793e00e ldr lr, [r3, lr]

a288: e7931001 ldr r1, [r3, r1]

a28c: e08ee000 add lr, lr, r0

a290: e02cc005 eor ip, ip, r5

a294: e0222004 eor r2, r2, r4

a298: e0810000 add r0, r1, r0

a29c: e7de100c ldrb r1, [lr, ip]

a2a0: e7d00002 ldrb r0, [r0, r2]

a2a4: e1800201 orr r0, r0, r1, lsl #4

a2a8: e6ef0070 uxtb r0, r0

a2ac: e8bd80f0 pop {r4, r5, r6, r7, pc}

a2b0: 00029dd0 ldrdeq r9, [r2], -r0

a2b4: 000000c4 andeq r0, r0, r4, asr #1

a2b8: 00000098 muleq r0, r8, r0

a2bc: 000000e0 andeq r0, r0, r0, ror #1

a2c0: 000000b4 strheq r0, [r0], -r4

a2c4: 000000dc ldrdeq r0, [r0], -ip

a2c8: 000000b8 strheq r0, [r0], -r8

a2cc: e92d4070 push {r4, r5, r6, lr}

a2d0: e28050b7 add r5, r0, #183 ; 0xb7

a2d4: e3a04000 mov r4, #0

a2d8: e6ef6074 uxtb r6, r4

a2dc: e1a01006 mov r1, r6

a2e0: e3a00000 mov r0, #0

a2e4: ebffffcc bl a21c <mcrypt\_mutex\_register@@Base+0x5a88>

a2e8: e1a01006 mov r1, r6

a2ec: e2844001 add r4, r4, #1

a2f0: e5e50001 strb r0, [r5, #1]!

a2f4: e3a00001 mov r0, #1

a2f8: ebffffc7 bl a21c <mcrypt\_mutex\_register@@Base+0x5a88>

a2fc: e3540c01 cmp r4, #256 ; 0x100

a300: e5c50100 strb r0, [r5, #256] ; 0x100

a304: 1afffff3 bne a2d8 <mcrypt\_mutex\_register@@Base+0x5b44>

a308: e8bd8070 pop {r4, r5, r6, pc}

a30c: e59f30c8 ldr r3, [pc, #200] ; a3dc <mcrypt\_mutex\_register@@Base+0x5c48>

a310: e59f10c8 ldr r1, [pc, #200] ; a3e0 <mcrypt\_mutex\_register@@Base+0x5c4c>

a314: e59f20c8 ldr r2, [pc, #200] ; a3e4 <mcrypt\_mutex\_register@@Base+0x5c50>

a318: e08f3003 add r3, pc, r3

a31c: e92d40f0 push {r4, r5, r6, r7, lr}

a320: e2804f6d add r4, r0, #436 ; 0x1b4

a324: e793e001 ldr lr, [r3, r1]

a328: e793c002 ldr ip, [r3, r2]

a32c: e2844003 add r4, r4, #3

a330: e28010b7 add r1, r0, #183 ; 0xb7

a334: e2802fae add r2, r0, #696 ; 0x2b8

a338: e5d16101 ldrb r6, [r1, #257] ; 0x101

a33c: e2060003 and r0, r6, #3

a340: e1a03126 lsr r3, r6, #2

a344: e7dc7000 ldrb r7, [ip, r0]

a348: e02350a6 eor r5, r3, r6, lsr #1

a34c: e7de0000 ldrb r0, [lr, r0]

a350: e0255006 eor r5, r5, r6

a354: e0255007 eor r5, r5, r7

a358: e0233006 eor r3, r3, r6

a35c: e0233000 eor r3, r3, r0

a360: e1a00805 lsl r0, r5, #16

a364: e1a07c05 lsl r7, r5, #24

a368: e0800403 add r0, r0, r3, lsl #8

a36c: e0833806 add r3, r3, r6, lsl #16

a370: e0800006 add r0, r0, r6

a374: e0833007 add r3, r3, r7

a378: e0800007 add r0, r0, r7

a37c: e0833405 add r3, r3, r5, lsl #8

a380: e5a20004 str r0, [r2, #4]!

a384: e5823800 str r3, [r2, #2048] ; 0x800

a388: e5f13001 ldrb r3, [r1, #1]!

a38c: e2036003 and r6, r3, #3

a390: e1a00123 lsr r0, r3, #2

a394: e7dc7006 ldrb r7, [ip, r6]

a398: e7de6006 ldrb r6, [lr, r6]

a39c: e02050a3 eor r5, r0, r3, lsr #1

a3a0: e0255003 eor r5, r5, r3

a3a4: e0200003 eor r0, r0, r3

a3a8: e0255007 eor r5, r5, r7

a3ac: e0200006 eor r0, r0, r6

a3b0: e0856c03 add r6, r5, r3, lsl #24

a3b4: e0803403 add r3, r0, r3, lsl #8

a3b8: e0833805 add r3, r3, r5, lsl #16

a3bc: e0866405 add r6, r6, r5, lsl #8

a3c0: e0865800 add r5, r6, r0, lsl #16

a3c4: e0833c00 add r3, r3, r0, lsl #24

a3c8: e1510004 cmp r1, r4

a3cc: e5825400 str r5, [r2, #1024] ; 0x400

a3d0: e5823c00 str r3, [r2, #3072] ; 0xc00

a3d4: 1affffd7 bne a338 <mcrypt\_mutex\_register@@Base+0x5ba4>

a3d8: e8bd80f0 pop {r4, r5, r6, r7, pc}

a3dc: 00029ce0 andeq r9, r2, r0, ror #25

a3e0: 000000d8 ldrdeq r0, [r0], -r8

a3e4: 00000094 muleq r0, r4, r0

a3e8: e92d4070 push {r4, r5, r6, lr}

a3ec: e1a03821 lsr r3, r1, #16

a3f0: e590e000 ldr lr, [r0]

a3f4: e1a04421 lsr r4, r1, #8

a3f8: e35e0003 cmp lr, #3

a3fc: e6ef4074 uxtb r4, r4

a400: e6ef3073 uxtb r3, r3

a404: e6efc071 uxtb ip, r1

a408: e1a05c21 lsr r5, r1, #24

a40c: 0a00001f beq a490 <mcrypt\_mutex\_register@@Base+0x5cfc>

a410: e35e0004 cmp lr, #4

a414: 0a00000d beq a450 <mcrypt\_mutex\_register@@Base+0x5cbc>

a418: e35e0002 cmp lr, #2

a41c: 0a00002b beq a4d0 <mcrypt\_mutex\_register@@Base+0x5d3c>

a420: e0802104 add r2, r0, r4, lsl #2

a424: e080c10c add ip, r0, ip, lsl #2

a428: e0803103 add r3, r0, r3, lsl #2

a42c: e59cc2bc ldr ip, [ip, #700] ; 0x2bc

a430: e59226bc ldr r2, [r2, #1724] ; 0x6bc

a434: e0800105 add r0, r0, r5, lsl #2

a438: e5933abc ldr r3, [r3, #2748] ; 0xabc

a43c: e02cc002 eor ip, ip, r2

a440: e5900ebc ldr r0, [r0, #3772] ; 0xebc

a444: e02cc003 eor ip, ip, r3

a448: e020000c eor r0, r0, ip

a44c: e8bd8070 pop {r4, r5, r6, pc}

a450: e080c00c add ip, r0, ip

a454: e0804004 add r4, r0, r4

a458: e0803003 add r3, r0, r3

a45c: e592600c ldr r6, [r2, #12]

a460: e0801005 add r1, r0, r5

a464: e5dcc1b8 ldrb ip, [ip, #440] ; 0x1b8

a468: e5d440b8 ldrb r4, [r4, #184] ; 0xb8

a46c: e5d330b8 ldrb r3, [r3, #184] ; 0xb8

a470: e5d111b8 ldrb r1, [r1, #440] ; 0x1b8

a474: e02cc006 eor ip, ip, r6

a478: e0244426 eor r4, r4, r6, lsr #8

a47c: e0233826 eor r3, r3, r6, lsr #16

a480: e6efc07c uxtb ip, ip

a484: e6ef4074 uxtb r4, r4

a488: e6ef3073 uxtb r3, r3

a48c: e0215c26 eor r5, r1, r6, lsr #24

a490: e080c00c add ip, r0, ip

a494: e0804004 add r4, r0, r4

a498: e0803003 add r3, r0, r3

a49c: e592e008 ldr lr, [r2, #8]

a4a0: e0801005 add r1, r0, r5

a4a4: e5dcc1b8 ldrb ip, [ip, #440] ; 0x1b8

a4a8: e5d441b8 ldrb r4, [r4, #440] ; 0x1b8

a4ac: e5d330b8 ldrb r3, [r3, #184] ; 0xb8

a4b0: e5d110b8 ldrb r1, [r1, #184] ; 0xb8

a4b4: e02cc00e eor ip, ip, lr

a4b8: e024442e eor r4, r4, lr, lsr #8

a4bc: e023382e eor r3, r3, lr, lsr #16

a4c0: e6efc07c uxtb ip, ip

a4c4: e6ef4074 uxtb r4, r4

a4c8: e6ef3073 uxtb r3, r3

a4cc: e0215c2e eor r5, r1, lr, lsr #24

a4d0: e0804004 add r4, r0, r4

a4d4: e080c00c add ip, r0, ip

a4d8: e0803003 add r3, r0, r3

a4dc: e5d411b8 ldrb r1, [r4, #440] ; 0x1b8

a4e0: e5dcc0b8 ldrb ip, [ip, #184] ; 0xb8

a4e4: e5924004 ldr r4, [r2, #4]

a4e8: e5d330b8 ldrb r3, [r3, #184] ; 0xb8

a4ec: e080e005 add lr, r0, r5

a4f0: e02cc004 eor ip, ip, r4

a4f4: e0211424 eor r1, r1, r4, lsr #8

a4f8: e0233824 eor r3, r3, r4, lsr #16

a4fc: e5dee1b8 ldrb lr, [lr, #440] ; 0x1b8

a500: e6e01071 uxtab r1, r0, r1

a504: e6e0c07c uxtab ip, r0, ip

a508: e6e03073 uxtab r3, r0, r3

a50c: e02eec24 eor lr, lr, r4, lsr #24

a510: e5dcc0b8 ldrb ip, [ip, #184] ; 0xb8

a514: e5d140b8 ldrb r4, [r1, #184] ; 0xb8

a518: e5d331b8 ldrb r3, [r3, #440] ; 0x1b8

a51c: e5921000 ldr r1, [r2]

a520: e080e00e add lr, r0, lr

a524: e02cc001 eor ip, ip, r1

a528: e5dee1b8 ldrb lr, [lr, #440] ; 0x1b8

a52c: e0244421 eor r4, r4, r1, lsr #8

a530: e0233821 eor r3, r3, r1, lsr #16

a534: e6efc07c uxtb ip, ip

a538: e6ef4074 uxtb r4, r4

a53c: e6ef3073 uxtb r3, r3

a540: e02e5c21 eor r5, lr, r1, lsr #24

a544: eaffffb5 b a420 <mcrypt\_mutex\_register@@Base+0x5c8c>

a548: e5903000 ldr r3, [r0]

a54c: e92d41f0 push {r4, r5, r6, r7, r8, lr}

a550: e3530003 cmp r3, #3

a554: 0a00007f beq a758 <mcrypt\_mutex\_register@@Base+0x5fc4>

a558: e3530004 cmp r3, #4

a55c: 0a00002f beq a620 <mcrypt\_mutex\_register@@Base+0x5e8c>

a560: e3530002 cmp r3, #2

a564: 18bd81f0 popne {r4, r5, r6, r7, r8, pc}

a568: e280cd4a add ip, r0, #4736 ; 0x1280

a56c: e2804f6d add r4, r0, #436 ; 0x1b4

a570: e28cc038 add ip, ip, #56 ; 0x38

a574: e2844003 add r4, r4, #3

a578: e280e0b7 add lr, r0, #183 ; 0xb7

a57c: e5fe2001 ldrb r2, [lr, #1]!

a580: e5d15004 ldrb r5, [r1, #4]

a584: e5d16000 ldrb r6, [r1]

a588: e15e0004 cmp lr, r4

a58c: e0255002 eor r5, r5, r2

a590: e0805005 add r5, r0, r5

a594: e5d530b8 ldrb r3, [r5, #184] ; 0xb8

a598: e0233006 eor r3, r3, r6

a59c: e0803103 add r3, r0, r3, lsl #2

a5a0: e59332bc ldr r3, [r3, #700] ; 0x2bc

a5a4: e5ac3004 str r3, [ip, #4]!

a5a8: e5de3100 ldrb r3, [lr, #256] ; 0x100

a5ac: e5d16005 ldrb r6, [r1, #5]

a5b0: e5d15001 ldrb r5, [r1, #1]

a5b4: e0266003 eor r6, r6, r3

a5b8: e0806006 add r6, r0, r6

a5bc: e5d660b8 ldrb r6, [r6, #184] ; 0xb8

a5c0: e0255006 eor r5, r5, r6

a5c4: e0805105 add r5, r0, r5, lsl #2

a5c8: e59556bc ldr r5, [r5, #1724] ; 0x6bc

a5cc: e58c5400 str r5, [ip, #1024] ; 0x400

a5d0: e5d16006 ldrb r6, [r1, #6]

a5d4: e5d15002 ldrb r5, [r1, #2]

a5d8: e0222006 eor r2, r2, r6

a5dc: e0802002 add r2, r0, r2

a5e0: e5d221b8 ldrb r2, [r2, #440] ; 0x1b8

a5e4: e0222005 eor r2, r2, r5

a5e8: e0802102 add r2, r0, r2, lsl #2

a5ec: e5922abc ldr r2, [r2, #2748] ; 0xabc

a5f0: e58c2800 str r2, [ip, #2048] ; 0x800

a5f4: e5d15007 ldrb r5, [r1, #7]

a5f8: e5d12003 ldrb r2, [r1, #3]

a5fc: e0233005 eor r3, r3, r5

a600: e0803003 add r3, r0, r3

a604: e5d331b8 ldrb r3, [r3, #440] ; 0x1b8

a608: e0233002 eor r3, r3, r2

a60c: e0803103 add r3, r0, r3, lsl #2

a610: e5933ebc ldr r3, [r3, #3772] ; 0xebc

a614: e58c3c00 str r3, [ip, #3072] ; 0xc00

a618: 1affffd7 bne a57c <mcrypt\_mutex\_register@@Base+0x5de8>

a61c: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

a620: e280cd4a add ip, r0, #4736 ; 0x1280

a624: e2804f6d add r4, r0, #436 ; 0x1b4

a628: e28cc038 add ip, ip, #56 ; 0x38

a62c: e2844003 add r4, r4, #3

a630: e280e0b7 add lr, r0, #183 ; 0xb7

a634: e5de3101 ldrb r3, [lr, #257] ; 0x101

a638: e5d1200c ldrb r2, [r1, #12]

a63c: e5d15008 ldrb r5, [r1, #8]

a640: e5d18004 ldrb r8, [r1, #4]

a644: e0222003 eor r2, r2, r3

a648: e0802002 add r2, r0, r2

a64c: e5d17000 ldrb r7, [r1]

a650: e5d261b8 ldrb r6, [r2, #440] ; 0x1b8

a654: e0266005 eor r6, r6, r5

a658: e0806006 add r6, r0, r6

a65c: e5d650b8 ldrb r5, [r6, #184] ; 0xb8

a660: e0255008 eor r5, r5, r8

a664: e0805005 add r5, r0, r5

a668: e5d520b8 ldrb r2, [r5, #184] ; 0xb8

a66c: e0222007 eor r2, r2, r7

a670: e0802102 add r2, r0, r2, lsl #2

a674: e59222bc ldr r2, [r2, #700] ; 0x2bc

a678: e5ac2004 str r2, [ip, #4]!

a67c: e5fe2001 ldrb r2, [lr, #1]!

a680: e5d1800d ldrb r8, [r1, #13]

a684: e5d17009 ldrb r7, [r1, #9]

a688: e5d16005 ldrb r6, [r1, #5]

a68c: e0288002 eor r8, r8, r2

a690: e0808008 add r8, r0, r8

a694: e5d15001 ldrb r5, [r1, #1]

a698: e5d881b8 ldrb r8, [r8, #440] ; 0x1b8

a69c: e15e0004 cmp lr, r4

a6a0: e0277008 eor r7, r7, r8

a6a4: e0807007 add r7, r0, r7

a6a8: e5d771b8 ldrb r7, [r7, #440] ; 0x1b8

a6ac: e0266007 eor r6, r6, r7

a6b0: e0806006 add r6, r0, r6

a6b4: e5d660b8 ldrb r6, [r6, #184] ; 0xb8

a6b8: e0255006 eor r5, r5, r6

a6bc: e0805105 add r5, r0, r5, lsl #2

a6c0: e59556bc ldr r5, [r5, #1724] ; 0x6bc

a6c4: e58c5400 str r5, [ip, #1024] ; 0x400

a6c8: e5d1600e ldrb r6, [r1, #14]

a6cc: e5d1500a ldrb r5, [r1, #10]

a6d0: e5d18006 ldrb r8, [r1, #6]

a6d4: e0222006 eor r2, r2, r6

a6d8: e0802002 add r2, r0, r2

a6dc: e5d17002 ldrb r7, [r1, #2]

a6e0: e5d260b8 ldrb r6, [r2, #184] ; 0xb8

a6e4: e0266005 eor r6, r6, r5

a6e8: e0806006 add r6, r0, r6

a6ec: e5d650b8 ldrb r5, [r6, #184] ; 0xb8

a6f0: e0255008 eor r5, r5, r8

a6f4: e0805005 add r5, r0, r5

a6f8: e5d521b8 ldrb r2, [r5, #440] ; 0x1b8

a6fc: e0222007 eor r2, r2, r7

a700: e0802102 add r2, r0, r2, lsl #2

a704: e5922abc ldr r2, [r2, #2748] ; 0xabc

a708: e58c2800 str r2, [ip, #2048] ; 0x800

a70c: e5d1500f ldrb r5, [r1, #15]

a710: e5d1200b ldrb r2, [r1, #11]

a714: e0233005 eor r3, r3, r5

a718: e0803003 add r3, r0, r3

a71c: e5d350b8 ldrb r5, [r3, #184] ; 0xb8

a720: e5d13007 ldrb r3, [r1, #7]

a724: e5d16003 ldrb r6, [r1, #3]

a728: e0255002 eor r5, r5, r2

a72c: e0805005 add r5, r0, r5

a730: e5d521b8 ldrb r2, [r5, #440] ; 0x1b8

a734: e0222003 eor r2, r2, r3

a738: e0802002 add r2, r0, r2

a73c: e5d231b8 ldrb r3, [r2, #440] ; 0x1b8

a740: e0233006 eor r3, r3, r6

a744: e0803103 add r3, r0, r3, lsl #2

a748: e5933ebc ldr r3, [r3, #3772] ; 0xebc

a74c: e58c3c00 str r3, [ip, #3072] ; 0xc00

a750: 1affffb7 bne a634 <mcrypt\_mutex\_register@@Base+0x5ea0>

a754: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

a758: e280cd4a add ip, r0, #4736 ; 0x1280

a75c: e2804f6d add r4, r0, #436 ; 0x1b4

a760: e28cc038 add ip, ip, #56 ; 0x38

a764: e2844003 add r4, r4, #3

a768: e280e0b7 add lr, r0, #183 ; 0xb7

a76c: e5de6101 ldrb r6, [lr, #257] ; 0x101

a770: e5d15008 ldrb r5, [r1, #8]

a774: e5d13004 ldrb r3, [r1, #4]

a778: e5d17000 ldrb r7, [r1]

a77c: e0255006 eor r5, r5, r6

a780: e0805005 add r5, r0, r5

a784: e5d520b8 ldrb r2, [r5, #184] ; 0xb8

a788: e0222003 eor r2, r2, r3

a78c: e0802002 add r2, r0, r2

a790: e5d230b8 ldrb r3, [r2, #184] ; 0xb8

a794: e0233007 eor r3, r3, r7

a798: e0803103 add r3, r0, r3, lsl #2

a79c: e59332bc ldr r3, [r3, #700] ; 0x2bc

a7a0: e5ac3004 str r3, [ip, #4]!

a7a4: e5d15009 ldrb r5, [r1, #9]

a7a8: e5d12005 ldrb r2, [r1, #5]

a7ac: e5d13001 ldrb r3, [r1, #1]

a7b0: e0255006 eor r5, r5, r6

a7b4: e0805005 add r5, r0, r5

a7b8: e5d551b8 ldrb r5, [r5, #440] ; 0x1b8

a7bc: e0222005 eor r2, r2, r5

a7c0: e0802002 add r2, r0, r2

a7c4: e5d220b8 ldrb r2, [r2, #184] ; 0xb8

a7c8: e0233002 eor r3, r3, r2

a7cc: e0803103 add r3, r0, r3, lsl #2

a7d0: e59336bc ldr r3, [r3, #1724] ; 0x6bc

a7d4: e58c3400 str r3, [ip, #1024] ; 0x400

a7d8: e5fe3001 ldrb r3, [lr, #1]!

a7dc: e5d1600a ldrb r6, [r1, #10]

a7e0: e5d12006 ldrb r2, [r1, #6]

a7e4: e5d17002 ldrb r7, [r1, #2]

a7e8: e0266003 eor r6, r6, r3

a7ec: e0806006 add r6, r0, r6

a7f0: e15e0004 cmp lr, r4

a7f4: e5d650b8 ldrb r5, [r6, #184] ; 0xb8

a7f8: e0255002 eor r5, r5, r2

a7fc: e0805005 add r5, r0, r5

a800: e5d521b8 ldrb r2, [r5, #440] ; 0x1b8

a804: e0222007 eor r2, r2, r7

a808: e0802102 add r2, r0, r2, lsl #2

a80c: e5922abc ldr r2, [r2, #2748] ; 0xabc

a810: e58c2800 str r2, [ip, #2048] ; 0x800

a814: e5d1200b ldrb r2, [r1, #11]

a818: e5d16007 ldrb r6, [r1, #7]

a81c: e5d15003 ldrb r5, [r1, #3]

a820: e0233002 eor r3, r3, r2

a824: e0803003 add r3, r0, r3

a828: e5d321b8 ldrb r2, [r3, #440] ; 0x1b8

a82c: e0222006 eor r2, r2, r6

a830: e0802002 add r2, r0, r2

a834: e5d231b8 ldrb r3, [r2, #440] ; 0x1b8

a838: e0233005 eor r3, r3, r5

a83c: e0803103 add r3, r0, r3, lsl #2

a840: e5933ebc ldr r3, [r3, #3772] ; 0xebc

a844: e58c3c00 str r3, [ip, #3072] ; 0xc00

a848: 1affffc7 bne a76c <mcrypt\_mutex\_register@@Base+0x5fd8>

a84c: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

a850: e92d4010 push {r4, lr}

a854: e3a0e008 mov lr, #8

a858: e59f4048 ldr r4, [pc, #72] ; a8a8 <mcrypt\_mutex\_register@@Base+0x6114>

a85c: e1a0cc21 lsr ip, r1, #24

a860: e31c0080 tst ip, #128 ; 0x80

a864: e1a0208c lsl r2, ip, #1

a868: e1a03c20 lsr r3, r0, #24

a86c: 10222004 eorne r2, r2, r4

a870: e1833401 orr r3, r3, r1, lsl #8

a874: e023300c eor r3, r3, ip

a878: e31c0001 tst ip, #1

a87c: e0233802 eor r3, r3, r2, lsl #16

a880: e0222ca1 eor r2, r2, r1, lsr #25

a884: 122220a6 eorne r2, r2, #166 ; 0xa6

a888: e25ee001 subs lr, lr, #1

a88c: e1a01402 lsl r1, r2, #8

a890: e1811c02 orr r1, r1, r2, lsl #24

a894: e1a00400 lsl r0, r0, #8

a898: e0211003 eor r1, r1, r3

a89c: 1affffee bne a85c <mcrypt\_mutex\_register@@Base+0x60c8>

a8a0: e1a00001 mov r0, r1

a8a4: e8bd8010 pop {r4, pc}

a8a8: 0000014d andeq r0, r0, sp, asr #2

a8ac: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

a8b0: e1a06000 mov r6, r0

a8b4: e3a04000 mov r4, #0

a8b8: e24dd02c sub sp, sp, #44 ; 0x2c

a8bc: e1a05002 mov r5, r2

a8c0: e58040b4 str r4, [r0, #180] ; 0xb4

a8c4: e3a07001 mov r7, #1

a8c8: e1a08001 mov r8, r1

a8cc: ebfffe7e bl a2cc <mcrypt\_mutex\_register@@Base+0x5b38>

a8d0: e58642b8 str r4, [r6, #696] ; 0x2b8

a8d4: e58670b4 str r7, [r6, #180] ; 0xb4

a8d8: e1a00006 mov r0, r6

a8dc: ebfffe8a bl a30c <mcrypt\_mutex\_register@@Base+0x5b78>

a8e0: e1a02185 lsl r2, r5, #3

a8e4: e58672b8 str r7, [r6, #696] ; 0x2b8

a8e8: e1a02322 lsr r2, r2, #6

a8ec: e1520004 cmp r2, r4

a8f0: e28da008 add sl, sp, #8

a8f4: e28d9018 add r9, sp, #24

a8f8: e5862000 str r2, [r6]

a8fc: 0a000014 beq a954 <mcrypt\_mutex\_register@@Base+0x61c0>

a900: e2883004 add r3, r8, #4

a904: e1a07009 mov r7, r9

a908: e58d9004 str r9, [sp, #4]

a90c: e1a0b00a mov fp, sl

a910: e1a09003 mov r9, r3

a914: e798e184 ldr lr, [r8, r4, lsl #3]

a918: e799c184 ldr ip, [r9, r4, lsl #3]

a91c: e1e05004 mvn r5, r4

a920: e1a0000e mov r0, lr

a924: e1a0100c mov r1, ip

a928: e48be004 str lr, [fp], #4

a92c: e487c004 str ip, [r7], #4

a930: e0855002 add r5, r5, r2

a934: ebffffc5 bl a850 <mcrypt\_mutex\_register@@Base+0x60bc>

a938: e5962000 ldr r2, [r6]

a93c: e0865105 add r5, r6, r5, lsl #2

a940: e2844001 add r4, r4, #1

a944: e1520004 cmp r2, r4

a948: e58500a4 str r0, [r5, #164] ; 0xa4

a94c: 8afffff0 bhi a914 <mcrypt\_mutex\_register@@Base+0x6180>

a950: e59d9004 ldr r9, [sp, #4]

a954: e1a07006 mov r7, r6

a958: e3a04000 mov r4, #0

a95c: e1a01004 mov r1, r4

a960: e1a0200a mov r2, sl

a964: e1a00006 mov r0, r6

a968: ebfffe9e bl a3e8 <mcrypt\_mutex\_register@@Base+0x5c54>

a96c: e59f8078 ldr r8, [pc, #120] ; a9ec <mcrypt\_mutex\_register@@Base+0x6258>

a970: e1a02009 mov r2, r9

a974: e0848008 add r8, r4, r8

a978: e1a01008 mov r1, r8

a97c: e2844402 add r4, r4, #33554432 ; 0x2000000

a980: e2844802 add r4, r4, #131072 ; 0x20000

a984: e2844c02 add r4, r4, #512 ; 0x200

a988: e2844002 add r4, r4, #2

a98c: e1a05000 mov r5, r0

a990: e1a00006 mov r0, r6

a994: ebfffe93 bl a3e8 <mcrypt\_mutex\_register@@Base+0x5c54>

a998: e1a02009 mov r2, r9

a99c: e1a01008 mov r1, r8

a9a0: e1a0b000 mov fp, r0

a9a4: e1a00006 mov r0, r6

a9a8: ebfffe8e bl a3e8 <mcrypt\_mutex\_register@@Base+0x5c54>

a9ac: e59f303c ldr r3, [pc, #60] ; a9f0 <mcrypt\_mutex\_register@@Base+0x625c>

a9b0: e1540003 cmp r4, r3

a9b4: e1a00c20 lsr r0, r0, #24

a9b8: e180040b orr r0, r0, fp, lsl #8

a9bc: e0853000 add r3, r5, r0

a9c0: e0830000 add r0, r3, r0

a9c4: e5873004 str r3, [r7, #4]

a9c8: e1a00be0 ror r0, r0, #23

a9cc: e5a70008 str r0, [r7, #8]!

a9d0: 1affffe1 bne a95c <mcrypt\_mutex\_register@@Base+0x61c8>

a9d4: e1a00006 mov r0, r6

a9d8: e28610a4 add r1, r6, #164 ; 0xa4

a9dc: ebfffed9 bl a548 <mcrypt\_mutex\_register@@Base+0x5db4>

a9e0: e3a00000 mov r0, #0

a9e4: e28dd02c add sp, sp, #44 ; 0x2c

a9e8: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

a9ec: 01010101 tsteq r1, r1, lsl #2

a9f0: 28282828 stmdacs r8!, {r3, r5, fp, sp}

a9f4: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

a9f8: e5902004 ldr r2, [r0, #4]

a9fc: e5914004 ldr r4, [r1, #4]

aa00: e5903008 ldr r3, [r0, #8]

aa04: e591b000 ldr fp, [r1]

aa08: e0248003 eor r8, r4, r3

aa0c: e02bb002 eor fp, fp, r2

aa10: e1a0e428 lsr lr, r8, #8

aa14: e1a0442b lsr r4, fp, #8

aa18: e6ef207b uxtb r2, fp

aa1c: e1a05c2b lsr r5, fp, #24

aa20: e1a0c82b lsr ip, fp, #16

aa24: e1a07c28 lsr r7, r8, #24

aa28: e6ef6078 uxtb r6, r8

aa2c: e1a03828 lsr r3, r8, #16

aa30: e6ef4074 uxtb r4, r4

aa34: e2822e4a add r2, r2, #1184 ; 0x4a0

aa38: e2855e7a add r5, r5, #1952 ; 0x7a0

aa3c: e2877e4a add r7, r7, #1184 ; 0x4a0

aa40: e2866e5a add r6, r6, #1440 ; 0x5a0

aa44: e6efe07e uxtb lr, lr

aa48: e2844e5a add r4, r4, #1440 ; 0x5a0

aa4c: e282200e add r2, r2, #14

aa50: e285500e add r5, r5, #14

aa54: e6efc07c uxtb ip, ip

aa58: e287700e add r7, r7, #14

aa5c: e286600e add r6, r6, #14

aa60: e28eee6a add lr, lr, #1696 ; 0x6a0

aa64: e6ef3073 uxtb r3, r3

aa68: e284400e add r4, r4, #14

aa6c: e28cce6a add ip, ip, #1696 ; 0x6a0

aa70: e28ee00e add lr, lr, #14

aa74: e2833e7a add r3, r3, #1952 ; 0x7a0

aa78: e0806106 add r6, r0, r6, lsl #2

aa7c: e0802102 add r2, r0, r2, lsl #2

aa80: e0805105 add r5, r0, r5, lsl #2

aa84: e28cc00e add ip, ip, #14

aa88: e0807107 add r7, r0, r7, lsl #2

aa8c: e283300e add r3, r3, #14

aa90: e0804104 add r4, r0, r4, lsl #2

aa94: e080e10e add lr, r0, lr, lsl #2

aa98: e5969004 ldr r9, [r6, #4]

aa9c: e5955004 ldr r5, [r5, #4]

aaa0: e5922004 ldr r2, [r2, #4]

aaa4: e5977004 ldr r7, [r7, #4]

aaa8: e0803103 add r3, r0, r3, lsl #2

aaac: e080c10c add ip, r0, ip, lsl #2

aab0: e5944004 ldr r4, [r4, #4]

aab4: e59ee004 ldr lr, [lr, #4]

aab8: e59cc004 ldr ip, [ip, #4]

aabc: e0222005 eor r2, r2, r5

aac0: e0277009 eor r7, r7, r9

aac4: e5939004 ldr r9, [r3, #4]

aac8: e0222004 eor r2, r2, r4

aacc: e027700e eor r7, r7, lr

aad0: e0277009 eor r7, r7, r9

aad4: e022200c eor r2, r2, ip

aad8: e590300c ldr r3, [r0, #12]

aadc: e590c024 ldr ip, [r0, #36] ; 0x24

aae0: e5915008 ldr r5, [r1, #8]

aae4: e087e002 add lr, r7, r2

aae8: e5909028 ldr r9, [r0, #40] ; 0x28

aaec: e08ee00c add lr, lr, ip

aaf0: e0255003 eor r5, r5, r3

aaf4: e591c00c ldr ip, [r1, #12]

aaf8: e5903010 ldr r3, [r0, #16]

aafc: e025500e eor r5, r5, lr

ab00: e02cc003 eor ip, ip, r3

ab04: e0822009 add r2, r2, r9

ab08: e0829087 add r9, r2, r7, lsl #1

ab0c: e0297fec eor r7, r9, ip, ror #31

ab10: e1a050e5 ror r5, r5, #1

ab14: e1a06427 lsr r6, r7, #8

ab18: e1a09425 lsr r9, r5, #8

ab1c: e6efa075 uxtb sl, r5

ab20: e1a02c25 lsr r2, r5, #24

ab24: e1a0e825 lsr lr, r5, #16

ab28: e1a03c27 lsr r3, r7, #24

ab2c: e6ef4077 uxtb r4, r7

ab30: e1a0c827 lsr ip, r7, #16

ab34: e28aae4a add sl, sl, #1184 ; 0x4a0

ab38: e2822e7a add r2, r2, #1952 ; 0x7a0

ab3c: e6ef9079 uxtb r9, r9

ab40: e6ef6076 uxtb r6, r6

ab44: e2833e4a add r3, r3, #1184 ; 0x4a0

ab48: e2844e5a add r4, r4, #1440 ; 0x5a0

ab4c: e28aa00e add sl, sl, #14

ab50: e282200e add r2, r2, #14

ab54: e2899e5a add r9, r9, #1440 ; 0x5a0

ab58: e6efe07e uxtb lr, lr

ab5c: e283300e add r3, r3, #14

ab60: e2866e6a add r6, r6, #1696 ; 0x6a0

ab64: e284400e add r4, r4, #14

ab68: e6efc07c uxtb ip, ip

ab6c: e289900e add r9, r9, #14

ab70: e28eee6a add lr, lr, #1696 ; 0x6a0

ab74: e286600e add r6, r6, #14

ab78: e28cce7a add ip, ip, #1952 ; 0x7a0

ab7c: e080a10a add sl, r0, sl, lsl #2

ab80: e0802102 add r2, r0, r2, lsl #2

ab84: e28ee00e add lr, lr, #14

ab88: e0803103 add r3, r0, r3, lsl #2

ab8c: e0804104 add r4, r0, r4, lsl #2

ab90: e28cc00e add ip, ip, #14

ab94: e0809109 add r9, r0, r9, lsl #2

ab98: e0806106 add r6, r0, r6, lsl #2

ab9c: e59aa004 ldr sl, [sl, #4]

aba0: e5944004 ldr r4, [r4, #4]

aba4: e5922004 ldr r2, [r2, #4]

aba8: e5933004 ldr r3, [r3, #4]

abac: e080c10c add ip, r0, ip, lsl #2

abb0: e080e10e add lr, r0, lr, lsl #2

abb4: e5999004 ldr r9, [r9, #4]

abb8: e5966004 ldr r6, [r6, #4]

abbc: e59ee004 ldr lr, [lr, #4]

abc0: e022200a eor r2, r2, sl

abc4: e0233004 eor r3, r3, r4

abc8: e59c4004 ldr r4, [ip, #4]

abcc: e0233006 eor r3, r3, r6

abd0: e0222009 eor r2, r2, r9

abd4: e0244003 eor r4, r4, r3

abd8: e022200e eor r2, r2, lr

abdc: e590c02c ldr ip, [r0, #44] ; 0x2c

abe0: e5903030 ldr r3, [r0, #48] ; 0x30

abe4: e084a002 add sl, r4, r2

abe8: e08aa00c add sl, sl, ip

abec: e0822003 add r2, r2, r3

abf0: e0823084 add r3, r2, r4, lsl #1

abf4: e02bb00a eor fp, fp, sl

abf8: e0234fe8 eor r4, r3, r8, ror #31

abfc: e1a0a0eb ror sl, fp, #1

ac00: e1a06824 lsr r6, r4, #16

ac04: e1a0b424 lsr fp, r4, #8

ac08: e1a0842a lsr r8, sl, #8

ac0c: e1a0cc24 lsr ip, r4, #24

ac10: e6efe074 uxtb lr, r4

ac14: e6ef307a uxtb r3, sl

ac18: e1a09c2a lsr r9, sl, #24

ac1c: e1a0282a lsr r2, sl, #16

ac20: e6efb07b uxtb fp, fp

ac24: e6ef6076 uxtb r6, r6

ac28: e28cce4a add ip, ip, #1184 ; 0x4a0

ac2c: e28eee5a add lr, lr, #1440 ; 0x5a0

ac30: e28bbe6a add fp, fp, #1696 ; 0x6a0

ac34: e2866e7a add r6, r6, #1952 ; 0x7a0

ac38: e6ef8078 uxtb r8, r8

ac3c: e2833e4a add r3, r3, #1184 ; 0x4a0

ac40: e2899e7a add r9, r9, #1952 ; 0x7a0

ac44: e28cc00e add ip, ip, #14

ac48: e28ee00e add lr, lr, #14

ac4c: e28bb00e add fp, fp, #14

ac50: e286600e add r6, r6, #14

ac54: e283300e add r3, r3, #14

ac58: e2888e5a add r8, r8, #1440 ; 0x5a0

ac5c: e289900e add r9, r9, #14

ac60: e6ef2072 uxtb r2, r2

ac64: e288800e add r8, r8, #14

ac68: e2822e6a add r2, r2, #1696 ; 0x6a0

ac6c: e080c10c add ip, r0, ip, lsl #2

ac70: e080e10e add lr, r0, lr, lsl #2

ac74: e080b10b add fp, r0, fp, lsl #2

ac78: e0806106 add r6, r0, r6, lsl #2

ac7c: e0803103 add r3, r0, r3, lsl #2

ac80: e0809109 add r9, r0, r9, lsl #2

ac84: e282200e add r2, r2, #14

ac88: e0808108 add r8, r0, r8, lsl #2

ac8c: e59ee004 ldr lr, [lr, #4]

ac90: e59bb004 ldr fp, [fp, #4]

ac94: e59cc004 ldr ip, [ip, #4]

ac98: e0802102 add r2, r0, r2, lsl #2

ac9c: e5966004 ldr r6, [r6, #4]

aca0: e5999004 ldr r9, [r9, #4]

aca4: e5933004 ldr r3, [r3, #4]

aca8: e5988004 ldr r8, [r8, #4]

acac: e5922004 ldr r2, [r2, #4]

acb0: e02cc00e eor ip, ip, lr

acb4: e0233009 eor r3, r3, r9

acb8: e02cc00b eor ip, ip, fp

acbc: e0233008 eor r3, r3, r8

acc0: e02cc006 eor ip, ip, r6

acc4: e0233002 eor r3, r3, r2

acc8: e5902034 ldr r2, [r0, #52] ; 0x34

accc: e08ce003 add lr, ip, r3

acd0: e5909038 ldr r9, [r0, #56] ; 0x38

acd4: e08ee002 add lr, lr, r2

acd8: e0833009 add r3, r3, r9

acdc: e02ee005 eor lr, lr, r5

ace0: e083c08c add ip, r3, ip, lsl #1

ace4: e02c9fe7 eor r9, ip, r7, ror #31

ace8: e1a0e0ee ror lr, lr, #1

acec: e1a08429 lsr r8, r9, #8

acf0: e1a0742e lsr r7, lr, #8

acf4: e1a06c29 lsr r6, r9, #24

acf8: e6ef3079 uxtb r3, r9

acfc: e1a05829 lsr r5, r9, #16

ad00: e6ef207e uxtb r2, lr

ad04: e1a0bc2e lsr fp, lr, #24

ad08: e1a0c82e lsr ip, lr, #16

ad0c: e2866e4a add r6, r6, #1184 ; 0x4a0

ad10: e2833e5a add r3, r3, #1440 ; 0x5a0

ad14: e6ef8078 uxtb r8, r8

ad18: e6ef7077 uxtb r7, r7

ad1c: e2822e4a add r2, r2, #1184 ; 0x4a0

ad20: e28bbe7a add fp, fp, #1952 ; 0x7a0

ad24: e286600e add r6, r6, #14

ad28: e283300e add r3, r3, #14

ad2c: e2888e6a add r8, r8, #1696 ; 0x6a0

ad30: e6ef5075 uxtb r5, r5

ad34: e282200e add r2, r2, #14

ad38: e2877e5a add r7, r7, #1440 ; 0x5a0

ad3c: e28bb00e add fp, fp, #14

ad40: e6efc07c uxtb ip, ip

ad44: e288800e add r8, r8, #14

ad48: e2855e7a add r5, r5, #1952 ; 0x7a0

ad4c: e287700e add r7, r7, #14

ad50: e28cce6a add ip, ip, #1696 ; 0x6a0

ad54: e0806106 add r6, r0, r6, lsl #2

ad58: e0803103 add r3, r0, r3, lsl #2

ad5c: e285500e add r5, r5, #14

ad60: e0802102 add r2, r0, r2, lsl #2

ad64: e080b10b add fp, r0, fp, lsl #2

ad68: e28cc00e add ip, ip, #14

ad6c: e0808108 add r8, r0, r8, lsl #2

ad70: e0807107 add r7, r0, r7, lsl #2

ad74: e5933004 ldr r3, [r3, #4]

ad78: e59bb004 ldr fp, [fp, #4]

ad7c: e5966004 ldr r6, [r6, #4]

ad80: e5922004 ldr r2, [r2, #4]

ad84: e0805105 add r5, r0, r5, lsl #2

ad88: e080c10c add ip, r0, ip, lsl #2

ad8c: e5977004 ldr r7, [r7, #4]

ad90: e5988004 ldr r8, [r8, #4]

ad94: e0266003 eor r6, r6, r3

ad98: e5955004 ldr r5, [r5, #4]

ad9c: e59cc004 ldr ip, [ip, #4]

ada0: e022200b eor r2, r2, fp

ada4: e0288006 eor r8, r8, r6

ada8: e0222007 eor r2, r2, r7

adac: e0283005 eor r3, r8, r5

adb0: e022200c eor r2, r2, ip

adb4: e590c03c ldr ip, [r0, #60] ; 0x3c

adb8: e083b002 add fp, r3, r2

adbc: e5908040 ldr r8, [r0, #64] ; 0x40

adc0: e08bb00c add fp, fp, ip

adc4: e02aa00b eor sl, sl, fp

adc8: e0828008 add r8, r2, r8

adcc: e0888083 add r8, r8, r3, lsl #1

add0: e0283fe4 eor r3, r8, r4, ror #31

add4: e1a0b0ea ror fp, sl, #1

add8: e1a07423 lsr r7, r3, #8

addc: e1a0842b lsr r8, fp, #8

ade0: e1a06c23 lsr r6, r3, #24

ade4: e6efc073 uxtb ip, r3

ade8: e1a05823 lsr r5, r3, #16

adec: e6ef207b uxtb r2, fp

adf0: e1a0ac2b lsr sl, fp, #24

adf4: e1a0482b lsr r4, fp, #16

adf8: e2866e4a add r6, r6, #1184 ; 0x4a0

adfc: e28cce5a add ip, ip, #1440 ; 0x5a0

ae00: e6ef7077 uxtb r7, r7

ae04: e6ef8078 uxtb r8, r8

ae08: e2822e4a add r2, r2, #1184 ; 0x4a0

ae0c: e28aae7a add sl, sl, #1952 ; 0x7a0

ae10: e286600e add r6, r6, #14

ae14: e28cc00e add ip, ip, #14

ae18: e2877e6a add r7, r7, #1696 ; 0x6a0

ae1c: e6ef5075 uxtb r5, r5

ae20: e282200e add r2, r2, #14

ae24: e2888e5a add r8, r8, #1440 ; 0x5a0

ae28: e28aa00e add sl, sl, #14

ae2c: e6ef4074 uxtb r4, r4

ae30: e287700e add r7, r7, #14

ae34: e2855e7a add r5, r5, #1952 ; 0x7a0

ae38: e288800e add r8, r8, #14

ae3c: e2844e6a add r4, r4, #1696 ; 0x6a0

ae40: e0806106 add r6, r0, r6, lsl #2

ae44: e080c10c add ip, r0, ip, lsl #2

ae48: e285500e add r5, r5, #14

ae4c: e0802102 add r2, r0, r2, lsl #2

ae50: e080a10a add sl, r0, sl, lsl #2

ae54: e284400e add r4, r4, #14

ae58: e0807107 add r7, r0, r7, lsl #2

ae5c: e0808108 add r8, r0, r8, lsl #2

ae60: e59cc004 ldr ip, [ip, #4]

ae64: e59aa004 ldr sl, [sl, #4]

ae68: e5966004 ldr r6, [r6, #4]

ae6c: e5922004 ldr r2, [r2, #4]

ae70: e0805105 add r5, r0, r5, lsl #2

ae74: e0804104 add r4, r0, r4, lsl #2

ae78: e5988004 ldr r8, [r8, #4]

ae7c: e5977004 ldr r7, [r7, #4]

ae80: e026600c eor r6, r6, ip

ae84: e5955004 ldr r5, [r5, #4]

ae88: e5944004 ldr r4, [r4, #4]

ae8c: e022200a eor r2, r2, sl

ae90: e0277006 eor r7, r7, r6

ae94: e0222008 eor r2, r2, r8

ae98: e0277005 eor r7, r7, r5

ae9c: e0222004 eor r2, r2, r4

aea0: e5905044 ldr r5, [r0, #68] ; 0x44

aea4: e0874002 add r4, r7, r2

aea8: e590c048 ldr ip, [r0, #72] ; 0x48

aeac: e0844005 add r4, r4, r5

aeb0: e082200c add r2, r2, ip

aeb4: e024400e eor r4, r4, lr

aeb8: e0827087 add r7, r2, r7, lsl #1

aebc: e027cfe9 eor ip, r7, r9, ror #31

aec0: e1a040e4 ror r4, r4, #1

aec4: e1a0a42c lsr sl, ip, #8

aec8: e1a09424 lsr r9, r4, #8

aecc: e1a0782c lsr r7, ip, #16

aed0: e1a06c2c lsr r6, ip, #24

aed4: e6ef507c uxtb r5, ip

aed8: e6ef2074 uxtb r2, r4

aedc: e1a08c24 lsr r8, r4, #24

aee0: e1a0e824 lsr lr, r4, #16

aee4: e2866e4a add r6, r6, #1184 ; 0x4a0

aee8: e2855e5a add r5, r5, #1440 ; 0x5a0

aeec: e6efa07a uxtb sl, sl

aef0: e6ef7077 uxtb r7, r7

aef4: e6ef9079 uxtb r9, r9

aef8: e2822e4a add r2, r2, #1184 ; 0x4a0

aefc: e2888e7a add r8, r8, #1952 ; 0x7a0

af00: e286600e add r6, r6, #14

af04: e285500e add r5, r5, #14

af08: e28aae6a add sl, sl, #1696 ; 0x6a0

af0c: e2877e7a add r7, r7, #1952 ; 0x7a0

af10: e282200e add r2, r2, #14

af14: e2899e5a add r9, r9, #1440 ; 0x5a0

af18: e288800e add r8, r8, #14

af1c: e6efe07e uxtb lr, lr

af20: e28aa00e add sl, sl, #14

af24: e287700e add r7, r7, #14

af28: e289900e add r9, r9, #14

af2c: e28eee6a add lr, lr, #1696 ; 0x6a0

af30: e0806106 add r6, r0, r6, lsl #2

af34: e0805105 add r5, r0, r5, lsl #2

af38: e0802102 add r2, r0, r2, lsl #2

af3c: e0808108 add r8, r0, r8, lsl #2

af40: e28ee00e add lr, lr, #14

af44: e080a10a add sl, r0, sl, lsl #2

af48: e0807107 add r7, r0, r7, lsl #2

af4c: e0809109 add r9, r0, r9, lsl #2

af50: e5966004 ldr r6, [r6, #4]

af54: e5988004 ldr r8, [r8, #4]

af58: e5955004 ldr r5, [r5, #4]

af5c: e5922004 ldr r2, [r2, #4]

af60: e080e10e add lr, r0, lr, lsl #2

af64: e59aa004 ldr sl, [sl, #4]

af68: e5977004 ldr r7, [r7, #4]

af6c: e5999004 ldr r9, [r9, #4]

af70: e59ee004 ldr lr, [lr, #4]

af74: e0255006 eor r5, r5, r6

af78: e0222008 eor r2, r2, r8

af7c: e025600a eor r6, r5, sl

af80: e0222009 eor r2, r2, r9

af84: e0268007 eor r8, r6, r7

af88: e022200e eor r2, r2, lr

af8c: e590e04c ldr lr, [r0, #76] ; 0x4c

af90: e088a002 add sl, r8, r2

af94: e5906050 ldr r6, [r0, #80] ; 0x50

af98: e08aa00e add sl, sl, lr

af9c: e02aa00b eor sl, sl, fp

afa0: e0826006 add r6, r2, r6

afa4: e0866088 add r6, r6, r8, lsl #1

afa8: e0268fe3 eor r8, r6, r3, ror #31

afac: e1a0a0ea ror sl, sl, #1

afb0: e1a05428 lsr r5, r8, #8

afb4: e1a0942a lsr r9, sl, #8

afb8: e1a07c28 lsr r7, r8, #24

afbc: e6ef6078 uxtb r6, r8

afc0: e1a0e828 lsr lr, r8, #16

afc4: e6ef307a uxtb r3, sl

afc8: e1a0bc2a lsr fp, sl, #24

afcc: e1a0282a lsr r2, sl, #16

afd0: e2877e4a add r7, r7, #1184 ; 0x4a0

afd4: e2866e5a add r6, r6, #1440 ; 0x5a0

afd8: e6ef5075 uxtb r5, r5

afdc: e6ef9079 uxtb r9, r9

afe0: e2833e4a add r3, r3, #1184 ; 0x4a0

afe4: e28bbe7a add fp, fp, #1952 ; 0x7a0

afe8: e287700e add r7, r7, #14

afec: e286600e add r6, r6, #14

aff0: e2855e6a add r5, r5, #1696 ; 0x6a0

aff4: e6efe07e uxtb lr, lr

aff8: e283300e add r3, r3, #14

affc: e2899e5a add r9, r9, #1440 ; 0x5a0

b000: e28bb00e add fp, fp, #14

b004: e6ef2072 uxtb r2, r2

b008: e285500e add r5, r5, #14

b00c: e28eee7a add lr, lr, #1952 ; 0x7a0

b010: e289900e add r9, r9, #14

b014: e2822e6a add r2, r2, #1696 ; 0x6a0

b018: e0807107 add r7, r0, r7, lsl #2

b01c: e0806106 add r6, r0, r6, lsl #2

b020: e28ee00e add lr, lr, #14

b024: e0803103 add r3, r0, r3, lsl #2

b028: e080b10b add fp, r0, fp, lsl #2

b02c: e282200e add r2, r2, #14

b030: e0805105 add r5, r0, r5, lsl #2

b034: e0809109 add r9, r0, r9, lsl #2

b038: e5977004 ldr r7, [r7, #4]

b03c: e59bb004 ldr fp, [fp, #4]

b040: e5966004 ldr r6, [r6, #4]

b044: e5933004 ldr r3, [r3, #4]

b048: e0802102 add r2, r0, r2, lsl #2

b04c: e080e10e add lr, r0, lr, lsl #2

b050: e5999004 ldr r9, [r9, #4]

b054: e5955004 ldr r5, [r5, #4]

b058: e0266007 eor r6, r6, r7

b05c: e59ee004 ldr lr, [lr, #4]

b060: e023300b eor r3, r3, fp

b064: e592b004 ldr fp, [r2, #4]

b068: e0255006 eor r5, r5, r6

b06c: e0233009 eor r3, r3, r9

b070: e025700e eor r7, r5, lr

b074: e023300b eor r3, r3, fp

b078: e5902054 ldr r2, [r0, #84] ; 0x54

b07c: e0879003 add r9, r7, r3

b080: e5905058 ldr r5, [r0, #88] ; 0x58

b084: e0899002 add r9, r9, r2

b088: e0299004 eor r9, r9, r4

b08c: e0835005 add r5, r3, r5

b090: e0855087 add r5, r5, r7, lsl #1

b094: e0257fec eor r7, r5, ip, ror #31

b098: e1a090e9 ror r9, r9, #1

b09c: e1a0e427 lsr lr, r7, #8

b0a0: e1a05429 lsr r5, r9, #8

b0a4: e1a06c27 lsr r6, r7, #24

b0a8: e6ef4077 uxtb r4, r7

b0ac: e1a0c827 lsr ip, r7, #16

b0b0: e6ef3079 uxtb r3, r9

b0b4: e1a0bc29 lsr fp, r9, #24

b0b8: e1a02829 lsr r2, r9, #16

b0bc: e2866e4a add r6, r6, #1184 ; 0x4a0

b0c0: e2844e5a add r4, r4, #1440 ; 0x5a0

b0c4: e6efe07e uxtb lr, lr

b0c8: e6ef5075 uxtb r5, r5

b0cc: e2833e4a add r3, r3, #1184 ; 0x4a0

b0d0: e28bbe7a add fp, fp, #1952 ; 0x7a0

b0d4: e286600e add r6, r6, #14

b0d8: e284400e add r4, r4, #14

b0dc: e28eee6a add lr, lr, #1696 ; 0x6a0

b0e0: e6efc07c uxtb ip, ip

b0e4: e283300e add r3, r3, #14

b0e8: e2855e5a add r5, r5, #1440 ; 0x5a0

b0ec: e28bb00e add fp, fp, #14

b0f0: e6ef2072 uxtb r2, r2

b0f4: e28ee00e add lr, lr, #14

b0f8: e28cce7a add ip, ip, #1952 ; 0x7a0

b0fc: e285500e add r5, r5, #14

b100: e2822e6a add r2, r2, #1696 ; 0x6a0

b104: e0806106 add r6, r0, r6, lsl #2

b108: e0804104 add r4, r0, r4, lsl #2

b10c: e28cc00e add ip, ip, #14

b110: e0803103 add r3, r0, r3, lsl #2

b114: e080b10b add fp, r0, fp, lsl #2

b118: e282200e add r2, r2, #14

b11c: e080e10e add lr, r0, lr, lsl #2

b120: e0805105 add r5, r0, r5, lsl #2

b124: e5966004 ldr r6, [r6, #4]

b128: e59bb004 ldr fp, [fp, #4]

b12c: e5944004 ldr r4, [r4, #4]

b130: e5933004 ldr r3, [r3, #4]

b134: e080c10c add ip, r0, ip, lsl #2

b138: e0802102 add r2, r0, r2, lsl #2

b13c: e5955004 ldr r5, [r5, #4]

b140: e59ee004 ldr lr, [lr, #4]

b144: e0244006 eor r4, r4, r6

b148: e59cc004 ldr ip, [ip, #4]

b14c: e5922004 ldr r2, [r2, #4]

b150: e023300b eor r3, r3, fp

b154: e02ee004 eor lr, lr, r4

b158: e0233005 eor r3, r3, r5

b15c: e0233002 eor r3, r3, r2

b160: e02ee00c eor lr, lr, ip

b164: e590205c ldr r2, [r0, #92] ; 0x5c

b168: e08ec003 add ip, lr, r3

b16c: e5906060 ldr r6, [r0, #96] ; 0x60

b170: e08cc002 add ip, ip, r2

b174: e0833006 add r3, r3, r6

b178: e02cc00a eor ip, ip, sl

b17c: e083e08e add lr, r3, lr, lsl #1

b180: e02e6fe8 eor r6, lr, r8, ror #31

b184: e1a0c0ec ror ip, ip, #1

b188: e1a02426 lsr r2, r6, #8

b18c: e1a0a42c lsr sl, ip, #8

b190: e1a08c26 lsr r8, r6, #24

b194: e6ef5076 uxtb r5, r6

b198: e1a04826 lsr r4, r6, #16

b19c: e6ef307c uxtb r3, ip

b1a0: e1a0bc2c lsr fp, ip, #24

b1a4: e1a0e82c lsr lr, ip, #16

b1a8: e2888e4a add r8, r8, #1184 ; 0x4a0

b1ac: e2855e5a add r5, r5, #1440 ; 0x5a0

b1b0: e6ef2072 uxtb r2, r2

b1b4: e6efa07a uxtb sl, sl

b1b8: e2833e4a add r3, r3, #1184 ; 0x4a0

b1bc: e28bbe7a add fp, fp, #1952 ; 0x7a0

b1c0: e288800e add r8, r8, #14

b1c4: e285500e add r5, r5, #14

b1c8: e2822e6a add r2, r2, #1696 ; 0x6a0

b1cc: e6ef4074 uxtb r4, r4

b1d0: e283300e add r3, r3, #14

b1d4: e28aae5a add sl, sl, #1440 ; 0x5a0

b1d8: e28bb00e add fp, fp, #14

b1dc: e6efe07e uxtb lr, lr

b1e0: e282200e add r2, r2, #14

b1e4: e2844e7a add r4, r4, #1952 ; 0x7a0

b1e8: e28aa00e add sl, sl, #14

b1ec: e28eee6a add lr, lr, #1696 ; 0x6a0

b1f0: e0808108 add r8, r0, r8, lsl #2

b1f4: e0805105 add r5, r0, r5, lsl #2

b1f8: e284400e add r4, r4, #14

b1fc: e0803103 add r3, r0, r3, lsl #2

b200: e080b10b add fp, r0, fp, lsl #2

b204: e28ee00e add lr, lr, #14

b208: e0802102 add r2, r0, r2, lsl #2

b20c: e080a10a add sl, r0, sl, lsl #2

b210: e5955004 ldr r5, [r5, #4]

b214: e59bb004 ldr fp, [fp, #4]

b218: e5988004 ldr r8, [r8, #4]

b21c: e5933004 ldr r3, [r3, #4]

b220: e0804104 add r4, r0, r4, lsl #2

b224: e080e10e add lr, r0, lr, lsl #2

b228: e59aa004 ldr sl, [sl, #4]

b22c: e5922004 ldr r2, [r2, #4]

b230: e0288005 eor r8, r8, r5

b234: e5944004 ldr r4, [r4, #4]

b238: e023300b eor r3, r3, fp

b23c: e59ee004 ldr lr, [lr, #4]

b240: e0222008 eor r2, r2, r8

b244: e023300a eor r3, r3, sl

b248: e0225004 eor r5, r2, r4

b24c: e023300e eor r3, r3, lr

b250: e590e064 ldr lr, [r0, #100] ; 0x64

b254: e0858003 add r8, r5, r3

b258: e5902068 ldr r2, [r0, #104] ; 0x68

b25c: e088800e add r8, r8, lr

b260: e0288009 eor r8, r8, r9

b264: e0832002 add r2, r3, r2

b268: e0822085 add r2, r2, r5, lsl #1

b26c: e0225fe7 eor r5, r2, r7, ror #31

b270: e1a080e8 ror r8, r8, #1

b274: e1a04425 lsr r4, r5, #8

b278: e1a0a428 lsr sl, r8, #8

b27c: e1a09c25 lsr r9, r5, #24

b280: e6efe075 uxtb lr, r5

b284: e1a07825 lsr r7, r5, #16

b288: e6ef2078 uxtb r2, r8

b28c: e1a0bc28 lsr fp, r8, #24

b290: e1a03828 lsr r3, r8, #16

b294: e2899e4a add r9, r9, #1184 ; 0x4a0

b298: e28eee5a add lr, lr, #1440 ; 0x5a0

b29c: e6ef4074 uxtb r4, r4

b2a0: e6efa07a uxtb sl, sl

b2a4: e2822e4a add r2, r2, #1184 ; 0x4a0

b2a8: e28bbe7a add fp, fp, #1952 ; 0x7a0

b2ac: e289900e add r9, r9, #14

b2b0: e28ee00e add lr, lr, #14

b2b4: e2844e6a add r4, r4, #1696 ; 0x6a0

b2b8: e6ef7077 uxtb r7, r7

b2bc: e282200e add r2, r2, #14

b2c0: e28aae5a add sl, sl, #1440 ; 0x5a0

b2c4: e28bb00e add fp, fp, #14

b2c8: e6ef3073 uxtb r3, r3

b2cc: e284400e add r4, r4, #14

b2d0: e2877e7a add r7, r7, #1952 ; 0x7a0

b2d4: e28aa00e add sl, sl, #14

b2d8: e2833e6a add r3, r3, #1696 ; 0x6a0

b2dc: e0809109 add r9, r0, r9, lsl #2

b2e0: e080e10e add lr, r0, lr, lsl #2

b2e4: e287700e add r7, r7, #14

b2e8: e0802102 add r2, r0, r2, lsl #2

b2ec: e080b10b add fp, r0, fp, lsl #2

b2f0: e283300e add r3, r3, #14

b2f4: e0804104 add r4, r0, r4, lsl #2

b2f8: e080a10a add sl, r0, sl, lsl #2

b2fc: e59ee004 ldr lr, [lr, #4]

b300: e59bb004 ldr fp, [fp, #4]

b304: e5999004 ldr r9, [r9, #4]

b308: e5922004 ldr r2, [r2, #4]

b30c: e0803103 add r3, r0, r3, lsl #2

b310: e0807107 add r7, r0, r7, lsl #2

b314: e59aa004 ldr sl, [sl, #4]

b318: e5944004 ldr r4, [r4, #4]

b31c: e029900e eor r9, r9, lr

b320: e5977004 ldr r7, [r7, #4]

b324: e022200b eor r2, r2, fp

b328: e593b004 ldr fp, [r3, #4]

b32c: e0244009 eor r4, r4, r9

b330: e022200a eor r2, r2, sl

b334: e024e007 eor lr, r4, r7

b338: e022200b eor r2, r2, fp

b33c: e590306c ldr r3, [r0, #108] ; 0x6c

b340: e08e7002 add r7, lr, r2

b344: e5904070 ldr r4, [r0, #112] ; 0x70

b348: e0877003 add r7, r7, r3

b34c: e027700c eor r7, r7, ip

b350: e0824004 add r4, r2, r4

b354: e084408e add r4, r4, lr, lsl #1

b358: e1a070e7 ror r7, r7, #1

b35c: e024efe6 eor lr, r4, r6, ror #31

b360: e1a09427 lsr r9, r7, #8

b364: e1a0a42e lsr sl, lr, #8

b368: e1a06c2e lsr r6, lr, #24

b36c: e6ef207e uxtb r2, lr

b370: e1a0482e lsr r4, lr, #16

b374: e6ef3077 uxtb r3, r7

b378: e1a0bc27 lsr fp, r7, #24

b37c: e1a0c827 lsr ip, r7, #16

b380: e2866e4a add r6, r6, #1184 ; 0x4a0

b384: e2822e5a add r2, r2, #1440 ; 0x5a0

b388: e6efa07a uxtb sl, sl

b38c: e6ef9079 uxtb r9, r9

b390: e2833e4a add r3, r3, #1184 ; 0x4a0

b394: e28bbe7a add fp, fp, #1952 ; 0x7a0

b398: e286600e add r6, r6, #14

b39c: e282200e add r2, r2, #14

b3a0: e28aae6a add sl, sl, #1696 ; 0x6a0

b3a4: e6ef4074 uxtb r4, r4

b3a8: e283300e add r3, r3, #14

b3ac: e2899e5a add r9, r9, #1440 ; 0x5a0

b3b0: e28bb00e add fp, fp, #14

b3b4: e6efc07c uxtb ip, ip

b3b8: e28aa00e add sl, sl, #14

b3bc: e2844e7a add r4, r4, #1952 ; 0x7a0

b3c0: e289900e add r9, r9, #14

b3c4: e28cce6a add ip, ip, #1696 ; 0x6a0

b3c8: e0806106 add r6, r0, r6, lsl #2

b3cc: e0802102 add r2, r0, r2, lsl #2

b3d0: e284400e add r4, r4, #14

b3d4: e0803103 add r3, r0, r3, lsl #2

b3d8: e080b10b add fp, r0, fp, lsl #2

b3dc: e28cc00e add ip, ip, #14

b3e0: e080a10a add sl, r0, sl, lsl #2

b3e4: e0809109 add r9, r0, r9, lsl #2

b3e8: e5922004 ldr r2, [r2, #4]

b3ec: e59bb004 ldr fp, [fp, #4]

b3f0: e5966004 ldr r6, [r6, #4]

b3f4: e5933004 ldr r3, [r3, #4]

b3f8: e080c10c add ip, r0, ip, lsl #2

b3fc: e0804104 add r4, r0, r4, lsl #2

b400: e59aa004 ldr sl, [sl, #4]

b404: e5999004 ldr r9, [r9, #4]

b408: e0266002 eor r6, r6, r2

b40c: e5944004 ldr r4, [r4, #4]

b410: e023300b eor r3, r3, fp

b414: e59cb004 ldr fp, [ip, #4]

b418: e026200a eor r2, r6, sl

b41c: e0233009 eor r3, r3, r9

b420: e0244002 eor r4, r4, r2

b424: e023300b eor r3, r3, fp

b428: e590c074 ldr ip, [r0, #116] ; 0x74

b42c: e0846003 add r6, r4, r3

b430: e5902078 ldr r2, [r0, #120] ; 0x78

b434: e086600c add r6, r6, ip

b438: e0266008 eor r6, r6, r8

b43c: e0832002 add r2, r3, r2

b440: e0822084 add r2, r2, r4, lsl #1

b444: e0222fe5 eor r2, r2, r5, ror #31

b448: e1a060e6 ror r6, r6, #1

b44c: e1a03422 lsr r3, r2, #8

b450: e1a0a426 lsr sl, r6, #8

b454: e1a09c22 lsr r9, r2, #24

b458: e6ef4072 uxtb r4, r2

b45c: e1a08822 lsr r8, r2, #16

b460: e6efc076 uxtb ip, r6

b464: e1a0bc26 lsr fp, r6, #24

b468: e1a05826 lsr r5, r6, #16

b46c: e2899e4a add r9, r9, #1184 ; 0x4a0

b470: e2844e5a add r4, r4, #1440 ; 0x5a0

b474: e6ef3073 uxtb r3, r3

b478: e6efa07a uxtb sl, sl

b47c: e28cce4a add ip, ip, #1184 ; 0x4a0

b480: e28bbe7a add fp, fp, #1952 ; 0x7a0

b484: e289900e add r9, r9, #14

b488: e284400e add r4, r4, #14

b48c: e2833e6a add r3, r3, #1696 ; 0x6a0

b490: e6ef8078 uxtb r8, r8

b494: e28cc00e add ip, ip, #14

b498: e28aae5a add sl, sl, #1440 ; 0x5a0

b49c: e28bb00e add fp, fp, #14

b4a0: e6ef5075 uxtb r5, r5

b4a4: e283300e add r3, r3, #14

b4a8: e2888e7a add r8, r8, #1952 ; 0x7a0

b4ac: e28aa00e add sl, sl, #14

b4b0: e2855e6a add r5, r5, #1696 ; 0x6a0

b4b4: e0809109 add r9, r0, r9, lsl #2

b4b8: e0804104 add r4, r0, r4, lsl #2

b4bc: e288800e add r8, r8, #14

b4c0: e080c10c add ip, r0, ip, lsl #2

b4c4: e080b10b add fp, r0, fp, lsl #2

b4c8: e285500e add r5, r5, #14

b4cc: e0803103 add r3, r0, r3, lsl #2

b4d0: e080a10a add sl, r0, sl, lsl #2

b4d4: e5944004 ldr r4, [r4, #4]

b4d8: e59bb004 ldr fp, [fp, #4]

b4dc: e5999004 ldr r9, [r9, #4]

b4e0: e59cc004 ldr ip, [ip, #4]

b4e4: e0808108 add r8, r0, r8, lsl #2

b4e8: e0805105 add r5, r0, r5, lsl #2

b4ec: e59aa004 ldr sl, [sl, #4]

b4f0: e5933004 ldr r3, [r3, #4]

b4f4: e0299004 eor r9, r9, r4

b4f8: e5988004 ldr r8, [r8, #4]

b4fc: e5955004 ldr r5, [r5, #4]

b500: e02cc00b eor ip, ip, fp

b504: e0233009 eor r3, r3, r9

b508: e02cc00a eor ip, ip, sl

b50c: e0234008 eor r4, r3, r8

b510: e02cc005 eor ip, ip, r5

b514: e590807c ldr r8, [r0, #124] ; 0x7c

b518: e084500c add r5, r4, ip

b51c: e5903080 ldr r3, [r0, #128] ; 0x80

b520: e0855008 add r5, r5, r8

b524: e0255007 eor r5, r5, r7

b528: e08c3003 add r3, ip, r3

b52c: e0833084 add r3, r3, r4, lsl #1

b530: e0234fee eor r4, r3, lr, ror #31

b534: e1a050e5 ror r5, r5, #1

b538: e1a09424 lsr r9, r4, #8

b53c: e1a0c425 lsr ip, r5, #8

b540: e1a0bc24 lsr fp, r4, #24

b544: e6ef8074 uxtb r8, r4

b548: e1a07824 lsr r7, r4, #16

b54c: e6efe075 uxtb lr, r5

b550: e1a0ac25 lsr sl, r5, #24

b554: e1a03825 lsr r3, r5, #16

b558: e28bbe4a add fp, fp, #1184 ; 0x4a0

b55c: e2888e5a add r8, r8, #1440 ; 0x5a0

b560: e6ef9079 uxtb r9, r9

b564: e6efc07c uxtb ip, ip

b568: e28eee4a add lr, lr, #1184 ; 0x4a0

b56c: e28aae7a add sl, sl, #1952 ; 0x7a0

b570: e28bb00e add fp, fp, #14

b574: e288800e add r8, r8, #14

b578: e2899e6a add r9, r9, #1696 ; 0x6a0

b57c: e6ef7077 uxtb r7, r7

b580: e28ee00e add lr, lr, #14

b584: e28cce5a add ip, ip, #1440 ; 0x5a0

b588: e28aa00e add sl, sl, #14

b58c: e6ef3073 uxtb r3, r3

b590: e289900e add r9, r9, #14

b594: e2877e7a add r7, r7, #1952 ; 0x7a0

b598: e28cc00e add ip, ip, #14

b59c: e2833e6a add r3, r3, #1696 ; 0x6a0

b5a0: e080b10b add fp, r0, fp, lsl #2

b5a4: e0808108 add r8, r0, r8, lsl #2

b5a8: e287700e add r7, r7, #14

b5ac: e080e10e add lr, r0, lr, lsl #2

b5b0: e080a10a add sl, r0, sl, lsl #2

b5b4: e283300e add r3, r3, #14

b5b8: e0809109 add r9, r0, r9, lsl #2

b5bc: e080c10c add ip, r0, ip, lsl #2

b5c0: e59bb004 ldr fp, [fp, #4]

b5c4: e59aa004 ldr sl, [sl, #4]

b5c8: e5988004 ldr r8, [r8, #4]

b5cc: e59ee004 ldr lr, [lr, #4]

b5d0: e0807107 add r7, r0, r7, lsl #2

b5d4: e0803103 add r3, r0, r3, lsl #2

b5d8: e5999004 ldr r9, [r9, #4]

b5dc: e59cc004 ldr ip, [ip, #4]

b5e0: e5933004 ldr r3, [r3, #4]

b5e4: e028800b eor r8, r8, fp

b5e8: e5977004 ldr r7, [r7, #4]

b5ec: e02ee00a eor lr, lr, sl

b5f0: e0288009 eor r8, r8, r9

b5f4: e02ee00c eor lr, lr, ip

b5f8: e0277008 eor r7, r7, r8

b5fc: e02ec003 eor ip, lr, r3

b600: e5908084 ldr r8, [r0, #132] ; 0x84

b604: e087300c add r3, r7, ip

b608: e590e088 ldr lr, [r0, #136] ; 0x88

b60c: e0833008 add r3, r3, r8

b610: e0266003 eor r6, r6, r3

b614: e08cc00e add ip, ip, lr

b618: e08cc087 add ip, ip, r7, lsl #1

b61c: e02c2fe2 eor r2, ip, r2, ror #31

b620: e1a060e6 ror r6, r6, #1

b624: e1a0a422 lsr sl, r2, #8

b628: e1a09426 lsr r9, r6, #8

b62c: e1a08c22 lsr r8, r2, #24

b630: e6ef3072 uxtb r3, r2

b634: e1a07822 lsr r7, r2, #16

b638: e6efc076 uxtb ip, r6

b63c: e1a0bc26 lsr fp, r6, #24

b640: e1a0e826 lsr lr, r6, #16

b644: e2888e4a add r8, r8, #1184 ; 0x4a0

b648: e2833e5a add r3, r3, #1440 ; 0x5a0

b64c: e6efa07a uxtb sl, sl

b650: e6ef9079 uxtb r9, r9

b654: e28cce4a add ip, ip, #1184 ; 0x4a0

b658: e28bbe7a add fp, fp, #1952 ; 0x7a0

b65c: e288800e add r8, r8, #14

b660: e283300e add r3, r3, #14

b664: e28aae6a add sl, sl, #1696 ; 0x6a0

b668: e6ef7077 uxtb r7, r7

b66c: e28cc00e add ip, ip, #14

b670: e2899e5a add r9, r9, #1440 ; 0x5a0

b674: e28bb00e add fp, fp, #14

b678: e6efe07e uxtb lr, lr

b67c: e28aa00e add sl, sl, #14

b680: e2877e7a add r7, r7, #1952 ; 0x7a0

b684: e289900e add r9, r9, #14

b688: e28eee6a add lr, lr, #1696 ; 0x6a0

b68c: e0808108 add r8, r0, r8, lsl #2

b690: e0803103 add r3, r0, r3, lsl #2

b694: e287700e add r7, r7, #14

b698: e080c10c add ip, r0, ip, lsl #2

b69c: e080b10b add fp, r0, fp, lsl #2

b6a0: e28ee00e add lr, lr, #14

b6a4: e080a10a add sl, r0, sl, lsl #2

b6a8: e0809109 add r9, r0, r9, lsl #2

b6ac: e5933004 ldr r3, [r3, #4]

b6b0: e59bb004 ldr fp, [fp, #4]

b6b4: e5988004 ldr r8, [r8, #4]

b6b8: e59cc004 ldr ip, [ip, #4]

b6bc: e0807107 add r7, r0, r7, lsl #2

b6c0: e080e10e add lr, r0, lr, lsl #2

b6c4: e59aa004 ldr sl, [sl, #4]

b6c8: e5999004 ldr r9, [r9, #4]

b6cc: e59ee004 ldr lr, [lr, #4]

b6d0: e0288003 eor r8, r8, r3

b6d4: e5977004 ldr r7, [r7, #4]

b6d8: e02cc00b eor ip, ip, fp

b6dc: e028300a eor r3, r8, sl

b6e0: e02cc009 eor ip, ip, r9

b6e4: e0277003 eor r7, r7, r3

b6e8: e02cc00e eor ip, ip, lr

b6ec: e590808c ldr r8, [r0, #140] ; 0x8c

b6f0: e087e00c add lr, r7, ip

b6f4: e5903090 ldr r3, [r0, #144] ; 0x90

b6f8: e08ee008 add lr, lr, r8

b6fc: e025500e eor r5, r5, lr

b700: e08c3003 add r3, ip, r3

b704: e0833087 add r3, r3, r7, lsl #1

b708: e1a050e5 ror r5, r5, #1

b70c: e0233fe4 eor r3, r3, r4, ror #31

b710: e1a09425 lsr r9, r5, #8

b714: e1a08423 lsr r8, r3, #8

b718: e6efb075 uxtb fp, r5

b71c: e1a0ec25 lsr lr, r5, #24

b720: e1a07825 lsr r7, r5, #16

b724: e1a0cc23 lsr ip, r3, #24

b728: e6efa073 uxtb sl, r3

b72c: e1a04823 lsr r4, r3, #16

b730: e28bbe4a add fp, fp, #1184 ; 0x4a0

b734: e28eee7a add lr, lr, #1952 ; 0x7a0

b738: e6ef9079 uxtb r9, r9

b73c: e6ef8078 uxtb r8, r8

b740: e28cce4a add ip, ip, #1184 ; 0x4a0

b744: e28aae5a add sl, sl, #1440 ; 0x5a0

b748: e28bb00e add fp, fp, #14

b74c: e28ee00e add lr, lr, #14

b750: e2899e5a add r9, r9, #1440 ; 0x5a0

b754: e6ef7077 uxtb r7, r7

b758: e28cc00e add ip, ip, #14

b75c: e2888e6a add r8, r8, #1696 ; 0x6a0

b760: e28aa00e add sl, sl, #14

b764: e6ef4074 uxtb r4, r4

b768: e289900e add r9, r9, #14

b76c: e2877e6a add r7, r7, #1696 ; 0x6a0

b770: e288800e add r8, r8, #14

b774: e2844e7a add r4, r4, #1952 ; 0x7a0

b778: e080b10b add fp, r0, fp, lsl #2

b77c: e080e10e add lr, r0, lr, lsl #2

b780: e287700e add r7, r7, #14

b784: e080c10c add ip, r0, ip, lsl #2

b788: e080a10a add sl, r0, sl, lsl #2

b78c: e284400e add r4, r4, #14

b790: e0809109 add r9, r0, r9, lsl #2

b794: e0808108 add r8, r0, r8, lsl #2

b798: e59bb004 ldr fp, [fp, #4]

b79c: e59aa004 ldr sl, [sl, #4]

b7a0: e59ee004 ldr lr, [lr, #4]

b7a4: e59cc004 ldr ip, [ip, #4]

b7a8: e0807107 add r7, r0, r7, lsl #2

b7ac: e0804104 add r4, r0, r4, lsl #2

b7b0: e5999004 ldr r9, [r9, #4]

b7b4: e5988004 ldr r8, [r8, #4]

b7b8: e5977004 ldr r7, [r7, #4]

b7bc: e5944004 ldr r4, [r4, #4]

b7c0: e02ee00b eor lr, lr, fp

b7c4: e02cc00a eor ip, ip, sl

b7c8: e02ee009 eor lr, lr, r9

b7cc: e02cc008 eor ip, ip, r8

b7d0: e02ee007 eor lr, lr, r7

b7d4: e02cc004 eor ip, ip, r4

b7d8: e5908094 ldr r8, [r0, #148] ; 0x94

b7dc: e5907098 ldr r7, [r0, #152] ; 0x98

b7e0: e08c400e add r4, ip, lr

b7e4: e0844008 add r4, r4, r8

b7e8: e08ee007 add lr, lr, r7

b7ec: e0266004 eor r6, r6, r4

b7f0: e08ee08c add lr, lr, ip, lsl #1

b7f4: e02e2fe2 eor r2, lr, r2, ror #31

b7f8: e1a060e6 ror r6, r6, #1

b7fc: e1a09422 lsr r9, r2, #8

b800: e1a0c426 lsr ip, r6, #8

b804: e1a0bc22 lsr fp, r2, #24

b808: e6ef8072 uxtb r8, r2

b80c: e6efe076 uxtb lr, r6

b810: e1a0ac26 lsr sl, r6, #24

b814: e1a07822 lsr r7, r2, #16

b818: e1a04826 lsr r4, r6, #16

b81c: e28bbe4a add fp, fp, #1184 ; 0x4a0

b820: e2888e5a add r8, r8, #1440 ; 0x5a0

b824: e28eee4a add lr, lr, #1184 ; 0x4a0

b828: e28aae7a add sl, sl, #1952 ; 0x7a0

b82c: e6ef9079 uxtb r9, r9

b830: e6efc07c uxtb ip, ip

b834: e28bb00e add fp, fp, #14

b838: e288800e add r8, r8, #14

b83c: e28ee00e add lr, lr, #14

b840: e28aa00e add sl, sl, #14

b844: e2899e6a add r9, r9, #1696 ; 0x6a0

b848: e6ef7077 uxtb r7, r7

b84c: e28cce5a add ip, ip, #1440 ; 0x5a0

b850: e6ef4074 uxtb r4, r4

b854: e289900e add r9, r9, #14

b858: e2877e7a add r7, r7, #1952 ; 0x7a0

b85c: e28cc00e add ip, ip, #14

b860: e080b10b add fp, r0, fp, lsl #2

b864: e2844e6a add r4, r4, #1696 ; 0x6a0

b868: e0808108 add r8, r0, r8, lsl #2

b86c: e080e10e add lr, r0, lr, lsl #2

b870: e080a10a add sl, r0, sl, lsl #2

b874: e287700e add r7, r7, #14

b878: e284400e add r4, r4, #14

b87c: e59bb004 ldr fp, [fp, #4]

b880: e0809109 add r9, r0, r9, lsl #2

b884: e5988004 ldr r8, [r8, #4]

b888: e080c10c add ip, r0, ip, lsl #2

b88c: e59aa004 ldr sl, [sl, #4]

b890: e59ee004 ldr lr, [lr, #4]

b894: e0807107 add r7, r0, r7, lsl #2

b898: e0804104 add r4, r0, r4, lsl #2

b89c: e5999004 ldr r9, [r9, #4]

b8a0: e59cc004 ldr ip, [ip, #4]

b8a4: e02ee00a eor lr, lr, sl

b8a8: e590a014 ldr sl, [r0, #20]

b8ac: e5944004 ldr r4, [r4, #4]

b8b0: e5977004 ldr r7, [r7, #4]

b8b4: e028800b eor r8, r8, fp

b8b8: e0288009 eor r8, r8, r9

b8bc: e02ee00c eor lr, lr, ip

b8c0: e026600a eor r6, r6, sl

b8c4: e590909c ldr r9, [r0, #156] ; 0x9c

b8c8: e0277008 eor r7, r7, r8

b8cc: e02ec004 eor ip, lr, r4

b8d0: e59080a0 ldr r8, [r0, #160] ; 0xa0

b8d4: e5816000 str r6, [r1]

b8d8: e5904018 ldr r4, [r0, #24]

b8dc: e087e00c add lr, r7, ip

b8e0: e0222004 eor r2, r2, r4

b8e4: e08ee009 add lr, lr, r9

b8e8: e5812004 str r2, [r1, #4]

b8ec: e025500e eor r5, r5, lr

b8f0: e590201c ldr r2, [r0, #28]

b8f4: e08cc008 add ip, ip, r8

b8f8: e02250e5 eor r5, r2, r5, ror #1

b8fc: e5815008 str r5, [r1, #8]

b900: e5902020 ldr r2, [r0, #32]

b904: e08cc087 add ip, ip, r7, lsl #1

b908: e0223fe3 eor r3, r2, r3, ror #31

b90c: e02cc003 eor ip, ip, r3

b910: e581c00c str ip, [r1, #12]

b914: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

b918: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

b91c: e8910050 ldm r1, {r4, r6}

b920: e5902014 ldr r2, [r0, #20]

b924: e5903018 ldr r3, [r0, #24]

b928: e0245002 eor r5, r4, r2

b92c: e0269003 eor r9, r6, r3

b930: e1a06425 lsr r6, r5, #8

b934: e1a04429 lsr r4, r9, #8

b938: e6ef3075 uxtb r3, r5

b93c: e1a08c25 lsr r8, r5, #24

b940: e1a0e825 lsr lr, r5, #16

b944: e1a02c29 lsr r2, r9, #24

b948: e6ef7079 uxtb r7, r9

b94c: e1a0c829 lsr ip, r9, #16

b950: e6ef6076 uxtb r6, r6

b954: e2833e4a add r3, r3, #1184 ; 0x4a0

b958: e2888e7a add r8, r8, #1952 ; 0x7a0

b95c: e2822e4a add r2, r2, #1184 ; 0x4a0

b960: e2877e5a add r7, r7, #1440 ; 0x5a0

b964: e6ef4074 uxtb r4, r4

b968: e2866e5a add r6, r6, #1440 ; 0x5a0

b96c: e283300e add r3, r3, #14

b970: e288800e add r8, r8, #14

b974: e6efe07e uxtb lr, lr

b978: e282200e add r2, r2, #14

b97c: e287700e add r7, r7, #14

b980: e2844e6a add r4, r4, #1696 ; 0x6a0

b984: e6efc07c uxtb ip, ip

b988: e286600e add r6, r6, #14

b98c: e28eee6a add lr, lr, #1696 ; 0x6a0

b990: e284400e add r4, r4, #14

b994: e28cce7a add ip, ip, #1952 ; 0x7a0

b998: e0803103 add r3, r0, r3, lsl #2

b99c: e0808108 add r8, r0, r8, lsl #2

b9a0: e28ee00e add lr, lr, #14

b9a4: e0802102 add r2, r0, r2, lsl #2

b9a8: e0807107 add r7, r0, r7, lsl #2

b9ac: e28cc00e add ip, ip, #14

b9b0: e0806106 add r6, r0, r6, lsl #2

b9b4: e5988004 ldr r8, [r8, #4]

b9b8: e5933004 ldr r3, [r3, #4]

b9bc: e0804104 add r4, r0, r4, lsl #2

b9c0: e080e10e add lr, r0, lr, lsl #2

b9c4: e5977004 ldr r7, [r7, #4]

b9c8: e5966004 ldr r6, [r6, #4]

b9cc: e5922004 ldr r2, [r2, #4]

b9d0: e080c10c add ip, r0, ip, lsl #2

b9d4: e5944004 ldr r4, [r4, #4]

b9d8: e59ee004 ldr lr, [lr, #4]

b9dc: e0233008 eor r3, r3, r8

b9e0: e59cc004 ldr ip, [ip, #4]

b9e4: e0222007 eor r2, r2, r7

b9e8: e0233006 eor r3, r3, r6

b9ec: e023300e eor r3, r3, lr

b9f0: e0222004 eor r2, r2, r4

b9f4: e590e020 ldr lr, [r0, #32]

b9f8: e59070a0 ldr r7, [r0, #160] ; 0xa0

b9fc: e591a00c ldr sl, [r1, #12]

ba00: e022200c eor r2, r2, ip

ba04: e02aa00e eor sl, sl, lr

ba08: e5914008 ldr r4, [r1, #8]

ba0c: e590e01c ldr lr, [r0, #28]

ba10: e0837007 add r7, r3, r7

ba14: e590c09c ldr ip, [r0, #156] ; 0x9c

ba18: e0877082 add r7, r7, r2, lsl #1

ba1c: e024400e eor r4, r4, lr

ba20: e0823003 add r3, r2, r3

ba24: e027700a eor r7, r7, sl

ba28: e083300c add r3, r3, ip

ba2c: e0233fe4 eor r3, r3, r4, ror #31

ba30: e1a070e7 ror r7, r7, #1

ba34: e1a0b423 lsr fp, r3, #8

ba38: e1a0a427 lsr sl, r7, #8

ba3c: e6ef8073 uxtb r8, r3

ba40: e1a0ec23 lsr lr, r3, #24

ba44: e1a06823 lsr r6, r3, #16

ba48: e1a0cc27 lsr ip, r7, #24

ba4c: e6ef4077 uxtb r4, r7

ba50: e1a02827 lsr r2, r7, #16

ba54: e2888e4a add r8, r8, #1184 ; 0x4a0

ba58: e28eee7a add lr, lr, #1952 ; 0x7a0

ba5c: e6efb07b uxtb fp, fp

ba60: e6efa07a uxtb sl, sl

ba64: e28cce4a add ip, ip, #1184 ; 0x4a0

ba68: e2844e5a add r4, r4, #1440 ; 0x5a0

ba6c: e288800e add r8, r8, #14

ba70: e28ee00e add lr, lr, #14

ba74: e28bbe5a add fp, fp, #1440 ; 0x5a0

ba78: e6ef6076 uxtb r6, r6

ba7c: e28cc00e add ip, ip, #14

ba80: e28aae6a add sl, sl, #1696 ; 0x6a0

ba84: e284400e add r4, r4, #14

ba88: e6ef2072 uxtb r2, r2

ba8c: e28bb00e add fp, fp, #14

ba90: e2866e6a add r6, r6, #1696 ; 0x6a0

ba94: e28aa00e add sl, sl, #14

ba98: e2822e7a add r2, r2, #1952 ; 0x7a0

ba9c: e0808108 add r8, r0, r8, lsl #2

baa0: e080e10e add lr, r0, lr, lsl #2

baa4: e286600e add r6, r6, #14

baa8: e080c10c add ip, r0, ip, lsl #2

baac: e0804104 add r4, r0, r4, lsl #2

bab0: e282200e add r2, r2, #14

bab4: e080b10b add fp, r0, fp, lsl #2

bab8: e080a10a add sl, r0, sl, lsl #2

babc: e5988004 ldr r8, [r8, #4]

bac0: e5944004 ldr r4, [r4, #4]

bac4: e59ee004 ldr lr, [lr, #4]

bac8: e0806106 add r6, r0, r6, lsl #2

bacc: e59cc004 ldr ip, [ip, #4]

bad0: e0802102 add r2, r0, r2, lsl #2

bad4: e59bb004 ldr fp, [fp, #4]

bad8: e59aa004 ldr sl, [sl, #4]

badc: e02ee008 eor lr, lr, r8

bae0: e02cc004 eor ip, ip, r4

bae4: e5968004 ldr r8, [r6, #4]

bae8: e5924004 ldr r4, [r2, #4]

baec: e02ee00b eor lr, lr, fp

baf0: e02cc00a eor ip, ip, sl

baf4: e5906098 ldr r6, [r0, #152] ; 0x98

baf8: e02cc004 eor ip, ip, r4

bafc: e02ee008 eor lr, lr, r8

bb00: e08e6006 add r6, lr, r6

bb04: e5904094 ldr r4, [r0, #148] ; 0x94

bb08: e086608c add r6, r6, ip, lsl #1

bb0c: e0266009 eor r6, r6, r9

bb10: e08ee004 add lr, lr, r4

bb14: e1a060e6 ror r6, r6, #1

bb18: e08ee00c add lr, lr, ip

bb1c: e02e4fe5 eor r4, lr, r5, ror #31

bb20: e1a0a426 lsr sl, r6, #8

bb24: e1a08826 lsr r8, r6, #16

bb28: e1a09424 lsr r9, r4, #8

bb2c: e1a0bc26 lsr fp, r6, #24

bb30: e6efe076 uxtb lr, r6

bb34: e6efc074 uxtb ip, r4

bb38: e1a05c24 lsr r5, r4, #24

bb3c: e1a02824 lsr r2, r4, #16

bb40: e6efa07a uxtb sl, sl

bb44: e6ef8078 uxtb r8, r8

bb48: e28bbe4a add fp, fp, #1184 ; 0x4a0

bb4c: e28eee5a add lr, lr, #1440 ; 0x5a0

bb50: e28aae6a add sl, sl, #1696 ; 0x6a0

bb54: e2888e7a add r8, r8, #1952 ; 0x7a0

bb58: e6ef9079 uxtb r9, r9

bb5c: e28cce4a add ip, ip, #1184 ; 0x4a0

bb60: e2855e7a add r5, r5, #1952 ; 0x7a0

bb64: e28bb00e add fp, fp, #14

bb68: e28ee00e add lr, lr, #14

bb6c: e28aa00e add sl, sl, #14

bb70: e288800e add r8, r8, #14

bb74: e28cc00e add ip, ip, #14

bb78: e2899e5a add r9, r9, #1440 ; 0x5a0

bb7c: e285500e add r5, r5, #14

bb80: e6ef2072 uxtb r2, r2

bb84: e289900e add r9, r9, #14

bb88: e2822e6a add r2, r2, #1696 ; 0x6a0

bb8c: e080b10b add fp, r0, fp, lsl #2

bb90: e080e10e add lr, r0, lr, lsl #2

bb94: e080a10a add sl, r0, sl, lsl #2

bb98: e0808108 add r8, r0, r8, lsl #2

bb9c: e080c10c add ip, r0, ip, lsl #2

bba0: e0805105 add r5, r0, r5, lsl #2

bba4: e282200e add r2, r2, #14

bba8: e0809109 add r9, r0, r9, lsl #2

bbac: e59bb004 ldr fp, [fp, #4]

bbb0: e59aa004 ldr sl, [sl, #4]

bbb4: e59ee004 ldr lr, [lr, #4]

bbb8: e0802102 add r2, r0, r2, lsl #2

bbbc: e5988004 ldr r8, [r8, #4]

bbc0: e5955004 ldr r5, [r5, #4]

bbc4: e59cc004 ldr ip, [ip, #4]

bbc8: e5999004 ldr r9, [r9, #4]

bbcc: e02ee00b eor lr, lr, fp

bbd0: e02cc005 eor ip, ip, r5

bbd4: e5925004 ldr r5, [r2, #4]

bbd8: e02cc009 eor ip, ip, r9

bbdc: e02e200a eor r2, lr, sl

bbe0: e590a090 ldr sl, [r0, #144] ; 0x90

bbe4: e0222008 eor r2, r2, r8

bbe8: e02cc005 eor ip, ip, r5

bbec: e590e08c ldr lr, [r0, #140] ; 0x8c

bbf0: e08ca00a add sl, ip, sl

bbf4: e08aa082 add sl, sl, r2, lsl #1

bbf8: e02aa007 eor sl, sl, r7

bbfc: e08cc00e add ip, ip, lr

bc00: e08cc002 add ip, ip, r2

bc04: e1a0a0ea ror sl, sl, #1

bc08: e02c3fe3 eor r3, ip, r3, ror #31

bc0c: e1a0842a lsr r8, sl, #8

bc10: e1a09423 lsr r9, r3, #8

bc14: e6efb073 uxtb fp, r3

bc18: e1a02c23 lsr r2, r3, #24

bc1c: e1a07823 lsr r7, r3, #16

bc20: e1a0cc2a lsr ip, sl, #24

bc24: e6efe07a uxtb lr, sl

bc28: e1a0582a lsr r5, sl, #16

bc2c: e28bbe4a add fp, fp, #1184 ; 0x4a0

bc30: e2822e7a add r2, r2, #1952 ; 0x7a0

bc34: e6ef9079 uxtb r9, r9

bc38: e6ef8078 uxtb r8, r8

bc3c: e28cce4a add ip, ip, #1184 ; 0x4a0

bc40: e28eee5a add lr, lr, #1440 ; 0x5a0

bc44: e28bb00e add fp, fp, #14

bc48: e282200e add r2, r2, #14

bc4c: e2899e5a add r9, r9, #1440 ; 0x5a0

bc50: e6ef7077 uxtb r7, r7

bc54: e28cc00e add ip, ip, #14

bc58: e2888e6a add r8, r8, #1696 ; 0x6a0

bc5c: e28ee00e add lr, lr, #14

bc60: e6ef5075 uxtb r5, r5

bc64: e289900e add r9, r9, #14

bc68: e2877e6a add r7, r7, #1696 ; 0x6a0

bc6c: e288800e add r8, r8, #14

bc70: e2855e7a add r5, r5, #1952 ; 0x7a0

bc74: e080b10b add fp, r0, fp, lsl #2

bc78: e0802102 add r2, r0, r2, lsl #2

bc7c: e287700e add r7, r7, #14

bc80: e080c10c add ip, r0, ip, lsl #2

bc84: e080e10e add lr, r0, lr, lsl #2

bc88: e285500e add r5, r5, #14

bc8c: e0809109 add r9, r0, r9, lsl #2

bc90: e0808108 add r8, r0, r8, lsl #2

bc94: e59bb004 ldr fp, [fp, #4]

bc98: e59ee004 ldr lr, [lr, #4]

bc9c: e5922004 ldr r2, [r2, #4]

bca0: e59cc004 ldr ip, [ip, #4]

bca4: e0805105 add r5, r0, r5, lsl #2

bca8: e0807107 add r7, r0, r7, lsl #2

bcac: e5999004 ldr r9, [r9, #4]

bcb0: e5988004 ldr r8, [r8, #4]

bcb4: e5977004 ldr r7, [r7, #4]

bcb8: e022200b eor r2, r2, fp

bcbc: e02cc00e eor ip, ip, lr

bcc0: e595e004 ldr lr, [r5, #4]

bcc4: e0222009 eor r2, r2, r9

bcc8: e02cc008 eor ip, ip, r8

bccc: e5909088 ldr r9, [r0, #136] ; 0x88

bcd0: e02cc00e eor ip, ip, lr

bcd4: e0222007 eor r2, r2, r7

bcd8: e590e084 ldr lr, [r0, #132] ; 0x84

bcdc: e0829009 add r9, r2, r9

bce0: e089908c add r9, r9, ip, lsl #1

bce4: e0299006 eor r9, r9, r6

bce8: e082200e add r2, r2, lr

bcec: e082200c add r2, r2, ip

bcf0: e022efe4 eor lr, r2, r4, ror #31

bcf4: e1a090e9 ror r9, r9, #1

bcf8: e1a0742e lsr r7, lr, #8

bcfc: e1a06429 lsr r6, r9, #8

bd00: e6efb07e uxtb fp, lr

bd04: e1a0cc2e lsr ip, lr, #24

bd08: e1a0582e lsr r5, lr, #16

bd0c: e1a02c29 lsr r2, r9, #24

bd10: e6ef8079 uxtb r8, r9

bd14: e1a04829 lsr r4, r9, #16

bd18: e28bbe4a add fp, fp, #1184 ; 0x4a0

bd1c: e28cce7a add ip, ip, #1952 ; 0x7a0

bd20: e6ef7077 uxtb r7, r7

bd24: e6ef6076 uxtb r6, r6

bd28: e2822e4a add r2, r2, #1184 ; 0x4a0

bd2c: e2888e5a add r8, r8, #1440 ; 0x5a0

bd30: e28bb00e add fp, fp, #14

bd34: e28cc00e add ip, ip, #14

bd38: e2877e5a add r7, r7, #1440 ; 0x5a0

bd3c: e6ef5075 uxtb r5, r5

bd40: e282200e add r2, r2, #14

bd44: e2866e6a add r6, r6, #1696 ; 0x6a0

bd48: e288800e add r8, r8, #14

bd4c: e6ef4074 uxtb r4, r4

bd50: e287700e add r7, r7, #14

bd54: e2855e6a add r5, r5, #1696 ; 0x6a0

bd58: e286600e add r6, r6, #14

bd5c: e2844e7a add r4, r4, #1952 ; 0x7a0

bd60: e080b10b add fp, r0, fp, lsl #2

bd64: e080c10c add ip, r0, ip, lsl #2

bd68: e285500e add r5, r5, #14

bd6c: e0802102 add r2, r0, r2, lsl #2

bd70: e0808108 add r8, r0, r8, lsl #2

bd74: e284400e add r4, r4, #14

bd78: e0807107 add r7, r0, r7, lsl #2

bd7c: e0806106 add r6, r0, r6, lsl #2

bd80: e59bb004 ldr fp, [fp, #4]

bd84: e5988004 ldr r8, [r8, #4]

bd88: e59cc004 ldr ip, [ip, #4]

bd8c: e5922004 ldr r2, [r2, #4]

bd90: e0805105 add r5, r0, r5, lsl #2

bd94: e0804104 add r4, r0, r4, lsl #2

bd98: e5977004 ldr r7, [r7, #4]

bd9c: e5966004 ldr r6, [r6, #4]

bda0: e5955004 ldr r5, [r5, #4]

bda4: e5944004 ldr r4, [r4, #4]

bda8: e02cc00b eor ip, ip, fp

bdac: e0222008 eor r2, r2, r8

bdb0: e02cc007 eor ip, ip, r7

bdb4: e0222006 eor r2, r2, r6

bdb8: e5908080 ldr r8, [r0, #128] ; 0x80

bdbc: e0222004 eor r2, r2, r4

bdc0: e02cc005 eor ip, ip, r5

bdc4: e590407c ldr r4, [r0, #124] ; 0x7c

bdc8: e08c8008 add r8, ip, r8

bdcc: e0888082 add r8, r8, r2, lsl #1

bdd0: e028800a eor r8, r8, sl

bdd4: e08cc004 add ip, ip, r4

bdd8: e08cc002 add ip, ip, r2

bddc: e1a080e8 ror r8, r8, #1

bde0: e02c3fe3 eor r3, ip, r3, ror #31

bde4: e1a02428 lsr r2, r8, #8

bde8: e1a0a828 lsr sl, r8, #16

bdec: e1a0b423 lsr fp, r3, #8

bdf0: e1a06c28 lsr r6, r8, #24

bdf4: e6ef4078 uxtb r4, r8

bdf8: e6ef5073 uxtb r5, r3

bdfc: e1a07c23 lsr r7, r3, #24

be00: e1a0c823 lsr ip, r3, #16

be04: e2866e4a add r6, r6, #1184 ; 0x4a0

be08: e2844e5a add r4, r4, #1440 ; 0x5a0

be0c: e6ef2072 uxtb r2, r2

be10: e6efa07a uxtb sl, sl

be14: e6efb07b uxtb fp, fp

be18: e2855e4a add r5, r5, #1184 ; 0x4a0

be1c: e2877e7a add r7, r7, #1952 ; 0x7a0

be20: e286600e add r6, r6, #14

be24: e284400e add r4, r4, #14

be28: e2822e6a add r2, r2, #1696 ; 0x6a0

be2c: e28aae7a add sl, sl, #1952 ; 0x7a0

be30: e285500e add r5, r5, #14

be34: e28bbe5a add fp, fp, #1440 ; 0x5a0

be38: e287700e add r7, r7, #14

be3c: e6efc07c uxtb ip, ip

be40: e282200e add r2, r2, #14

be44: e28aa00e add sl, sl, #14

be48: e28bb00e add fp, fp, #14

be4c: e28cce6a add ip, ip, #1696 ; 0x6a0

be50: e0806106 add r6, r0, r6, lsl #2

be54: e0804104 add r4, r0, r4, lsl #2

be58: e0805105 add r5, r0, r5, lsl #2

be5c: e0807107 add r7, r0, r7, lsl #2

be60: e28cc00e add ip, ip, #14

be64: e0802102 add r2, r0, r2, lsl #2

be68: e080a10a add sl, r0, sl, lsl #2

be6c: e080b10b add fp, r0, fp, lsl #2

be70: e5944004 ldr r4, [r4, #4]

be74: e5977004 ldr r7, [r7, #4]

be78: e5966004 ldr r6, [r6, #4]

be7c: e5955004 ldr r5, [r5, #4]

be80: e080c10c add ip, r0, ip, lsl #2

be84: e5922004 ldr r2, [r2, #4]

be88: e59aa004 ldr sl, [sl, #4]

be8c: e59bb004 ldr fp, [fp, #4]

be90: e0266004 eor r6, r6, r4

be94: e0255007 eor r5, r5, r7

be98: e59c7004 ldr r7, [ip, #4]

be9c: e0222006 eor r2, r2, r6

bea0: e025500b eor r5, r5, fp

bea4: e590c078 ldr ip, [r0, #120] ; 0x78

bea8: e022400a eor r4, r2, sl

beac: e0252007 eor r2, r5, r7

beb0: e082500c add r5, r2, ip

beb4: e590c074 ldr ip, [r0, #116] ; 0x74

beb8: e0855084 add r5, r5, r4, lsl #1

bebc: e0255009 eor r5, r5, r9

bec0: e082200c add r2, r2, ip

bec4: e0822004 add r2, r2, r4

bec8: e1a050e5 ror r5, r5, #1

becc: e0222fee eor r2, r2, lr, ror #31

bed0: e1a07425 lsr r7, r5, #8

bed4: e1a09422 lsr r9, r2, #8

bed8: e6efb072 uxtb fp, r2

bedc: e1a0cc22 lsr ip, r2, #24

bee0: e1a06822 lsr r6, r2, #16

bee4: e1a0ec25 lsr lr, r5, #24

bee8: e6efa075 uxtb sl, r5

beec: e1a04825 lsr r4, r5, #16

bef0: e28bbe4a add fp, fp, #1184 ; 0x4a0

bef4: e28cce7a add ip, ip, #1952 ; 0x7a0

bef8: e6ef9079 uxtb r9, r9

befc: e6ef7077 uxtb r7, r7

bf00: e28eee4a add lr, lr, #1184 ; 0x4a0

bf04: e28aae5a add sl, sl, #1440 ; 0x5a0

bf08: e28bb00e add fp, fp, #14

bf0c: e28cc00e add ip, ip, #14

bf10: e2899e5a add r9, r9, #1440 ; 0x5a0

bf14: e6ef6076 uxtb r6, r6

bf18: e28ee00e add lr, lr, #14

bf1c: e2877e6a add r7, r7, #1696 ; 0x6a0

bf20: e28aa00e add sl, sl, #14

bf24: e6ef4074 uxtb r4, r4

bf28: e289900e add r9, r9, #14

bf2c: e2866e6a add r6, r6, #1696 ; 0x6a0

bf30: e287700e add r7, r7, #14

bf34: e2844e7a add r4, r4, #1952 ; 0x7a0

bf38: e080b10b add fp, r0, fp, lsl #2

bf3c: e080c10c add ip, r0, ip, lsl #2

bf40: e286600e add r6, r6, #14

bf44: e080e10e add lr, r0, lr, lsl #2

bf48: e080a10a add sl, r0, sl, lsl #2

bf4c: e284400e add r4, r4, #14

bf50: e0809109 add r9, r0, r9, lsl #2

bf54: e0807107 add r7, r0, r7, lsl #2

bf58: e59bb004 ldr fp, [fp, #4]

bf5c: e59aa004 ldr sl, [sl, #4]

bf60: e59cc004 ldr ip, [ip, #4]

bf64: e59ee004 ldr lr, [lr, #4]

bf68: e0806106 add r6, r0, r6, lsl #2

bf6c: e0804104 add r4, r0, r4, lsl #2

bf70: e5999004 ldr r9, [r9, #4]

bf74: e5977004 ldr r7, [r7, #4]

bf78: e5966004 ldr r6, [r6, #4]

bf7c: e5944004 ldr r4, [r4, #4]

bf80: e02cc00b eor ip, ip, fp

bf84: e02ee00a eor lr, lr, sl

bf88: e02cc009 eor ip, ip, r9

bf8c: e02ee007 eor lr, lr, r7

bf90: e5907070 ldr r7, [r0, #112] ; 0x70

bf94: e02ee004 eor lr, lr, r4

bf98: e02cc006 eor ip, ip, r6

bf9c: e590406c ldr r4, [r0, #108] ; 0x6c

bfa0: e08c7007 add r7, ip, r7

bfa4: e087708e add r7, r7, lr, lsl #1

bfa8: e0277008 eor r7, r7, r8

bfac: e08cc004 add ip, ip, r4

bfb0: e08cc00e add ip, ip, lr

bfb4: e02ccfe3 eor ip, ip, r3, ror #31

bfb8: e1a070e7 ror r7, r7, #1

bfbc: e1a0a42c lsr sl, ip, #8

bfc0: e1a09427 lsr r9, r7, #8

bfc4: e6ef807c uxtb r8, ip

bfc8: e1a0ec2c lsr lr, ip, #24

bfcc: e1a0682c lsr r6, ip, #16

bfd0: e1a03c27 lsr r3, r7, #24

bfd4: e6efb077 uxtb fp, r7

bfd8: e1a04827 lsr r4, r7, #16

bfdc: e2888e4a add r8, r8, #1184 ; 0x4a0

bfe0: e28eee7a add lr, lr, #1952 ; 0x7a0

bfe4: e6efa07a uxtb sl, sl

bfe8: e6ef9079 uxtb r9, r9

bfec: e2833e4a add r3, r3, #1184 ; 0x4a0

bff0: e28bbe5a add fp, fp, #1440 ; 0x5a0

bff4: e288800e add r8, r8, #14

bff8: e28ee00e add lr, lr, #14

bffc: e28aae5a add sl, sl, #1440 ; 0x5a0

c000: e6ef6076 uxtb r6, r6

c004: e283300e add r3, r3, #14

c008: e2899e6a add r9, r9, #1696 ; 0x6a0

c00c: e28bb00e add fp, fp, #14

c010: e6ef4074 uxtb r4, r4

c014: e28aa00e add sl, sl, #14

c018: e2866e6a add r6, r6, #1696 ; 0x6a0

c01c: e289900e add r9, r9, #14

c020: e2844e7a add r4, r4, #1952 ; 0x7a0

c024: e0808108 add r8, r0, r8, lsl #2

c028: e080e10e add lr, r0, lr, lsl #2

c02c: e286600e add r6, r6, #14

c030: e0803103 add r3, r0, r3, lsl #2

c034: e080b10b add fp, r0, fp, lsl #2

c038: e284400e add r4, r4, #14

c03c: e080a10a add sl, r0, sl, lsl #2

c040: e0809109 add r9, r0, r9, lsl #2

c044: e5988004 ldr r8, [r8, #4]

c048: e59bb004 ldr fp, [fp, #4]

c04c: e59ee004 ldr lr, [lr, #4]

c050: e0806106 add r6, r0, r6, lsl #2

c054: e5933004 ldr r3, [r3, #4]

c058: e0804104 add r4, r0, r4, lsl #2

c05c: e59aa004 ldr sl, [sl, #4]

c060: e5999004 ldr r9, [r9, #4]

c064: e5944004 ldr r4, [r4, #4]

c068: e02ee008 eor lr, lr, r8

c06c: e023300b eor r3, r3, fp

c070: e5968004 ldr r8, [r6, #4]

c074: e02ee00a eor lr, lr, sl

c078: e0233009 eor r3, r3, r9

c07c: e5906068 ldr r6, [r0, #104] ; 0x68

c080: e0233004 eor r3, r3, r4

c084: e02ee008 eor lr, lr, r8

c088: e5904064 ldr r4, [r0, #100] ; 0x64

c08c: e08e6006 add r6, lr, r6

c090: e0866083 add r6, r6, r3, lsl #1

c094: e08ee004 add lr, lr, r4

c098: e08ee003 add lr, lr, r3

c09c: e0266005 eor r6, r6, r5

c0a0: e02e2fe2 eor r2, lr, r2, ror #31

c0a4: e1a060e6 ror r6, r6, #1

c0a8: e1a09422 lsr r9, r2, #8

c0ac: e1a04426 lsr r4, r6, #8

c0b0: e6efb072 uxtb fp, r2

c0b4: e1a05c22 lsr r5, r2, #24

c0b8: e1a08822 lsr r8, r2, #16

c0bc: e1a03c26 lsr r3, r6, #24

c0c0: e6efa076 uxtb sl, r6

c0c4: e1a0e826 lsr lr, r6, #16

c0c8: e28bbe4a add fp, fp, #1184 ; 0x4a0

c0cc: e2855e7a add r5, r5, #1952 ; 0x7a0

c0d0: e6ef9079 uxtb r9, r9

c0d4: e6ef4074 uxtb r4, r4

c0d8: e2833e4a add r3, r3, #1184 ; 0x4a0

c0dc: e28bb00e add fp, fp, #14

c0e0: e285500e add r5, r5, #14

c0e4: e2899e5a add r9, r9, #1440 ; 0x5a0

c0e8: e6ef8078 uxtb r8, r8

c0ec: e28aae5a add sl, sl, #1440 ; 0x5a0

c0f0: e283300e add r3, r3, #14

c0f4: e2844e6a add r4, r4, #1696 ; 0x6a0

c0f8: e28aa00e add sl, sl, #14

c0fc: e289900e add r9, r9, #14

c100: e2888e6a add r8, r8, #1696 ; 0x6a0

c104: e6efe07e uxtb lr, lr

c108: e284400e add r4, r4, #14

c10c: e080b10b add fp, r0, fp, lsl #2

c110: e28eee7a add lr, lr, #1952 ; 0x7a0

c114: e0805105 add r5, r0, r5, lsl #2

c118: e288800e add r8, r8, #14

c11c: e0803103 add r3, r0, r3, lsl #2

c120: e080a10a add sl, r0, sl, lsl #2

c124: e28ee00e add lr, lr, #14

c128: e0809109 add r9, r0, r9, lsl #2

c12c: e59bb004 ldr fp, [fp, #4]

c130: e5955004 ldr r5, [r5, #4]

c134: e0804104 add r4, r0, r4, lsl #2

c138: e0808108 add r8, r0, r8, lsl #2

c13c: e59aa004 ldr sl, [sl, #4]

c140: e5999004 ldr r9, [r9, #4]

c144: e5933004 ldr r3, [r3, #4]

c148: e080e10e add lr, r0, lr, lsl #2

c14c: e5944004 ldr r4, [r4, #4]

c150: e5988004 ldr r8, [r8, #4]

c154: e025500b eor r5, r5, fp

c158: e59ee004 ldr lr, [lr, #4]

c15c: e023300a eor r3, r3, sl

c160: e0255009 eor r5, r5, r9

c164: e0255008 eor r5, r5, r8

c168: e0233004 eor r3, r3, r4

c16c: e5908060 ldr r8, [r0, #96] ; 0x60

c170: e023300e eor r3, r3, lr

c174: e590405c ldr r4, [r0, #92] ; 0x5c

c178: e0858008 add r8, r5, r8

c17c: e0888083 add r8, r8, r3, lsl #1

c180: e0854004 add r4, r5, r4

c184: e0277008 eor r7, r7, r8

c188: e0844003 add r4, r4, r3

c18c: e0244fec eor r4, r4, ip, ror #31

c190: e1a070e7 ror r7, r7, #1

c194: e1a0a424 lsr sl, r4, #8

c198: e1a09427 lsr r9, r7, #8

c19c: e6ef8074 uxtb r8, r4

c1a0: e1a0ec24 lsr lr, r4, #24

c1a4: e1a05824 lsr r5, r4, #16

c1a8: e1a0cc27 lsr ip, r7, #24

c1ac: e6efb077 uxtb fp, r7

c1b0: e1a03827 lsr r3, r7, #16

c1b4: e2888e4a add r8, r8, #1184 ; 0x4a0

c1b8: e28eee7a add lr, lr, #1952 ; 0x7a0

c1bc: e6efa07a uxtb sl, sl

c1c0: e6ef9079 uxtb r9, r9

c1c4: e28cce4a add ip, ip, #1184 ; 0x4a0

c1c8: e28bbe5a add fp, fp, #1440 ; 0x5a0

c1cc: e288800e add r8, r8, #14

c1d0: e28ee00e add lr, lr, #14

c1d4: e28aae5a add sl, sl, #1440 ; 0x5a0

c1d8: e6ef5075 uxtb r5, r5

c1dc: e28cc00e add ip, ip, #14

c1e0: e2899e6a add r9, r9, #1696 ; 0x6a0

c1e4: e28bb00e add fp, fp, #14

c1e8: e6ef3073 uxtb r3, r3

c1ec: e28aa00e add sl, sl, #14

c1f0: e2855e6a add r5, r5, #1696 ; 0x6a0

c1f4: e289900e add r9, r9, #14

c1f8: e2833e7a add r3, r3, #1952 ; 0x7a0

c1fc: e0808108 add r8, r0, r8, lsl #2

c200: e080e10e add lr, r0, lr, lsl #2

c204: e285500e add r5, r5, #14

c208: e080c10c add ip, r0, ip, lsl #2

c20c: e080b10b add fp, r0, fp, lsl #2

c210: e283300e add r3, r3, #14

c214: e080a10a add sl, r0, sl, lsl #2

c218: e0809109 add r9, r0, r9, lsl #2

c21c: e5988004 ldr r8, [r8, #4]

c220: e59bb004 ldr fp, [fp, #4]

c224: e59ee004 ldr lr, [lr, #4]

c228: e0805105 add r5, r0, r5, lsl #2

c22c: e59cc004 ldr ip, [ip, #4]

c230: e0803103 add r3, r0, r3, lsl #2

c234: e59aa004 ldr sl, [sl, #4]

c238: e5999004 ldr r9, [r9, #4]

c23c: e5933004 ldr r3, [r3, #4]

c240: e02ee008 eor lr, lr, r8

c244: e02cc00b eor ip, ip, fp

c248: e5958004 ldr r8, [r5, #4]

c24c: e02ee00a eor lr, lr, sl

c250: e02cc009 eor ip, ip, r9

c254: e5905058 ldr r5, [r0, #88] ; 0x58

c258: e02cc003 eor ip, ip, r3

c25c: e02ee008 eor lr, lr, r8

c260: e5903054 ldr r3, [r0, #84] ; 0x54

c264: e08e5005 add r5, lr, r5

c268: e085508c add r5, r5, ip, lsl #1

c26c: e0266005 eor r6, r6, r5

c270: e08ee003 add lr, lr, r3

c274: e08ee00c add lr, lr, ip

c278: e02eefe2 eor lr, lr, r2, ror #31

c27c: e1a060e6 ror r6, r6, #1

c280: e1a0942e lsr r9, lr, #8

c284: e1a08426 lsr r8, r6, #8

c288: e6efb07e uxtb fp, lr

c28c: e1a0cc2e lsr ip, lr, #24

c290: e1a0582e lsr r5, lr, #16

c294: e1a03c26 lsr r3, r6, #24

c298: e6efa076 uxtb sl, r6

c29c: e1a02826 lsr r2, r6, #16

c2a0: e28bbe4a add fp, fp, #1184 ; 0x4a0

c2a4: e28cce7a add ip, ip, #1952 ; 0x7a0

c2a8: e6ef9079 uxtb r9, r9

c2ac: e6ef8078 uxtb r8, r8

c2b0: e2833e4a add r3, r3, #1184 ; 0x4a0

c2b4: e28aae5a add sl, sl, #1440 ; 0x5a0

c2b8: e28bb00e add fp, fp, #14

c2bc: e28cc00e add ip, ip, #14

c2c0: e2899e5a add r9, r9, #1440 ; 0x5a0

c2c4: e6ef5075 uxtb r5, r5

c2c8: e283300e add r3, r3, #14

c2cc: e2888e6a add r8, r8, #1696 ; 0x6a0

c2d0: e28aa00e add sl, sl, #14

c2d4: e6ef2072 uxtb r2, r2

c2d8: e289900e add r9, r9, #14

c2dc: e2855e6a add r5, r5, #1696 ; 0x6a0

c2e0: e288800e add r8, r8, #14

c2e4: e2822e7a add r2, r2, #1952 ; 0x7a0

c2e8: e080b10b add fp, r0, fp, lsl #2

c2ec: e080c10c add ip, r0, ip, lsl #2

c2f0: e285500e add r5, r5, #14

c2f4: e0803103 add r3, r0, r3, lsl #2

c2f8: e080a10a add sl, r0, sl, lsl #2

c2fc: e282200e add r2, r2, #14

c300: e0809109 add r9, r0, r9, lsl #2

c304: e0808108 add r8, r0, r8, lsl #2

c308: e59bb004 ldr fp, [fp, #4]

c30c: e59aa004 ldr sl, [sl, #4]

c310: e59cc004 ldr ip, [ip, #4]

c314: e5933004 ldr r3, [r3, #4]

c318: e0805105 add r5, r0, r5, lsl #2

c31c: e0802102 add r2, r0, r2, lsl #2

c320: e5999004 ldr r9, [r9, #4]

c324: e5988004 ldr r8, [r8, #4]

c328: e5955004 ldr r5, [r5, #4]

c32c: e5922004 ldr r2, [r2, #4]

c330: e02cc00b eor ip, ip, fp

c334: e023300a eor r3, r3, sl

c338: e02cc009 eor ip, ip, r9

c33c: e0233008 eor r3, r3, r8

c340: e5908050 ldr r8, [r0, #80] ; 0x50

c344: e0233002 eor r3, r3, r2

c348: e02cc005 eor ip, ip, r5

c34c: e590204c ldr r2, [r0, #76] ; 0x4c

c350: e08c8008 add r8, ip, r8

c354: e0888083 add r8, r8, r3, lsl #1

c358: e0277008 eor r7, r7, r8

c35c: e08cc002 add ip, ip, r2

c360: e08cc003 add ip, ip, r3

c364: e1a080e7 ror r8, r7, #1

c368: e02c4fe4 eor r4, ip, r4, ror #31

c36c: e1a09428 lsr r9, r8, #8

c370: e1a0a424 lsr sl, r4, #8

c374: e6ef7074 uxtb r7, r4

c378: e1a03c24 lsr r3, r4, #24

c37c: e1a05824 lsr r5, r4, #16

c380: e1a0cc28 lsr ip, r8, #24

c384: e6efb078 uxtb fp, r8

c388: e1a02828 lsr r2, r8, #16

c38c: e2877e4a add r7, r7, #1184 ; 0x4a0

c390: e2833e7a add r3, r3, #1952 ; 0x7a0

c394: e6efa07a uxtb sl, sl

c398: e6ef9079 uxtb r9, r9

c39c: e28cce4a add ip, ip, #1184 ; 0x4a0

c3a0: e28bbe5a add fp, fp, #1440 ; 0x5a0

c3a4: e287700e add r7, r7, #14

c3a8: e283300e add r3, r3, #14

c3ac: e28aae5a add sl, sl, #1440 ; 0x5a0

c3b0: e6ef5075 uxtb r5, r5

c3b4: e28cc00e add ip, ip, #14

c3b8: e2899e6a add r9, r9, #1696 ; 0x6a0

c3bc: e28bb00e add fp, fp, #14

c3c0: e6ef2072 uxtb r2, r2

c3c4: e28aa00e add sl, sl, #14

c3c8: e2855e6a add r5, r5, #1696 ; 0x6a0

c3cc: e289900e add r9, r9, #14

c3d0: e2822e7a add r2, r2, #1952 ; 0x7a0

c3d4: e0807107 add r7, r0, r7, lsl #2

c3d8: e0803103 add r3, r0, r3, lsl #2

c3dc: e285500e add r5, r5, #14

c3e0: e080c10c add ip, r0, ip, lsl #2

c3e4: e080b10b add fp, r0, fp, lsl #2

c3e8: e282200e add r2, r2, #14

c3ec: e080a10a add sl, r0, sl, lsl #2

c3f0: e0809109 add r9, r0, r9, lsl #2

c3f4: e5977004 ldr r7, [r7, #4]

c3f8: e59bb004 ldr fp, [fp, #4]

c3fc: e5933004 ldr r3, [r3, #4]

c400: e0805105 add r5, r0, r5, lsl #2

c404: e59cc004 ldr ip, [ip, #4]

c408: e0802102 add r2, r0, r2, lsl #2

c40c: e59aa004 ldr sl, [sl, #4]

c410: e5999004 ldr r9, [r9, #4]

c414: e0233007 eor r3, r3, r7

c418: e02cc00b eor ip, ip, fp

c41c: e5957004 ldr r7, [r5, #4]

c420: e5925004 ldr r5, [r2, #4]

c424: e023300a eor r3, r3, sl

c428: e02cc009 eor ip, ip, r9

c42c: e5902048 ldr r2, [r0, #72] ; 0x48

c430: e02cc005 eor ip, ip, r5

c434: e0233007 eor r3, r3, r7

c438: e5905044 ldr r5, [r0, #68] ; 0x44

c43c: e0832002 add r2, r3, r2

c440: e082208c add r2, r2, ip, lsl #1

c444: e0266002 eor r6, r6, r2

c448: e0833005 add r3, r3, r5

c44c: e083300c add r3, r3, ip

c450: e1a060e6 ror r6, r6, #1

c454: e023efee eor lr, r3, lr, ror #31

c458: e1a02426 lsr r2, r6, #8

c45c: e1a0a42e lsr sl, lr, #8

c460: e1a07c26 lsr r7, r6, #24

c464: e6efc076 uxtb ip, r6

c468: e1a09826 lsr r9, r6, #16

c46c: e6ef507e uxtb r5, lr

c470: e1a0bc2e lsr fp, lr, #24

c474: e1a0382e lsr r3, lr, #16

c478: e2877e4a add r7, r7, #1184 ; 0x4a0

c47c: e28cce5a add ip, ip, #1440 ; 0x5a0

c480: e6ef2072 uxtb r2, r2

c484: e6efa07a uxtb sl, sl

c488: e2855e4a add r5, r5, #1184 ; 0x4a0

c48c: e28bbe7a add fp, fp, #1952 ; 0x7a0

c490: e287700e add r7, r7, #14

c494: e28cc00e add ip, ip, #14

c498: e2822e6a add r2, r2, #1696 ; 0x6a0

c49c: e6ef9079 uxtb r9, r9

c4a0: e285500e add r5, r5, #14

c4a4: e28aae5a add sl, sl, #1440 ; 0x5a0

c4a8: e28bb00e add fp, fp, #14

c4ac: e6ef3073 uxtb r3, r3

c4b0: e282200e add r2, r2, #14

c4b4: e2899e7a add r9, r9, #1952 ; 0x7a0

c4b8: e28aa00e add sl, sl, #14

c4bc: e2833e6a add r3, r3, #1696 ; 0x6a0

c4c0: e0807107 add r7, r0, r7, lsl #2

c4c4: e080c10c add ip, r0, ip, lsl #2

c4c8: e289900e add r9, r9, #14

c4cc: e0805105 add r5, r0, r5, lsl #2

c4d0: e080b10b add fp, r0, fp, lsl #2

c4d4: e283300e add r3, r3, #14

c4d8: e0802102 add r2, r0, r2, lsl #2

c4dc: e080a10a add sl, r0, sl, lsl #2

c4e0: e59cc004 ldr ip, [ip, #4]

c4e4: e59bb004 ldr fp, [fp, #4]

c4e8: e5977004 ldr r7, [r7, #4]

c4ec: e5955004 ldr r5, [r5, #4]

c4f0: e0809109 add r9, r0, r9, lsl #2

c4f4: e0803103 add r3, r0, r3, lsl #2

c4f8: e59aa004 ldr sl, [sl, #4]

c4fc: e5922004 ldr r2, [r2, #4]

c500: e027700c eor r7, r7, ip

c504: e5999004 ldr r9, [r9, #4]

c508: e5933004 ldr r3, [r3, #4]

c50c: e025500b eor r5, r5, fp

c510: e0222007 eor r2, r2, r7

c514: e025500a eor r5, r5, sl

c518: e5907040 ldr r7, [r0, #64] ; 0x40

c51c: e0222009 eor r2, r2, r9

c520: e0255003 eor r5, r5, r3

c524: e590c03c ldr ip, [r0, #60] ; 0x3c

c528: e0857007 add r7, r5, r7

c52c: e0877082 add r7, r7, r2, lsl #1

c530: e085500c add r5, r5, ip

c534: e0277008 eor r7, r7, r8

c538: e0852002 add r2, r5, r2

c53c: e022cfe4 eor ip, r2, r4, ror #31

c540: e1a070e7 ror r7, r7, #1

c544: e1a0a42c lsr sl, ip, #8

c548: e1a09427 lsr r9, r7, #8

c54c: e6ef807c uxtb r8, ip

c550: e1a03c2c lsr r3, ip, #24

c554: e1a0482c lsr r4, ip, #16

c558: e1a05c27 lsr r5, r7, #24

c55c: e6efb077 uxtb fp, r7

c560: e1a02827 lsr r2, r7, #16

c564: e2888e4a add r8, r8, #1184 ; 0x4a0

c568: e2833e7a add r3, r3, #1952 ; 0x7a0

c56c: e6efa07a uxtb sl, sl

c570: e6ef9079 uxtb r9, r9

c574: e2855e4a add r5, r5, #1184 ; 0x4a0

c578: e28bbe5a add fp, fp, #1440 ; 0x5a0

c57c: e288800e add r8, r8, #14

c580: e283300e add r3, r3, #14

c584: e28aae5a add sl, sl, #1440 ; 0x5a0

c588: e6ef4074 uxtb r4, r4

c58c: e285500e add r5, r5, #14

c590: e2899e6a add r9, r9, #1696 ; 0x6a0

c594: e28bb00e add fp, fp, #14

c598: e6ef2072 uxtb r2, r2

c59c: e28aa00e add sl, sl, #14

c5a0: e2844e6a add r4, r4, #1696 ; 0x6a0

c5a4: e289900e add r9, r9, #14

c5a8: e2822e7a add r2, r2, #1952 ; 0x7a0

c5ac: e0808108 add r8, r0, r8, lsl #2

c5b0: e0803103 add r3, r0, r3, lsl #2

c5b4: e284400e add r4, r4, #14

c5b8: e0805105 add r5, r0, r5, lsl #2

c5bc: e080b10b add fp, r0, fp, lsl #2

c5c0: e282200e add r2, r2, #14

c5c4: e080a10a add sl, r0, sl, lsl #2

c5c8: e0809109 add r9, r0, r9, lsl #2

c5cc: e5988004 ldr r8, [r8, #4]

c5d0: e59bb004 ldr fp, [fp, #4]

c5d4: e5933004 ldr r3, [r3, #4]

c5d8: e0804104 add r4, r0, r4, lsl #2

c5dc: e5955004 ldr r5, [r5, #4]

c5e0: e0802102 add r2, r0, r2, lsl #2

c5e4: e59aa004 ldr sl, [sl, #4]

c5e8: e5999004 ldr r9, [r9, #4]

c5ec: e0233008 eor r3, r3, r8

c5f0: e025500b eor r5, r5, fp

c5f4: e5948004 ldr r8, [r4, #4]

c5f8: e5924004 ldr r4, [r2, #4]

c5fc: e023300a eor r3, r3, sl

c600: e0255009 eor r5, r5, r9

c604: e5902038 ldr r2, [r0, #56] ; 0x38

c608: e0255004 eor r5, r5, r4

c60c: e0233008 eor r3, r3, r8

c610: e5904034 ldr r4, [r0, #52] ; 0x34

c614: e0832002 add r2, r3, r2

c618: e0822085 add r2, r2, r5, lsl #1

c61c: e0222006 eor r2, r2, r6

c620: e0833004 add r3, r3, r4

c624: e0833005 add r3, r3, r5

c628: e1a040e2 ror r4, r2, #1

c62c: e0233fee eor r3, r3, lr, ror #31

c630: e1a09424 lsr r9, r4, #8

c634: e1a0a423 lsr sl, r3, #8

c638: e6ef8073 uxtb r8, r3

c63c: e1a02c23 lsr r2, r3, #24

c640: e1a06823 lsr r6, r3, #16

c644: e1a05c24 lsr r5, r4, #24

c648: e6efb074 uxtb fp, r4

c64c: e1a0e824 lsr lr, r4, #16

c650: e2888e4a add r8, r8, #1184 ; 0x4a0

c654: e2822e7a add r2, r2, #1952 ; 0x7a0

c658: e6efa07a uxtb sl, sl

c65c: e6ef9079 uxtb r9, r9

c660: e2855e4a add r5, r5, #1184 ; 0x4a0

c664: e28bbe5a add fp, fp, #1440 ; 0x5a0

c668: e288800e add r8, r8, #14

c66c: e282200e add r2, r2, #14

c670: e28aae5a add sl, sl, #1440 ; 0x5a0

c674: e6ef6076 uxtb r6, r6

c678: e285500e add r5, r5, #14

c67c: e2899e6a add r9, r9, #1696 ; 0x6a0

c680: e28bb00e add fp, fp, #14

c684: e6efe07e uxtb lr, lr

c688: e28aa00e add sl, sl, #14

c68c: e2866e6a add r6, r6, #1696 ; 0x6a0

c690: e289900e add r9, r9, #14

c694: e28eee7a add lr, lr, #1952 ; 0x7a0

c698: e0808108 add r8, r0, r8, lsl #2

c69c: e0802102 add r2, r0, r2, lsl #2

c6a0: e286600e add r6, r6, #14

c6a4: e0805105 add r5, r0, r5, lsl #2

c6a8: e080b10b add fp, r0, fp, lsl #2

c6ac: e28ee00e add lr, lr, #14

c6b0: e080a10a add sl, r0, sl, lsl #2

c6b4: e0809109 add r9, r0, r9, lsl #2

c6b8: e5988004 ldr r8, [r8, #4]

c6bc: e59bb004 ldr fp, [fp, #4]

c6c0: e5922004 ldr r2, [r2, #4]

c6c4: e0806106 add r6, r0, r6, lsl #2

c6c8: e5955004 ldr r5, [r5, #4]

c6cc: e080e10e add lr, r0, lr, lsl #2

c6d0: e59aa004 ldr sl, [sl, #4]

c6d4: e5999004 ldr r9, [r9, #4]

c6d8: e0222008 eor r2, r2, r8

c6dc: e025500b eor r5, r5, fp

c6e0: e5968004 ldr r8, [r6, #4]

c6e4: e59e6004 ldr r6, [lr, #4]

c6e8: e022200a eor r2, r2, sl

c6ec: e0255009 eor r5, r5, r9

c6f0: e590e030 ldr lr, [r0, #48] ; 0x30

c6f4: e0255006 eor r5, r5, r6

c6f8: e0222008 eor r2, r2, r8

c6fc: e082e00e add lr, r2, lr

c700: e08ee085 add lr, lr, r5, lsl #1

c704: e02ee007 eor lr, lr, r7

c708: e590602c ldr r6, [r0, #44] ; 0x2c

c70c: e1a070ee ror r7, lr, #1

c710: e0822006 add r2, r2, r6

c714: e1a06c27 lsr r6, r7, #24

c718: e0822005 add r2, r2, r5

c71c: e2866e4a add r6, r6, #1184 ; 0x4a0

c720: e1a05427 lsr r5, r7, #8

c724: e0222fec eor r2, r2, ip, ror #31

c728: e286600e add r6, r6, #14

c72c: e6ef5075 uxtb r5, r5

c730: e285ce6a add ip, r5, #1696 ; 0x6a0

c734: e6ef8072 uxtb r8, r2

c738: e0805106 add r5, r0, r6, lsl #2

c73c: e6efb077 uxtb fp, r7

c740: e1a06c22 lsr r6, r2, #24

c744: e1a0e422 lsr lr, r2, #8

c748: e28bbe5a add fp, fp, #1440 ; 0x5a0

c74c: e2888e4a add r8, r8, #1184 ; 0x4a0

c750: e2866e7a add r6, r6, #1952 ; 0x7a0

c754: e28bb00e add fp, fp, #14

c758: e288800e add r8, r8, #14

c75c: e286600e add r6, r6, #14

c760: e1a0a822 lsr sl, r2, #16

c764: e1a09827 lsr r9, r7, #16

c768: e6efe07e uxtb lr, lr

c76c: e080b10b add fp, r0, fp, lsl #2

c770: e28eee5a add lr, lr, #1440 ; 0x5a0

c774: e0808108 add r8, r0, r8, lsl #2

c778: e0806106 add r6, r0, r6, lsl #2

c77c: e28cc00e add ip, ip, #14

c780: e6ef9079 uxtb r9, r9

c784: e6efa07a uxtb sl, sl

c788: e28ee00e add lr, lr, #14

c78c: e5955004 ldr r5, [r5, #4]

c790: e2899e7a add r9, r9, #1952 ; 0x7a0

c794: e59bb004 ldr fp, [fp, #4]

c798: e28aae6a add sl, sl, #1696 ; 0x6a0

c79c: e5988004 ldr r8, [r8, #4]

c7a0: e5966004 ldr r6, [r6, #4]

c7a4: e080c10c add ip, r0, ip, lsl #2

c7a8: e289900e add r9, r9, #14

c7ac: e28aa00e add sl, sl, #14

c7b0: e080e10e add lr, r0, lr, lsl #2

c7b4: e0266008 eor r6, r6, r8

c7b8: e5908004 ldr r8, [r0, #4]

c7bc: e59cc004 ldr ip, [ip, #4]

c7c0: e59ee004 ldr lr, [lr, #4]

c7c4: e0809109 add r9, r0, r9, lsl #2

c7c8: e080a10a add sl, r0, sl, lsl #2

c7cc: e0222008 eor r2, r2, r8

c7d0: e025500b eor r5, r5, fp

c7d4: e5999004 ldr r9, [r9, #4]

c7d8: e59aa004 ldr sl, [sl, #4]

c7dc: e5908028 ldr r8, [r0, #40] ; 0x28

c7e0: e026600e eor r6, r6, lr

c7e4: e025500c eor r5, r5, ip

c7e8: e590e024 ldr lr, [r0, #36] ; 0x24

c7ec: e5812000 str r2, [r1]

c7f0: e590c008 ldr ip, [r0, #8]

c7f4: e026600a eor r6, r6, sl

c7f8: e02cc007 eor ip, ip, r7

c7fc: e581c004 str ip, [r1, #4]

c800: e590c00c ldr ip, [r0, #12]

c804: e0255009 eor r5, r5, r9

c808: e086e00e add lr, r6, lr

c80c: e08ee005 add lr, lr, r5

c810: e0862008 add r2, r6, r8

c814: e02c3fe3 eor r3, ip, r3, ror #31

c818: e023300e eor r3, r3, lr

c81c: e0822085 add r2, r2, r5, lsl #1

c820: e0222004 eor r2, r2, r4

c824: e5813008 str r3, [r1, #8]

c828: e5903010 ldr r3, [r0, #16]

c82c: e02320e2 eor r2, r3, r2, ror #1

c830: e581200c str r2, [r1, #12]

c834: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c838: e59f0000 ldr r0, [pc] ; c840 <mcrypt\_mutex\_register@@Base+0x80ac>

c83c: e12fff1e bx lr

c840: 000022bc ; <UNDEFINED> instruction: 0x000022bc

c844: e3a00010 mov r0, #16

c848: e12fff1e bx lr

c84c: e3a00001 mov r0, #1

c850: e12fff1e bx lr

c854: e3a00020 mov r0, #32

c858: e12fff1e bx lr

c85c: e3a03003 mov r3, #3

c860: e5803000 str r3, [r0]

c864: e59f0004 ldr r0, [pc, #4] ; c870 <mcrypt\_mutex\_register@@Base+0x80dc>

c868: e08f0000 add r0, pc, r0

c86c: e12fff1e bx lr

c870: 00013f38 andeq r3, r1, r8, lsr pc

c874: e59f0004 ldr r0, [pc, #4] ; c880 <mcrypt\_mutex\_register@@Base+0x80ec>

c878: e08f0000 add r0, pc, r0

c87c: e12fff1e bx lr

c880: 00013f34 andeq r3, r1, r4, lsr pc

c884: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

c888: e24dd0f8 sub sp, sp, #248 ; 0xf8

c88c: ebffffec bl c844 <mcrypt\_mutex\_register@@Base+0x80b0>

c890: e59f3158 ldr r3, [pc, #344] ; c9f0 <mcrypt\_mutex\_register@@Base+0x825c>

c894: e59fc158 ldr ip, [pc, #344] ; c9f4 <mcrypt\_mutex\_register@@Base+0x8260>

c898: e08f3003 add r3, pc, r3

c89c: e08fc00c add ip, pc, ip

c8a0: e1a0400d mov r4, sp

c8a4: e28d8020 add r8, sp, #32

c8a8: e1a06000 mov r6, r0

c8ac: e893000f ldm r3, {r0, r1, r2, r3}

c8b0: e884000f stm r4, {r0, r1, r2, r3}

c8b4: e89c000f ldm ip, {r0, r1, r2, r3}

c8b8: e28dc010 add ip, sp, #16

c8bc: e88c000f stm ip, {r0, r1, r2, r3}

c8c0: e888000f stm r8, {r0, r1, r2, r3}

c8c4: ebffffdb bl c838 <mcrypt\_mutex\_register@@Base+0x80a4>

c8c8: ebffd689 bl 22f4 <malloc@plt>

c8cc: e2509000 subs r9, r0, #0

c8d0: 0a000044 beq c9e8 <mcrypt\_mutex\_register@@Base+0x8254>

c8d4: e1a01004 mov r1, r4

c8d8: e3a02010 mov r2, #16

c8dc: ebfff7f2 bl a8ac <mcrypt\_mutex\_register@@Base+0x6118>

c8e0: e1a01008 mov r1, r8

c8e4: e1a00009 mov r0, r9

c8e8: ebfff841 bl a9f4 <mcrypt\_mutex\_register@@Base+0x6260>

c8ec: e3560000 cmp r6, #0

c8f0: e28da030 add sl, sp, #48 ; 0x30

c8f4: da00000b ble c928 <mcrypt\_mutex\_register@@Base+0x8194>

c8f8: e59f70f8 ldr r7, [pc, #248] ; c9f8 <mcrypt\_mutex\_register@@Base+0x8264>

c8fc: e0886006 add r6, r8, r6

c900: e08f7007 add r7, pc, r7

c904: e1a0500a mov r5, sl

c908: e1a04008 mov r4, r8

c90c: e4d42001 ldrb r2, [r4], #1

c910: e1a00005 mov r0, r5

c914: e1a01007 mov r1, r7

c918: ebffd693 bl 236c <sprintf@plt>

c91c: e1540006 cmp r4, r6

c920: e2855002 add r5, r5, #2

c924: 1afffff8 bne c90c <mcrypt\_mutex\_register@@Base+0x8178>

c928: e59f50cc ldr r5, [pc, #204] ; c9fc <mcrypt\_mutex\_register@@Base+0x8268>

c92c: e1a0000a mov r0, sl

c930: e08f5005 add r5, pc, r5

c934: e1a01005 mov r1, r5

c938: ebffd643 bl 224c <strcmp@plt>

c93c: e2504000 subs r4, r0, #0

c940: 1a00001c bne c9b8 <mcrypt\_mutex\_register@@Base+0x8224>

c944: e1a01008 mov r1, r8

c948: e1a00009 mov r0, r9

c94c: ebfffbf1 bl b918 <mcrypt\_mutex\_register@@Base+0x7184>

c950: e1a00009 mov r0, r9

c954: ebffd648 bl 227c <free@plt>

c958: e59d2020 ldr r2, [sp, #32]

c95c: e59d3010 ldr r3, [sp, #16]

c960: e1520003 cmp r2, r3

c964: 0a000005 beq c980 <mcrypt\_mutex\_register@@Base+0x81ec>

c968: e59f0090 ldr r0, [pc, #144] ; ca00 <mcrypt\_mutex\_register@@Base+0x826c>

c96c: e08f0000 add r0, pc, r0

c970: ebffd65c bl 22e8 <puts@plt>

c974: e3e00000 mvn r0, #0

c978: e28dd0f8 add sp, sp, #248 ; 0xf8

c97c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

c980: e59d2024 ldr r2, [sp, #36] ; 0x24

c984: e59d3014 ldr r3, [sp, #20]

c988: e1520003 cmp r2, r3

c98c: 1afffff5 bne c968 <mcrypt\_mutex\_register@@Base+0x81d4>

c990: e59d2028 ldr r2, [sp, #40] ; 0x28

c994: e59d3018 ldr r3, [sp, #24]

c998: e1520003 cmp r2, r3

c99c: 1afffff1 bne c968 <mcrypt\_mutex\_register@@Base+0x81d4>

c9a0: e59d202c ldr r2, [sp, #44] ; 0x2c

c9a4: e59d301c ldr r3, [sp, #28]

c9a8: e1520003 cmp r2, r3

c9ac: 01a00004 moveq r0, r4

c9b0: 1affffec bne c968 <mcrypt\_mutex\_register@@Base+0x81d4>

c9b4: eaffffef b c978 <mcrypt\_mutex\_register@@Base+0x81e4>

c9b8: e59f0044 ldr r0, [pc, #68] ; ca04 <mcrypt\_mutex\_register@@Base+0x8270>

c9bc: e08f0000 add r0, pc, r0

c9c0: ebffd648 bl 22e8 <puts@plt>

c9c4: e59f003c ldr r0, [pc, #60] ; ca08 <mcrypt\_mutex\_register@@Base+0x8274>

c9c8: e1a0200a mov r2, sl

c9cc: e1a01005 mov r1, r5

c9d0: e08f0000 add r0, pc, r0

c9d4: ebffd622 bl 2264 <printf@plt>

c9d8: e1a00009 mov r0, r9

c9dc: ebffd626 bl 227c <free@plt>

c9e0: e3e00000 mvn r0, #0

c9e4: eaffffe3 b c978 <mcrypt\_mutex\_register@@Base+0x81e4>

c9e8: e3e00000 mvn r0, #0

c9ec: eaffffe1 b c978 <mcrypt\_mutex\_register@@Base+0x81e4>

c9f0: 00013f1c andeq r3, r1, ip, lsl pc

c9f4: 00013f2c andeq r3, r1, ip, lsr #30

c9f8: 00013d60 andeq r3, r1, r0, ror #26

c9fc: 00013eac andeq r3, r1, ip, lsr #29

ca00: 00013d40 andeq r3, r1, r0, asr #26

ca04: 00013cc0 andeq r3, r1, r0, asr #25

ca08: 00013cc4 andeq r3, r1, r4, asr #25

ca0c: e59f0000 ldr r0, [pc] ; ca14 <mcrypt\_mutex\_register@@Base+0x8280>

ca10: e12fff1e bx lr

ca14: 01310a59 teqeq r1, r9, asr sl

ca18: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

ca1c: e2404001 sub r4, r0, #1

ca20: e1a08002 mov r8, r2

ca24: e1a09000 mov r9, r0

ca28: e1a07001 mov r7, r1

ca2c: e1a02004 mov r2, r4

ca30: e3a03000 mov r3, #0

ca34: e5e23001 strb r3, [r2, #1]!

ca38: e2833001 add r3, r3, #1

ca3c: e3530c01 cmp r3, #256 ; 0x100

ca40: 1afffffb bne ca34 <mcrypt\_mutex\_register@@Base+0x82a0>

ca44: e3a03000 mov r3, #0

ca48: e1a0a003 mov sl, r3

ca4c: e28960ff add r6, r9, #255 ; 0xff

ca50: e2695001 rsb r5, r9, #1

ca54: e5c93100 strb r3, [r9, #256] ; 0x100

ca58: e5c93101 strb r3, [r9, #257] ; 0x101

ca5c: e0850004 add r0, r5, r4

ca60: e1a01008 mov r1, r8

ca64: eb003cad bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

ca68: e5f42001 ldrb r2, [r4, #1]!

ca6c: e1560004 cmp r6, r4

ca70: e7d73001 ldrb r3, [r7, r1]

ca74: e0833002 add r3, r3, r2

ca78: e083300a add r3, r3, sl

ca7c: e6efa073 uxtb sl, r3

ca80: e7d9300a ldrb r3, [r9, sl]

ca84: e5c43000 strb r3, [r4]

ca88: e7c9200a strb r2, [r9, sl]

ca8c: 1afffff2 bne ca5c <mcrypt\_mutex\_register@@Base+0x82c8>

ca90: e3a00000 mov r0, #0

ca94: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

ca98: e3520000 cmp r2, #0

ca9c: e92d4030 push {r4, r5, lr}

caa0: e5d03100 ldrb r3, [r0, #256] ; 0x100

caa4: e5d05101 ldrb r5, [r0, #257] ; 0x101

caa8: da000012 ble caf8 <mcrypt\_mutex\_register@@Base+0x8364>

caac: e2411001 sub r1, r1, #1

cab0: e0812002 add r2, r1, r2

cab4: e2833001 add r3, r3, #1

cab8: e6ef3073 uxtb r3, r3

cabc: e7d0e003 ldrb lr, [r0, r3]

cac0: e08ec005 add ip, lr, r5

cac4: e6ef507c uxtb r5, ip

cac8: e7d0c005 ldrb ip, [r0, r5]

cacc: e7c0c003 strb ip, [r0, r3]

cad0: e7c0e005 strb lr, [r0, r5]

cad4: e7d0c003 ldrb ip, [r0, r3]

cad8: e5f14001 ldrb r4, [r1, #1]!

cadc: e08cc00e add ip, ip, lr

cae0: e1520001 cmp r2, r1

cae4: e6efc07c uxtb ip, ip

cae8: e7d0c00c ldrb ip, [r0, ip]

caec: e02cc004 eor ip, ip, r4

caf0: e5c1c000 strb ip, [r1]

caf4: 1affffee bne cab4 <mcrypt\_mutex\_register@@Base+0x8320>

caf8: e5c05101 strb r5, [r0, #257] ; 0x101

cafc: e5c03100 strb r3, [r0, #256] ; 0x100

cb00: e8bd8030 pop {r4, r5, pc}

cb04: eaffffe3 b ca98 <mcrypt\_mutex\_register@@Base+0x8304>

cb08: e59f0000 ldr r0, [pc] ; cb10 <mcrypt\_mutex\_register@@Base+0x837c>

cb0c: e12fff1e bx lr

cb10: 00000102 andeq r0, r0, r2, lsl #2

cb14: e3a00001 mov r0, #1

cb18: e12fff1e bx lr

cb1c: e3a00000 mov r0, #0

cb20: e12fff1e bx lr

cb24: e3a00000 mov r0, #0

cb28: e12fff1e bx lr

cb2c: e3a00c01 mov r0, #256 ; 0x100

cb30: e12fff1e bx lr

cb34: e3a03000 mov r3, #0

cb38: e5803000 str r3, [r0]

cb3c: e1a00003 mov r0, r3

cb40: e12fff1e bx lr

cb44: e59f0004 ldr r0, [pc, #4] ; cb50 <mcrypt\_mutex\_register@@Base+0x83bc>

cb48: e08f0000 add r0, pc, r0

cb4c: e12fff1e bx lr

cb50: 00013cb8 ; <UNDEFINED> instruction: 0x00013cb8

cb54: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

cb58: e24ddf41 sub sp, sp, #260 ; 0x104

cb5c: ebfffff2 bl cb2c <mcrypt\_mutex\_register@@Base+0x8398>

cb60: e1a01000 mov r1, r0

cb64: e3a00001 mov r0, #1

cb68: ebffd5b1 bl 2234 <calloc@plt>

cb6c: e2509000 subs r9, r0, #0

cb70: 12495001 subne r5, r9, #1

cb74: 13a0400a movne r4, #10

cb78: 12696001 rsbne r6, r9, #1

cb7c: 1a000005 bne cb98 <mcrypt\_mutex\_register@@Base+0x8404>

cb80: ea000053 b ccd4 <mcrypt\_mutex\_register@@Base+0x8540>

cb84: e2742000 rsbs r2, r4, #0

cb88: e2844002 add r4, r4, #2

cb8c: e6ef2072 uxtb r2, r2

cb90: 52623000 rsbpl r3, r2, #0

cb94: e5e53001 strb r3, [r5, #1]!

cb98: ebffffe3 bl cb2c <mcrypt\_mutex\_register@@Base+0x8398>

cb9c: e0862005 add r2, r6, r5

cba0: e6ef3074 uxtb r3, r4

cba4: e1500002 cmp r0, r2

cba8: cafffff5 bgt cb84 <mcrypt\_mutex\_register@@Base+0x83f0>

cbac: e3a04000 mov r4, #0

cbb0: e28d3010 add r3, sp, #16

cbb4: e58d3008 str r3, [sp, #8]

cbb8: e4c34001 strb r4, [r3], #1

cbbc: e2844001 add r4, r4, #1

cbc0: e3540014 cmp r4, #20

cbc4: 1afffffb bne cbb8 <mcrypt\_mutex\_register@@Base+0x8424>

cbc8: ebffffce bl cb08 <mcrypt\_mutex\_register@@Base+0x8374>

cbcc: ebffd5c8 bl 22f4 <malloc@plt>

cbd0: e250a000 subs sl, r0, #0

cbd4: 0a00003e beq ccd4 <mcrypt\_mutex\_register@@Base+0x8540>

cbd8: e59de008 ldr lr, [sp, #8]

cbdc: e28dc024 add ip, sp, #36 ; 0x24

cbe0: e1a0b00c mov fp, ip

cbe4: e8be000f ldm lr!, {r0, r1, r2, r3}

cbe8: e28d5038 add r5, sp, #56 ; 0x38

cbec: e59f8120 ldr r8, [pc, #288] ; cd14 <mcrypt\_mutex\_register@@Base+0x8580>

cbf0: e1a0600b mov r6, fp

cbf4: e59ee000 ldr lr, [lr]

cbf8: e8ac000f stmia ip!, {r0, r1, r2, r3}

cbfc: e08f8008 add r8, pc, r8

cc00: e58ce000 str lr, [ip]

cc04: ebffffc8 bl cb2c <mcrypt\_mutex\_register@@Base+0x8398>

cc08: e3a03000 mov r3, #0

cc0c: e1a01009 mov r1, r9

cc10: e58d3000 str r3, [sp]

cc14: e58d500c str r5, [sp, #12]

cc18: e1a07005 mov r7, r5

cc1c: e1a02000 mov r2, r0

cc20: e1a0000a mov r0, sl

cc24: ebffff7b bl ca18 <mcrypt\_mutex\_register@@Base+0x8284>

cc28: e1a02004 mov r2, r4

cc2c: e1a0100b mov r1, fp

cc30: e1a0000a mov r0, sl

cc34: ebffff97 bl ca98 <mcrypt\_mutex\_register@@Base+0x8304>

cc38: e4d62001 ldrb r2, [r6], #1

cc3c: e1a00005 mov r0, r5

cc40: e1a01008 mov r1, r8

cc44: ebffd5c8 bl 236c <sprintf@plt>

cc48: e1570006 cmp r7, r6

cc4c: e2855002 add r5, r5, #2

cc50: 1afffff8 bne cc38 <mcrypt\_mutex\_register@@Base+0x84a4>

cc54: e59f50bc ldr r5, [pc, #188] ; cd18 <mcrypt\_mutex\_register@@Base+0x8584>

cc58: e59d000c ldr r0, [sp, #12]

cc5c: e08f5005 add r5, pc, r5

cc60: e1a01005 mov r1, r5

cc64: ebffd578 bl 224c <strcmp@plt>

cc68: e2504000 subs r4, r0, #0

cc6c: 1a00001a bne ccdc <mcrypt\_mutex\_register@@Base+0x8548>

cc70: ebffffad bl cb2c <mcrypt\_mutex\_register@@Base+0x8398>

cc74: e1a03004 mov r3, r4

cc78: e1a01009 mov r1, r9

cc7c: e58d4000 str r4, [sp]

cc80: e1a02000 mov r2, r0

cc84: e1a0000a mov r0, sl

cc88: ebffff62 bl ca18 <mcrypt\_mutex\_register@@Base+0x8284>

cc8c: e3a02014 mov r2, #20

cc90: e1a0100b mov r1, fp

cc94: e1a0000a mov r0, sl

cc98: ebffff99 bl cb04 <mcrypt\_mutex\_register@@Base+0x8370>

cc9c: e1a00009 mov r0, r9

cca0: ebffd575 bl 227c <free@plt>

cca4: e1a0000a mov r0, sl

cca8: ebffd573 bl 227c <free@plt>

ccac: e59d1008 ldr r1, [sp, #8]

ccb0: e1a0000b mov r0, fp

ccb4: ebffd564 bl 224c <strcmp@plt>

ccb8: e3500000 cmp r0, #0

ccbc: 1a000001 bne ccc8 <mcrypt\_mutex\_register@@Base+0x8534>

ccc0: e28ddf41 add sp, sp, #260 ; 0x104

ccc4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

ccc8: e59f004c ldr r0, [pc, #76] ; cd1c <mcrypt\_mutex\_register@@Base+0x8588>

cccc: e08f0000 add r0, pc, r0

ccd0: ebffd584 bl 22e8 <puts@plt>

ccd4: e3e00000 mvn r0, #0

ccd8: eafffff8 b ccc0 <mcrypt\_mutex\_register@@Base+0x852c>

ccdc: e59f003c ldr r0, [pc, #60] ; cd20 <mcrypt\_mutex\_register@@Base+0x858c>

cce0: e08f0000 add r0, pc, r0

cce4: ebffd57f bl 22e8 <puts@plt>

cce8: e59f0034 ldr r0, [pc, #52] ; cd24 <mcrypt\_mutex\_register@@Base+0x8590>

ccec: e59d200c ldr r2, [sp, #12]

ccf0: e1a01005 mov r1, r5

ccf4: e08f0000 add r0, pc, r0

ccf8: ebffd559 bl 2264 <printf@plt>

ccfc: e1a00009 mov r0, r9

cd00: ebffd55d bl 227c <free@plt>

cd04: e1a0000a mov r0, sl

cd08: ebffd55b bl 227c <free@plt>

cd0c: e3e00000 mvn r0, #0

cd10: eaffffea b ccc0 <mcrypt\_mutex\_register@@Base+0x852c>

cd14: 00013a64 andeq r3, r1, r4, ror #20

cd18: 00013ba8 andeq r3, r1, r8, lsr #23

cd1c: 000139e0 andeq r3, r1, r0, ror #19

cd20: 0001399c muleq r1, ip, r9

cd24: 000139a0 andeq r3, r1, r0, lsr #19

cd28: e59f0000 ldr r0, [pc] ; cd30 <mcrypt\_mutex\_register@@Base+0x859c>

cd2c: e12fff1e bx lr

cd30: 01317d82 teqeq r1, r2, lsl #27

cd34: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

cd38: e1b02122 lsrs r2, r2, #2

cd3c: e59f47ac ldr r4, [pc, #1964] ; d4f0 <mcrypt\_mutex\_register@@Base+0x8d5c>

cd40: e24dd08c sub sp, sp, #140 ; 0x8c

cd44: e08f4004 add r4, pc, r4

cd48: 01a05002 moveq r5, r2

cd4c: 0a000008 beq cd74 <mcrypt\_mutex\_register@@Base+0x85e0>

cd50: e1a05102 lsl r5, r2, #2

cd54: e081e005 add lr, r1, r5

cd58: e28d3024 add r3, sp, #36 ; 0x24

cd5c: e491c004 ldr ip, [r1], #4

cd60: e151000e cmp r1, lr

cd64: e5a3c004 str ip, [r3, #4]!

cd68: 1afffffb bne cd5c <mcrypt\_mutex\_register@@Base+0x85c8>

cd6c: e3520007 cmp r2, #7

cd70: 8a000006 bhi cd90 <mcrypt\_mutex\_register@@Base+0x85fc>

cd74: e28d3028 add r3, sp, #40 ; 0x28

cd78: e0833005 add r3, r3, r5

cd7c: e3a01000 mov r1, #0

cd80: e2822001 add r2, r2, #1

cd84: e3520008 cmp r2, #8

cd88: e4831004 str r1, [r3], #4

cd8c: 1afffffb bne cd80 <mcrypt\_mutex\_register@@Base+0x85ec>

cd90: e59f375c ldr r3, [pc, #1884] ; d4f4 <mcrypt\_mutex\_register@@Base+0x8d60>

cd94: e59d2044 ldr r2, [sp, #68] ; 0x44

cd98: e59da038 ldr sl, [sp, #56] ; 0x38

cd9c: e58d2010 str r2, [sp, #16]

cda0: e7948003 ldr r8, [r4, r3]

cda4: e59d3040 ldr r3, [sp, #64] ; 0x40

cda8: e59d6028 ldr r6, [sp, #40] ; 0x28

cdac: e58d3018 str r3, [sp, #24]

cdb0: e59d303c ldr r3, [sp, #60] ; 0x3c

cdb4: e59fb73c ldr fp, [pc, #1852] ; d4f8 <mcrypt\_mutex\_register@@Base+0x8d64>

cdb8: e58d300c str r3, [sp, #12]

cdbc: e59d3034 ldr r3, [sp, #52] ; 0x34

cdc0: e59fc734 ldr ip, [pc, #1844] ; d4fc <mcrypt\_mutex\_register@@Base+0x8d68>

cdc4: e58d3014 str r3, [sp, #20]

cdc8: e59d3030 ldr r3, [sp, #48] ; 0x30

cdcc: e59f572c ldr r5, [pc, #1836] ; d500 <mcrypt\_mutex\_register@@Base+0x8d6c>

cdd0: e58d3008 str r3, [sp, #8]

cdd4: e59d302c ldr r3, [sp, #44] ; 0x2c

cdd8: e58d301c str r3, [sp, #28]

cddc: e3a03013 mov r3, #19

cde0: e58d3004 str r3, [sp, #4]

cde4: e28d3044 add r3, sp, #68 ; 0x44

cde8: e58d3020 str r3, [sp, #32]

cdec: e28d3064 add r3, sp, #100 ; 0x64

cdf0: e58d3024 str r3, [sp, #36] ; 0x24

cdf4: e59f1708 ldr r1, [pc, #1800] ; d504 <mcrypt\_mutex\_register@@Base+0x8d70>

cdf8: e59d4020 ldr r4, [sp, #32]

cdfc: e59d7024 ldr r7, [sp, #36] ; 0x24

ce00: e59de004 ldr lr, [sp, #4]

ce04: e08b1001 add r1, fp, r1

ce08: e1a02004 mov r2, r4

ce0c: e1a03007 mov r3, r7

ce10: e5a41004 str r1, [r4, #4]!

ce14: e0811005 add r1, r1, r5

ce18: e151000b cmp r1, fp

ce1c: e5a7e004 str lr, [r7, #4]!

ce20: e28ee011 add lr, lr, #17

ce24: 1afffff9 bne ce10 <mcrypt\_mutex\_register@@Base+0x867c>

ce28: e59d1068 ldr r1, [sp, #104] ; 0x68

ce2c: e59d4010 ldr r4, [sp, #16]

ce30: e59de048 ldr lr, [sp, #72] ; 0x48

ce34: e201101f and r1, r1, #31

ce38: e084e00e add lr, r4, lr

ce3c: e2611020 rsb r1, r1, #32

ce40: e59d706c ldr r7, [sp, #108] ; 0x6c

ce44: e1a0117e ror r1, lr, r1

ce48: e207701f and r7, r7, #31

ce4c: e1a04821 lsr r4, r1, #16

ce50: e1a0e421 lsr lr, r1, #8

ce54: e6ef4074 uxtb r4, r4

ce58: e1a09c21 lsr r9, r1, #24

ce5c: e2844c01 add r4, r4, #256 ; 0x100

ce60: e6efe07e uxtb lr, lr

ce64: e28eec02 add lr, lr, #512 ; 0x200

ce68: e6ef1071 uxtb r1, r1

ce6c: e7989109 ldr r9, [r8, r9, lsl #2]

ce70: e7984104 ldr r4, [r8, r4, lsl #2]

ce74: e2811c03 add r1, r1, #768 ; 0x300

ce78: e798e10e ldr lr, [r8, lr, lsl #2]

ce7c: e7981101 ldr r1, [r8, r1, lsl #2]

ce80: e0244009 eor r4, r4, r9

ce84: e044400e sub r4, r4, lr

ce88: e59de018 ldr lr, [sp, #24]

ce8c: e0844001 add r4, r4, r1

ce90: e59d104c ldr r1, [sp, #76] ; 0x4c

ce94: e02ee004 eor lr, lr, r4

ce98: e021100e eor r1, r1, lr

ce9c: e2677020 rsb r7, r7, #32

cea0: e58de018 str lr, [sp, #24]

cea4: e1a07771 ror r7, r1, r7

cea8: e59d9070 ldr r9, [sp, #112] ; 0x70

ceac: e1a04427 lsr r4, r7, #8

ceb0: e1a0e827 lsr lr, r7, #16

ceb4: e6ef4074 uxtb r4, r4

ceb8: e1a01c27 lsr r1, r7, #24

cebc: e2844c02 add r4, r4, #512 ; 0x200

cec0: e6ef7077 uxtb r7, r7

cec4: e7981101 ldr r1, [r8, r1, lsl #2]

cec8: e7984104 ldr r4, [r8, r4, lsl #2]

cecc: e6efe07e uxtb lr, lr

ced0: e2877c03 add r7, r7, #768 ; 0x300

ced4: e28eec01 add lr, lr, #256 ; 0x100

ced8: e7987107 ldr r7, [r8, r7, lsl #2]

cedc: e0844001 add r4, r4, r1

cee0: e59d100c ldr r1, [sp, #12]

cee4: e798e10e ldr lr, [r8, lr, lsl #2]

cee8: e0211007 eor r1, r1, r7

ceec: e1a07001 mov r7, r1

cef0: e044400e sub r4, r4, lr

cef4: e59d1050 ldr r1, [sp, #80] ; 0x50

cef8: e024e007 eor lr, r4, r7

cefc: e209901f and r9, r9, #31

cf00: e041100e sub r1, r1, lr

cf04: e2699020 rsb r9, r9, #32

cf08: e58de00c str lr, [sp, #12]

cf0c: e1a01971 ror r1, r1, r9

cf10: e59d7074 ldr r7, [sp, #116] ; 0x74

cf14: e1a09821 lsr r9, r1, #16

cf18: e1a04421 lsr r4, r1, #8

cf1c: e6ef9079 uxtb r9, r9

cf20: e1a0ec21 lsr lr, r1, #24

cf24: e2899c01 add r9, r9, #256 ; 0x100

cf28: e6ef4074 uxtb r4, r4

cf2c: e2844c02 add r4, r4, #512 ; 0x200

cf30: e6ef1071 uxtb r1, r1

cf34: e7989109 ldr r9, [r8, r9, lsl #2]

cf38: e798e10e ldr lr, [r8, lr, lsl #2]

cf3c: e2811c03 add r1, r1, #768 ; 0x300

cf40: e7984104 ldr r4, [r8, r4, lsl #2]

cf44: e7981101 ldr r1, [r8, r1, lsl #2]

cf48: e08ee009 add lr, lr, r9

cf4c: e02ee004 eor lr, lr, r4

cf50: e04ee001 sub lr, lr, r1

cf54: e59d1054 ldr r1, [sp, #84] ; 0x54

cf58: e02aa00e eor sl, sl, lr

cf5c: e207701f and r7, r7, #31

cf60: e08a1001 add r1, sl, r1

cf64: e2677020 rsb r7, r7, #32

cf68: e59d4078 ldr r4, [sp, #120] ; 0x78

cf6c: e1a07771 ror r7, r1, r7

cf70: e204401f and r4, r4, #31

cf74: e1a09827 lsr r9, r7, #16

cf78: e1a0e427 lsr lr, r7, #8

cf7c: e6ef9079 uxtb r9, r9

cf80: e1a01c27 lsr r1, r7, #24

cf84: e2899c01 add r9, r9, #256 ; 0x100

cf88: e6efe07e uxtb lr, lr

cf8c: e28eec02 add lr, lr, #512 ; 0x200

cf90: e6ef7077 uxtb r7, r7

cf94: e7989109 ldr r9, [r8, r9, lsl #2]

cf98: e7981101 ldr r1, [r8, r1, lsl #2]

cf9c: e2877c03 add r7, r7, #768 ; 0x300

cfa0: e798e10e ldr lr, [r8, lr, lsl #2]

cfa4: e7987107 ldr r7, [r8, r7, lsl #2]

cfa8: e0211009 eor r1, r1, r9

cfac: e041100e sub r1, r1, lr

cfb0: e0811007 add r1, r1, r7

cfb4: e59d7014 ldr r7, [sp, #20]

cfb8: e59de058 ldr lr, [sp, #88] ; 0x58

cfbc: e0277001 eor r7, r7, r1

cfc0: e027100e eor r1, r7, lr

cfc4: e2644020 rsb r4, r4, #32

cfc8: e58d7014 str r7, [sp, #20]

cfcc: e1a04471 ror r4, r1, r4

cfd0: e59d907c ldr r9, [sp, #124] ; 0x7c

cfd4: e1a07424 lsr r7, r4, #8

cfd8: e1a01824 lsr r1, r4, #16

cfdc: e6ef7077 uxtb r7, r7

cfe0: e1a0ec24 lsr lr, r4, #24

cfe4: e2877c02 add r7, r7, #512 ; 0x200

cfe8: e6ef1071 uxtb r1, r1

cfec: e2811c01 add r1, r1, #256 ; 0x100

cff0: e798e10e ldr lr, [r8, lr, lsl #2]

cff4: e6ef4074 uxtb r4, r4

cff8: e7987107 ldr r7, [r8, r7, lsl #2]

cffc: e7981101 ldr r1, [r8, r1, lsl #2]

d000: e2844c03 add r4, r4, #768 ; 0x300

d004: e087700e add r7, r7, lr

d008: e0477001 sub r7, r7, r1

d00c: e7984104 ldr r4, [r8, r4, lsl #2]

d010: e59d1008 ldr r1, [sp, #8]

d014: e59de05c ldr lr, [sp, #92] ; 0x5c

d018: e0211004 eor r1, r1, r4

d01c: e0271001 eor r1, r7, r1

d020: e209901f and r9, r9, #31

d024: e04ee001 sub lr, lr, r1

d028: e2699020 rsb r9, r9, #32

d02c: e58d1008 str r1, [sp, #8]

d030: e1a0e97e ror lr, lr, r9

d034: e59d1004 ldr r1, [sp, #4]

d038: e1a0782e lsr r7, lr, #16

d03c: e1a0442e lsr r4, lr, #8

d040: e6ef7077 uxtb r7, r7

d044: e1a09c2e lsr r9, lr, #24

d048: e2877c01 add r7, r7, #256 ; 0x100

d04c: e6ef4074 uxtb r4, r4

d050: e2844c02 add r4, r4, #512 ; 0x200

d054: e6efe07e uxtb lr, lr

d058: e7987107 ldr r7, [r8, r7, lsl #2]

d05c: e7989109 ldr r9, [r8, r9, lsl #2]

d060: e28eec03 add lr, lr, #768 ; 0x300

d064: e7984104 ldr r4, [r8, r4, lsl #2]

d068: e0899007 add r9, r9, r7

d06c: e798e10e ldr lr, [r8, lr, lsl #2]

d070: e0299004 eor r9, r9, r4

d074: e049900e sub r9, r9, lr

d078: e59de01c ldr lr, [sp, #28]

d07c: e59d4080 ldr r4, [sp, #128] ; 0x80

d080: e02ee009 eor lr, lr, r9

d084: e59d7060 ldr r7, [sp, #96] ; 0x60

d088: e1a0900e mov r9, lr

d08c: e58de01c str lr, [sp, #28]

d090: e204e01f and lr, r4, #31

d094: e26ee020 rsb lr, lr, #32

d098: e0894007 add r4, r9, r7

d09c: e2811088 add r1, r1, #136 ; 0x88

d0a0: e1a0ee74 ror lr, r4, lr

d0a4: e1a0982e lsr r9, lr, #16

d0a8: e1a0742e lsr r7, lr, #8

d0ac: e6ef9079 uxtb r9, r9

d0b0: e1a04c2e lsr r4, lr, #24

d0b4: e2899c01 add r9, r9, #256 ; 0x100

d0b8: e6ef7077 uxtb r7, r7

d0bc: e2877c02 add r7, r7, #512 ; 0x200

d0c0: e6efe07e uxtb lr, lr

d0c4: e7989109 ldr r9, [r8, r9, lsl #2]

d0c8: e7984104 ldr r4, [r8, r4, lsl #2]

d0cc: e28eec03 add lr, lr, #768 ; 0x300

d0d0: e7987107 ldr r7, [r8, r7, lsl #2]

d0d4: e0244009 eor r4, r4, r9

d0d8: e798e10e ldr lr, [r8, lr, lsl #2]

d0dc: e0444007 sub r4, r4, r7

d0e0: e084400e add r4, r4, lr

d0e4: e59d7084 ldr r7, [sp, #132] ; 0x84

d0e8: e0249006 eor r9, r4, r6

d0ec: e59d6064 ldr r6, [sp, #100] ; 0x64

d0f0: e207e01f and lr, r7, #31

d0f4: e0294006 eor r4, r9, r6

d0f8: e26ee020 rsb lr, lr, #32

d0fc: e1a0ee74 ror lr, r4, lr

d100: e1a0642e lsr r6, lr, #8

d104: e1a0482e lsr r4, lr, #16

d108: e6ef6076 uxtb r6, r6

d10c: e1a07c2e lsr r7, lr, #24

d110: e2866c02 add r6, r6, #512 ; 0x200

d114: e6ef4074 uxtb r4, r4

d118: e2844c01 add r4, r4, #256 ; 0x100

d11c: e6efe07e uxtb lr, lr

d120: e7986106 ldr r6, [r8, r6, lsl #2]

d124: e7987107 ldr r7, [r8, r7, lsl #2]

d128: e7984104 ldr r4, [r8, r4, lsl #2]

d12c: e28eec03 add lr, lr, #768 ; 0x300

d130: e0866007 add r6, r6, r7

d134: e0466004 sub r6, r6, r4

d138: e798e10e ldr lr, [r8, lr, lsl #2]

d13c: e59d4010 ldr r4, [sp, #16]

d140: e02ee004 eor lr, lr, r4

d144: e026e00e eor lr, r6, lr

d148: e58de010 str lr, [sp, #16]

d14c: e1a0e00b mov lr, fp

d150: e5a2e004 str lr, [r2, #4]!

d154: e08ee005 add lr, lr, r5

d158: e15e000c cmp lr, ip

d15c: e5a31004 str r1, [r3, #4]!

d160: e2811011 add r1, r1, #17

d164: 1afffff9 bne d150 <mcrypt\_mutex\_register@@Base+0x89bc>

d168: e59d3068 ldr r3, [sp, #104] ; 0x68

d16c: e59d1010 ldr r1, [sp, #16]

d170: e59d2048 ldr r2, [sp, #72] ; 0x48

d174: e203301f and r3, r3, #31

d178: e0812002 add r2, r1, r2

d17c: e2633020 rsb r3, r3, #32

d180: e59dc004 ldr ip, [sp, #4]

d184: e1a03372 ror r3, r2, r3

d188: e59f2378 ldr r2, [pc, #888] ; d508 <mcrypt\_mutex\_register@@Base+0x8d74>

d18c: e28cce11 add ip, ip, #272 ; 0x110

d190: e1a01823 lsr r1, r3, #16

d194: e15c0002 cmp ip, r2

d198: e1a02423 lsr r2, r3, #8

d19c: e6ef1071 uxtb r1, r1

d1a0: e58dc004 str ip, [sp, #4]

d1a4: e2811c01 add r1, r1, #256 ; 0x100

d1a8: e1a0cc23 lsr ip, r3, #24

d1ac: e6ef2072 uxtb r2, r2

d1b0: e2822c02 add r2, r2, #512 ; 0x200

d1b4: e6ef3073 uxtb r3, r3

d1b8: e798c10c ldr ip, [r8, ip, lsl #2]

d1bc: e7981101 ldr r1, [r8, r1, lsl #2]

d1c0: e2833c03 add r3, r3, #768 ; 0x300

d1c4: e7982102 ldr r2, [r8, r2, lsl #2]

d1c8: e7983103 ldr r3, [r8, r3, lsl #2]

d1cc: e021100c eor r1, r1, ip

d1d0: e0411002 sub r1, r1, r2

d1d4: e0811003 add r1, r1, r3

d1d8: e59d3018 ldr r3, [sp, #24]

d1dc: e59d206c ldr r2, [sp, #108] ; 0x6c

d1e0: e59dc04c ldr ip, [sp, #76] ; 0x4c

d1e4: e0233001 eor r3, r3, r1

d1e8: e1a01003 mov r1, r3

d1ec: e202301f and r3, r2, #31

d1f0: e2633020 rsb r3, r3, #32

d1f4: e021200c eor r2, r1, ip

d1f8: e58d1018 str r1, [sp, #24]

d1fc: e1a03372 ror r3, r2, r3

d200: e59fc304 ldr ip, [pc, #772] ; d50c <mcrypt\_mutex\_register@@Base+0x8d78>

d204: e1a01423 lsr r1, r3, #8

d208: e1a02823 lsr r2, r3, #16

d20c: e6ef1071 uxtb r1, r1

d210: e08ec00c add ip, lr, ip

d214: e2811c02 add r1, r1, #512 ; 0x200

d218: e1a0ec23 lsr lr, r3, #24

d21c: e6ef2072 uxtb r2, r2

d220: e6ef3073 uxtb r3, r3

d224: e2833c03 add r3, r3, #768 ; 0x300

d228: e2822c01 add r2, r2, #256 ; 0x100

d22c: e798e10e ldr lr, [r8, lr, lsl #2]

d230: e7981101 ldr r1, [r8, r1, lsl #2]

d234: e7984103 ldr r4, [r8, r3, lsl #2]

d238: e7982102 ldr r2, [r8, r2, lsl #2]

d23c: e59d700c ldr r7, [sp, #12]

d240: e081100e add r1, r1, lr

d244: e59d3070 ldr r3, [sp, #112] ; 0x70

d248: e0411002 sub r1, r1, r2

d24c: e0277004 eor r7, r7, r4

d250: e59d2050 ldr r2, [sp, #80] ; 0x50

d254: e021e007 eor lr, r1, r7

d258: e203301f and r3, r3, #31

d25c: e042100e sub r1, r2, lr

d260: e2633020 rsb r3, r3, #32

d264: e58de00c str lr, [sp, #12]

d268: e1a01371 ror r1, r1, r3

d26c: e59d4074 ldr r4, [sp, #116] ; 0x74

d270: e1a02821 lsr r2, r1, #16

d274: e1a0e421 lsr lr, r1, #8

d278: e6ef2072 uxtb r2, r2

d27c: e1a03c21 lsr r3, r1, #24

d280: e2822c01 add r2, r2, #256 ; 0x100

d284: e6efe07e uxtb lr, lr

d288: e28eec02 add lr, lr, #512 ; 0x200

d28c: e6ef1071 uxtb r1, r1

d290: e7982102 ldr r2, [r8, r2, lsl #2]

d294: e7983103 ldr r3, [r8, r3, lsl #2]

d298: e2811c03 add r1, r1, #768 ; 0x300

d29c: e798e10e ldr lr, [r8, lr, lsl #2]

d2a0: e7981101 ldr r1, [r8, r1, lsl #2]

d2a4: e0833002 add r3, r3, r2

d2a8: e023300e eor r3, r3, lr

d2ac: e0433001 sub r3, r3, r1

d2b0: e59d2054 ldr r2, [sp, #84] ; 0x54

d2b4: e02aa003 eor sl, sl, r3

d2b8: e204401f and r4, r4, #31

d2bc: e2644020 rsb r4, r4, #32

d2c0: e08a2002 add r2, sl, r2

d2c4: e59de078 ldr lr, [sp, #120] ; 0x78

d2c8: e1a02472 ror r2, r2, r4

d2cc: e20ee01f and lr, lr, #31

d2d0: e1a04822 lsr r4, r2, #16

d2d4: e1a01422 lsr r1, r2, #8

d2d8: e6ef4074 uxtb r4, r4

d2dc: e1a03c22 lsr r3, r2, #24

d2e0: e2844c01 add r4, r4, #256 ; 0x100

d2e4: e6ef1071 uxtb r1, r1

d2e8: e2811c02 add r1, r1, #512 ; 0x200

d2ec: e6ef2072 uxtb r2, r2

d2f0: e7984104 ldr r4, [r8, r4, lsl #2]

d2f4: e7983103 ldr r3, [r8, r3, lsl #2]

d2f8: e2822c03 add r2, r2, #768 ; 0x300

d2fc: e7981101 ldr r1, [r8, r1, lsl #2]

d300: e0233004 eor r3, r3, r4

d304: e7982102 ldr r2, [r8, r2, lsl #2]

d308: e0433001 sub r3, r3, r1

d30c: e0833002 add r3, r3, r2

d310: e59d2058 ldr r2, [sp, #88] ; 0x58

d314: e59d4014 ldr r4, [sp, #20]

d318: e26ee020 rsb lr, lr, #32

d31c: e0244003 eor r4, r4, r3

d320: e0243002 eor r3, r4, r2

d324: e59d207c ldr r2, [sp, #124] ; 0x7c

d328: e1a03e73 ror r3, r3, lr

d32c: e202e01f and lr, r2, #31

d330: e1a01423 lsr r1, r3, #8

d334: e1a02823 lsr r2, r3, #16

d338: e58d4014 str r4, [sp, #20]

d33c: e6ef1071 uxtb r1, r1

d340: e1a04c23 lsr r4, r3, #24

d344: e6ef3073 uxtb r3, r3

d348: e2833c03 add r3, r3, #768 ; 0x300

d34c: e2811c02 add r1, r1, #512 ; 0x200

d350: e6ef2072 uxtb r2, r2

d354: e2822c01 add r2, r2, #256 ; 0x100

d358: e7987103 ldr r7, [r8, r3, lsl #2]

d35c: e7984104 ldr r4, [r8, r4, lsl #2]

d360: e7981101 ldr r1, [r8, r1, lsl #2]

d364: e59d3008 ldr r3, [sp, #8]

d368: e7982102 ldr r2, [r8, r2, lsl #2]

d36c: e0811004 add r1, r1, r4

d370: e0233007 eor r3, r3, r7

d374: e1a07003 mov r7, r3

d378: e0411002 sub r1, r1, r2

d37c: e59d305c ldr r3, [sp, #92] ; 0x5c

d380: e0212007 eor r2, r1, r7

d384: e0433002 sub r3, r3, r2

d388: e26ee020 rsb lr, lr, #32

d38c: e58d2008 str r2, [sp, #8]

d390: e1a0ee73 ror lr, r3, lr

d394: e59d701c ldr r7, [sp, #28]

d398: e1a0282e lsr r2, lr, #16

d39c: e1a0342e lsr r3, lr, #8

d3a0: e6ef2072 uxtb r2, r2

d3a4: e1a01c2e lsr r1, lr, #24

d3a8: e2822c01 add r2, r2, #256 ; 0x100

d3ac: e6ef3073 uxtb r3, r3

d3b0: e2833c02 add r3, r3, #512 ; 0x200

d3b4: e6efe07e uxtb lr, lr

d3b8: e7982102 ldr r2, [r8, r2, lsl #2]

d3bc: e7981101 ldr r1, [r8, r1, lsl #2]

d3c0: e28eec03 add lr, lr, #768 ; 0x300

d3c4: e7983103 ldr r3, [r8, r3, lsl #2]

d3c8: e798e10e ldr lr, [r8, lr, lsl #2]

d3cc: e0811002 add r1, r1, r2

d3d0: e0211003 eor r1, r1, r3

d3d4: e59d3080 ldr r3, [sp, #128] ; 0x80

d3d8: e041100e sub r1, r1, lr

d3dc: e59d2060 ldr r2, [sp, #96] ; 0x60

d3e0: e0277001 eor r7, r7, r1

d3e4: e203301f and r3, r3, #31

d3e8: e2633020 rsb r3, r3, #32

d3ec: e0872002 add r2, r7, r2

d3f0: e59de084 ldr lr, [sp, #132] ; 0x84

d3f4: e1a02372 ror r2, r2, r3

d3f8: e20ee01f and lr, lr, #31

d3fc: e1a04822 lsr r4, r2, #16

d400: e1a01422 lsr r1, r2, #8

d404: e6ef4074 uxtb r4, r4

d408: e1a03c22 lsr r3, r2, #24

d40c: e2844c01 add r4, r4, #256 ; 0x100

d410: e6ef1071 uxtb r1, r1

d414: e2811c02 add r1, r1, #512 ; 0x200

d418: e6ef2072 uxtb r2, r2

d41c: e7984104 ldr r4, [r8, r4, lsl #2]

d420: e7983103 ldr r3, [r8, r3, lsl #2]

d424: e2822c03 add r2, r2, #768 ; 0x300

d428: e7981101 ldr r1, [r8, r1, lsl #2]

d42c: e7982102 ldr r2, [r8, r2, lsl #2]

d430: e0233004 eor r3, r3, r4

d434: e0433001 sub r3, r3, r1

d438: e0833002 add r3, r3, r2

d43c: e59d2064 ldr r2, [sp, #100] ; 0x64

d440: e0236009 eor r6, r3, r9

d444: e0263002 eor r3, r6, r2

d448: e26ee020 rsb lr, lr, #32

d44c: e59d9010 ldr r9, [sp, #16]

d450: e1a03e73 ror r3, r3, lr

d454: e24bb549 sub fp, fp, #306184192 ; 0x12400000

d458: e1a01423 lsr r1, r3, #8

d45c: e1a02823 lsr r2, r3, #16

d460: e6ef1071 uxtb r1, r1

d464: e1a0ec23 lsr lr, r3, #24

d468: e2811c02 add r1, r1, #512 ; 0x200

d46c: e6ef2072 uxtb r2, r2

d470: e6ef3073 uxtb r3, r3

d474: e2822c01 add r2, r2, #256 ; 0x100

d478: e2833c03 add r3, r3, #768 ; 0x300

d47c: e798e10e ldr lr, [r8, lr, lsl #2]

d480: e7981101 ldr r1, [r8, r1, lsl #2]

d484: e7982102 ldr r2, [r8, r2, lsl #2]

d488: e7983103 ldr r3, [r8, r3, lsl #2]

d48c: e081100e add r1, r1, lr

d490: e0411002 sub r1, r1, r2

d494: e029e003 eor lr, r9, r3

d498: e021300e eor r3, r1, lr

d49c: e5803010 str r3, [r0, #16]

d4a0: e58d701c str r7, [sp, #28]

d4a4: e58d3010 str r3, [sp, #16]

d4a8: e5806000 str r6, [r0]

d4ac: e59d2008 ldr r2, [sp, #8]

d4b0: e59d4014 ldr r4, [sp, #20]

d4b4: e59de00c ldr lr, [sp, #12]

d4b8: e59d1018 ldr r1, [sp, #24]

d4bc: e24bb985 sub fp, fp, #2179072 ; 0x214000

d4c0: e580701c str r7, [r0, #28]

d4c4: e5802004 str r2, [r0, #4]

d4c8: e5804018 str r4, [r0, #24]

d4cc: e580a008 str sl, [r0, #8]

d4d0: e580e014 str lr, [r0, #20]

d4d4: e580100c str r1, [r0, #12]

d4d8: e24bbe5f sub fp, fp, #1520 ; 0x5f0

d4dc: e2800020 add r0, r0, #32

d4e0: 1afffe43 bne cdf4 <mcrypt\_mutex\_register@@Base+0x8660>

d4e4: e3a00000 mov r0, #0

d4e8: e28dd08c add sp, sp, #140 ; 0x8c

d4ec: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

d4f0: 000272b4 ; <UNDEFINED> instruction: 0x000272b4

d4f4: 000000c0 andeq r0, r0, r0, asr #1

d4f8: d151d6a1 cmple r1, r1, lsr #13

d4fc: 482133a9 stmdami r1!, {r0, r3, r5, r7, r8, r9, ip, sp}

d500: 6ed9eba1 vfnmsvs.f64 d30, d25, d17

d504: 8930a2f8 ldmdbhi r0!, {r3, r4, r5, r6, r7, r9, sp, pc}

d508: 00000cd3 ldrdeq r0, [r0], -r3

d50c: ed9eba10 vldr s22, [lr, #64] ; 0x40

d510: e5903000 ldr r3, [r0]

d514: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

d518: e203301f and r3, r3, #31

d51c: e5902010 ldr r2, [r0, #16]

d520: e591800c ldr r8, [r1, #12]

d524: e2633020 rsb r3, r3, #32

d528: e0882002 add r2, r8, r2

d52c: e59f4ff8 ldr r4, [pc, #4088] ; e52c <mcrypt\_mutex\_register@@Base+0x9d98>

d530: e1a02372 ror r2, r2, r3

d534: e59f3ff4 ldr r3, [pc, #4084] ; e530 <mcrypt\_mutex\_register@@Base+0x9d9c>

d538: e08f4004 add r4, pc, r4

d53c: e1a0e822 lsr lr, r2, #16

d540: e1a0c422 lsr ip, r2, #8

d544: e7943003 ldr r3, [r4, r3]

d548: e6efe07e uxtb lr, lr

d54c: e28eec01 add lr, lr, #256 ; 0x100

d550: e1a04c22 lsr r4, r2, #24

d554: e6efc07c uxtb ip, ip

d558: e28ccc02 add ip, ip, #512 ; 0x200

d55c: e6ef2072 uxtb r2, r2

d560: e793e10e ldr lr, [r3, lr, lsl #2]

d564: e793b104 ldr fp, [r3, r4, lsl #2]

d568: e2822c03 add r2, r2, #768 ; 0x300

d56c: e793410c ldr r4, [r3, ip, lsl #2]

d570: e02bb00e eor fp, fp, lr

d574: e793e102 ldr lr, [r3, r2, lsl #2]

d578: e04bb004 sub fp, fp, r4

d57c: e5914008 ldr r4, [r1, #8]

d580: e08bb00e add fp, fp, lr

d584: e024400b eor r4, r4, fp

d588: e5814008 str r4, [r1, #8]

d58c: e590c004 ldr ip, [r0, #4]

d590: e5902014 ldr r2, [r0, #20]

d594: e20cc01f and ip, ip, #31

d598: e26cc020 rsb ip, ip, #32

d59c: e0222004 eor r2, r2, r4

d5a0: e591e004 ldr lr, [r1, #4]

d5a4: e1a02c72 ror r2, r2, ip

d5a8: e591c000 ldr ip, [r1]

d5ac: e1a05422 lsr r5, r2, #8

d5b0: e1a06822 lsr r6, r2, #16

d5b4: e6ef5075 uxtb r5, r5

d5b8: e1a07c22 lsr r7, r2, #24

d5bc: e2855c02 add r5, r5, #512 ; 0x200

d5c0: e6ef6076 uxtb r6, r6

d5c4: e6ef2072 uxtb r2, r2

d5c8: e2866c01 add r6, r6, #256 ; 0x100

d5cc: e2822c03 add r2, r2, #768 ; 0x300

d5d0: e7937107 ldr r7, [r3, r7, lsl #2]

d5d4: e7935105 ldr r5, [r3, r5, lsl #2]

d5d8: e7936106 ldr r6, [r3, r6, lsl #2]

d5dc: e7932102 ldr r2, [r3, r2, lsl #2]

d5e0: e0855007 add r5, r5, r7

d5e4: e02ee002 eor lr, lr, r2

d5e8: e0455006 sub r5, r5, r6

d5ec: e025500e eor r5, r5, lr

d5f0: e5815004 str r5, [r1, #4]

d5f4: e590e008 ldr lr, [r0, #8]

d5f8: e5902018 ldr r2, [r0, #24]

d5fc: e20ee01f and lr, lr, #31

d600: e26ee020 rsb lr, lr, #32

d604: e0422005 sub r2, r2, r5

d608: e1a02e72 ror r2, r2, lr

d60c: e1a06822 lsr r6, r2, #16

d610: e1a0e422 lsr lr, r2, #8

d614: e6ef6076 uxtb r6, r6

d618: e1a07c22 lsr r7, r2, #24

d61c: e2866c01 add r6, r6, #256 ; 0x100

d620: e6efe07e uxtb lr, lr

d624: e28eec02 add lr, lr, #512 ; 0x200

d628: e6ef2072 uxtb r2, r2

d62c: e7936106 ldr r6, [r3, r6, lsl #2]

d630: e7939107 ldr r9, [r3, r7, lsl #2]

d634: e2822c03 add r2, r2, #768 ; 0x300

d638: e793e10e ldr lr, [r3, lr, lsl #2]

d63c: e7937102 ldr r7, [r3, r2, lsl #2]

d640: e0899006 add r9, r9, r6

d644: e029900e eor r9, r9, lr

d648: e0499007 sub r9, r9, r7

d64c: e029900c eor r9, r9, ip

d650: e5819000 str r9, [r1]

d654: e590c00c ldr ip, [r0, #12]

d658: e590201c ldr r2, [r0, #28]

d65c: e20cc01f and ip, ip, #31

d660: e26cc020 rsb ip, ip, #32

d664: e0892002 add r2, r9, r2

d668: e1a02c72 ror r2, r2, ip

d66c: e1a0c822 lsr ip, r2, #16

d670: e1a0e422 lsr lr, r2, #8

d674: e6efc07c uxtb ip, ip

d678: e1a07c22 lsr r7, r2, #24

d67c: e28ccc01 add ip, ip, #256 ; 0x100

d680: e6efe07e uxtb lr, lr

d684: e28eec02 add lr, lr, #512 ; 0x200

d688: e6ef2072 uxtb r2, r2

d68c: e793610c ldr r6, [r3, ip, lsl #2]

d690: e793a107 ldr sl, [r3, r7, lsl #2]

d694: e2822c03 add r2, r2, #768 ; 0x300

d698: e793c10e ldr ip, [r3, lr, lsl #2]

d69c: e02aa006 eor sl, sl, r6

d6a0: e7936102 ldr r6, [r3, r2, lsl #2]

d6a4: e04aa00c sub sl, sl, ip

d6a8: e08aa006 add sl, sl, r6

d6ac: e02a6008 eor r6, sl, r8

d6b0: e581600c str r6, [r1, #12]

d6b4: e5902020 ldr r2, [r0, #32]

d6b8: e590c030 ldr ip, [r0, #48] ; 0x30

d6bc: e202201f and r2, r2, #31

d6c0: e086c00c add ip, r6, ip

d6c4: e2622020 rsb r2, r2, #32

d6c8: e1a0227c ror r2, ip, r2

d6cc: e1a0e822 lsr lr, r2, #16

d6d0: e1a0c422 lsr ip, r2, #8

d6d4: e6efe07e uxtb lr, lr

d6d8: e1a08c22 lsr r8, r2, #24

d6dc: e28e7c01 add r7, lr, #256 ; 0x100

d6e0: e6efc07c uxtb ip, ip

d6e4: e28ccc02 add ip, ip, #512 ; 0x200

d6e8: e6ef2072 uxtb r2, r2

d6ec: e793b107 ldr fp, [r3, r7, lsl #2]

d6f0: e793e108 ldr lr, [r3, r8, lsl #2]

d6f4: e2822c03 add r2, r2, #768 ; 0x300

d6f8: e793c10c ldr ip, [r3, ip, lsl #2]

d6fc: e02ee00b eor lr, lr, fp

d700: e793b102 ldr fp, [r3, r2, lsl #2]

d704: e04ee00c sub lr, lr, ip

d708: e08ee00b add lr, lr, fp

d70c: e02eb004 eor fp, lr, r4

d710: e581b008 str fp, [r1, #8]

d714: e590c024 ldr ip, [r0, #36] ; 0x24

d718: e5902034 ldr r2, [r0, #52] ; 0x34

d71c: e20cc01f and ip, ip, #31

d720: e26cc020 rsb ip, ip, #32

d724: e022200b eor r2, r2, fp

d728: e1a02c72 ror r2, r2, ip

d72c: e1a0e422 lsr lr, r2, #8

d730: e1a0c822 lsr ip, r2, #16

d734: e6efe07e uxtb lr, lr

d738: e6ef7072 uxtb r7, r2

d73c: e28eec02 add lr, lr, #512 ; 0x200

d740: e1a02c22 lsr r2, r2, #24

d744: e6efc07c uxtb ip, ip

d748: e2877c03 add r7, r7, #768 ; 0x300

d74c: e28ccc01 add ip, ip, #256 ; 0x100

d750: e7938102 ldr r8, [r3, r2, lsl #2]

d754: e793410e ldr r4, [r3, lr, lsl #2]

d758: e793210c ldr r2, [r3, ip, lsl #2]

d75c: e793e107 ldr lr, [r3, r7, lsl #2]

d760: e0844008 add r4, r4, r8

d764: e0444002 sub r4, r4, r2

d768: e025500e eor r5, r5, lr

d76c: e0255004 eor r5, r5, r4

d770: e5815004 str r5, [r1, #4]

d774: e590c028 ldr ip, [r0, #40] ; 0x28

d778: e5902038 ldr r2, [r0, #56] ; 0x38

d77c: e20cc01f and ip, ip, #31

d780: e26cc020 rsb ip, ip, #32

d784: e0422005 sub r2, r2, r5

d788: e1a02c72 ror r2, r2, ip

d78c: e1a0e822 lsr lr, r2, #16

d790: e1a0c422 lsr ip, r2, #8

d794: e6efe07e uxtb lr, lr

d798: e1a04c22 lsr r4, r2, #24

d79c: e28eec01 add lr, lr, #256 ; 0x100

d7a0: e6efc07c uxtb ip, ip

d7a4: e28ccc02 add ip, ip, #512 ; 0x200

d7a8: e6ef2072 uxtb r2, r2

d7ac: e7937104 ldr r7, [r3, r4, lsl #2]

d7b0: e793e10e ldr lr, [r3, lr, lsl #2]

d7b4: e2822c03 add r2, r2, #768 ; 0x300

d7b8: e793c10c ldr ip, [r3, ip, lsl #2]

d7bc: e7932102 ldr r2, [r3, r2, lsl #2]

d7c0: e087700e add r7, r7, lr

d7c4: e027700c eor r7, r7, ip

d7c8: e0477002 sub r7, r7, r2

d7cc: e0299007 eor r9, r9, r7

d7d0: e5819000 str r9, [r1]

d7d4: e590c02c ldr ip, [r0, #44] ; 0x2c

d7d8: e590203c ldr r2, [r0, #60] ; 0x3c

d7dc: e20cc01f and ip, ip, #31

d7e0: e26cc020 rsb ip, ip, #32

d7e4: e0892002 add r2, r9, r2

d7e8: e1a02c72 ror r2, r2, ip

d7ec: e1a0e822 lsr lr, r2, #16

d7f0: e1a0c422 lsr ip, r2, #8

d7f4: e6efe07e uxtb lr, lr

d7f8: e1a04c22 lsr r4, r2, #24

d7fc: e28eec01 add lr, lr, #256 ; 0x100

d800: e6efc07c uxtb ip, ip

d804: e28ccc02 add ip, ip, #512 ; 0x200

d808: e6ef2072 uxtb r2, r2

d80c: e793a10e ldr sl, [r3, lr, lsl #2]

d810: e7938104 ldr r8, [r3, r4, lsl #2]

d814: e2822c03 add r2, r2, #768 ; 0x300

d818: e793c10c ldr ip, [r3, ip, lsl #2]

d81c: e028800a eor r8, r8, sl

d820: e793a102 ldr sl, [r3, r2, lsl #2]

d824: e048800c sub r8, r8, ip

d828: e088800a add r8, r8, sl

d82c: e028a006 eor sl, r8, r6

d830: e581a00c str sl, [r1, #12]

d834: e590c040 ldr ip, [r0, #64] ; 0x40

d838: e5902050 ldr r2, [r0, #80] ; 0x50

d83c: e20cc01f and ip, ip, #31

d840: e26cc020 rsb ip, ip, #32

d844: e08a2002 add r2, sl, r2

d848: e1a02c72 ror r2, r2, ip

d84c: e1a0e822 lsr lr, r2, #16

d850: e1a0c422 lsr ip, r2, #8

d854: e6efe07e uxtb lr, lr

d858: e1a04c22 lsr r4, r2, #24

d85c: e6efc07c uxtb ip, ip

d860: e28eec01 add lr, lr, #256 ; 0x100

d864: e28c6c02 add r6, ip, #512 ; 0x200

d868: e6ef2072 uxtb r2, r2

d86c: e793c104 ldr ip, [r3, r4, lsl #2]

d870: e2822c03 add r2, r2, #768 ; 0x300

d874: e793e10e ldr lr, [r3, lr, lsl #2]

d878: e7934106 ldr r4, [r3, r6, lsl #2]

d87c: e02cc00e eor ip, ip, lr

d880: e793e102 ldr lr, [r3, r2, lsl #2]

d884: e04cc004 sub ip, ip, r4

d888: e08cc00e add ip, ip, lr

d88c: e02ce00b eor lr, ip, fp

d890: e581e008 str lr, [r1, #8]

d894: e590c044 ldr ip, [r0, #68] ; 0x44

d898: e5902054 ldr r2, [r0, #84] ; 0x54

d89c: e20cc01f and ip, ip, #31

d8a0: e26cc020 rsb ip, ip, #32

d8a4: e022200e eor r2, r2, lr

d8a8: e1a02c72 ror r2, r2, ip

d8ac: e1a04422 lsr r4, r2, #8

d8b0: e1a0c822 lsr ip, r2, #16

d8b4: e6ef4074 uxtb r4, r4

d8b8: e6ef6072 uxtb r6, r2

d8bc: e2844c02 add r4, r4, #512 ; 0x200

d8c0: e1a02c22 lsr r2, r2, #24

d8c4: e6efc07c uxtb ip, ip

d8c8: e28ccc01 add ip, ip, #256 ; 0x100

d8cc: e2866c03 add r6, r6, #768 ; 0x300

d8d0: e7937102 ldr r7, [r3, r2, lsl #2]

d8d4: e7934104 ldr r4, [r3, r4, lsl #2]

d8d8: e793210c ldr r2, [r3, ip, lsl #2]

d8dc: e7936106 ldr r6, [r3, r6, lsl #2]

d8e0: e0844007 add r4, r4, r7

d8e4: e0255006 eor r5, r5, r6

d8e8: e0444002 sub r4, r4, r2

d8ec: e0244005 eor r4, r4, r5

d8f0: e5814004 str r4, [r1, #4]

d8f4: e590c048 ldr ip, [r0, #72] ; 0x48

d8f8: e5902058 ldr r2, [r0, #88] ; 0x58

d8fc: e20cc01f and ip, ip, #31

d900: e26cc020 rsb ip, ip, #32

d904: e0422004 sub r2, r2, r4

d908: e1a02c72 ror r2, r2, ip

d90c: e1a05822 lsr r5, r2, #16

d910: e1a0c422 lsr ip, r2, #8

d914: e6ef5075 uxtb r5, r5

d918: e1a06c22 lsr r6, r2, #24

d91c: e2855c01 add r5, r5, #256 ; 0x100

d920: e6efc07c uxtb ip, ip

d924: e28ccc02 add ip, ip, #512 ; 0x200

d928: e6ef2072 uxtb r2, r2

d92c: e7937106 ldr r7, [r3, r6, lsl #2]

d930: e7935105 ldr r5, [r3, r5, lsl #2]

d934: e2822c03 add r2, r2, #768 ; 0x300

d938: e793c10c ldr ip, [r3, ip, lsl #2]

d93c: e7932102 ldr r2, [r3, r2, lsl #2]

d940: e0877005 add r7, r7, r5

d944: e027700c eor r7, r7, ip

d948: e0477002 sub r7, r7, r2

d94c: e0277009 eor r7, r7, r9

d950: e5817000 str r7, [r1]

d954: e590c04c ldr ip, [r0, #76] ; 0x4c

d958: e590205c ldr r2, [r0, #92] ; 0x5c

d95c: e20cc01f and ip, ip, #31

d960: e26cc020 rsb ip, ip, #32

d964: e0872002 add r2, r7, r2

d968: e1a02c72 ror r2, r2, ip

d96c: e1a05822 lsr r5, r2, #16

d970: e1a0c422 lsr ip, r2, #8

d974: e6ef5075 uxtb r5, r5

d978: e1a06c22 lsr r6, r2, #24

d97c: e2855c01 add r5, r5, #256 ; 0x100

d980: e6efc07c uxtb ip, ip

d984: e28ccc02 add ip, ip, #512 ; 0x200

d988: e6ef2072 uxtb r2, r2

d98c: e7938105 ldr r8, [r3, r5, lsl #2]

d990: e7936106 ldr r6, [r3, r6, lsl #2]

d994: e2822c03 add r2, r2, #768 ; 0x300

d998: e793c10c ldr ip, [r3, ip, lsl #2]

d99c: e0266008 eor r6, r6, r8

d9a0: e7938102 ldr r8, [r3, r2, lsl #2]

d9a4: e046600c sub r6, r6, ip

d9a8: e0866008 add r6, r6, r8

d9ac: e026800a eor r8, r6, sl

d9b0: e581800c str r8, [r1, #12]

d9b4: e590c060 ldr ip, [r0, #96] ; 0x60

d9b8: e5902070 ldr r2, [r0, #112] ; 0x70

d9bc: e20cc01f and ip, ip, #31

d9c0: e26cc020 rsb ip, ip, #32

d9c4: e0882002 add r2, r8, r2

d9c8: e1a02c72 ror r2, r2, ip

d9cc: e1a05822 lsr r5, r2, #16

d9d0: e1a0c422 lsr ip, r2, #8

d9d4: e6ef5075 uxtb r5, r5

d9d8: e1a06c22 lsr r6, r2, #24

d9dc: e2855c01 add r5, r5, #256 ; 0x100

d9e0: e6efc07c uxtb ip, ip

d9e4: e28ccc02 add ip, ip, #512 ; 0x200

d9e8: e6ef2072 uxtb r2, r2

d9ec: e793b106 ldr fp, [r3, r6, lsl #2]

d9f0: e2822c03 add r2, r2, #768 ; 0x300

d9f4: e7936105 ldr r6, [r3, r5, lsl #2]

d9f8: e793510c ldr r5, [r3, ip, lsl #2]

d9fc: e02bb006 eor fp, fp, r6

da00: e793c102 ldr ip, [r3, r2, lsl #2]

da04: e04bb005 sub fp, fp, r5

da08: e08bb00c add fp, fp, ip

da0c: e02bc00e eor ip, fp, lr

da10: e581c008 str ip, [r1, #8]

da14: e590e064 ldr lr, [r0, #100] ; 0x64

da18: e5902074 ldr r2, [r0, #116] ; 0x74

da1c: e20ee01f and lr, lr, #31

da20: e022200c eor r2, r2, ip

da24: e26ee020 rsb lr, lr, #32

da28: e1a02e72 ror r2, r2, lr

da2c: e1a05422 lsr r5, r2, #8

da30: e1a0e822 lsr lr, r2, #16

da34: e6ef5075 uxtb r5, r5

da38: e6ef6072 uxtb r6, r2

da3c: e2855c02 add r5, r5, #512 ; 0x200

da40: e1a02c22 lsr r2, r2, #24

da44: e6efe07e uxtb lr, lr

da48: e28eec01 add lr, lr, #256 ; 0x100

da4c: e2866c03 add r6, r6, #768 ; 0x300

da50: e7939102 ldr r9, [r3, r2, lsl #2]

da54: e7935105 ldr r5, [r3, r5, lsl #2]

da58: e793210e ldr r2, [r3, lr, lsl #2]

da5c: e7936106 ldr r6, [r3, r6, lsl #2]

da60: e0855009 add r5, r5, r9

da64: e0244006 eor r4, r4, r6

da68: e0455002 sub r5, r5, r2

da6c: e0255004 eor r5, r5, r4

da70: e5815004 str r5, [r1, #4]

da74: e590e068 ldr lr, [r0, #104] ; 0x68

da78: e5902078 ldr r2, [r0, #120] ; 0x78

da7c: e20ee01f and lr, lr, #31

da80: e26ee020 rsb lr, lr, #32

da84: e0422005 sub r2, r2, r5

da88: e1a02e72 ror r2, r2, lr

da8c: e1a04822 lsr r4, r2, #16

da90: e1a0e422 lsr lr, r2, #8

da94: e6ef4074 uxtb r4, r4

da98: e1a06c22 lsr r6, r2, #24

da9c: e2844c01 add r4, r4, #256 ; 0x100

daa0: e6efe07e uxtb lr, lr

daa4: e28eec02 add lr, lr, #512 ; 0x200

daa8: e6ef2072 uxtb r2, r2

daac: e7939106 ldr r9, [r3, r6, lsl #2]

dab0: e7934104 ldr r4, [r3, r4, lsl #2]

dab4: e2822c03 add r2, r2, #768 ; 0x300

dab8: e793e10e ldr lr, [r3, lr, lsl #2]

dabc: e7932102 ldr r2, [r3, r2, lsl #2]

dac0: e0899004 add r9, r9, r4

dac4: e029900e eor r9, r9, lr

dac8: e0499002 sub r9, r9, r2

dacc: e0277009 eor r7, r7, r9

dad0: e5817000 str r7, [r1]

dad4: e590e06c ldr lr, [r0, #108] ; 0x6c

dad8: e590207c ldr r2, [r0, #124] ; 0x7c

dadc: e20ee01f and lr, lr, #31

dae0: e26ee020 rsb lr, lr, #32

dae4: e0872002 add r2, r7, r2

dae8: e1a02e72 ror r2, r2, lr

daec: e1a04822 lsr r4, r2, #16

daf0: e1a0e422 lsr lr, r2, #8

daf4: e6ef4074 uxtb r4, r4

daf8: e1a06c22 lsr r6, r2, #24

dafc: e2844c01 add r4, r4, #256 ; 0x100

db00: e6efe07e uxtb lr, lr

db04: e28eec02 add lr, lr, #512 ; 0x200

db08: e6ef2072 uxtb r2, r2

db0c: e793a106 ldr sl, [r3, r6, lsl #2]

db10: e2822c03 add r2, r2, #768 ; 0x300

db14: e7936104 ldr r6, [r3, r4, lsl #2]

db18: e793e10e ldr lr, [r3, lr, lsl #2]

db1c: e02aa006 eor sl, sl, r6

db20: e7936102 ldr r6, [r3, r2, lsl #2]

db24: e04aa00e sub sl, sl, lr

db28: e08aa006 add sl, sl, r6

db2c: e02a6008 eor r6, sl, r8

db30: e581600c str r6, [r1, #12]

db34: e590e080 ldr lr, [r0, #128] ; 0x80

db38: e5902090 ldr r2, [r0, #144] ; 0x90

db3c: e20ee01f and lr, lr, #31

db40: e26ee020 rsb lr, lr, #32

db44: e0862002 add r2, r6, r2

db48: e1a02e72 ror r2, r2, lr

db4c: e1a04822 lsr r4, r2, #16

db50: e1a0e422 lsr lr, r2, #8

db54: e6ef4074 uxtb r4, r4

db58: e1a08c22 lsr r8, r2, #24

db5c: e2844c01 add r4, r4, #256 ; 0x100

db60: e6efe07e uxtb lr, lr

db64: e28e9c02 add r9, lr, #512 ; 0x200

db68: e6ef2072 uxtb r2, r2

db6c: e793b104 ldr fp, [r3, r4, lsl #2]

db70: e793e108 ldr lr, [r3, r8, lsl #2]

db74: e2822c03 add r2, r2, #768 ; 0x300

db78: e7938109 ldr r8, [r3, r9, lsl #2]

db7c: e02ee00b eor lr, lr, fp

db80: e793b102 ldr fp, [r3, r2, lsl #2]

db84: e04ee008 sub lr, lr, r8

db88: e08ee00b add lr, lr, fp

db8c: e02eb00c eor fp, lr, ip

db90: e581b008 str fp, [r1, #8]

db94: e5902084 ldr r2, [r0, #132] ; 0x84

db98: e590c094 ldr ip, [r0, #148] ; 0x94

db9c: e202201f and r2, r2, #31

dba0: e2622020 rsb r2, r2, #32

dba4: e02cc00b eor ip, ip, fp

dba8: e1a0c27c ror ip, ip, r2

dbac: e1a0e42c lsr lr, ip, #8

dbb0: e1a0282c lsr r2, ip, #16

dbb4: e6efe07e uxtb lr, lr

dbb8: e6ef807c uxtb r8, ip

dbbc: e28eec02 add lr, lr, #512 ; 0x200

dbc0: e1a0cc2c lsr ip, ip, #24

dbc4: e6ef2072 uxtb r2, r2

dbc8: e2888c03 add r8, r8, #768 ; 0x300

dbcc: e2822c01 add r2, r2, #256 ; 0x100

dbd0: e793410e ldr r4, [r3, lr, lsl #2]

dbd4: e793e10c ldr lr, [r3, ip, lsl #2]

dbd8: e7932102 ldr r2, [r3, r2, lsl #2]

dbdc: e793c108 ldr ip, [r3, r8, lsl #2]

dbe0: e084400e add r4, r4, lr

dbe4: e025500c eor r5, r5, ip

dbe8: e0444002 sub r4, r4, r2

dbec: e0244005 eor r4, r4, r5

dbf0: e5814004 str r4, [r1, #4]

dbf4: e5902088 ldr r2, [r0, #136] ; 0x88

dbf8: e590c098 ldr ip, [r0, #152] ; 0x98

dbfc: e202201f and r2, r2, #31

dc00: e2622020 rsb r2, r2, #32

dc04: e04cc004 sub ip, ip, r4

dc08: e1a0c27c ror ip, ip, r2

dc0c: e1a0e82c lsr lr, ip, #16

dc10: e1a0242c lsr r2, ip, #8

dc14: e6efe07e uxtb lr, lr

dc18: e1a05c2c lsr r5, ip, #24

dc1c: e28eec01 add lr, lr, #256 ; 0x100

dc20: e6ef2072 uxtb r2, r2

dc24: e2828c02 add r8, r2, #512 ; 0x200

dc28: e6efc07c uxtb ip, ip

dc2c: e793910e ldr r9, [r3, lr, lsl #2]

dc30: e7932105 ldr r2, [r3, r5, lsl #2]

dc34: e28ccc03 add ip, ip, #768 ; 0x300

dc38: e793e108 ldr lr, [r3, r8, lsl #2]

dc3c: e0822009 add r2, r2, r9

dc40: e793910c ldr r9, [r3, ip, lsl #2]

dc44: e022200e eor r2, r2, lr

dc48: e0422009 sub r2, r2, r9

dc4c: e0229007 eor r9, r2, r7

dc50: e5819000 str r9, [r1]

dc54: e590208c ldr r2, [r0, #140] ; 0x8c

dc58: e590c09c ldr ip, [r0, #156] ; 0x9c

dc5c: e202201f and r2, r2, #31

dc60: e2622020 rsb r2, r2, #32

dc64: e089c00c add ip, r9, ip

dc68: e1a0c27c ror ip, ip, r2

dc6c: e1a0e82c lsr lr, ip, #16

dc70: e1a0242c lsr r2, ip, #8

dc74: e6efe07e uxtb lr, lr

dc78: e1a05c2c lsr r5, ip, #24

dc7c: e28eec01 add lr, lr, #256 ; 0x100

dc80: e6ef2072 uxtb r2, r2

dc84: e2822c02 add r2, r2, #512 ; 0x200

dc88: e6efc07c uxtb ip, ip

dc8c: e793a105 ldr sl, [r3, r5, lsl #2]

dc90: e28ccc03 add ip, ip, #768 ; 0x300

dc94: e793510e ldr r5, [r3, lr, lsl #2]

dc98: e793e102 ldr lr, [r3, r2, lsl #2]

dc9c: e02aa005 eor sl, sl, r5

dca0: e793210c ldr r2, [r3, ip, lsl #2]

dca4: e04aa00e sub sl, sl, lr

dca8: e08aa002 add sl, sl, r2

dcac: e02aa006 eor sl, sl, r6

dcb0: e581a00c str sl, [r1, #12]

dcb4: e59020a0 ldr r2, [r0, #160] ; 0xa0

dcb8: e590c0b0 ldr ip, [r0, #176] ; 0xb0

dcbc: e202201f and r2, r2, #31

dcc0: e2622020 rsb r2, r2, #32

dcc4: e08ac00c add ip, sl, ip

dcc8: e1a0c27c ror ip, ip, r2

dccc: e1a0e82c lsr lr, ip, #16

dcd0: e1a0242c lsr r2, ip, #8

dcd4: e6efe07e uxtb lr, lr

dcd8: e1a05c2c lsr r5, ip, #24

dcdc: e28eec01 add lr, lr, #256 ; 0x100

dce0: e6ef2072 uxtb r2, r2

dce4: e2822c02 add r2, r2, #512 ; 0x200

dce8: e6efc07c uxtb ip, ip

dcec: e7938105 ldr r8, [r3, r5, lsl #2]

dcf0: e793e10e ldr lr, [r3, lr, lsl #2]

dcf4: e28ccc03 add ip, ip, #768 ; 0x300

dcf8: e7935102 ldr r5, [r3, r2, lsl #2]

dcfc: e028800e eor r8, r8, lr

dd00: e793e10c ldr lr, [r3, ip, lsl #2]

dd04: e0488005 sub r8, r8, r5

dd08: e088800e add r8, r8, lr

dd0c: e028e00b eor lr, r8, fp

dd10: e581e008 str lr, [r1, #8]

dd14: e59020a4 ldr r2, [r0, #164] ; 0xa4

dd18: e590c0b4 ldr ip, [r0, #180] ; 0xb4

dd1c: e202201f and r2, r2, #31

dd20: e2622020 rsb r2, r2, #32

dd24: e02cc00e eor ip, ip, lr

dd28: e1a0c27c ror ip, ip, r2

dd2c: e1a0242c lsr r2, ip, #8

dd30: e1a0682c lsr r6, ip, #16

dd34: e6ef2072 uxtb r2, r2

dd38: e6ef707c uxtb r7, ip

dd3c: e2822c02 add r2, r2, #512 ; 0x200

dd40: e1a0cc2c lsr ip, ip, #24

dd44: e6ef6076 uxtb r6, r6

dd48: e2877c03 add r7, r7, #768 ; 0x300

dd4c: e2866c01 add r6, r6, #256 ; 0x100

dd50: e793810c ldr r8, [r3, ip, lsl #2]

dd54: e7932102 ldr r2, [r3, r2, lsl #2]

dd58: e7935106 ldr r5, [r3, r6, lsl #2]

dd5c: e793c107 ldr ip, [r3, r7, lsl #2]

dd60: e0822008 add r2, r2, r8

dd64: e024400c eor r4, r4, ip

dd68: e0422005 sub r2, r2, r5

dd6c: e0225004 eor r5, r2, r4

dd70: e5815004 str r5, [r1, #4]

dd74: e59020a8 ldr r2, [r0, #168] ; 0xa8

dd78: e590c0b8 ldr ip, [r0, #184] ; 0xb8

dd7c: e202201f and r2, r2, #31

dd80: e2622020 rsb r2, r2, #32

dd84: e04cc005 sub ip, ip, r5

dd88: e1a0c27c ror ip, ip, r2

dd8c: e1a0482c lsr r4, ip, #16

dd90: e1a0242c lsr r2, ip, #8

dd94: e6ef4074 uxtb r4, r4

dd98: e1a06c2c lsr r6, ip, #24

dd9c: e2844c01 add r4, r4, #256 ; 0x100

dda0: e6ef2072 uxtb r2, r2

dda4: e2822c02 add r2, r2, #512 ; 0x200

dda8: e6efc07c uxtb ip, ip

ddac: e7937106 ldr r7, [r3, r6, lsl #2]

ddb0: e7934104 ldr r4, [r3, r4, lsl #2]

ddb4: e28ccc03 add ip, ip, #768 ; 0x300

ddb8: e7936102 ldr r6, [r3, r2, lsl #2]

ddbc: e0877004 add r7, r7, r4

ddc0: e793210c ldr r2, [r3, ip, lsl #2]

ddc4: e0277006 eor r7, r7, r6

ddc8: e0477002 sub r7, r7, r2

ddcc: e0272009 eor r2, r7, r9

ddd0: e5812000 str r2, [r1]

ddd4: e59040ac ldr r4, [r0, #172] ; 0xac

ddd8: e590c0bc ldr ip, [r0, #188] ; 0xbc

dddc: e204401f and r4, r4, #31

dde0: e2644020 rsb r4, r4, #32

dde4: e082c00c add ip, r2, ip

dde8: e1a0c47c ror ip, ip, r4

ddec: e1a0682c lsr r6, ip, #16

ddf0: e1a0442c lsr r4, ip, #8

ddf4: e6ef6076 uxtb r6, r6

ddf8: e1a07c2c lsr r7, ip, #24

ddfc: e2866c01 add r6, r6, #256 ; 0x100

de00: e6ef4074 uxtb r4, r4

de04: e2848c02 add r8, r4, #512 ; 0x200

de08: e6efc07c uxtb ip, ip

de0c: e7934107 ldr r4, [r3, r7, lsl #2]

de10: e28ccc03 add ip, ip, #768 ; 0x300

de14: e7937106 ldr r7, [r3, r6, lsl #2]

de18: e7936108 ldr r6, [r3, r8, lsl #2]

de1c: e793c10c ldr ip, [r3, ip, lsl #2]

de20: e0244007 eor r4, r4, r7

de24: e0444006 sub r4, r4, r6

de28: e084400c add r4, r4, ip

de2c: e02aa004 eor sl, sl, r4

de30: e581a00c str sl, [r1, #12]

de34: e59040cc ldr r4, [r0, #204] ; 0xcc

de38: e590c0dc ldr ip, [r0, #220] ; 0xdc

de3c: e204401f and r4, r4, #31

de40: e2644020 rsb r4, r4, #32

de44: e082c00c add ip, r2, ip

de48: e1a0c47c ror ip, ip, r4

de4c: e1a0682c lsr r6, ip, #16

de50: e1a0442c lsr r4, ip, #8

de54: e6ef6076 uxtb r6, r6

de58: e1a07c2c lsr r7, ip, #24

de5c: e2866c01 add r6, r6, #256 ; 0x100

de60: e6ef4074 uxtb r4, r4

de64: e2848c02 add r8, r4, #512 ; 0x200

de68: e6efc07c uxtb ip, ip

de6c: e7934107 ldr r4, [r3, r7, lsl #2]

de70: e28ccc03 add ip, ip, #768 ; 0x300

de74: e7937106 ldr r7, [r3, r6, lsl #2]

de78: e7936108 ldr r6, [r3, r8, lsl #2]

de7c: e793c10c ldr ip, [r3, ip, lsl #2]

de80: e0244007 eor r4, r4, r7

de84: e0444006 sub r4, r4, r6

de88: e084400c add r4, r4, ip

de8c: e02aa004 eor sl, sl, r4

de90: e581a00c str sl, [r1, #12]

de94: e59040c8 ldr r4, [r0, #200] ; 0xc8

de98: e590c0d8 ldr ip, [r0, #216] ; 0xd8

de9c: e204401f and r4, r4, #31

dea0: e2644020 rsb r4, r4, #32

dea4: e04cc005 sub ip, ip, r5

dea8: e1a0c47c ror ip, ip, r4

deac: e1a0682c lsr r6, ip, #16

deb0: e1a0442c lsr r4, ip, #8

deb4: e6ef6076 uxtb r6, r6

deb8: e1a08c2c lsr r8, ip, #24

debc: e2867c01 add r7, r6, #256 ; 0x100

dec0: e6ef4074 uxtb r4, r4

dec4: e2844c02 add r4, r4, #512 ; 0x200

dec8: e6efc07c uxtb ip, ip

decc: e7936108 ldr r6, [r3, r8, lsl #2]

ded0: e7937107 ldr r7, [r3, r7, lsl #2]

ded4: e28ccc03 add ip, ip, #768 ; 0x300

ded8: e7939104 ldr r9, [r3, r4, lsl #2]

dedc: e0866007 add r6, r6, r7

dee0: e793710c ldr r7, [r3, ip, lsl #2]

dee4: e0266009 eor r6, r6, r9

dee8: e0466007 sub r6, r6, r7

deec: e0267002 eor r7, r6, r2

def0: e5817000 str r7, [r1]

def4: e59020c4 ldr r2, [r0, #196] ; 0xc4

def8: e590c0d4 ldr ip, [r0, #212] ; 0xd4

defc: e202201f and r2, r2, #31

df00: e2622020 rsb r2, r2, #32

df04: e02cc00e eor ip, ip, lr

df08: e1a0c27c ror ip, ip, r2

df0c: e1a0242c lsr r2, ip, #8

df10: e1a0482c lsr r4, ip, #16

df14: e6ef2072 uxtb r2, r2

df18: e1a06c2c lsr r6, ip, #24

df1c: e2822c02 add r2, r2, #512 ; 0x200

df20: e6ef4074 uxtb r4, r4

df24: e2844c01 add r4, r4, #256 ; 0x100

df28: e6efc07c uxtb ip, ip

df2c: e7936106 ldr r6, [r3, r6, lsl #2]

df30: e7932102 ldr r2, [r3, r2, lsl #2]

df34: e28ccc03 add ip, ip, #768 ; 0x300

df38: e7934104 ldr r4, [r3, r4, lsl #2]

df3c: e0822006 add r2, r2, r6

df40: e793c10c ldr ip, [r3, ip, lsl #2]

df44: e0422004 sub r2, r2, r4

df48: e022200c eor r2, r2, ip

df4c: e0222005 eor r2, r2, r5

df50: e5812004 str r2, [r1, #4]

df54: e590c0c0 ldr ip, [r0, #192] ; 0xc0

df58: e59040d0 ldr r4, [r0, #208] ; 0xd0

df5c: e20cc01f and ip, ip, #31

df60: e08a4004 add r4, sl, r4

df64: e26cc020 rsb ip, ip, #32

df68: e1a0cc74 ror ip, r4, ip

df6c: e1a0582c lsr r5, ip, #16

df70: e1a0442c lsr r4, ip, #8

df74: e6ef5075 uxtb r5, r5

df78: e1a08c2c lsr r8, ip, #24

df7c: e2856c01 add r6, r5, #256 ; 0x100

df80: e6ef4074 uxtb r4, r4

df84: e2844c02 add r4, r4, #512 ; 0x200

df88: e6efc07c uxtb ip, ip

df8c: e7935108 ldr r5, [r3, r8, lsl #2]

df90: e28ccc03 add ip, ip, #768 ; 0x300

df94: e7938106 ldr r8, [r3, r6, lsl #2]

df98: e793b104 ldr fp, [r3, r4, lsl #2]

df9c: e0255008 eor r5, r5, r8

dfa0: e793810c ldr r8, [r3, ip, lsl #2]

dfa4: e045500b sub r5, r5, fp

dfa8: e0855008 add r5, r5, r8

dfac: e025800e eor r8, r5, lr

dfb0: e5818008 str r8, [r1, #8]

dfb4: e590c0ec ldr ip, [r0, #236] ; 0xec

dfb8: e590e0fc ldr lr, [r0, #252] ; 0xfc

dfbc: e20cc01f and ip, ip, #31

dfc0: e087e00e add lr, r7, lr

dfc4: e26cc020 rsb ip, ip, #32

dfc8: e1a0cc7e ror ip, lr, ip

dfcc: e1a0482c lsr r4, ip, #16

dfd0: e1a0e42c lsr lr, ip, #8

dfd4: e6ef4074 uxtb r4, r4

dfd8: e1a05c2c lsr r5, ip, #24

dfdc: e6efe07e uxtb lr, lr

dfe0: e2844c01 add r4, r4, #256 ; 0x100

dfe4: e28e6c02 add r6, lr, #512 ; 0x200

dfe8: e6efe07c uxtb lr, ip

dfec: e7934104 ldr r4, [r3, r4, lsl #2]

dff0: e793c105 ldr ip, [r3, r5, lsl #2]

dff4: e28eec03 add lr, lr, #768 ; 0x300

dff8: e7935106 ldr r5, [r3, r6, lsl #2]

dffc: e02cc004 eor ip, ip, r4

e000: e793410e ldr r4, [r3, lr, lsl #2]

e004: e04cc005 sub ip, ip, r5

e008: e08cc004 add ip, ip, r4

e00c: e02c400a eor r4, ip, sl

e010: e581400c str r4, [r1, #12]

e014: e590c0e8 ldr ip, [r0, #232] ; 0xe8

e018: e590e0f8 ldr lr, [r0, #248] ; 0xf8

e01c: e20cc01f and ip, ip, #31

e020: e04ee002 sub lr, lr, r2

e024: e26cc020 rsb ip, ip, #32

e028: e1a0cc7e ror ip, lr, ip

e02c: e1a0582c lsr r5, ip, #16

e030: e1a0e42c lsr lr, ip, #8

e034: e6ef5075 uxtb r5, r5

e038: e1a06c2c lsr r6, ip, #24

e03c: e2855c01 add r5, r5, #256 ; 0x100

e040: e6efe07e uxtb lr, lr

e044: e28eec02 add lr, lr, #512 ; 0x200

e048: e6efc07c uxtb ip, ip

e04c: e7939106 ldr r9, [r3, r6, lsl #2]

e050: e28ccc03 add ip, ip, #768 ; 0x300

e054: e7936105 ldr r6, [r3, r5, lsl #2]

e058: e793e10e ldr lr, [r3, lr, lsl #2]

e05c: e0899006 add r9, r9, r6

e060: e793610c ldr r6, [r3, ip, lsl #2]

e064: e029900e eor r9, r9, lr

e068: e0499006 sub r9, r9, r6

e06c: e0296007 eor r6, r9, r7

e070: e5816000 str r6, [r1]

e074: e590e0e4 ldr lr, [r0, #228] ; 0xe4

e078: e590c0f4 ldr ip, [r0, #244] ; 0xf4

e07c: e20ee01f and lr, lr, #31

e080: e26ee020 rsb lr, lr, #32

e084: e02cc008 eor ip, ip, r8

e088: e1a0ce7c ror ip, ip, lr

e08c: e1a0e42c lsr lr, ip, #8

e090: e1a0582c lsr r5, ip, #16

e094: e6efe07e uxtb lr, lr

e098: e6ef707c uxtb r7, ip

e09c: e28eec02 add lr, lr, #512 ; 0x200

e0a0: e1a0cc2c lsr ip, ip, #24

e0a4: e6ef5075 uxtb r5, r5

e0a8: e2855c01 add r5, r5, #256 ; 0x100

e0ac: e2877c03 add r7, r7, #768 ; 0x300

e0b0: e793910c ldr r9, [r3, ip, lsl #2]

e0b4: e793e10e ldr lr, [r3, lr, lsl #2]

e0b8: e793c105 ldr ip, [r3, r5, lsl #2]

e0bc: e7937107 ldr r7, [r3, r7, lsl #2]

e0c0: e08ee009 add lr, lr, r9

e0c4: e04ee00c sub lr, lr, ip

e0c8: e0222007 eor r2, r2, r7

e0cc: e022200e eor r2, r2, lr

e0d0: e5812004 str r2, [r1, #4]

e0d4: e590e0e0 ldr lr, [r0, #224] ; 0xe0

e0d8: e590c0f0 ldr ip, [r0, #240] ; 0xf0

e0dc: e20ee01f and lr, lr, #31

e0e0: e26ee020 rsb lr, lr, #32

e0e4: e084c00c add ip, r4, ip

e0e8: e1a0ce7c ror ip, ip, lr

e0ec: e1a0582c lsr r5, ip, #16

e0f0: e1a0e42c lsr lr, ip, #8

e0f4: e6ef5075 uxtb r5, r5

e0f8: e1a07c2c lsr r7, ip, #24

e0fc: e6efe07e uxtb lr, lr

e100: e2855c01 add r5, r5, #256 ; 0x100

e104: e28e9c02 add r9, lr, #512 ; 0x200

e108: e6efc07c uxtb ip, ip

e10c: e793b107 ldr fp, [r3, r7, lsl #2]

e110: e28cec03 add lr, ip, #768 ; 0x300

e114: e7935105 ldr r5, [r3, r5, lsl #2]

e118: e793c109 ldr ip, [r3, r9, lsl #2]

e11c: e02bb005 eor fp, fp, r5

e120: e793510e ldr r5, [r3, lr, lsl #2]

e124: e04bb00c sub fp, fp, ip

e128: e08bb005 add fp, fp, r5

e12c: e02b5008 eor r5, fp, r8

e130: e5815008 str r5, [r1, #8]

e134: e590e10c ldr lr, [r0, #268] ; 0x10c

e138: e590c11c ldr ip, [r0, #284] ; 0x11c

e13c: e20ee01f and lr, lr, #31

e140: e26ee020 rsb lr, lr, #32

e144: e086c00c add ip, r6, ip

e148: e1a0ce7c ror ip, ip, lr

e14c: e1a0782c lsr r7, ip, #16

e150: e1a0e42c lsr lr, ip, #8

e154: e6ef7077 uxtb r7, r7

e158: e1a08c2c lsr r8, ip, #24

e15c: e2877c01 add r7, r7, #256 ; 0x100

e160: e6efe07e uxtb lr, lr

e164: e28e9c02 add r9, lr, #512 ; 0x200

e168: e6efe07c uxtb lr, ip

e16c: e793a108 ldr sl, [r3, r8, lsl #2]

e170: e793c107 ldr ip, [r3, r7, lsl #2]

e174: e28eec03 add lr, lr, #768 ; 0x300

e178: e7937109 ldr r7, [r3, r9, lsl #2]

e17c: e02aa00c eor sl, sl, ip

e180: e793c10e ldr ip, [r3, lr, lsl #2]

e184: e04aa007 sub sl, sl, r7

e188: e08aa00c add sl, sl, ip

e18c: e02ac004 eor ip, sl, r4

e190: e581c00c str ip, [r1, #12]

e194: e590e108 ldr lr, [r0, #264] ; 0x108

e198: e5904118 ldr r4, [r0, #280] ; 0x118

e19c: e20ee01f and lr, lr, #31

e1a0: e26ee020 rsb lr, lr, #32

e1a4: e0444002 sub r4, r4, r2

e1a8: e1a04e74 ror r4, r4, lr

e1ac: e1a07824 lsr r7, r4, #16

e1b0: e1a0e424 lsr lr, r4, #8

e1b4: e6ef7077 uxtb r7, r7

e1b8: e1a08c24 lsr r8, r4, #24

e1bc: e2877c01 add r7, r7, #256 ; 0x100

e1c0: e6efe07e uxtb lr, lr

e1c4: e28eec02 add lr, lr, #512 ; 0x200

e1c8: e6ef4074 uxtb r4, r4

e1cc: e7939108 ldr r9, [r3, r8, lsl #2]

e1d0: e2844c03 add r4, r4, #768 ; 0x300

e1d4: e7938107 ldr r8, [r3, r7, lsl #2]

e1d8: e793710e ldr r7, [r3, lr, lsl #2]

e1dc: e0899008 add r9, r9, r8

e1e0: e793e104 ldr lr, [r3, r4, lsl #2]

e1e4: e0299007 eor r9, r9, r7

e1e8: e049900e sub r9, r9, lr

e1ec: e0299006 eor r9, r9, r6

e1f0: e5819000 str r9, [r1]

e1f4: e590e104 ldr lr, [r0, #260] ; 0x104

e1f8: e5904114 ldr r4, [r0, #276] ; 0x114

e1fc: e20ee01f and lr, lr, #31

e200: e26ee020 rsb lr, lr, #32

e204: e0244005 eor r4, r4, r5

e208: e1a04e74 ror r4, r4, lr

e20c: e1a0e424 lsr lr, r4, #8

e210: e1a06824 lsr r6, r4, #16

e214: e6efe07e uxtb lr, lr

e218: e1a07c24 lsr r7, r4, #24

e21c: e28eec02 add lr, lr, #512 ; 0x200

e220: e6ef6076 uxtb r6, r6

e224: e2866c01 add r6, r6, #256 ; 0x100

e228: e7937107 ldr r7, [r3, r7, lsl #2]

e22c: e793e10e ldr lr, [r3, lr, lsl #2]

e230: e6ef4074 uxtb r4, r4

e234: e7936106 ldr r6, [r3, r6, lsl #2]

e238: e2844c03 add r4, r4, #768 ; 0x300

e23c: e08ee007 add lr, lr, r7

e240: e7934104 ldr r4, [r3, r4, lsl #2]

e244: e04ee006 sub lr, lr, r6

e248: e022200e eor r2, r2, lr

e24c: e0222004 eor r2, r2, r4

e250: e5812004 str r2, [r1, #4]

e254: e590e100 ldr lr, [r0, #256] ; 0x100

e258: e5904110 ldr r4, [r0, #272] ; 0x110

e25c: e20ee01f and lr, lr, #31

e260: e26ee020 rsb lr, lr, #32

e264: e08c4004 add r4, ip, r4

e268: e1a04e74 ror r4, r4, lr

e26c: e1a06824 lsr r6, r4, #16

e270: e1a0e424 lsr lr, r4, #8

e274: e6ef6076 uxtb r6, r6

e278: e1a07c24 lsr r7, r4, #24

e27c: e2866c01 add r6, r6, #256 ; 0x100

e280: e6efe07e uxtb lr, lr

e284: e28eec02 add lr, lr, #512 ; 0x200

e288: e6ef4074 uxtb r4, r4

e28c: e793b106 ldr fp, [r3, r6, lsl #2]

e290: e7938107 ldr r8, [r3, r7, lsl #2]

e294: e2844c03 add r4, r4, #768 ; 0x300

e298: e793e10e ldr lr, [r3, lr, lsl #2]

e29c: e028800b eor r8, r8, fp

e2a0: e793b104 ldr fp, [r3, r4, lsl #2]

e2a4: e048800e sub r8, r8, lr

e2a8: e088800b add r8, r8, fp

e2ac: e028b005 eor fp, r8, r5

e2b0: e581b008 str fp, [r1, #8]

e2b4: e590e12c ldr lr, [r0, #300] ; 0x12c

e2b8: e590413c ldr r4, [r0, #316] ; 0x13c

e2bc: e20ee01f and lr, lr, #31

e2c0: e0894004 add r4, r9, r4

e2c4: e26ee020 rsb lr, lr, #32

e2c8: e1a04e74 ror r4, r4, lr

e2cc: e1a05824 lsr r5, r4, #16

e2d0: e1a0e424 lsr lr, r4, #8

e2d4: e6ef5075 uxtb r5, r5

e2d8: e1a06c24 lsr r6, r4, #24

e2dc: e2855c01 add r5, r5, #256 ; 0x100

e2e0: e6efe07e uxtb lr, lr

e2e4: e28eec02 add lr, lr, #512 ; 0x200

e2e8: e6ef4074 uxtb r4, r4

e2ec: e793a105 ldr sl, [r3, r5, lsl #2]

e2f0: e7937106 ldr r7, [r3, r6, lsl #2]

e2f4: e2844c03 add r4, r4, #768 ; 0x300

e2f8: e793e10e ldr lr, [r3, lr, lsl #2]

e2fc: e027700a eor r7, r7, sl

e300: e793a104 ldr sl, [r3, r4, lsl #2]

e304: e047700e sub r7, r7, lr

e308: e087700a add r7, r7, sl

e30c: e027a00c eor sl, r7, ip

e310: e581a00c str sl, [r1, #12]

e314: e590c128 ldr ip, [r0, #296] ; 0x128

e318: e5904138 ldr r4, [r0, #312] ; 0x138

e31c: e20cc01f and ip, ip, #31

e320: e26cc020 rsb ip, ip, #32

e324: e0444002 sub r4, r4, r2

e328: e1a04c74 ror r4, r4, ip

e32c: e1a0e824 lsr lr, r4, #16

e330: e1a0c424 lsr ip, r4, #8

e334: e6efe07e uxtb lr, lr

e338: e1a05c24 lsr r5, r4, #24

e33c: e28eec01 add lr, lr, #256 ; 0x100

e340: e6efc07c uxtb ip, ip

e344: e28ccc02 add ip, ip, #512 ; 0x200

e348: e6ef4074 uxtb r4, r4

e34c: e7936105 ldr r6, [r3, r5, lsl #2]

e350: e2844c03 add r4, r4, #768 ; 0x300

e354: e793510e ldr r5, [r3, lr, lsl #2]

e358: e793e10c ldr lr, [r3, ip, lsl #2]

e35c: e0866005 add r6, r6, r5

e360: e793c104 ldr ip, [r3, r4, lsl #2]

e364: e026600e eor r6, r6, lr

e368: e046600c sub r6, r6, ip

e36c: e0299006 eor r9, r9, r6

e370: e5819000 str r9, [r1]

e374: e590c124 ldr ip, [r0, #292] ; 0x124

e378: e5904134 ldr r4, [r0, #308] ; 0x134

e37c: e20cc01f and ip, ip, #31

e380: e26cc020 rsb ip, ip, #32

e384: e024400b eor r4, r4, fp

e388: e1a04c74 ror r4, r4, ip

e38c: e1a0c424 lsr ip, r4, #8

e390: e1a0e824 lsr lr, r4, #16

e394: e6efc07c uxtb ip, ip

e398: e1a05c24 lsr r5, r4, #24

e39c: e28ccc02 add ip, ip, #512 ; 0x200

e3a0: e6efe07e uxtb lr, lr

e3a4: e28e6c01 add r6, lr, #256 ; 0x100

e3a8: e6ef4074 uxtb r4, r4

e3ac: e793e10c ldr lr, [r3, ip, lsl #2]

e3b0: e2844c03 add r4, r4, #768 ; 0x300

e3b4: e793c105 ldr ip, [r3, r5, lsl #2]

e3b8: e7935106 ldr r5, [r3, r6, lsl #2]

e3bc: e08ee00c add lr, lr, ip

e3c0: e793c104 ldr ip, [r3, r4, lsl #2]

e3c4: e04ee005 sub lr, lr, r5

e3c8: e02ee00c eor lr, lr, ip

e3cc: e02ee002 eor lr, lr, r2

e3d0: e581e004 str lr, [r1, #4]

e3d4: e5902120 ldr r2, [r0, #288] ; 0x120

e3d8: e5904130 ldr r4, [r0, #304] ; 0x130

e3dc: e202201f and r2, r2, #31

e3e0: e2622020 rsb r2, r2, #32

e3e4: e08a4004 add r4, sl, r4

e3e8: e1a04274 ror r4, r4, r2

e3ec: e1a0c824 lsr ip, r4, #16

e3f0: e1a02424 lsr r2, r4, #8

e3f4: e6efc07c uxtb ip, ip

e3f8: e1a05c24 lsr r5, r4, #24

e3fc: e28ccc01 add ip, ip, #256 ; 0x100

e400: e6ef2072 uxtb r2, r2

e404: e2822c02 add r2, r2, #512 ; 0x200

e408: e6ef4074 uxtb r4, r4

e40c: e793810c ldr r8, [r3, ip, lsl #2]

e410: e7935105 ldr r5, [r3, r5, lsl #2]

e414: e2844c03 add r4, r4, #768 ; 0x300

e418: e7932102 ldr r2, [r3, r2, lsl #2]

e41c: e0255008 eor r5, r5, r8

e420: e7938104 ldr r8, [r3, r4, lsl #2]

e424: e0455002 sub r5, r5, r2

e428: e0855008 add r5, r5, r8

e42c: e025800b eor r8, r5, fp

e430: e5818008 str r8, [r1, #8]

e434: e590214c ldr r2, [r0, #332] ; 0x14c

e438: e590415c ldr r4, [r0, #348] ; 0x15c

e43c: e202201f and r2, r2, #31

e440: e2622020 rsb r2, r2, #32

e444: e0894004 add r4, r9, r4

e448: e1a04274 ror r4, r4, r2

e44c: e1a0c824 lsr ip, r4, #16

e450: e1a02424 lsr r2, r4, #8

e454: e6efc07c uxtb ip, ip

e458: e1a06c24 lsr r6, r4, #24

e45c: e28c5c01 add r5, ip, #256 ; 0x100

e460: e6ef2072 uxtb r2, r2

e464: e2822c02 add r2, r2, #512 ; 0x200

e468: e6ef4074 uxtb r4, r4

e46c: e7937105 ldr r7, [r3, r5, lsl #2]

e470: e793c106 ldr ip, [r3, r6, lsl #2]

e474: e2844c03 add r4, r4, #768 ; 0x300

e478: e7932102 ldr r2, [r3, r2, lsl #2]

e47c: e02cc007 eor ip, ip, r7

e480: e7937104 ldr r7, [r3, r4, lsl #2]

e484: e04cc002 sub ip, ip, r2

e488: e08cc007 add ip, ip, r7

e48c: e02c700a eor r7, ip, sl

e490: e581700c str r7, [r1, #12]

e494: e5902148 ldr r2, [r0, #328] ; 0x148

e498: e5904158 ldr r4, [r0, #344] ; 0x158

e49c: e202201f and r2, r2, #31

e4a0: e2622020 rsb r2, r2, #32

e4a4: e044400e sub r4, r4, lr

e4a8: e1a04274 ror r4, r4, r2

e4ac: e1a0c824 lsr ip, r4, #16

e4b0: e1a02424 lsr r2, r4, #8

e4b4: e6efc07c uxtb ip, ip

e4b8: e1a05c24 lsr r5, r4, #24

e4bc: e28ccc01 add ip, ip, #256 ; 0x100

e4c0: e6ef2072 uxtb r2, r2

e4c4: e2822c02 add r2, r2, #512 ; 0x200

e4c8: e6ef4074 uxtb r4, r4

e4cc: e7936105 ldr r6, [r3, r5, lsl #2]

e4d0: e2844c03 add r4, r4, #768 ; 0x300

e4d4: e793510c ldr r5, [r3, ip, lsl #2]

e4d8: e793c102 ldr ip, [r3, r2, lsl #2]

e4dc: e0866005 add r6, r6, r5

e4e0: e7932104 ldr r2, [r3, r4, lsl #2]

e4e4: e026600c eor r6, r6, ip

e4e8: e0466002 sub r6, r6, r2

e4ec: e0266009 eor r6, r6, r9

e4f0: e5816000 str r6, [r1]

e4f4: e590c144 ldr ip, [r0, #324] ; 0x144

e4f8: e5902154 ldr r2, [r0, #340] ; 0x154

e4fc: e20cc01f and ip, ip, #31

e500: e26cc020 rsb ip, ip, #32

e504: e0222008 eor r2, r2, r8

e508: e1a02c72 ror r2, r2, ip

e50c: e1a05422 lsr r5, r2, #8

e510: e1a0c822 lsr ip, r2, #16

e514: e6ef5075 uxtb r5, r5

e518: e1a04c22 lsr r4, r2, #24

e51c: e6ef9072 uxtb r9, r2

e520: e2855c02 add r5, r5, #512 ; 0x200

e524: e6efc07c uxtb ip, ip

e528: ea000001 b e534 <mcrypt\_mutex\_register@@Base+0x9da0>

e52c: 00026ac0 andeq r6, r2, r0, asr #21

e530: 000000c0 andeq r0, r0, r0, asr #1

e534: e2899c03 add r9, r9, #768 ; 0x300

e538: e28ccc01 add ip, ip, #256 ; 0x100

e53c: e7932105 ldr r2, [r3, r5, lsl #2]

e540: e7935104 ldr r5, [r3, r4, lsl #2]

e544: e793c10c ldr ip, [r3, ip, lsl #2]

e548: e7934109 ldr r4, [r3, r9, lsl #2]

e54c: e0822005 add r2, r2, r5

e550: e042200c sub r2, r2, ip

e554: e02ee004 eor lr, lr, r4

e558: e02ee002 eor lr, lr, r2

e55c: e581e004 str lr, [r1, #4]

e560: e5902140 ldr r2, [r0, #320] ; 0x140

e564: e5904150 ldr r4, [r0, #336] ; 0x150

e568: e202201f and r2, r2, #31

e56c: e2622020 rsb r2, r2, #32

e570: e0874004 add r4, r7, r4

e574: e1a04274 ror r4, r4, r2

e578: e1a0c824 lsr ip, r4, #16

e57c: e1a02424 lsr r2, r4, #8

e580: e6efc07c uxtb ip, ip

e584: e1a05c24 lsr r5, r4, #24

e588: e28ccc01 add ip, ip, #256 ; 0x100

e58c: e6ef2072 uxtb r2, r2

e590: e2822c02 add r2, r2, #512 ; 0x200

e594: e6ef4074 uxtb r4, r4

e598: e793910c ldr r9, [r3, ip, lsl #2]

e59c: e7935105 ldr r5, [r3, r5, lsl #2]

e5a0: e2844c03 add r4, r4, #768 ; 0x300

e5a4: e793c102 ldr ip, [r3, r2, lsl #2]

e5a8: e0255009 eor r5, r5, r9

e5ac: e7932104 ldr r2, [r3, r4, lsl #2]

e5b0: e045500c sub r5, r5, ip

e5b4: e0855002 add r5, r5, r2

e5b8: e0255008 eor r5, r5, r8

e5bc: e5815008 str r5, [r1, #8]

e5c0: e590216c ldr r2, [r0, #364] ; 0x16c

e5c4: e590417c ldr r4, [r0, #380] ; 0x17c

e5c8: e202201f and r2, r2, #31

e5cc: e2622020 rsb r2, r2, #32

e5d0: e0864004 add r4, r6, r4

e5d4: e1a04274 ror r4, r4, r2

e5d8: e1a0c824 lsr ip, r4, #16

e5dc: e1a02424 lsr r2, r4, #8

e5e0: e6efc07c uxtb ip, ip

e5e4: e1a09c24 lsr r9, r4, #24

e5e8: e28c8c01 add r8, ip, #256 ; 0x100

e5ec: e6ef2072 uxtb r2, r2

e5f0: e2822c02 add r2, r2, #512 ; 0x200

e5f4: e6ef4074 uxtb r4, r4

e5f8: e793c109 ldr ip, [r3, r9, lsl #2]

e5fc: e2844c03 add r4, r4, #768 ; 0x300

e600: e7939108 ldr r9, [r3, r8, lsl #2]

e604: e7938102 ldr r8, [r3, r2, lsl #2]

e608: e02cc009 eor ip, ip, r9

e60c: e7932104 ldr r2, [r3, r4, lsl #2]

e610: e04cc008 sub ip, ip, r8

e614: e08cc002 add ip, ip, r2

e618: e02cc007 eor ip, ip, r7

e61c: e581c00c str ip, [r1, #12]

e620: e5904168 ldr r4, [r0, #360] ; 0x168

e624: e5902178 ldr r2, [r0, #376] ; 0x178

e628: e204401f and r4, r4, #31

e62c: e2644020 rsb r4, r4, #32

e630: e042200e sub r2, r2, lr

e634: e1a02472 ror r2, r2, r4

e638: e1a07822 lsr r7, r2, #16

e63c: e1a04422 lsr r4, r2, #8

e640: e6ef7077 uxtb r7, r7

e644: e1a08c22 lsr r8, r2, #24

e648: e2877c01 add r7, r7, #256 ; 0x100

e64c: e6ef4074 uxtb r4, r4

e650: e2849c02 add r9, r4, #512 ; 0x200

e654: e6ef4072 uxtb r4, r2

e658: e2844c03 add r4, r4, #768 ; 0x300

e65c: e7932108 ldr r2, [r3, r8, lsl #2]

e660: e7938107 ldr r8, [r3, r7, lsl #2]

e664: e7937109 ldr r7, [r3, r9, lsl #2]

e668: e7934104 ldr r4, [r3, r4, lsl #2]

e66c: e0822008 add r2, r2, r8

e670: e0222007 eor r2, r2, r7

e674: e0422004 sub r2, r2, r4

e678: e0266002 eor r6, r6, r2

e67c: e5816000 str r6, [r1]

e680: e5904164 ldr r4, [r0, #356] ; 0x164

e684: e5902174 ldr r2, [r0, #372] ; 0x174

e688: e204401f and r4, r4, #31

e68c: e2644020 rsb r4, r4, #32

e690: e0222005 eor r2, r2, r5

e694: e1a02472 ror r2, r2, r4

e698: e1a04422 lsr r4, r2, #8

e69c: e1a06822 lsr r6, r2, #16

e6a0: e6ef4074 uxtb r4, r4

e6a4: e6ef7072 uxtb r7, r2

e6a8: e2844c02 add r4, r4, #512 ; 0x200

e6ac: e1a02c22 lsr r2, r2, #24

e6b0: e6ef6076 uxtb r6, r6

e6b4: e2877c03 add r7, r7, #768 ; 0x300

e6b8: e2866c01 add r6, r6, #256 ; 0x100

e6bc: e7934104 ldr r4, [r3, r4, lsl #2]

e6c0: e7932102 ldr r2, [r3, r2, lsl #2]

e6c4: e7937107 ldr r7, [r3, r7, lsl #2]

e6c8: e7936106 ldr r6, [r3, r6, lsl #2]

e6cc: e0842002 add r2, r4, r2

e6d0: e0422006 sub r2, r2, r6

e6d4: e02ee007 eor lr, lr, r7

e6d8: e02ee002 eor lr, lr, r2

e6dc: e581e004 str lr, [r1, #4]

e6e0: e590e160 ldr lr, [r0, #352] ; 0x160

e6e4: e5900170 ldr r0, [r0, #368] ; 0x170

e6e8: e20e201f and r2, lr, #31

e6ec: e08cc000 add ip, ip, r0

e6f0: e2622020 rsb r2, r2, #32

e6f4: e1a0227c ror r2, ip, r2

e6f8: e1a0e822 lsr lr, r2, #16

e6fc: e1a0c422 lsr ip, r2, #8

e700: e6efe07e uxtb lr, lr

e704: e1a00c22 lsr r0, r2, #24

e708: e28eec01 add lr, lr, #256 ; 0x100

e70c: e6efc07c uxtb ip, ip

e710: e28ccc02 add ip, ip, #512 ; 0x200

e714: e6ef2072 uxtb r2, r2

e718: e7930100 ldr r0, [r3, r0, lsl #2]

e71c: e793e10e ldr lr, [r3, lr, lsl #2]

e720: e2822c03 add r2, r2, #768 ; 0x300

e724: e793c10c ldr ip, [r3, ip, lsl #2]

e728: e7932102 ldr r2, [r3, r2, lsl #2]

e72c: e020000e eor r0, r0, lr

e730: e040300c sub r3, r0, ip

e734: e0833002 add r3, r3, r2

e738: e0255003 eor r5, r5, r3

e73c: e5815008 str r5, [r1, #8]

e740: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

e744: e5903160 ldr r3, [r0, #352] ; 0x160

e748: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

e74c: e203301f and r3, r3, #31

e750: e5902170 ldr r2, [r0, #368] ; 0x170

e754: e591800c ldr r8, [r1, #12]

e758: e2633020 rsb r3, r3, #32

e75c: e0882002 add r2, r8, r2

e760: e59f4ff8 ldr r4, [pc, #4088] ; f760 <mcrypt\_mutex\_register@@Base+0xafcc>

e764: e1a02372 ror r2, r2, r3

e768: e59f3ff4 ldr r3, [pc, #4084] ; f764 <mcrypt\_mutex\_register@@Base+0xafd0>

e76c: e08f4004 add r4, pc, r4

e770: e1a0e822 lsr lr, r2, #16

e774: e1a0c422 lsr ip, r2, #8

e778: e7943003 ldr r3, [r4, r3]

e77c: e6efe07e uxtb lr, lr

e780: e28eec01 add lr, lr, #256 ; 0x100

e784: e1a04c22 lsr r4, r2, #24

e788: e6efc07c uxtb ip, ip

e78c: e28ccc02 add ip, ip, #512 ; 0x200

e790: e6ef2072 uxtb r2, r2

e794: e793e10e ldr lr, [r3, lr, lsl #2]

e798: e793b104 ldr fp, [r3, r4, lsl #2]

e79c: e2822c03 add r2, r2, #768 ; 0x300

e7a0: e793410c ldr r4, [r3, ip, lsl #2]

e7a4: e02bb00e eor fp, fp, lr

e7a8: e793e102 ldr lr, [r3, r2, lsl #2]

e7ac: e04bb004 sub fp, fp, r4

e7b0: e5914008 ldr r4, [r1, #8]

e7b4: e08bb00e add fp, fp, lr

e7b8: e024400b eor r4, r4, fp

e7bc: e5814008 str r4, [r1, #8]

e7c0: e590c164 ldr ip, [r0, #356] ; 0x164

e7c4: e5902174 ldr r2, [r0, #372] ; 0x174

e7c8: e20cc01f and ip, ip, #31

e7cc: e26cc020 rsb ip, ip, #32

e7d0: e0222004 eor r2, r2, r4

e7d4: e591e004 ldr lr, [r1, #4]

e7d8: e1a02c72 ror r2, r2, ip

e7dc: e591c000 ldr ip, [r1]

e7e0: e1a05422 lsr r5, r2, #8

e7e4: e1a06822 lsr r6, r2, #16

e7e8: e6ef5075 uxtb r5, r5

e7ec: e1a07c22 lsr r7, r2, #24

e7f0: e2855c02 add r5, r5, #512 ; 0x200

e7f4: e6ef6076 uxtb r6, r6

e7f8: e6ef2072 uxtb r2, r2

e7fc: e2866c01 add r6, r6, #256 ; 0x100

e800: e2822c03 add r2, r2, #768 ; 0x300

e804: e7937107 ldr r7, [r3, r7, lsl #2]

e808: e7935105 ldr r5, [r3, r5, lsl #2]

e80c: e7936106 ldr r6, [r3, r6, lsl #2]

e810: e7932102 ldr r2, [r3, r2, lsl #2]

e814: e0855007 add r5, r5, r7

e818: e02ee002 eor lr, lr, r2

e81c: e0455006 sub r5, r5, r6

e820: e025500e eor r5, r5, lr

e824: e5815004 str r5, [r1, #4]

e828: e590e168 ldr lr, [r0, #360] ; 0x168

e82c: e5902178 ldr r2, [r0, #376] ; 0x178

e830: e20ee01f and lr, lr, #31

e834: e26ee020 rsb lr, lr, #32

e838: e0422005 sub r2, r2, r5

e83c: e1a02e72 ror r2, r2, lr

e840: e1a06822 lsr r6, r2, #16

e844: e1a0e422 lsr lr, r2, #8

e848: e6ef6076 uxtb r6, r6

e84c: e1a07c22 lsr r7, r2, #24

e850: e2866c01 add r6, r6, #256 ; 0x100

e854: e6efe07e uxtb lr, lr

e858: e28eec02 add lr, lr, #512 ; 0x200

e85c: e6ef2072 uxtb r2, r2

e860: e7936106 ldr r6, [r3, r6, lsl #2]

e864: e7939107 ldr r9, [r3, r7, lsl #2]

e868: e2822c03 add r2, r2, #768 ; 0x300

e86c: e793e10e ldr lr, [r3, lr, lsl #2]

e870: e7937102 ldr r7, [r3, r2, lsl #2]

e874: e0899006 add r9, r9, r6

e878: e029900e eor r9, r9, lr

e87c: e0499007 sub r9, r9, r7

e880: e029900c eor r9, r9, ip

e884: e5819000 str r9, [r1]

e888: e590c16c ldr ip, [r0, #364] ; 0x16c

e88c: e590217c ldr r2, [r0, #380] ; 0x17c

e890: e20cc01f and ip, ip, #31

e894: e26cc020 rsb ip, ip, #32

e898: e0892002 add r2, r9, r2

e89c: e1a02c72 ror r2, r2, ip

e8a0: e1a0c822 lsr ip, r2, #16

e8a4: e1a0e422 lsr lr, r2, #8

e8a8: e6efc07c uxtb ip, ip

e8ac: e1a07c22 lsr r7, r2, #24

e8b0: e28ccc01 add ip, ip, #256 ; 0x100

e8b4: e6efe07e uxtb lr, lr

e8b8: e28eec02 add lr, lr, #512 ; 0x200

e8bc: e6ef2072 uxtb r2, r2

e8c0: e793610c ldr r6, [r3, ip, lsl #2]

e8c4: e793a107 ldr sl, [r3, r7, lsl #2]

e8c8: e2822c03 add r2, r2, #768 ; 0x300

e8cc: e793c10e ldr ip, [r3, lr, lsl #2]

e8d0: e02aa006 eor sl, sl, r6

e8d4: e7936102 ldr r6, [r3, r2, lsl #2]

e8d8: e04aa00c sub sl, sl, ip

e8dc: e08aa006 add sl, sl, r6

e8e0: e02a6008 eor r6, sl, r8

e8e4: e581600c str r6, [r1, #12]

e8e8: e5902140 ldr r2, [r0, #320] ; 0x140

e8ec: e590c150 ldr ip, [r0, #336] ; 0x150

e8f0: e202201f and r2, r2, #31

e8f4: e086c00c add ip, r6, ip

e8f8: e2622020 rsb r2, r2, #32

e8fc: e1a0227c ror r2, ip, r2

e900: e1a0e822 lsr lr, r2, #16

e904: e1a0c422 lsr ip, r2, #8

e908: e6efe07e uxtb lr, lr

e90c: e1a08c22 lsr r8, r2, #24

e910: e28e7c01 add r7, lr, #256 ; 0x100

e914: e6efc07c uxtb ip, ip

e918: e28ccc02 add ip, ip, #512 ; 0x200

e91c: e6ef2072 uxtb r2, r2

e920: e793b107 ldr fp, [r3, r7, lsl #2]

e924: e793e108 ldr lr, [r3, r8, lsl #2]

e928: e2822c03 add r2, r2, #768 ; 0x300

e92c: e793c10c ldr ip, [r3, ip, lsl #2]

e930: e02ee00b eor lr, lr, fp

e934: e793b102 ldr fp, [r3, r2, lsl #2]

e938: e04ee00c sub lr, lr, ip

e93c: e08ee00b add lr, lr, fp

e940: e02eb004 eor fp, lr, r4

e944: e581b008 str fp, [r1, #8]

e948: e590c144 ldr ip, [r0, #324] ; 0x144

e94c: e5902154 ldr r2, [r0, #340] ; 0x154

e950: e20cc01f and ip, ip, #31

e954: e26cc020 rsb ip, ip, #32

e958: e022200b eor r2, r2, fp

e95c: e1a02c72 ror r2, r2, ip

e960: e1a0e422 lsr lr, r2, #8

e964: e1a0c822 lsr ip, r2, #16

e968: e6efe07e uxtb lr, lr

e96c: e6ef7072 uxtb r7, r2

e970: e28eec02 add lr, lr, #512 ; 0x200

e974: e1a02c22 lsr r2, r2, #24

e978: e6efc07c uxtb ip, ip

e97c: e2877c03 add r7, r7, #768 ; 0x300

e980: e28ccc01 add ip, ip, #256 ; 0x100

e984: e7938102 ldr r8, [r3, r2, lsl #2]

e988: e793410e ldr r4, [r3, lr, lsl #2]

e98c: e793210c ldr r2, [r3, ip, lsl #2]

e990: e793e107 ldr lr, [r3, r7, lsl #2]

e994: e0844008 add r4, r4, r8

e998: e0444002 sub r4, r4, r2

e99c: e025500e eor r5, r5, lr

e9a0: e0255004 eor r5, r5, r4

e9a4: e5815004 str r5, [r1, #4]

e9a8: e590c148 ldr ip, [r0, #328] ; 0x148

e9ac: e5902158 ldr r2, [r0, #344] ; 0x158

e9b0: e20cc01f and ip, ip, #31

e9b4: e26cc020 rsb ip, ip, #32

e9b8: e0422005 sub r2, r2, r5

e9bc: e1a02c72 ror r2, r2, ip

e9c0: e1a0e822 lsr lr, r2, #16

e9c4: e1a0c422 lsr ip, r2, #8

e9c8: e6efe07e uxtb lr, lr

e9cc: e1a04c22 lsr r4, r2, #24

e9d0: e28eec01 add lr, lr, #256 ; 0x100

e9d4: e6efc07c uxtb ip, ip

e9d8: e28ccc02 add ip, ip, #512 ; 0x200

e9dc: e6ef2072 uxtb r2, r2

e9e0: e7937104 ldr r7, [r3, r4, lsl #2]

e9e4: e793e10e ldr lr, [r3, lr, lsl #2]

e9e8: e2822c03 add r2, r2, #768 ; 0x300

e9ec: e793c10c ldr ip, [r3, ip, lsl #2]

e9f0: e7932102 ldr r2, [r3, r2, lsl #2]

e9f4: e087700e add r7, r7, lr

e9f8: e027700c eor r7, r7, ip

e9fc: e0477002 sub r7, r7, r2

ea00: e0299007 eor r9, r9, r7

ea04: e5819000 str r9, [r1]

ea08: e590c14c ldr ip, [r0, #332] ; 0x14c

ea0c: e590215c ldr r2, [r0, #348] ; 0x15c

ea10: e20cc01f and ip, ip, #31

ea14: e26cc020 rsb ip, ip, #32

ea18: e0892002 add r2, r9, r2

ea1c: e1a02c72 ror r2, r2, ip

ea20: e1a0e822 lsr lr, r2, #16

ea24: e1a0c422 lsr ip, r2, #8

ea28: e6efe07e uxtb lr, lr

ea2c: e1a04c22 lsr r4, r2, #24

ea30: e28eec01 add lr, lr, #256 ; 0x100

ea34: e6efc07c uxtb ip, ip

ea38: e28ccc02 add ip, ip, #512 ; 0x200

ea3c: e6ef2072 uxtb r2, r2

ea40: e793a10e ldr sl, [r3, lr, lsl #2]

ea44: e7938104 ldr r8, [r3, r4, lsl #2]

ea48: e2822c03 add r2, r2, #768 ; 0x300

ea4c: e793c10c ldr ip, [r3, ip, lsl #2]

ea50: e028800a eor r8, r8, sl

ea54: e793a102 ldr sl, [r3, r2, lsl #2]

ea58: e048800c sub r8, r8, ip

ea5c: e088800a add r8, r8, sl

ea60: e028a006 eor sl, r8, r6

ea64: e581a00c str sl, [r1, #12]

ea68: e590c120 ldr ip, [r0, #288] ; 0x120

ea6c: e5902130 ldr r2, [r0, #304] ; 0x130

ea70: e20cc01f and ip, ip, #31

ea74: e26cc020 rsb ip, ip, #32

ea78: e08a2002 add r2, sl, r2

ea7c: e1a02c72 ror r2, r2, ip

ea80: e1a0e822 lsr lr, r2, #16

ea84: e1a0c422 lsr ip, r2, #8

ea88: e6efe07e uxtb lr, lr

ea8c: e1a04c22 lsr r4, r2, #24

ea90: e6efc07c uxtb ip, ip

ea94: e28eec01 add lr, lr, #256 ; 0x100

ea98: e28c6c02 add r6, ip, #512 ; 0x200

ea9c: e6ef2072 uxtb r2, r2

eaa0: e793c104 ldr ip, [r3, r4, lsl #2]

eaa4: e2822c03 add r2, r2, #768 ; 0x300

eaa8: e793e10e ldr lr, [r3, lr, lsl #2]

eaac: e7934106 ldr r4, [r3, r6, lsl #2]

eab0: e02cc00e eor ip, ip, lr

eab4: e793e102 ldr lr, [r3, r2, lsl #2]

eab8: e04cc004 sub ip, ip, r4

eabc: e08cc00e add ip, ip, lr

eac0: e02ce00b eor lr, ip, fp

eac4: e581e008 str lr, [r1, #8]

eac8: e590c124 ldr ip, [r0, #292] ; 0x124

eacc: e5902134 ldr r2, [r0, #308] ; 0x134

ead0: e20cc01f and ip, ip, #31

ead4: e26cc020 rsb ip, ip, #32

ead8: e022200e eor r2, r2, lr

eadc: e1a02c72 ror r2, r2, ip

eae0: e1a04422 lsr r4, r2, #8

eae4: e1a0c822 lsr ip, r2, #16

eae8: e6ef4074 uxtb r4, r4

eaec: e6ef6072 uxtb r6, r2

eaf0: e2844c02 add r4, r4, #512 ; 0x200

eaf4: e1a02c22 lsr r2, r2, #24

eaf8: e6efc07c uxtb ip, ip

eafc: e28ccc01 add ip, ip, #256 ; 0x100

eb00: e2866c03 add r6, r6, #768 ; 0x300

eb04: e7937102 ldr r7, [r3, r2, lsl #2]

eb08: e7934104 ldr r4, [r3, r4, lsl #2]

eb0c: e793210c ldr r2, [r3, ip, lsl #2]

eb10: e7936106 ldr r6, [r3, r6, lsl #2]

eb14: e0844007 add r4, r4, r7

eb18: e0255006 eor r5, r5, r6

eb1c: e0444002 sub r4, r4, r2

eb20: e0244005 eor r4, r4, r5

eb24: e5814004 str r4, [r1, #4]

eb28: e590c128 ldr ip, [r0, #296] ; 0x128

eb2c: e5902138 ldr r2, [r0, #312] ; 0x138

eb30: e20cc01f and ip, ip, #31

eb34: e26cc020 rsb ip, ip, #32

eb38: e0422004 sub r2, r2, r4

eb3c: e1a02c72 ror r2, r2, ip

eb40: e1a05822 lsr r5, r2, #16

eb44: e1a0c422 lsr ip, r2, #8

eb48: e6ef5075 uxtb r5, r5

eb4c: e1a06c22 lsr r6, r2, #24

eb50: e2855c01 add r5, r5, #256 ; 0x100

eb54: e6efc07c uxtb ip, ip

eb58: e28ccc02 add ip, ip, #512 ; 0x200

eb5c: e6ef2072 uxtb r2, r2

eb60: e7937106 ldr r7, [r3, r6, lsl #2]

eb64: e7935105 ldr r5, [r3, r5, lsl #2]

eb68: e2822c03 add r2, r2, #768 ; 0x300

eb6c: e793c10c ldr ip, [r3, ip, lsl #2]

eb70: e7932102 ldr r2, [r3, r2, lsl #2]

eb74: e0877005 add r7, r7, r5

eb78: e027700c eor r7, r7, ip

eb7c: e0477002 sub r7, r7, r2

eb80: e0277009 eor r7, r7, r9

eb84: e5817000 str r7, [r1]

eb88: e590c12c ldr ip, [r0, #300] ; 0x12c

eb8c: e590213c ldr r2, [r0, #316] ; 0x13c

eb90: e20cc01f and ip, ip, #31

eb94: e26cc020 rsb ip, ip, #32

eb98: e0872002 add r2, r7, r2

eb9c: e1a02c72 ror r2, r2, ip

eba0: e1a05822 lsr r5, r2, #16

eba4: e1a0c422 lsr ip, r2, #8

eba8: e6ef5075 uxtb r5, r5

ebac: e1a06c22 lsr r6, r2, #24

ebb0: e2855c01 add r5, r5, #256 ; 0x100

ebb4: e6efc07c uxtb ip, ip

ebb8: e28ccc02 add ip, ip, #512 ; 0x200

ebbc: e6ef2072 uxtb r2, r2

ebc0: e7938105 ldr r8, [r3, r5, lsl #2]

ebc4: e7936106 ldr r6, [r3, r6, lsl #2]

ebc8: e2822c03 add r2, r2, #768 ; 0x300

ebcc: e793c10c ldr ip, [r3, ip, lsl #2]

ebd0: e0266008 eor r6, r6, r8

ebd4: e7938102 ldr r8, [r3, r2, lsl #2]

ebd8: e046600c sub r6, r6, ip

ebdc: e0866008 add r6, r6, r8

ebe0: e026800a eor r8, r6, sl

ebe4: e581800c str r8, [r1, #12]

ebe8: e590c100 ldr ip, [r0, #256] ; 0x100

ebec: e5902110 ldr r2, [r0, #272] ; 0x110

ebf0: e20cc01f and ip, ip, #31

ebf4: e26cc020 rsb ip, ip, #32

ebf8: e0882002 add r2, r8, r2

ebfc: e1a02c72 ror r2, r2, ip

ec00: e1a05822 lsr r5, r2, #16

ec04: e1a0c422 lsr ip, r2, #8

ec08: e6ef5075 uxtb r5, r5

ec0c: e1a06c22 lsr r6, r2, #24

ec10: e2855c01 add r5, r5, #256 ; 0x100

ec14: e6efc07c uxtb ip, ip

ec18: e28ccc02 add ip, ip, #512 ; 0x200

ec1c: e6ef2072 uxtb r2, r2

ec20: e793b106 ldr fp, [r3, r6, lsl #2]

ec24: e2822c03 add r2, r2, #768 ; 0x300

ec28: e7936105 ldr r6, [r3, r5, lsl #2]

ec2c: e793510c ldr r5, [r3, ip, lsl #2]

ec30: e02bb006 eor fp, fp, r6

ec34: e793c102 ldr ip, [r3, r2, lsl #2]

ec38: e04bb005 sub fp, fp, r5

ec3c: e08bb00c add fp, fp, ip

ec40: e02bc00e eor ip, fp, lr

ec44: e581c008 str ip, [r1, #8]

ec48: e590e104 ldr lr, [r0, #260] ; 0x104

ec4c: e5902114 ldr r2, [r0, #276] ; 0x114

ec50: e20ee01f and lr, lr, #31

ec54: e022200c eor r2, r2, ip

ec58: e26ee020 rsb lr, lr, #32

ec5c: e1a02e72 ror r2, r2, lr

ec60: e1a05422 lsr r5, r2, #8

ec64: e1a0e822 lsr lr, r2, #16

ec68: e6ef5075 uxtb r5, r5

ec6c: e6ef6072 uxtb r6, r2

ec70: e2855c02 add r5, r5, #512 ; 0x200

ec74: e1a02c22 lsr r2, r2, #24

ec78: e6efe07e uxtb lr, lr

ec7c: e28eec01 add lr, lr, #256 ; 0x100

ec80: e2866c03 add r6, r6, #768 ; 0x300

ec84: e7939102 ldr r9, [r3, r2, lsl #2]

ec88: e7935105 ldr r5, [r3, r5, lsl #2]

ec8c: e793210e ldr r2, [r3, lr, lsl #2]

ec90: e7936106 ldr r6, [r3, r6, lsl #2]

ec94: e0855009 add r5, r5, r9

ec98: e0244006 eor r4, r4, r6

ec9c: e0455002 sub r5, r5, r2

eca0: e0255004 eor r5, r5, r4

eca4: e5815004 str r5, [r1, #4]

eca8: e590e108 ldr lr, [r0, #264] ; 0x108

ecac: e5902118 ldr r2, [r0, #280] ; 0x118

ecb0: e20ee01f and lr, lr, #31

ecb4: e26ee020 rsb lr, lr, #32

ecb8: e0422005 sub r2, r2, r5

ecbc: e1a02e72 ror r2, r2, lr

ecc0: e1a04822 lsr r4, r2, #16

ecc4: e1a0e422 lsr lr, r2, #8

ecc8: e6ef4074 uxtb r4, r4

eccc: e1a06c22 lsr r6, r2, #24

ecd0: e2844c01 add r4, r4, #256 ; 0x100

ecd4: e6efe07e uxtb lr, lr

ecd8: e28eec02 add lr, lr, #512 ; 0x200

ecdc: e6ef2072 uxtb r2, r2

ece0: e7939106 ldr r9, [r3, r6, lsl #2]

ece4: e7934104 ldr r4, [r3, r4, lsl #2]

ece8: e2822c03 add r2, r2, #768 ; 0x300

ecec: e793e10e ldr lr, [r3, lr, lsl #2]

ecf0: e7932102 ldr r2, [r3, r2, lsl #2]

ecf4: e0899004 add r9, r9, r4

ecf8: e029900e eor r9, r9, lr

ecfc: e0499002 sub r9, r9, r2

ed00: e0277009 eor r7, r7, r9

ed04: e5817000 str r7, [r1]

ed08: e590e10c ldr lr, [r0, #268] ; 0x10c

ed0c: e590211c ldr r2, [r0, #284] ; 0x11c

ed10: e20ee01f and lr, lr, #31

ed14: e26ee020 rsb lr, lr, #32

ed18: e0872002 add r2, r7, r2

ed1c: e1a02e72 ror r2, r2, lr

ed20: e1a04822 lsr r4, r2, #16

ed24: e1a0e422 lsr lr, r2, #8

ed28: e6ef4074 uxtb r4, r4

ed2c: e1a06c22 lsr r6, r2, #24

ed30: e2844c01 add r4, r4, #256 ; 0x100

ed34: e6efe07e uxtb lr, lr

ed38: e28eec02 add lr, lr, #512 ; 0x200

ed3c: e6ef2072 uxtb r2, r2

ed40: e793a106 ldr sl, [r3, r6, lsl #2]

ed44: e2822c03 add r2, r2, #768 ; 0x300

ed48: e7936104 ldr r6, [r3, r4, lsl #2]

ed4c: e793e10e ldr lr, [r3, lr, lsl #2]

ed50: e02aa006 eor sl, sl, r6

ed54: e7936102 ldr r6, [r3, r2, lsl #2]

ed58: e04aa00e sub sl, sl, lr

ed5c: e08aa006 add sl, sl, r6

ed60: e02a6008 eor r6, sl, r8

ed64: e581600c str r6, [r1, #12]

ed68: e590e0e0 ldr lr, [r0, #224] ; 0xe0

ed6c: e59020f0 ldr r2, [r0, #240] ; 0xf0

ed70: e20ee01f and lr, lr, #31

ed74: e26ee020 rsb lr, lr, #32

ed78: e0862002 add r2, r6, r2

ed7c: e1a02e72 ror r2, r2, lr

ed80: e1a04822 lsr r4, r2, #16

ed84: e1a0e422 lsr lr, r2, #8

ed88: e6ef4074 uxtb r4, r4

ed8c: e1a08c22 lsr r8, r2, #24

ed90: e2844c01 add r4, r4, #256 ; 0x100

ed94: e6efe07e uxtb lr, lr

ed98: e28e9c02 add r9, lr, #512 ; 0x200

ed9c: e6ef2072 uxtb r2, r2

eda0: e793b104 ldr fp, [r3, r4, lsl #2]

eda4: e793e108 ldr lr, [r3, r8, lsl #2]

eda8: e2822c03 add r2, r2, #768 ; 0x300

edac: e7938109 ldr r8, [r3, r9, lsl #2]

edb0: e02ee00b eor lr, lr, fp

edb4: e793b102 ldr fp, [r3, r2, lsl #2]

edb8: e04ee008 sub lr, lr, r8

edbc: e08ee00b add lr, lr, fp

edc0: e02eb00c eor fp, lr, ip

edc4: e581b008 str fp, [r1, #8]

edc8: e59020e4 ldr r2, [r0, #228] ; 0xe4

edcc: e590c0f4 ldr ip, [r0, #244] ; 0xf4

edd0: e202201f and r2, r2, #31

edd4: e2622020 rsb r2, r2, #32

edd8: e02cc00b eor ip, ip, fp

eddc: e1a0c27c ror ip, ip, r2

ede0: e1a0e42c lsr lr, ip, #8

ede4: e1a0282c lsr r2, ip, #16

ede8: e6efe07e uxtb lr, lr

edec: e6ef807c uxtb r8, ip

edf0: e28eec02 add lr, lr, #512 ; 0x200

edf4: e1a0cc2c lsr ip, ip, #24

edf8: e6ef2072 uxtb r2, r2

edfc: e2888c03 add r8, r8, #768 ; 0x300

ee00: e2822c01 add r2, r2, #256 ; 0x100

ee04: e793410e ldr r4, [r3, lr, lsl #2]

ee08: e793e10c ldr lr, [r3, ip, lsl #2]

ee0c: e7932102 ldr r2, [r3, r2, lsl #2]

ee10: e793c108 ldr ip, [r3, r8, lsl #2]

ee14: e084400e add r4, r4, lr

ee18: e025500c eor r5, r5, ip

ee1c: e0444002 sub r4, r4, r2

ee20: e0244005 eor r4, r4, r5

ee24: e5814004 str r4, [r1, #4]

ee28: e59020e8 ldr r2, [r0, #232] ; 0xe8

ee2c: e590c0f8 ldr ip, [r0, #248] ; 0xf8

ee30: e202201f and r2, r2, #31

ee34: e2622020 rsb r2, r2, #32

ee38: e04cc004 sub ip, ip, r4

ee3c: e1a0c27c ror ip, ip, r2

ee40: e1a0e82c lsr lr, ip, #16

ee44: e1a0242c lsr r2, ip, #8

ee48: e6efe07e uxtb lr, lr

ee4c: e1a05c2c lsr r5, ip, #24

ee50: e28eec01 add lr, lr, #256 ; 0x100

ee54: e6ef2072 uxtb r2, r2

ee58: e2828c02 add r8, r2, #512 ; 0x200

ee5c: e6efc07c uxtb ip, ip

ee60: e793910e ldr r9, [r3, lr, lsl #2]

ee64: e7932105 ldr r2, [r3, r5, lsl #2]

ee68: e28ccc03 add ip, ip, #768 ; 0x300

ee6c: e793e108 ldr lr, [r3, r8, lsl #2]

ee70: e0822009 add r2, r2, r9

ee74: e793910c ldr r9, [r3, ip, lsl #2]

ee78: e022200e eor r2, r2, lr

ee7c: e0422009 sub r2, r2, r9

ee80: e0229007 eor r9, r2, r7

ee84: e5819000 str r9, [r1]

ee88: e59020ec ldr r2, [r0, #236] ; 0xec

ee8c: e590c0fc ldr ip, [r0, #252] ; 0xfc

ee90: e202201f and r2, r2, #31

ee94: e2622020 rsb r2, r2, #32

ee98: e089c00c add ip, r9, ip

ee9c: e1a0c27c ror ip, ip, r2

eea0: e1a0e82c lsr lr, ip, #16

eea4: e1a0242c lsr r2, ip, #8

eea8: e6efe07e uxtb lr, lr

eeac: e1a05c2c lsr r5, ip, #24

eeb0: e28eec01 add lr, lr, #256 ; 0x100

eeb4: e6ef2072 uxtb r2, r2

eeb8: e2822c02 add r2, r2, #512 ; 0x200

eebc: e6efc07c uxtb ip, ip

eec0: e793a105 ldr sl, [r3, r5, lsl #2]

eec4: e28ccc03 add ip, ip, #768 ; 0x300

eec8: e793510e ldr r5, [r3, lr, lsl #2]

eecc: e793e102 ldr lr, [r3, r2, lsl #2]

eed0: e02aa005 eor sl, sl, r5

eed4: e793210c ldr r2, [r3, ip, lsl #2]

eed8: e04aa00e sub sl, sl, lr

eedc: e08aa002 add sl, sl, r2

eee0: e02aa006 eor sl, sl, r6

eee4: e581a00c str sl, [r1, #12]

eee8: e59020c0 ldr r2, [r0, #192] ; 0xc0

eeec: e590c0d0 ldr ip, [r0, #208] ; 0xd0

eef0: e202201f and r2, r2, #31

eef4: e2622020 rsb r2, r2, #32

eef8: e08ac00c add ip, sl, ip

eefc: e1a0c27c ror ip, ip, r2

ef00: e1a0e82c lsr lr, ip, #16

ef04: e1a0242c lsr r2, ip, #8

ef08: e6efe07e uxtb lr, lr

ef0c: e1a05c2c lsr r5, ip, #24

ef10: e28eec01 add lr, lr, #256 ; 0x100

ef14: e6ef2072 uxtb r2, r2

ef18: e2822c02 add r2, r2, #512 ; 0x200

ef1c: e6efc07c uxtb ip, ip

ef20: e7938105 ldr r8, [r3, r5, lsl #2]

ef24: e793e10e ldr lr, [r3, lr, lsl #2]

ef28: e28ccc03 add ip, ip, #768 ; 0x300

ef2c: e7935102 ldr r5, [r3, r2, lsl #2]

ef30: e028800e eor r8, r8, lr

ef34: e793e10c ldr lr, [r3, ip, lsl #2]

ef38: e0488005 sub r8, r8, r5

ef3c: e088800e add r8, r8, lr

ef40: e028e00b eor lr, r8, fp

ef44: e581e008 str lr, [r1, #8]

ef48: e59020c4 ldr r2, [r0, #196] ; 0xc4

ef4c: e590c0d4 ldr ip, [r0, #212] ; 0xd4

ef50: e202201f and r2, r2, #31

ef54: e2622020 rsb r2, r2, #32

ef58: e02cc00e eor ip, ip, lr

ef5c: e1a0c27c ror ip, ip, r2

ef60: e1a0242c lsr r2, ip, #8

ef64: e1a0682c lsr r6, ip, #16

ef68: e6ef2072 uxtb r2, r2

ef6c: e6ef707c uxtb r7, ip

ef70: e2822c02 add r2, r2, #512 ; 0x200

ef74: e1a0cc2c lsr ip, ip, #24

ef78: e6ef6076 uxtb r6, r6

ef7c: e2877c03 add r7, r7, #768 ; 0x300

ef80: e2866c01 add r6, r6, #256 ; 0x100

ef84: e793810c ldr r8, [r3, ip, lsl #2]

ef88: e7932102 ldr r2, [r3, r2, lsl #2]

ef8c: e7935106 ldr r5, [r3, r6, lsl #2]

ef90: e793c107 ldr ip, [r3, r7, lsl #2]

ef94: e0822008 add r2, r2, r8

ef98: e024400c eor r4, r4, ip

ef9c: e0422005 sub r2, r2, r5

efa0: e0225004 eor r5, r2, r4

efa4: e5815004 str r5, [r1, #4]

efa8: e59020c8 ldr r2, [r0, #200] ; 0xc8

efac: e590c0d8 ldr ip, [r0, #216] ; 0xd8

efb0: e202201f and r2, r2, #31

efb4: e2622020 rsb r2, r2, #32

efb8: e04cc005 sub ip, ip, r5

efbc: e1a0c27c ror ip, ip, r2

efc0: e1a0482c lsr r4, ip, #16

efc4: e1a0242c lsr r2, ip, #8

efc8: e6ef4074 uxtb r4, r4

efcc: e1a06c2c lsr r6, ip, #24

efd0: e2844c01 add r4, r4, #256 ; 0x100

efd4: e6ef2072 uxtb r2, r2

efd8: e2822c02 add r2, r2, #512 ; 0x200

efdc: e6efc07c uxtb ip, ip

efe0: e7937106 ldr r7, [r3, r6, lsl #2]

efe4: e7934104 ldr r4, [r3, r4, lsl #2]

efe8: e28ccc03 add ip, ip, #768 ; 0x300

efec: e7936102 ldr r6, [r3, r2, lsl #2]

eff0: e0877004 add r7, r7, r4

eff4: e793210c ldr r2, [r3, ip, lsl #2]

eff8: e0277006 eor r7, r7, r6

effc: e0477002 sub r7, r7, r2

f000: e0272009 eor r2, r7, r9

f004: e5812000 str r2, [r1]

f008: e59040cc ldr r4, [r0, #204] ; 0xcc

f00c: e590c0dc ldr ip, [r0, #220] ; 0xdc

f010: e204401f and r4, r4, #31

f014: e2644020 rsb r4, r4, #32

f018: e082c00c add ip, r2, ip

f01c: e1a0c47c ror ip, ip, r4

f020: e1a0682c lsr r6, ip, #16

f024: e1a0442c lsr r4, ip, #8

f028: e6ef6076 uxtb r6, r6

f02c: e1a07c2c lsr r7, ip, #24

f030: e2866c01 add r6, r6, #256 ; 0x100

f034: e6ef4074 uxtb r4, r4

f038: e2848c02 add r8, r4, #512 ; 0x200

f03c: e6efc07c uxtb ip, ip

f040: e7934107 ldr r4, [r3, r7, lsl #2]

f044: e28ccc03 add ip, ip, #768 ; 0x300

f048: e7937106 ldr r7, [r3, r6, lsl #2]

f04c: e7936108 ldr r6, [r3, r8, lsl #2]

f050: e793c10c ldr ip, [r3, ip, lsl #2]

f054: e0244007 eor r4, r4, r7

f058: e0444006 sub r4, r4, r6

f05c: e084400c add r4, r4, ip

f060: e02aa004 eor sl, sl, r4

f064: e581a00c str sl, [r1, #12]

f068: e59040ac ldr r4, [r0, #172] ; 0xac

f06c: e590c0bc ldr ip, [r0, #188] ; 0xbc

f070: e204401f and r4, r4, #31

f074: e2644020 rsb r4, r4, #32

f078: e082c00c add ip, r2, ip

f07c: e1a0c47c ror ip, ip, r4

f080: e1a0682c lsr r6, ip, #16

f084: e1a0442c lsr r4, ip, #8

f088: e6ef6076 uxtb r6, r6

f08c: e1a07c2c lsr r7, ip, #24

f090: e2866c01 add r6, r6, #256 ; 0x100

f094: e6ef4074 uxtb r4, r4

f098: e2848c02 add r8, r4, #512 ; 0x200

f09c: e6efc07c uxtb ip, ip

f0a0: e7934107 ldr r4, [r3, r7, lsl #2]

f0a4: e28ccc03 add ip, ip, #768 ; 0x300

f0a8: e7937106 ldr r7, [r3, r6, lsl #2]

f0ac: e7936108 ldr r6, [r3, r8, lsl #2]

f0b0: e793c10c ldr ip, [r3, ip, lsl #2]

f0b4: e0244007 eor r4, r4, r7

f0b8: e0444006 sub r4, r4, r6

f0bc: e084400c add r4, r4, ip

f0c0: e02aa004 eor sl, sl, r4

f0c4: e581a00c str sl, [r1, #12]

f0c8: e59040a8 ldr r4, [r0, #168] ; 0xa8

f0cc: e590c0b8 ldr ip, [r0, #184] ; 0xb8

f0d0: e204401f and r4, r4, #31

f0d4: e2644020 rsb r4, r4, #32

f0d8: e04cc005 sub ip, ip, r5

f0dc: e1a0c47c ror ip, ip, r4

f0e0: e1a0682c lsr r6, ip, #16

f0e4: e1a0442c lsr r4, ip, #8

f0e8: e6ef6076 uxtb r6, r6

f0ec: e1a08c2c lsr r8, ip, #24

f0f0: e2867c01 add r7, r6, #256 ; 0x100

f0f4: e6ef4074 uxtb r4, r4

f0f8: e2844c02 add r4, r4, #512 ; 0x200

f0fc: e6efc07c uxtb ip, ip

f100: e7936108 ldr r6, [r3, r8, lsl #2]

f104: e7937107 ldr r7, [r3, r7, lsl #2]

f108: e28ccc03 add ip, ip, #768 ; 0x300

f10c: e7939104 ldr r9, [r3, r4, lsl #2]

f110: e0866007 add r6, r6, r7

f114: e793710c ldr r7, [r3, ip, lsl #2]

f118: e0266009 eor r6, r6, r9

f11c: e0466007 sub r6, r6, r7

f120: e0267002 eor r7, r6, r2

f124: e5817000 str r7, [r1]

f128: e59020a4 ldr r2, [r0, #164] ; 0xa4

f12c: e590c0b4 ldr ip, [r0, #180] ; 0xb4

f130: e202201f and r2, r2, #31

f134: e2622020 rsb r2, r2, #32

f138: e02cc00e eor ip, ip, lr

f13c: e1a0c27c ror ip, ip, r2

f140: e1a0242c lsr r2, ip, #8

f144: e1a0482c lsr r4, ip, #16

f148: e6ef2072 uxtb r2, r2

f14c: e1a06c2c lsr r6, ip, #24

f150: e2822c02 add r2, r2, #512 ; 0x200

f154: e6ef4074 uxtb r4, r4

f158: e2844c01 add r4, r4, #256 ; 0x100

f15c: e6efc07c uxtb ip, ip

f160: e7936106 ldr r6, [r3, r6, lsl #2]

f164: e7932102 ldr r2, [r3, r2, lsl #2]

f168: e28ccc03 add ip, ip, #768 ; 0x300

f16c: e7934104 ldr r4, [r3, r4, lsl #2]

f170: e0822006 add r2, r2, r6

f174: e793c10c ldr ip, [r3, ip, lsl #2]

f178: e0422004 sub r2, r2, r4

f17c: e022200c eor r2, r2, ip

f180: e0222005 eor r2, r2, r5

f184: e5812004 str r2, [r1, #4]

f188: e590c0a0 ldr ip, [r0, #160] ; 0xa0

f18c: e59040b0 ldr r4, [r0, #176] ; 0xb0

f190: e20cc01f and ip, ip, #31

f194: e08a4004 add r4, sl, r4

f198: e26cc020 rsb ip, ip, #32

f19c: e1a0cc74 ror ip, r4, ip

f1a0: e1a0582c lsr r5, ip, #16

f1a4: e1a0442c lsr r4, ip, #8

f1a8: e6ef5075 uxtb r5, r5

f1ac: e1a08c2c lsr r8, ip, #24

f1b0: e2856c01 add r6, r5, #256 ; 0x100

f1b4: e6ef4074 uxtb r4, r4

f1b8: e2844c02 add r4, r4, #512 ; 0x200

f1bc: e6efc07c uxtb ip, ip

f1c0: e7935108 ldr r5, [r3, r8, lsl #2]

f1c4: e28ccc03 add ip, ip, #768 ; 0x300

f1c8: e7938106 ldr r8, [r3, r6, lsl #2]

f1cc: e793b104 ldr fp, [r3, r4, lsl #2]

f1d0: e0255008 eor r5, r5, r8

f1d4: e793810c ldr r8, [r3, ip, lsl #2]

f1d8: e045500b sub r5, r5, fp

f1dc: e0855008 add r5, r5, r8

f1e0: e025800e eor r8, r5, lr

f1e4: e5818008 str r8, [r1, #8]

f1e8: e590c08c ldr ip, [r0, #140] ; 0x8c

f1ec: e590e09c ldr lr, [r0, #156] ; 0x9c

f1f0: e20cc01f and ip, ip, #31

f1f4: e087e00e add lr, r7, lr

f1f8: e26cc020 rsb ip, ip, #32

f1fc: e1a0cc7e ror ip, lr, ip

f200: e1a0482c lsr r4, ip, #16

f204: e1a0e42c lsr lr, ip, #8

f208: e6ef4074 uxtb r4, r4

f20c: e1a05c2c lsr r5, ip, #24

f210: e6efe07e uxtb lr, lr

f214: e2844c01 add r4, r4, #256 ; 0x100

f218: e28e6c02 add r6, lr, #512 ; 0x200

f21c: e6efe07c uxtb lr, ip

f220: e7934104 ldr r4, [r3, r4, lsl #2]

f224: e793c105 ldr ip, [r3, r5, lsl #2]

f228: e28eec03 add lr, lr, #768 ; 0x300

f22c: e7935106 ldr r5, [r3, r6, lsl #2]

f230: e02cc004 eor ip, ip, r4

f234: e793410e ldr r4, [r3, lr, lsl #2]

f238: e04cc005 sub ip, ip, r5

f23c: e08cc004 add ip, ip, r4

f240: e02c400a eor r4, ip, sl

f244: e581400c str r4, [r1, #12]

f248: e590c088 ldr ip, [r0, #136] ; 0x88

f24c: e590e098 ldr lr, [r0, #152] ; 0x98

f250: e20cc01f and ip, ip, #31

f254: e04ee002 sub lr, lr, r2

f258: e26cc020 rsb ip, ip, #32

f25c: e1a0cc7e ror ip, lr, ip

f260: e1a0582c lsr r5, ip, #16

f264: e1a0e42c lsr lr, ip, #8

f268: e6ef5075 uxtb r5, r5

f26c: e1a06c2c lsr r6, ip, #24

f270: e2855c01 add r5, r5, #256 ; 0x100

f274: e6efe07e uxtb lr, lr

f278: e28eec02 add lr, lr, #512 ; 0x200

f27c: e6efc07c uxtb ip, ip

f280: e7939106 ldr r9, [r3, r6, lsl #2]

f284: e28ccc03 add ip, ip, #768 ; 0x300

f288: e7936105 ldr r6, [r3, r5, lsl #2]

f28c: e793e10e ldr lr, [r3, lr, lsl #2]

f290: e0899006 add r9, r9, r6

f294: e793610c ldr r6, [r3, ip, lsl #2]

f298: e029900e eor r9, r9, lr

f29c: e0499006 sub r9, r9, r6

f2a0: e0296007 eor r6, r9, r7

f2a4: e5816000 str r6, [r1]

f2a8: e590e084 ldr lr, [r0, #132] ; 0x84

f2ac: e590c094 ldr ip, [r0, #148] ; 0x94

f2b0: e20ee01f and lr, lr, #31

f2b4: e26ee020 rsb lr, lr, #32

f2b8: e02cc008 eor ip, ip, r8

f2bc: e1a0ce7c ror ip, ip, lr

f2c0: e1a0e42c lsr lr, ip, #8

f2c4: e1a0582c lsr r5, ip, #16

f2c8: e6efe07e uxtb lr, lr

f2cc: e6ef707c uxtb r7, ip

f2d0: e28eec02 add lr, lr, #512 ; 0x200

f2d4: e1a0cc2c lsr ip, ip, #24

f2d8: e6ef5075 uxtb r5, r5

f2dc: e2855c01 add r5, r5, #256 ; 0x100

f2e0: e2877c03 add r7, r7, #768 ; 0x300

f2e4: e793910c ldr r9, [r3, ip, lsl #2]

f2e8: e793e10e ldr lr, [r3, lr, lsl #2]

f2ec: e793c105 ldr ip, [r3, r5, lsl #2]

f2f0: e7937107 ldr r7, [r3, r7, lsl #2]

f2f4: e08ee009 add lr, lr, r9

f2f8: e04ee00c sub lr, lr, ip

f2fc: e0222007 eor r2, r2, r7

f300: e022200e eor r2, r2, lr

f304: e5812004 str r2, [r1, #4]

f308: e590e080 ldr lr, [r0, #128] ; 0x80

f30c: e590c090 ldr ip, [r0, #144] ; 0x90

f310: e20ee01f and lr, lr, #31

f314: e26ee020 rsb lr, lr, #32

f318: e084c00c add ip, r4, ip

f31c: e1a0ce7c ror ip, ip, lr

f320: e1a0582c lsr r5, ip, #16

f324: e1a0e42c lsr lr, ip, #8

f328: e6ef5075 uxtb r5, r5

f32c: e1a07c2c lsr r7, ip, #24

f330: e6efe07e uxtb lr, lr

f334: e2855c01 add r5, r5, #256 ; 0x100

f338: e28e9c02 add r9, lr, #512 ; 0x200

f33c: e6efc07c uxtb ip, ip

f340: e793b107 ldr fp, [r3, r7, lsl #2]

f344: e28cec03 add lr, ip, #768 ; 0x300

f348: e7935105 ldr r5, [r3, r5, lsl #2]

f34c: e793c109 ldr ip, [r3, r9, lsl #2]

f350: e02bb005 eor fp, fp, r5

f354: e793510e ldr r5, [r3, lr, lsl #2]

f358: e04bb00c sub fp, fp, ip

f35c: e08bb005 add fp, fp, r5

f360: e02b5008 eor r5, fp, r8

f364: e5815008 str r5, [r1, #8]

f368: e590e06c ldr lr, [r0, #108] ; 0x6c

f36c: e590c07c ldr ip, [r0, #124] ; 0x7c

f370: e20ee01f and lr, lr, #31

f374: e26ee020 rsb lr, lr, #32

f378: e086c00c add ip, r6, ip

f37c: e1a0ce7c ror ip, ip, lr

f380: e1a0782c lsr r7, ip, #16

f384: e1a0e42c lsr lr, ip, #8

f388: e6ef7077 uxtb r7, r7

f38c: e1a08c2c lsr r8, ip, #24

f390: e2877c01 add r7, r7, #256 ; 0x100

f394: e6efe07e uxtb lr, lr

f398: e28e9c02 add r9, lr, #512 ; 0x200

f39c: e6efe07c uxtb lr, ip

f3a0: e793a108 ldr sl, [r3, r8, lsl #2]

f3a4: e793c107 ldr ip, [r3, r7, lsl #2]

f3a8: e28eec03 add lr, lr, #768 ; 0x300

f3ac: e7937109 ldr r7, [r3, r9, lsl #2]

f3b0: e02aa00c eor sl, sl, ip

f3b4: e793c10e ldr ip, [r3, lr, lsl #2]

f3b8: e04aa007 sub sl, sl, r7

f3bc: e08aa00c add sl, sl, ip

f3c0: e02ac004 eor ip, sl, r4

f3c4: e581c00c str ip, [r1, #12]

f3c8: e590e068 ldr lr, [r0, #104] ; 0x68

f3cc: e5904078 ldr r4, [r0, #120] ; 0x78

f3d0: e20ee01f and lr, lr, #31

f3d4: e26ee020 rsb lr, lr, #32

f3d8: e0444002 sub r4, r4, r2

f3dc: e1a04e74 ror r4, r4, lr

f3e0: e1a07824 lsr r7, r4, #16

f3e4: e1a0e424 lsr lr, r4, #8

f3e8: e6ef7077 uxtb r7, r7

f3ec: e1a08c24 lsr r8, r4, #24

f3f0: e2877c01 add r7, r7, #256 ; 0x100

f3f4: e6efe07e uxtb lr, lr

f3f8: e28eec02 add lr, lr, #512 ; 0x200

f3fc: e6ef4074 uxtb r4, r4

f400: e7939108 ldr r9, [r3, r8, lsl #2]

f404: e2844c03 add r4, r4, #768 ; 0x300

f408: e7938107 ldr r8, [r3, r7, lsl #2]

f40c: e793710e ldr r7, [r3, lr, lsl #2]

f410: e0899008 add r9, r9, r8

f414: e793e104 ldr lr, [r3, r4, lsl #2]

f418: e0299007 eor r9, r9, r7

f41c: e049900e sub r9, r9, lr

f420: e0299006 eor r9, r9, r6

f424: e5819000 str r9, [r1]

f428: e590e064 ldr lr, [r0, #100] ; 0x64

f42c: e5904074 ldr r4, [r0, #116] ; 0x74

f430: e20ee01f and lr, lr, #31

f434: e26ee020 rsb lr, lr, #32

f438: e0244005 eor r4, r4, r5

f43c: e1a04e74 ror r4, r4, lr

f440: e1a0e424 lsr lr, r4, #8

f444: e1a06824 lsr r6, r4, #16

f448: e6efe07e uxtb lr, lr

f44c: e1a07c24 lsr r7, r4, #24

f450: e28eec02 add lr, lr, #512 ; 0x200

f454: e6ef6076 uxtb r6, r6

f458: e2866c01 add r6, r6, #256 ; 0x100

f45c: e7937107 ldr r7, [r3, r7, lsl #2]

f460: e793e10e ldr lr, [r3, lr, lsl #2]

f464: e6ef4074 uxtb r4, r4

f468: e7936106 ldr r6, [r3, r6, lsl #2]

f46c: e2844c03 add r4, r4, #768 ; 0x300

f470: e08ee007 add lr, lr, r7

f474: e7934104 ldr r4, [r3, r4, lsl #2]

f478: e04ee006 sub lr, lr, r6

f47c: e022200e eor r2, r2, lr

f480: e0222004 eor r2, r2, r4

f484: e5812004 str r2, [r1, #4]

f488: e590e060 ldr lr, [r0, #96] ; 0x60

f48c: e5904070 ldr r4, [r0, #112] ; 0x70

f490: e20ee01f and lr, lr, #31

f494: e26ee020 rsb lr, lr, #32

f498: e08c4004 add r4, ip, r4

f49c: e1a04e74 ror r4, r4, lr

f4a0: e1a06824 lsr r6, r4, #16

f4a4: e1a0e424 lsr lr, r4, #8

f4a8: e6ef6076 uxtb r6, r6

f4ac: e1a07c24 lsr r7, r4, #24

f4b0: e2866c01 add r6, r6, #256 ; 0x100

f4b4: e6efe07e uxtb lr, lr

f4b8: e28eec02 add lr, lr, #512 ; 0x200

f4bc: e6ef4074 uxtb r4, r4

f4c0: e793b106 ldr fp, [r3, r6, lsl #2]

f4c4: e7938107 ldr r8, [r3, r7, lsl #2]

f4c8: e2844c03 add r4, r4, #768 ; 0x300

f4cc: e793e10e ldr lr, [r3, lr, lsl #2]

f4d0: e028800b eor r8, r8, fp

f4d4: e793b104 ldr fp, [r3, r4, lsl #2]

f4d8: e048800e sub r8, r8, lr

f4dc: e088800b add r8, r8, fp

f4e0: e028b005 eor fp, r8, r5

f4e4: e581b008 str fp, [r1, #8]

f4e8: e590e04c ldr lr, [r0, #76] ; 0x4c

f4ec: e590405c ldr r4, [r0, #92] ; 0x5c

f4f0: e20ee01f and lr, lr, #31

f4f4: e0894004 add r4, r9, r4

f4f8: e26ee020 rsb lr, lr, #32

f4fc: e1a04e74 ror r4, r4, lr

f500: e1a05824 lsr r5, r4, #16

f504: e1a0e424 lsr lr, r4, #8

f508: e6ef5075 uxtb r5, r5

f50c: e1a06c24 lsr r6, r4, #24

f510: e2855c01 add r5, r5, #256 ; 0x100

f514: e6efe07e uxtb lr, lr

f518: e28eec02 add lr, lr, #512 ; 0x200

f51c: e6ef4074 uxtb r4, r4

f520: e793a105 ldr sl, [r3, r5, lsl #2]

f524: e7937106 ldr r7, [r3, r6, lsl #2]

f528: e2844c03 add r4, r4, #768 ; 0x300

f52c: e793e10e ldr lr, [r3, lr, lsl #2]

f530: e027700a eor r7, r7, sl

f534: e793a104 ldr sl, [r3, r4, lsl #2]

f538: e047700e sub r7, r7, lr

f53c: e087700a add r7, r7, sl

f540: e027a00c eor sl, r7, ip

f544: e581a00c str sl, [r1, #12]

f548: e590c048 ldr ip, [r0, #72] ; 0x48

f54c: e5904058 ldr r4, [r0, #88] ; 0x58

f550: e20cc01f and ip, ip, #31

f554: e26cc020 rsb ip, ip, #32

f558: e0444002 sub r4, r4, r2

f55c: e1a04c74 ror r4, r4, ip

f560: e1a0e824 lsr lr, r4, #16

f564: e1a0c424 lsr ip, r4, #8

f568: e6efe07e uxtb lr, lr

f56c: e1a05c24 lsr r5, r4, #24

f570: e28eec01 add lr, lr, #256 ; 0x100

f574: e6efc07c uxtb ip, ip

f578: e28ccc02 add ip, ip, #512 ; 0x200

f57c: e6ef4074 uxtb r4, r4

f580: e7936105 ldr r6, [r3, r5, lsl #2]

f584: e2844c03 add r4, r4, #768 ; 0x300

f588: e793510e ldr r5, [r3, lr, lsl #2]

f58c: e793e10c ldr lr, [r3, ip, lsl #2]

f590: e0866005 add r6, r6, r5

f594: e793c104 ldr ip, [r3, r4, lsl #2]

f598: e026600e eor r6, r6, lr

f59c: e046600c sub r6, r6, ip

f5a0: e0299006 eor r9, r9, r6

f5a4: e5819000 str r9, [r1]

f5a8: e590c044 ldr ip, [r0, #68] ; 0x44

f5ac: e5904054 ldr r4, [r0, #84] ; 0x54

f5b0: e20cc01f and ip, ip, #31

f5b4: e26cc020 rsb ip, ip, #32

f5b8: e024400b eor r4, r4, fp

f5bc: e1a04c74 ror r4, r4, ip

f5c0: e1a0c424 lsr ip, r4, #8

f5c4: e1a0e824 lsr lr, r4, #16

f5c8: e6efc07c uxtb ip, ip

f5cc: e1a05c24 lsr r5, r4, #24

f5d0: e28ccc02 add ip, ip, #512 ; 0x200

f5d4: e6efe07e uxtb lr, lr

f5d8: e28e6c01 add r6, lr, #256 ; 0x100

f5dc: e6ef4074 uxtb r4, r4

f5e0: e793e10c ldr lr, [r3, ip, lsl #2]

f5e4: e2844c03 add r4, r4, #768 ; 0x300

f5e8: e793c105 ldr ip, [r3, r5, lsl #2]

f5ec: e7935106 ldr r5, [r3, r6, lsl #2]

f5f0: e08ee00c add lr, lr, ip

f5f4: e793c104 ldr ip, [r3, r4, lsl #2]

f5f8: e04ee005 sub lr, lr, r5

f5fc: e02ee00c eor lr, lr, ip

f600: e02ee002 eor lr, lr, r2

f604: e581e004 str lr, [r1, #4]

f608: e5902040 ldr r2, [r0, #64] ; 0x40

f60c: e5904050 ldr r4, [r0, #80] ; 0x50

f610: e202201f and r2, r2, #31

f614: e2622020 rsb r2, r2, #32

f618: e08a4004 add r4, sl, r4

f61c: e1a04274 ror r4, r4, r2

f620: e1a0c824 lsr ip, r4, #16

f624: e1a02424 lsr r2, r4, #8

f628: e6efc07c uxtb ip, ip

f62c: e1a05c24 lsr r5, r4, #24

f630: e28ccc01 add ip, ip, #256 ; 0x100

f634: e6ef2072 uxtb r2, r2

f638: e2822c02 add r2, r2, #512 ; 0x200

f63c: e6ef4074 uxtb r4, r4

f640: e793810c ldr r8, [r3, ip, lsl #2]

f644: e7935105 ldr r5, [r3, r5, lsl #2]

f648: e2844c03 add r4, r4, #768 ; 0x300

f64c: e7932102 ldr r2, [r3, r2, lsl #2]

f650: e0255008 eor r5, r5, r8

f654: e7938104 ldr r8, [r3, r4, lsl #2]

f658: e0455002 sub r5, r5, r2

f65c: e0855008 add r5, r5, r8

f660: e025800b eor r8, r5, fp

f664: e5818008 str r8, [r1, #8]

f668: e590202c ldr r2, [r0, #44] ; 0x2c

f66c: e590403c ldr r4, [r0, #60] ; 0x3c

f670: e202201f and r2, r2, #31

f674: e2622020 rsb r2, r2, #32

f678: e0894004 add r4, r9, r4

f67c: e1a04274 ror r4, r4, r2

f680: e1a0c824 lsr ip, r4, #16

f684: e1a02424 lsr r2, r4, #8

f688: e6efc07c uxtb ip, ip

f68c: e1a06c24 lsr r6, r4, #24

f690: e28c5c01 add r5, ip, #256 ; 0x100

f694: e6ef2072 uxtb r2, r2

f698: e2822c02 add r2, r2, #512 ; 0x200

f69c: e6ef4074 uxtb r4, r4

f6a0: e7937105 ldr r7, [r3, r5, lsl #2]

f6a4: e793c106 ldr ip, [r3, r6, lsl #2]

f6a8: e2844c03 add r4, r4, #768 ; 0x300

f6ac: e7932102 ldr r2, [r3, r2, lsl #2]

f6b0: e02cc007 eor ip, ip, r7

f6b4: e7937104 ldr r7, [r3, r4, lsl #2]

f6b8: e04cc002 sub ip, ip, r2

f6bc: e08cc007 add ip, ip, r7

f6c0: e02c700a eor r7, ip, sl

f6c4: e581700c str r7, [r1, #12]

f6c8: e5902028 ldr r2, [r0, #40] ; 0x28

f6cc: e5904038 ldr r4, [r0, #56] ; 0x38

f6d0: e202201f and r2, r2, #31

f6d4: e2622020 rsb r2, r2, #32

f6d8: e044400e sub r4, r4, lr

f6dc: e1a04274 ror r4, r4, r2

f6e0: e1a0c824 lsr ip, r4, #16

f6e4: e1a02424 lsr r2, r4, #8

f6e8: e6efc07c uxtb ip, ip

f6ec: e1a05c24 lsr r5, r4, #24

f6f0: e28ccc01 add ip, ip, #256 ; 0x100

f6f4: e6ef2072 uxtb r2, r2

f6f8: e2822c02 add r2, r2, #512 ; 0x200

f6fc: e6ef4074 uxtb r4, r4

f700: e7936105 ldr r6, [r3, r5, lsl #2]

f704: e2844c03 add r4, r4, #768 ; 0x300

f708: e793510c ldr r5, [r3, ip, lsl #2]

f70c: e793c102 ldr ip, [r3, r2, lsl #2]

f710: e0866005 add r6, r6, r5

f714: e7932104 ldr r2, [r3, r4, lsl #2]

f718: e026600c eor r6, r6, ip

f71c: e0466002 sub r6, r6, r2

f720: e0266009 eor r6, r6, r9

f724: e5816000 str r6, [r1]

f728: e590c024 ldr ip, [r0, #36] ; 0x24

f72c: e5902034 ldr r2, [r0, #52] ; 0x34

f730: e20cc01f and ip, ip, #31

f734: e26cc020 rsb ip, ip, #32

f738: e0222008 eor r2, r2, r8

f73c: e1a02c72 ror r2, r2, ip

f740: e1a05422 lsr r5, r2, #8

f744: e1a0c822 lsr ip, r2, #16

f748: e6ef5075 uxtb r5, r5

f74c: e1a04c22 lsr r4, r2, #24

f750: e6ef9072 uxtb r9, r2

f754: e2855c02 add r5, r5, #512 ; 0x200

f758: e6efc07c uxtb ip, ip

f75c: ea000001 b f768 <mcrypt\_mutex\_register@@Base+0xafd4>

f760: 0002588c andeq r5, r2, ip, lsl #17

f764: 000000c0 andeq r0, r0, r0, asr #1

f768: e2899c03 add r9, r9, #768 ; 0x300

f76c: e28ccc01 add ip, ip, #256 ; 0x100

f770: e7932105 ldr r2, [r3, r5, lsl #2]

f774: e7935104 ldr r5, [r3, r4, lsl #2]

f778: e793c10c ldr ip, [r3, ip, lsl #2]

f77c: e7934109 ldr r4, [r3, r9, lsl #2]

f780: e0822005 add r2, r2, r5

f784: e042200c sub r2, r2, ip

f788: e02ee004 eor lr, lr, r4

f78c: e02ee002 eor lr, lr, r2

f790: e581e004 str lr, [r1, #4]

f794: e5902020 ldr r2, [r0, #32]

f798: e5904030 ldr r4, [r0, #48] ; 0x30

f79c: e202201f and r2, r2, #31

f7a0: e2622020 rsb r2, r2, #32

f7a4: e0874004 add r4, r7, r4

f7a8: e1a04274 ror r4, r4, r2

f7ac: e1a0c824 lsr ip, r4, #16

f7b0: e1a02424 lsr r2, r4, #8

f7b4: e6efc07c uxtb ip, ip

f7b8: e1a05c24 lsr r5, r4, #24

f7bc: e28ccc01 add ip, ip, #256 ; 0x100

f7c0: e6ef2072 uxtb r2, r2

f7c4: e2822c02 add r2, r2, #512 ; 0x200

f7c8: e6ef4074 uxtb r4, r4

f7cc: e793910c ldr r9, [r3, ip, lsl #2]

f7d0: e7935105 ldr r5, [r3, r5, lsl #2]

f7d4: e2844c03 add r4, r4, #768 ; 0x300

f7d8: e793c102 ldr ip, [r3, r2, lsl #2]

f7dc: e0255009 eor r5, r5, r9

f7e0: e7932104 ldr r2, [r3, r4, lsl #2]

f7e4: e045500c sub r5, r5, ip

f7e8: e0855002 add r5, r5, r2

f7ec: e0255008 eor r5, r5, r8

f7f0: e5815008 str r5, [r1, #8]

f7f4: e590200c ldr r2, [r0, #12]

f7f8: e590401c ldr r4, [r0, #28]

f7fc: e202201f and r2, r2, #31

f800: e2622020 rsb r2, r2, #32

f804: e0864004 add r4, r6, r4

f808: e1a04274 ror r4, r4, r2

f80c: e1a0c824 lsr ip, r4, #16

f810: e1a02424 lsr r2, r4, #8

f814: e6efc07c uxtb ip, ip

f818: e1a09c24 lsr r9, r4, #24

f81c: e28c8c01 add r8, ip, #256 ; 0x100

f820: e6ef2072 uxtb r2, r2

f824: e2822c02 add r2, r2, #512 ; 0x200

f828: e6ef4074 uxtb r4, r4

f82c: e793c109 ldr ip, [r3, r9, lsl #2]

f830: e2844c03 add r4, r4, #768 ; 0x300

f834: e7939108 ldr r9, [r3, r8, lsl #2]

f838: e7938102 ldr r8, [r3, r2, lsl #2]

f83c: e02cc009 eor ip, ip, r9

f840: e7932104 ldr r2, [r3, r4, lsl #2]

f844: e04cc008 sub ip, ip, r8

f848: e08cc002 add ip, ip, r2

f84c: e02cc007 eor ip, ip, r7

f850: e581c00c str ip, [r1, #12]

f854: e5904008 ldr r4, [r0, #8]

f858: e5902018 ldr r2, [r0, #24]

f85c: e204401f and r4, r4, #31

f860: e2644020 rsb r4, r4, #32

f864: e042200e sub r2, r2, lr

f868: e1a02472 ror r2, r2, r4

f86c: e1a07822 lsr r7, r2, #16

f870: e1a04422 lsr r4, r2, #8

f874: e6ef7077 uxtb r7, r7

f878: e1a08c22 lsr r8, r2, #24

f87c: e2877c01 add r7, r7, #256 ; 0x100

f880: e6ef4074 uxtb r4, r4

f884: e2849c02 add r9, r4, #512 ; 0x200

f888: e6ef4072 uxtb r4, r2

f88c: e2844c03 add r4, r4, #768 ; 0x300

f890: e7932108 ldr r2, [r3, r8, lsl #2]

f894: e7938107 ldr r8, [r3, r7, lsl #2]

f898: e7937109 ldr r7, [r3, r9, lsl #2]

f89c: e7934104 ldr r4, [r3, r4, lsl #2]

f8a0: e0822008 add r2, r2, r8

f8a4: e0222007 eor r2, r2, r7

f8a8: e0422004 sub r2, r2, r4

f8ac: e0266002 eor r6, r6, r2

f8b0: e5816000 str r6, [r1]

f8b4: e5904004 ldr r4, [r0, #4]

f8b8: e5902014 ldr r2, [r0, #20]

f8bc: e204401f and r4, r4, #31

f8c0: e2644020 rsb r4, r4, #32

f8c4: e0222005 eor r2, r2, r5

f8c8: e1a02472 ror r2, r2, r4

f8cc: e1a04422 lsr r4, r2, #8

f8d0: e1a06822 lsr r6, r2, #16

f8d4: e6ef4074 uxtb r4, r4

f8d8: e6ef7072 uxtb r7, r2

f8dc: e2844c02 add r4, r4, #512 ; 0x200

f8e0: e1a02c22 lsr r2, r2, #24

f8e4: e6ef6076 uxtb r6, r6

f8e8: e2877c03 add r7, r7, #768 ; 0x300

f8ec: e2866c01 add r6, r6, #256 ; 0x100

f8f0: e7934104 ldr r4, [r3, r4, lsl #2]

f8f4: e7932102 ldr r2, [r3, r2, lsl #2]

f8f8: e7937107 ldr r7, [r3, r7, lsl #2]

f8fc: e7936106 ldr r6, [r3, r6, lsl #2]

f900: e0842002 add r2, r4, r2

f904: e0422006 sub r2, r2, r6

f908: e02ee007 eor lr, lr, r7

f90c: e02ee002 eor lr, lr, r2

f910: e581e004 str lr, [r1, #4]

f914: e590e000 ldr lr, [r0]

f918: e5900010 ldr r0, [r0, #16]

f91c: e20e201f and r2, lr, #31

f920: e08cc000 add ip, ip, r0

f924: e2622020 rsb r2, r2, #32

f928: e1a0227c ror r2, ip, r2

f92c: e1a0e822 lsr lr, r2, #16

f930: e1a0c422 lsr ip, r2, #8

f934: e6efe07e uxtb lr, lr

f938: e1a00c22 lsr r0, r2, #24

f93c: e28eec01 add lr, lr, #256 ; 0x100

f940: e6efc07c uxtb ip, ip

f944: e28ccc02 add ip, ip, #512 ; 0x200

f948: e6ef2072 uxtb r2, r2

f94c: e7930100 ldr r0, [r3, r0, lsl #2]

f950: e793e10e ldr lr, [r3, lr, lsl #2]

f954: e2822c03 add r2, r2, #768 ; 0x300

f958: e793c10c ldr ip, [r3, ip, lsl #2]

f95c: e7932102 ldr r2, [r3, r2, lsl #2]

f960: e020000e eor r0, r0, lr

f964: e040300c sub r3, r0, ip

f968: e0833002 add r3, r3, r2

f96c: e0255003 eor r5, r5, r3

f970: e5815008 str r5, [r1, #8]

f974: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

f978: e3a00d06 mov r0, #384 ; 0x180

f97c: e12fff1e bx lr

f980: e3a00010 mov r0, #16

f984: e12fff1e bx lr

f988: e3a00001 mov r0, #1

f98c: e12fff1e bx lr

f990: e3a00020 mov r0, #32

f994: e12fff1e bx lr

f998: e3a03003 mov r3, #3

f99c: e5803000 str r3, [r0]

f9a0: e59f0004 ldr r0, [pc, #4] ; f9ac <mcrypt\_mutex\_register@@Base+0xb218>

f9a4: e08f0000 add r0, pc, r0

f9a8: e12fff1e bx lr

f9ac: 00010e8c andeq r0, r1, ip, lsl #29

f9b0: e59f0004 ldr r0, [pc, #4] ; f9bc <mcrypt\_mutex\_register@@Base+0xb228>

f9b4: e08f0000 add r0, pc, r0

f9b8: e12fff1e bx lr

f9bc: 00010e88 andeq r0, r1, r8, lsl #29

f9c0: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

f9c4: e24dd0e8 sub sp, sp, #232 ; 0xe8

f9c8: ebffffec bl f980 <mcrypt\_mutex\_register@@Base+0xb1ec>

f9cc: e1a06000 mov r6, r0

f9d0: ebffffee bl f990 <mcrypt\_mutex\_register@@Base+0xb1fc>

f9d4: e1a01000 mov r1, r0

f9d8: e3a00001 mov r0, #1

f9dc: ebffca14 bl 2234 <calloc@plt>

f9e0: e2507000 subs r7, r0, #0

f9e4: 12475001 subne r5, r7, #1

f9e8: 13a0400a movne r4, #10

f9ec: 12678001 rsbne r8, r7, #1

f9f0: 1a000005 bne fa0c <mcrypt\_mutex\_register@@Base+0xb278>

f9f4: ea000060 b fb7c <mcrypt\_mutex\_register@@Base+0xb3e8>

f9f8: e2742000 rsbs r2, r4, #0

f9fc: e2844002 add r4, r4, #2

fa00: e6ef2072 uxtb r2, r2

fa04: 52623000 rsbpl r3, r2, #0

fa08: e5e53001 strb r3, [r5, #1]!

fa0c: ebffffdf bl f990 <mcrypt\_mutex\_register@@Base+0xb1fc>

fa10: e0882005 add r2, r8, r5

fa14: e6ef3074 uxtb r3, r4

fa18: e1500002 cmp r0, r2

fa1c: cafffff5 bgt f9f8 <mcrypt\_mutex\_register@@Base+0xb264>

fa20: e3560000 cmp r6, #0

fa24: da000039 ble fb10 <mcrypt\_mutex\_register@@Base+0xb37c>

fa28: e1a0400d mov r4, sp

fa2c: e1a02004 mov r2, r4

fa30: e3a03000 mov r3, #0

fa34: e4c23001 strb r3, [r2], #1

fa38: e2833001 add r3, r3, #1

fa3c: e1560003 cmp r6, r3

fa40: 1afffffb bne fa34 <mcrypt\_mutex\_register@@Base+0xb2a0>

fa44: ebffffcb bl f978 <mcrypt\_mutex\_register@@Base+0xb1e4>

fa48: ebffca29 bl 22f4 <malloc@plt>

fa4c: e2509000 subs r9, r0, #0

fa50: 0a000049 beq fb7c <mcrypt\_mutex\_register@@Base+0xb3e8>

fa54: e28d8010 add r8, sp, #16

fa58: e1a01004 mov r1, r4

fa5c: e1a02006 mov r2, r6

fa60: e1a00008 mov r0, r8

fa64: ebffca07 bl 2288 <memcpy@plt>

fa68: ebffffc8 bl f990 <mcrypt\_mutex\_register@@Base+0xb1fc>

fa6c: e1a01007 mov r1, r7

fa70: e28da020 add sl, sp, #32

fa74: e0886006 add r6, r8, r6

fa78: e1a05008 mov r5, r8

fa7c: e1a0400a mov r4, sl

fa80: e1a02000 mov r2, r0

fa84: e1a00009 mov r0, r9

fa88: ebfff4a9 bl cd34 <mcrypt\_mutex\_register@@Base+0x85a0>

fa8c: e1a01008 mov r1, r8

fa90: e1a00009 mov r0, r9

fa94: ebfff69d bl d510 <mcrypt\_mutex\_register@@Base+0x8d7c>

fa98: e1a00007 mov r0, r7

fa9c: ebffc9f6 bl 227c <free@plt>

faa0: e59f70f0 ldr r7, [pc, #240] ; fb98 <mcrypt\_mutex\_register@@Base+0xb404>

faa4: e08f7007 add r7, pc, r7

faa8: e4d52001 ldrb r2, [r5], #1

faac: e1a00004 mov r0, r4

fab0: e1a01007 mov r1, r7

fab4: ebffca2c bl 236c <sprintf@plt>

fab8: e1560005 cmp r6, r5

fabc: e2844002 add r4, r4, #2

fac0: 1afffff8 bne faa8 <mcrypt\_mutex\_register@@Base+0xb314>

fac4: e59f40d0 ldr r4, [pc, #208] ; fb9c <mcrypt\_mutex\_register@@Base+0xb408>

fac8: e1a0000a mov r0, sl

facc: e08f4004 add r4, pc, r4

fad0: e1a01004 mov r1, r4

fad4: ebffc9dc bl 224c <strcmp@plt>

fad8: e3500000 cmp r0, #0

fadc: 1a00001c bne fb54 <mcrypt\_mutex\_register@@Base+0xb3c0>

fae0: e1a01008 mov r1, r8

fae4: e1a00009 mov r0, r9

fae8: ebfffb15 bl e744 <mcrypt\_mutex\_register@@Base+0x9fb0>

faec: e1a00009 mov r0, r9

faf0: ebffc9e1 bl 227c <free@plt>

faf4: e1a00008 mov r0, r8

faf8: e1a0100d mov r1, sp

fafc: ebffc9d2 bl 224c <strcmp@plt>

fb00: e3500000 cmp r0, #0

fb04: 1a00001e bne fb84 <mcrypt\_mutex\_register@@Base+0xb3f0>

fb08: e28dd0e8 add sp, sp, #232 ; 0xe8

fb0c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

fb10: ebffff98 bl f978 <mcrypt\_mutex\_register@@Base+0xb1e4>

fb14: ebffc9f6 bl 22f4 <malloc@plt>

fb18: e2509000 subs r9, r0, #0

fb1c: 0a000016 beq fb7c <mcrypt\_mutex\_register@@Base+0xb3e8>

fb20: ebffff9a bl f990 <mcrypt\_mutex\_register@@Base+0xb1fc>

fb24: e28d8010 add r8, sp, #16

fb28: e1a01007 mov r1, r7

fb2c: e28da020 add sl, sp, #32

fb30: e1a02000 mov r2, r0

fb34: e1a00009 mov r0, r9

fb38: ebfff47d bl cd34 <mcrypt\_mutex\_register@@Base+0x85a0>

fb3c: e1a01008 mov r1, r8

fb40: e1a00009 mov r0, r9

fb44: ebfff671 bl d510 <mcrypt\_mutex\_register@@Base+0x8d7c>

fb48: e1a00007 mov r0, r7

fb4c: ebffc9ca bl 227c <free@plt>

fb50: eaffffdb b fac4 <mcrypt\_mutex\_register@@Base+0xb330>

fb54: e59f0044 ldr r0, [pc, #68] ; fba0 <mcrypt\_mutex\_register@@Base+0xb40c>

fb58: e08f0000 add r0, pc, r0

fb5c: ebffc9e1 bl 22e8 <puts@plt>

fb60: e59f003c ldr r0, [pc, #60] ; fba4 <mcrypt\_mutex\_register@@Base+0xb410>

fb64: e1a0200a mov r2, sl

fb68: e1a01004 mov r1, r4

fb6c: e08f0000 add r0, pc, r0

fb70: ebffc9bb bl 2264 <printf@plt>

fb74: e1a00009 mov r0, r9

fb78: ebffc9bf bl 227c <free@plt>

fb7c: e3e00000 mvn r0, #0

fb80: eaffffe0 b fb08 <mcrypt\_mutex\_register@@Base+0xb374>

fb84: e59f001c ldr r0, [pc, #28] ; fba8 <mcrypt\_mutex\_register@@Base+0xb414>

fb88: e08f0000 add r0, pc, r0

fb8c: ebffc9d5 bl 22e8 <puts@plt>

fb90: e3e00000 mvn r0, #0

fb94: eaffffdb b fb08 <mcrypt\_mutex\_register@@Base+0xb374>

fb98: 00010bbc ; <UNDEFINED> instruction: 0x00010bbc

fb9c: 00010d7c andeq r0, r1, ip, ror sp

fba0: 00010b24 andeq r0, r1, r4, lsr #22

fba4: 00010b28 andeq r0, r1, r8, lsr #22

fba8: 00010b24 andeq r0, r1, r4, lsr #22

fbac: e59f0000 ldr r0, [pc] ; fbb4 <mcrypt\_mutex\_register@@Base+0xb420>

fbb0: e12fff1e bx lr

fbb4: 01315731 teqeq r1, r1, lsr r7

fbb8: e3a0c001 mov ip, #1

fbbc: e3510000 cmp r1, #0

fbc0: e1a0221c lsl r2, ip, r2

fbc4: 0a00000a beq fbf4 <mcrypt\_mutex\_register@@Base+0xb460>

fbc8: e3a0c000 mov ip, #0

fbcc: e3110001 tst r1, #1

fbd0: 102cc000 eorne ip, ip, r0

fbd4: e1a00080 lsl r0, r0, #1

fbd8: e1120000 tst r2, r0

fbdc: e1a010a1 lsr r1, r1, #1

fbe0: 10200003 eorne r0, r0, r3

fbe4: e3510000 cmp r1, #0

fbe8: 1afffff7 bne fbcc <mcrypt\_mutex\_register@@Base+0xb438>

fbec: e1a0000c mov r0, ip

fbf0: e12fff1e bx lr

fbf4: e1a0c001 mov ip, r1

fbf8: e1a0000c mov r0, ip

fbfc: e12fff1e bx lr

fc00: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

fc04: e59f612c ldr r6, [pc, #300] ; fd38 <mcrypt\_mutex\_register@@Base+0xb5a4>

fc08: e59f312c ldr r3, [pc, #300] ; fd3c <mcrypt\_mutex\_register@@Base+0xb5a8>

fc0c: e08f6006 add r6, pc, r6

fc10: e59fa128 ldr sl, [pc, #296] ; fd40 <mcrypt\_mutex\_register@@Base+0xb5ac>

fc14: e7968003 ldr r8, [r6, r3]

fc18: e59f7124 ldr r7, [pc, #292] ; fd44 <mcrypt\_mutex\_register@@Base+0xb5b0>

fc1c: e2889d7f add r9, r8, #8128 ; 0x1fc0

fc20: e2485001 sub r5, r8, #1

fc24: e289903f add r9, r9, #63 ; 0x3f

fc28: e2688001 rsb r8, r8, #1

fc2c: e0884005 add r4, r8, r5

fc30: e024400a eor r4, r4, sl

fc34: e1a01004 mov r1, r4

fc38: e1a03007 mov r3, r7

fc3c: e3a0200d mov r2, #13

fc40: e1a00004 mov r0, r4

fc44: ebffffdb bl fbb8 <mcrypt\_mutex\_register@@Base+0xb424>

fc48: e1a03007 mov r3, r7

fc4c: e1a01004 mov r1, r4

fc50: e3a0200d mov r2, #13

fc54: ebffffd7 bl fbb8 <mcrypt\_mutex\_register@@Base+0xb424>

fc58: e5e50001 strb r0, [r5, #1]!

fc5c: e1590005 cmp r9, r5

fc60: 1afffff1 bne fc2c <mcrypt\_mutex\_register@@Base+0xb498>

fc64: e59f30dc ldr r3, [pc, #220] ; fd48 <mcrypt\_mutex\_register@@Base+0xb5b4>

fc68: e59fa0dc ldr sl, [pc, #220] ; fd4c <mcrypt\_mutex\_register@@Base+0xb5b8>

fc6c: e59f70dc ldr r7, [pc, #220] ; fd50 <mcrypt\_mutex\_register@@Base+0xb5bc>

fc70: e7968003 ldr r8, [r6, r3]

fc74: e2889e7f add r9, r8, #2032 ; 0x7f0

fc78: e2485001 sub r5, r8, #1

fc7c: e289900f add r9, r9, #15

fc80: e2688001 rsb r8, r8, #1

fc84: e0884005 add r4, r8, r5

fc88: e024400a eor r4, r4, sl

fc8c: e1a01004 mov r1, r4

fc90: e1a03007 mov r3, r7

fc94: e3a0200b mov r2, #11

fc98: e1a00004 mov r0, r4

fc9c: ebffffc5 bl fbb8 <mcrypt\_mutex\_register@@Base+0xb424>

fca0: e1a03007 mov r3, r7

fca4: e1a01004 mov r1, r4

fca8: e3a0200b mov r2, #11

fcac: ebffffc1 bl fbb8 <mcrypt\_mutex\_register@@Base+0xb424>

fcb0: e5e50001 strb r0, [r5, #1]!

fcb4: e1550009 cmp r5, r9

fcb8: 1afffff1 bne fc84 <mcrypt\_mutex\_register@@Base+0xb4f0>

fcbc: e59f2090 ldr r2, [pc, #144] ; fd54 <mcrypt\_mutex\_register@@Base+0xb5c0>

fcc0: e3a03000 mov r3, #0

fcc4: e796c002 ldr ip, [r6, r2]

fcc8: e28ce004 add lr, ip, #4

fccc: e1a01203 lsl r1, r3, #4

fcd0: e1a04583 lsl r4, r3, #11

fcd4: e1a02383 lsl r2, r3, #7

fcd8: e1a00703 lsl r0, r3, #14

fcdc: e2044902 and r4, r4, #32768 ; 0x8000

fce0: e1a05903 lsl r5, r3, #18

fce4: e2000502 and r0, r0, #8388608 ; 0x800000

fce8: e6ef1071 uxtb r1, r1

fcec: e2022902 and r2, r2, #32768 ; 0x8000

fcf0: e1811004 orr r1, r1, r4

fcf4: e1822000 orr r2, r2, r0

fcf8: e1a04c83 lsl r4, r3, #25

fcfc: e2036080 and r6, r3, #128 ; 0x80

fd00: e1a00a83 lsl r0, r3, #21

fd04: e2055502 and r5, r5, #8388608 ; 0x800000

fd08: e1811005 orr r1, r1, r5

fd0c: e2044102 and r4, r4, #-2147483648 ; 0x80000000

fd10: e1822006 orr r2, r2, r6

fd14: e2000102 and r0, r0, #-2147483648 ; 0x80000000

fd18: e1811004 orr r1, r1, r4

fd1c: e1822000 orr r2, r2, r0

fd20: e78c1003 str r1, [ip, r3]

fd24: e783200e str r2, [r3, lr]

fd28: e2833008 add r3, r3, #8

fd2c: e3530b02 cmp r3, #2048 ; 0x800

fd30: 1affffe5 bne fccc <mcrypt\_mutex\_register@@Base+0xb538>

fd34: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

fd38: 000243ec andeq r4, r2, ip, ror #7

fd3c: 000000a8 andeq r0, r0, r8, lsr #1

fd40: 00001fff strdeq r1, [r0], -pc ; <UNPREDICTABLE>

fd44: 00002911 andeq r2, r0, r1, lsl r9

fd48: 000000cc andeq r0, r0, ip, asr #1

fd4c: 000007ff strdeq r0, [r0], -pc ; <UNPREDICTABLE>

fd50: 00000aa7 andeq r0, r0, r7, lsr #21

fd54: 000000d0 ldrdeq r0, [r0], -r0 ; <UNPREDICTABLE>

fd58: e1a03002 mov r3, r2

fd5c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

fd60: e24dd024 sub sp, sp, #36 ; 0x24

fd64: e5914000 ldr r4, [r1]

fd68: e5912004 ldr r2, [r1, #4]

fd6c: e59f5234 ldr r5, [pc, #564] ; ffa8 <mcrypt\_mutex\_register@@Base+0xb814>

fd70: e5931000 ldr r1, [r3]

fd74: e58d3008 str r3, [sp, #8]

fd78: e59fe22c ldr lr, [pc, #556] ; ffac <mcrypt\_mutex\_register@@Base+0xb818>

fd7c: e0243002 eor r3, r4, r2

fd80: e08f5005 add r5, pc, r5

fd84: e0033001 and r3, r3, r1

fd88: e0222003 eor r2, r2, r3

fd8c: e59f921c ldr r9, [pc, #540] ; ffb0 <mcrypt\_mutex\_register@@Base+0xb81c>

fd90: e59fc21c ldr ip, [pc, #540] ; ffb4 <mcrypt\_mutex\_register@@Base+0xb820>

fd94: e795600e ldr r6, [r5, lr]

fd98: e0241003 eor r1, r4, r3

fd9c: e1a07005 mov r7, r5

fda0: e59f3210 ldr r3, [pc, #528] ; ffb8 <mcrypt\_mutex\_register@@Base+0xb824>

fda4: e59fa210 ldr sl, [pc, #528] ; ffbc <mcrypt\_mutex\_register@@Base+0xb828>

fda8: e0025009 and r5, r2, r9

fdac: e009e822 and lr, r9, r2, lsr #16

fdb0: e797c00c ldr ip, [r7, ip]

fdb4: e7d68005 ldrb r8, [r6, r5]

fdb8: e58d700c str r7, [sp, #12]

fdbc: e7973003 ldr r3, [r7, r3]

fdc0: e58d6004 str r6, [sp, #4]

fdc4: e7d6600e ldrb r6, [r6, lr]

fdc8: e00ae422 and lr, sl, r2, lsr #8

fdcc: e001400a and r4, r1, sl

fdd0: e7dc500e ldrb r5, [ip, lr]

fdd4: e00ae821 and lr, sl, r1, lsr #16

fdd8: e7dc7004 ldrb r7, [ip, r4]

fddc: e7dc400e ldrb r4, [ip, lr]

fde0: e1a0e401 lsl lr, r1, #8

fde4: e18eec22 orr lr, lr, r2, lsr #24

fde8: e1a02402 lsl r2, r2, #8

fdec: e1822c21 orr r2, r2, r1, lsr #24

fdf0: e0022009 and r2, r2, r9

fdf4: e0091421 and r1, r9, r1, lsr #8

fdf8: e59d9004 ldr r9, [sp, #4]

fdfc: e793b188 ldr fp, [r3, r8, lsl #3]

fe00: e0838188 add r8, r3, r8, lsl #3

fe04: e7d91001 ldrb r1, [r9, r1]

fe08: e7d99002 ldrb r9, [r9, r2]

fe0c: e7932187 ldr r2, [r3, r7, lsl #3]

fe10: e00ee00a and lr, lr, sl

fe14: e182b22b orr fp, r2, fp, lsr #4

fe18: e7932186 ldr r2, [r3, r6, lsl #3]

fe1c: e598a004 ldr sl, [r8, #4]

fe20: e0837187 add r7, r3, r7, lsl #3

fe24: e18bb322 orr fp, fp, r2, lsr #6

fe28: e7932185 ldr r2, [r3, r5, lsl #3]

fe2c: e0836186 add r6, r3, r6, lsl #3

fe30: e0835185 add r5, r3, r5, lsl #3

fe34: e18bb2a2 orr fp, fp, r2, lsr #5

fe38: e7932184 ldr r2, [r3, r4, lsl #3]

fe3c: e7dce00e ldrb lr, [ip, lr]

fe40: e58da010 str sl, [sp, #16]

fe44: e5966004 ldr r6, [r6, #4]

fe48: e597a004 ldr sl, [r7, #4]

fe4c: e18b2122 orr r2, fp, r2, lsr #2

fe50: e5955004 ldr r5, [r5, #4]

fe54: e793b181 ldr fp, [r3, r1, lsl #3]

fe58: e0831181 add r1, r3, r1, lsl #3

fe5c: e58da014 str sl, [sp, #20]

fe60: e58d6018 str r6, [sp, #24]

fe64: e58d501c str r5, [sp, #28]

fe68: e591a004 ldr sl, [r1, #4]

fe6c: e59d1008 ldr r1, [sp, #8]

fe70: e0834184 add r4, r3, r4, lsl #3

fe74: e793518e ldr r5, [r3, lr, lsl #3]

fe78: e5911004 ldr r1, [r1, #4]

fe7c: e18220ab orr r2, r2, fp, lsr #1

fe80: e594b004 ldr fp, [r4, #4]

fe84: e7934189 ldr r4, [r3, r9, lsl #3]

fe88: e083e18e add lr, r3, lr, lsl #3

fe8c: e18223a5 orr r2, r2, r5, lsr #7

fe90: e59e6004 ldr r6, [lr, #4]

fe94: e18221a4 orr r2, r2, r4, lsr #3

fe98: e1a0e2a1 lsr lr, r1, #5

fe9c: e0833189 add r3, r3, r9, lsl #3

fea0: e20eec07 and lr, lr, #1792 ; 0x700

fea4: e59d9004 ldr r9, [sp, #4]

fea8: e18eec22 orr lr, lr, r2, lsr #24

feac: e1a08802 lsl r8, r2, #16

feb0: e7d9700e ldrb r7, [r9, lr]

feb4: e1a0e181 lsl lr, r1, #3

feb8: e20eec1f and lr, lr, #7936 ; 0x1f00

febc: e18eec28 orr lr, lr, r8, lsr #24

fec0: e59d8010 ldr r8, [sp, #16]

fec4: e5935004 ldr r5, [r3, #4]

fec8: e59d3014 ldr r3, [sp, #20]

fecc: e1a04401 lsl r4, r1, #8

fed0: e1833228 orr r3, r3, r8, lsr #4

fed4: e2044c1f and r4, r4, #7936 ; 0x1f00

fed8: e6ef8072 uxtb r8, r2

fedc: e1844008 orr r4, r4, r8

fee0: e1a01121 lsr r1, r1, #2

fee4: e1a02402 lsl r2, r2, #8

fee8: e2011c07 and r1, r1, #1792 ; 0x700

feec: e7dce00e ldrb lr, [ip, lr]

fef0: e1812c22 orr r2, r1, r2, lsr #24

fef4: e7dc1004 ldrb r1, [ip, r4]

fef8: e59d4018 ldr r4, [sp, #24]

fefc: e7d92002 ldrb r2, [r9, r2]

ff00: e1817c07 orr r7, r1, r7, lsl #24

ff04: e187740e orr r7, r7, lr, lsl #8

ff08: e59de01c ldr lr, [sp, #28]

ff0c: e1833324 orr r3, r3, r4, lsr #6

ff10: e5904000 ldr r4, [r0]

ff14: e18332ae orr r3, r3, lr, lsr #5

ff18: e183b12b orr fp, r3, fp, lsr #2

ff1c: e59d3008 ldr r3, [sp, #8]

ff20: e1877802 orr r7, r7, r2, lsl #16

ff24: e0244007 eor r4, r4, r7

ff28: e5804000 str r4, [r0]

ff2c: e18ba0aa orr sl, fp, sl, lsr #1

ff30: e5933004 ldr r3, [r3, #4]

ff34: e18aa3a6 orr sl, sl, r6, lsr #7

ff38: e18a51a5 orr r5, sl, r5, lsr #3

ff3c: e1a0eaa3 lsr lr, r3, #21

ff40: e1a04805 lsl r4, r5, #16

ff44: e1a02423 lsr r2, r3, #8

ff48: e1a016a3 lsr r1, r3, #13

ff4c: e20eec07 and lr, lr, #1792 ; 0x700

ff50: e6ef6075 uxtb r6, r5

ff54: e18eec25 orr lr, lr, r5, lsr #24

ff58: e2022c1f and r2, r2, #7936 ; 0x1f00

ff5c: e2011c1f and r1, r1, #7936 ; 0x1f00

ff60: e1a03923 lsr r3, r3, #18

ff64: e1a05405 lsl r5, r5, #8

ff68: e1822006 orr r2, r2, r6

ff6c: e1811c24 orr r1, r1, r4, lsr #24

ff70: e2033c07 and r3, r3, #1792 ; 0x700

ff74: e7d9e00e ldrb lr, [r9, lr]

ff78: e1835c25 orr r5, r3, r5, lsr #24

ff7c: e7dc3002 ldrb r3, [ip, r2]

ff80: e7dcc001 ldrb ip, [ip, r1]

ff84: e7d91005 ldrb r1, [r9, r5]

ff88: e1833c0e orr r3, r3, lr, lsl #24

ff8c: e5902004 ldr r2, [r0, #4]

ff90: e183340c orr r3, r3, ip, lsl #8

ff94: e1833801 orr r3, r3, r1, lsl #16

ff98: e0233002 eor r3, r3, r2

ff9c: e5803004 str r3, [r0, #4]

ffa0: e28dd024 add sp, sp, #36 ; 0x24

ffa4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

ffa8: 00024278 andeq r4, r2, r8, ror r2

ffac: 000000cc andeq r0, r0, ip, asr #1

ffb0: 000007ff strdeq r0, [r0], -pc ; <UNPREDICTABLE>

ffb4: 000000a8 andeq r0, r0, r8, lsr #1

ffb8: 000000d0 ldrdeq r0, [r0], -r0 ; <UNPREDICTABLE>

ffbc: 00001fff strdeq r1, [r0], -pc ; <UNPREDICTABLE>

ffc0: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

ffc4: e1a05000 mov r5, r0

ffc8: e59f4148 ldr r4, [pc, #328] ; 10118 <mcrypt\_mutex\_register@@Base+0xb984>

ffcc: e59f3148 ldr r3, [pc, #328] ; 1011c <mcrypt\_mutex\_register@@Base+0xb988>

ffd0: e08f4004 add r4, pc, r4

ffd4: e24dd03c sub sp, sp, #60 ; 0x3c

ffd8: e7946003 ldr r6, [r4, r3]

ffdc: e1a09001 mov r9, r1

ffe0: e5963000 ldr r3, [r6]

ffe4: e3530000 cmp r3, #0

ffe8: 0a000046 beq 10108 <mcrypt\_mutex\_register@@Base+0xb974>

ffec: e59f312c ldr r3, [pc, #300] ; 10120 <mcrypt\_mutex\_register@@Base+0xb98c>

fff0: e5992004 ldr r2, [r9, #4]

fff4: e599700c ldr r7, [r9, #12]

fff8: e58d2008 str r2, [sp, #8]

fffc: e7943003 ldr r3, [r4, r3]

10000: e5992000 ldr r2, [r9]

10004: e58d301c str r3, [sp, #28]

10008: e8930110 ldm r3, {r4, r8}

1000c: e2853d06 add r3, r5, #384 ; 0x180

10010: e58d300c str r3, [sp, #12]

10014: e28d3028 add r3, sp, #40 ; 0x28

10018: e58d3018 str r3, [sp, #24]

1001c: e28d3030 add r3, sp, #48 ; 0x30

10020: e58d3010 str r3, [sp, #16]

10024: e28d3020 add r3, sp, #32

10028: e58d2004 str r2, [sp, #4]

1002c: e599a008 ldr sl, [r9, #8]

10030: e599c014 ldr ip, [r9, #20]

10034: e5990010 ldr r0, [r9, #16]

10038: e599601c ldr r6, [r9, #28]

1003c: e1a01004 mov r1, r4

10040: e5999018 ldr r9, [r9, #24]

10044: e1a02008 mov r2, r8

10048: e58d3014 str r3, [sp, #20]

1004c: ea000009 b 10078 <mcrypt\_mutex\_register@@Base+0xb8e4>

10050: e1a00009 mov r0, r9

10054: e1a09003 mov r9, r3

10058: e59d301c ldr r3, [sp, #28]

1005c: e1a0c006 mov ip, r6

10060: e58da004 str sl, [sp, #4]

10064: e58d7008 str r7, [sp, #8]

10068: e1a06002 mov r6, r2

1006c: e59d7028 ldr r7, [sp, #40] ; 0x28

10070: e8930006 ldm r3, {r1, r2}

10074: e59da02c ldr sl, [sp, #44] ; 0x2c

10078: e096e007 adds lr, r6, r7

1007c: e0883009 add r3, r8, r9

10080: 23a0b001 movcs fp, #1

10084: 33a0b000 movcc fp, #0

10088: e083300a add r3, r3, sl

1008c: e09ee004 adds lr, lr, r4

10090: e58dc028 str ip, [sp, #40] ; 0x28

10094: e08b3003 add r3, fp, r3

10098: 23a0c001 movcs ip, #1

1009c: 33a0c000 movcc ip, #0

100a0: e58d002c str r0, [sp, #44] ; 0x2c

100a4: e0944001 adds r4, r4, r1

100a8: e59d0008 ldr r0, [sp, #8]

100ac: e083300c add r3, r3, ip

100b0: e59db004 ldr fp, [sp, #4]

100b4: 23a0c001 movcs ip, #1

100b8: 33a0c000 movcc ip, #0

100bc: e08cc002 add ip, ip, r2

100c0: e59d1010 ldr r1, [sp, #16]

100c4: e59d2018 ldr r2, [sp, #24]

100c8: e58d0020 str r0, [sp, #32]

100cc: e59d0014 ldr r0, [sp, #20]

100d0: e58d3034 str r3, [sp, #52] ; 0x34

100d4: e58db024 str fp, [sp, #36] ; 0x24

100d8: e58de030 str lr, [sp, #48] ; 0x30

100dc: e088800c add r8, r8, ip

100e0: ebffff1c bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

100e4: e1cd22d0 ldrd r2, [sp, #32]

100e8: e59d100c ldr r1, [sp, #12]

100ec: e2855008 add r5, r5, #8

100f0: e14520f8 strd r2, [r5, #-8]

100f4: e1510005 cmp r1, r5

100f8: 1affffd4 bne 10050 <mcrypt\_mutex\_register@@Base+0xb8bc>

100fc: e3a00000 mov r0, #0

10100: e28dd03c add sp, sp, #60 ; 0x3c

10104: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

10108: ebfffebc bl fc00 <mcrypt\_mutex\_register@@Base+0xb46c>

1010c: e3a03001 mov r3, #1

10110: e5863000 str r3, [r6]

10114: eaffffb4 b ffec <mcrypt\_mutex\_register@@Base+0xb858>

10118: 00024028 andeq r4, r2, r8, lsr #32

1011c: 000000c8 andeq r0, r0, r8, asr #1

10120: 0000009c muleq r0, ip, r0

10124: e1a02000 mov r2, r0

10128: e92d41f0 push {r4, r5, r6, r7, r8, lr}

1012c: e1a04000 mov r4, r0

10130: e1a07001 mov r7, r1

10134: e4920008 ldr r0, [r2], #8

10138: e591100c ldr r1, [r1, #12]

1013c: e594c004 ldr ip, [r4, #4]

10140: e5973008 ldr r3, [r7, #8]

10144: e0911000 adds r1, r1, r0

10148: 23a0e001 movcs lr, #1

1014c: 33a0e000 movcc lr, #0

10150: e083300c add r3, r3, ip

10154: e8971001 ldm r7, {r0, ip}

10158: e24dd010 sub sp, sp, #16

1015c: e083300e add r3, r3, lr

10160: e1a0500d mov r5, sp

10164: e28d6008 add r6, sp, #8

10168: e88d000a stm sp, {r1, r3}

1016c: e58d000c str r0, [sp, #12]

10170: e1a01005 mov r1, r5

10174: e1a00006 mov r0, r6

10178: e58dc008 str ip, [sp, #8]

1017c: ebfffef5 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10180: e59d3000 ldr r3, [sp]

10184: e5942010 ldr r2, [r4, #16]

10188: e5941014 ldr r1, [r4, #20]

1018c: e0932002 adds r2, r3, r2

10190: e99d1008 ldmib sp, {r3, ip}

10194: e5940018 ldr r0, [r4, #24]

10198: e59de00c ldr lr, [sp, #12]

1019c: e0811003 add r1, r1, r3

101a0: e594301c ldr r3, [r4, #28]

101a4: e58d2000 str r2, [sp]

101a8: 23a02001 movcs r2, #1

101ac: 33a02000 movcc r2, #0

101b0: e09c0000 adds r0, ip, r0

101b4: e0811002 add r1, r1, r2

101b8: e083300e add r3, r3, lr

101bc: 23a02001 movcs r2, #1

101c0: 33a02000 movcc r2, #0

101c4: e0833002 add r3, r3, r2

101c8: e58d1004 str r1, [sp, #4]

101cc: e2842020 add r2, r4, #32

101d0: e1a01006 mov r1, r6

101d4: e58d0008 str r0, [sp, #8]

101d8: e1a00005 mov r0, r5

101dc: e58d300c str r3, [sp, #12]

101e0: ebfffedc bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

101e4: e59d3008 ldr r3, [sp, #8]

101e8: e5942028 ldr r2, [r4, #40] ; 0x28

101ec: e594102c ldr r1, [r4, #44] ; 0x2c

101f0: e0932002 adds r2, r3, r2

101f4: e59d300c ldr r3, [sp, #12]

101f8: e594c030 ldr ip, [r4, #48] ; 0x30

101fc: e59d0000 ldr r0, [sp]

10200: e59de004 ldr lr, [sp, #4]

10204: e0811003 add r1, r1, r3

10208: e5943034 ldr r3, [r4, #52] ; 0x34

1020c: e58d2008 str r2, [sp, #8]

10210: 23a02001 movcs r2, #1

10214: 33a02000 movcc r2, #0

10218: e090000c adds r0, r0, ip

1021c: e0811002 add r1, r1, r2

10220: e083300e add r3, r3, lr

10224: 23a02001 movcs r2, #1

10228: 33a02000 movcc r2, #0

1022c: e0833002 add r3, r3, r2

10230: e58d100c str r1, [sp, #12]

10234: e2842038 add r2, r4, #56 ; 0x38

10238: e1a01005 mov r1, r5

1023c: e58d0000 str r0, [sp]

10240: e1a00006 mov r0, r6

10244: e58d3004 str r3, [sp, #4]

10248: ebfffec2 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

1024c: e59d3000 ldr r3, [sp]

10250: e5942040 ldr r2, [r4, #64] ; 0x40

10254: e5941044 ldr r1, [r4, #68] ; 0x44

10258: e0932002 adds r2, r3, r2

1025c: e59d3004 ldr r3, [sp, #4]

10260: e594c048 ldr ip, [r4, #72] ; 0x48

10264: e59d0008 ldr r0, [sp, #8]

10268: e59de00c ldr lr, [sp, #12]

1026c: e0811003 add r1, r1, r3

10270: e594304c ldr r3, [r4, #76] ; 0x4c

10274: e58d2000 str r2, [sp]

10278: 23a02001 movcs r2, #1

1027c: 33a02000 movcc r2, #0

10280: e090000c adds r0, r0, ip

10284: e0811002 add r1, r1, r2

10288: e083300e add r3, r3, lr

1028c: 23a02001 movcs r2, #1

10290: 33a02000 movcc r2, #0

10294: e0833002 add r3, r3, r2

10298: e58d1004 str r1, [sp, #4]

1029c: e2842050 add r2, r4, #80 ; 0x50

102a0: e1a01006 mov r1, r6

102a4: e58d0008 str r0, [sp, #8]

102a8: e1a00005 mov r0, r5

102ac: e58d300c str r3, [sp, #12]

102b0: ebfffea8 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

102b4: e59d3008 ldr r3, [sp, #8]

102b8: e5942058 ldr r2, [r4, #88] ; 0x58

102bc: e594105c ldr r1, [r4, #92] ; 0x5c

102c0: e0932002 adds r2, r3, r2

102c4: e59d300c ldr r3, [sp, #12]

102c8: e594c060 ldr ip, [r4, #96] ; 0x60

102cc: e59d0000 ldr r0, [sp]

102d0: e59de004 ldr lr, [sp, #4]

102d4: e0811003 add r1, r1, r3

102d8: e5943064 ldr r3, [r4, #100] ; 0x64

102dc: e58d2008 str r2, [sp, #8]

102e0: 23a02001 movcs r2, #1

102e4: 33a02000 movcc r2, #0

102e8: e090000c adds r0, r0, ip

102ec: e0811002 add r1, r1, r2

102f0: e083300e add r3, r3, lr

102f4: 23a02001 movcs r2, #1

102f8: 33a02000 movcc r2, #0

102fc: e0833002 add r3, r3, r2

10300: e58d100c str r1, [sp, #12]

10304: e2842068 add r2, r4, #104 ; 0x68

10308: e1a01005 mov r1, r5

1030c: e58d0000 str r0, [sp]

10310: e1a00006 mov r0, r6

10314: e58d3004 str r3, [sp, #4]

10318: ebfffe8e bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

1031c: e59d3000 ldr r3, [sp]

10320: e5942070 ldr r2, [r4, #112] ; 0x70

10324: e5941074 ldr r1, [r4, #116] ; 0x74

10328: e0932002 adds r2, r3, r2

1032c: e59d3004 ldr r3, [sp, #4]

10330: e594c078 ldr ip, [r4, #120] ; 0x78

10334: e59d0008 ldr r0, [sp, #8]

10338: e59de00c ldr lr, [sp, #12]

1033c: e0811003 add r1, r1, r3

10340: e594307c ldr r3, [r4, #124] ; 0x7c

10344: e58d2000 str r2, [sp]

10348: 23a02001 movcs r2, #1

1034c: 33a02000 movcc r2, #0

10350: e090000c adds r0, r0, ip

10354: e0811002 add r1, r1, r2

10358: e083300e add r3, r3, lr

1035c: 23a02001 movcs r2, #1

10360: 33a02000 movcc r2, #0

10364: e0833002 add r3, r3, r2

10368: e58d1004 str r1, [sp, #4]

1036c: e2842080 add r2, r4, #128 ; 0x80

10370: e1a01006 mov r1, r6

10374: e58d0008 str r0, [sp, #8]

10378: e1a00005 mov r0, r5

1037c: e58d300c str r3, [sp, #12]

10380: ebfffe74 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10384: e59d3008 ldr r3, [sp, #8]

10388: e5942088 ldr r2, [r4, #136] ; 0x88

1038c: e594108c ldr r1, [r4, #140] ; 0x8c

10390: e0932002 adds r2, r3, r2

10394: e59d300c ldr r3, [sp, #12]

10398: e594c090 ldr ip, [r4, #144] ; 0x90

1039c: e59d0000 ldr r0, [sp]

103a0: e59de004 ldr lr, [sp, #4]

103a4: e0811003 add r1, r1, r3

103a8: e5943094 ldr r3, [r4, #148] ; 0x94

103ac: e58d2008 str r2, [sp, #8]

103b0: 23a02001 movcs r2, #1

103b4: 33a02000 movcc r2, #0

103b8: e090000c adds r0, r0, ip

103bc: e0811002 add r1, r1, r2

103c0: e083300e add r3, r3, lr

103c4: 23a02001 movcs r2, #1

103c8: 33a02000 movcc r2, #0

103cc: e0833002 add r3, r3, r2

103d0: e58d100c str r1, [sp, #12]

103d4: e2842098 add r2, r4, #152 ; 0x98

103d8: e1a01005 mov r1, r5

103dc: e58d0000 str r0, [sp]

103e0: e1a00006 mov r0, r6

103e4: e58d3004 str r3, [sp, #4]

103e8: ebfffe5a bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

103ec: e59d3000 ldr r3, [sp]

103f0: e59420a0 ldr r2, [r4, #160] ; 0xa0

103f4: e59410a4 ldr r1, [r4, #164] ; 0xa4

103f8: e0932002 adds r2, r3, r2

103fc: e59d3004 ldr r3, [sp, #4]

10400: e594c0a8 ldr ip, [r4, #168] ; 0xa8

10404: e59d0008 ldr r0, [sp, #8]

10408: e59de00c ldr lr, [sp, #12]

1040c: e0811003 add r1, r1, r3

10410: e59430ac ldr r3, [r4, #172] ; 0xac

10414: e58d2000 str r2, [sp]

10418: 23a02001 movcs r2, #1

1041c: 33a02000 movcc r2, #0

10420: e090000c adds r0, r0, ip

10424: e0811002 add r1, r1, r2

10428: e083300e add r3, r3, lr

1042c: 23a02001 movcs r2, #1

10430: 33a02000 movcc r2, #0

10434: e0833002 add r3, r3, r2

10438: e58d1004 str r1, [sp, #4]

1043c: e28420b0 add r2, r4, #176 ; 0xb0

10440: e1a01006 mov r1, r6

10444: e58d0008 str r0, [sp, #8]

10448: e1a00005 mov r0, r5

1044c: e58d300c str r3, [sp, #12]

10450: ebfffe40 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10454: e59d3008 ldr r3, [sp, #8]

10458: e59420b8 ldr r2, [r4, #184] ; 0xb8

1045c: e59410bc ldr r1, [r4, #188] ; 0xbc

10460: e0932002 adds r2, r3, r2

10464: e59d300c ldr r3, [sp, #12]

10468: e594c0c0 ldr ip, [r4, #192] ; 0xc0

1046c: e59d0000 ldr r0, [sp]

10470: e59de004 ldr lr, [sp, #4]

10474: e0811003 add r1, r1, r3

10478: e59430c4 ldr r3, [r4, #196] ; 0xc4

1047c: e58d2008 str r2, [sp, #8]

10480: 23a02001 movcs r2, #1

10484: 33a02000 movcc r2, #0

10488: e090000c adds r0, r0, ip

1048c: e0811002 add r1, r1, r2

10490: e083300e add r3, r3, lr

10494: 23a02001 movcs r2, #1

10498: 33a02000 movcc r2, #0

1049c: e0833002 add r3, r3, r2

104a0: e58d100c str r1, [sp, #12]

104a4: e28420c8 add r2, r4, #200 ; 0xc8

104a8: e1a01005 mov r1, r5

104ac: e58d0000 str r0, [sp]

104b0: e1a00006 mov r0, r6

104b4: e58d3004 str r3, [sp, #4]

104b8: ebfffe26 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

104bc: e59d3000 ldr r3, [sp]

104c0: e59420d0 ldr r2, [r4, #208] ; 0xd0

104c4: e59410d4 ldr r1, [r4, #212] ; 0xd4

104c8: e0932002 adds r2, r3, r2

104cc: e59d3004 ldr r3, [sp, #4]

104d0: e594c0d8 ldr ip, [r4, #216] ; 0xd8

104d4: e59d0008 ldr r0, [sp, #8]

104d8: e59de00c ldr lr, [sp, #12]

104dc: e0811003 add r1, r1, r3

104e0: e59430dc ldr r3, [r4, #220] ; 0xdc

104e4: e58d2000 str r2, [sp]

104e8: 23a02001 movcs r2, #1

104ec: 33a02000 movcc r2, #0

104f0: e090000c adds r0, r0, ip

104f4: e0811002 add r1, r1, r2

104f8: e083300e add r3, r3, lr

104fc: 23a02001 movcs r2, #1

10500: 33a02000 movcc r2, #0

10504: e0833002 add r3, r3, r2

10508: e58d1004 str r1, [sp, #4]

1050c: e28420e0 add r2, r4, #224 ; 0xe0

10510: e1a01006 mov r1, r6

10514: e58d0008 str r0, [sp, #8]

10518: e1a00005 mov r0, r5

1051c: e58d300c str r3, [sp, #12]

10520: ebfffe0c bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10524: e59d3008 ldr r3, [sp, #8]

10528: e59420e8 ldr r2, [r4, #232] ; 0xe8

1052c: e59410ec ldr r1, [r4, #236] ; 0xec

10530: e0932002 adds r2, r3, r2

10534: e59d300c ldr r3, [sp, #12]

10538: e594c0f0 ldr ip, [r4, #240] ; 0xf0

1053c: e59d0000 ldr r0, [sp]

10540: e59de004 ldr lr, [sp, #4]

10544: e0811003 add r1, r1, r3

10548: e59430f4 ldr r3, [r4, #244] ; 0xf4

1054c: e58d2008 str r2, [sp, #8]

10550: 23a02001 movcs r2, #1

10554: 33a02000 movcc r2, #0

10558: e090000c adds r0, r0, ip

1055c: e0811002 add r1, r1, r2

10560: e083300e add r3, r3, lr

10564: 23a02001 movcs r2, #1

10568: 33a02000 movcc r2, #0

1056c: e0833002 add r3, r3, r2

10570: e58d100c str r1, [sp, #12]

10574: e28420f8 add r2, r4, #248 ; 0xf8

10578: e1a01005 mov r1, r5

1057c: e58d0000 str r0, [sp]

10580: e1a00006 mov r0, r6

10584: e58d3004 str r3, [sp, #4]

10588: ebfffdf2 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

1058c: e59d3000 ldr r3, [sp]

10590: e5942100 ldr r2, [r4, #256] ; 0x100

10594: e5941104 ldr r1, [r4, #260] ; 0x104

10598: e0932002 adds r2, r3, r2

1059c: e59d3004 ldr r3, [sp, #4]

105a0: e594c108 ldr ip, [r4, #264] ; 0x108

105a4: e59d0008 ldr r0, [sp, #8]

105a8: e59de00c ldr lr, [sp, #12]

105ac: e0811003 add r1, r1, r3

105b0: e594310c ldr r3, [r4, #268] ; 0x10c

105b4: e58d2000 str r2, [sp]

105b8: 23a02001 movcs r2, #1

105bc: 33a02000 movcc r2, #0

105c0: e090000c adds r0, r0, ip

105c4: e0811002 add r1, r1, r2

105c8: e083300e add r3, r3, lr

105cc: 23a02001 movcs r2, #1

105d0: 33a02000 movcc r2, #0

105d4: e0833002 add r3, r3, r2

105d8: e58d1004 str r1, [sp, #4]

105dc: e2842e11 add r2, r4, #272 ; 0x110

105e0: e1a01006 mov r1, r6

105e4: e58d0008 str r0, [sp, #8]

105e8: e1a00005 mov r0, r5

105ec: e58d300c str r3, [sp, #12]

105f0: ebfffdd8 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

105f4: e59d3008 ldr r3, [sp, #8]

105f8: e5942118 ldr r2, [r4, #280] ; 0x118

105fc: e594111c ldr r1, [r4, #284] ; 0x11c

10600: e0932002 adds r2, r3, r2

10604: e59d300c ldr r3, [sp, #12]

10608: e594c120 ldr ip, [r4, #288] ; 0x120

1060c: e59d0000 ldr r0, [sp]

10610: e59de004 ldr lr, [sp, #4]

10614: e0811003 add r1, r1, r3

10618: e5943124 ldr r3, [r4, #292] ; 0x124

1061c: e58d2008 str r2, [sp, #8]

10620: 23a02001 movcs r2, #1

10624: 33a02000 movcc r2, #0

10628: e090000c adds r0, r0, ip

1062c: e0811002 add r1, r1, r2

10630: e083300e add r3, r3, lr

10634: 23a02001 movcs r2, #1

10638: 33a02000 movcc r2, #0

1063c: e0833002 add r3, r3, r2

10640: e58d100c str r1, [sp, #12]

10644: e2842f4a add r2, r4, #296 ; 0x128

10648: e1a01005 mov r1, r5

1064c: e58d0000 str r0, [sp]

10650: e1a00006 mov r0, r6

10654: e58d3004 str r3, [sp, #4]

10658: ebfffdbe bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

1065c: e59d3000 ldr r3, [sp]

10660: e5942130 ldr r2, [r4, #304] ; 0x130

10664: e5941134 ldr r1, [r4, #308] ; 0x134

10668: e0932002 adds r2, r3, r2

1066c: e59d3004 ldr r3, [sp, #4]

10670: e594c138 ldr ip, [r4, #312] ; 0x138

10674: e59d0008 ldr r0, [sp, #8]

10678: e59de00c ldr lr, [sp, #12]

1067c: e0811003 add r1, r1, r3

10680: e594313c ldr r3, [r4, #316] ; 0x13c

10684: e58d2000 str r2, [sp]

10688: 23a02001 movcs r2, #1

1068c: 33a02000 movcc r2, #0

10690: e090000c adds r0, r0, ip

10694: e0811002 add r1, r1, r2

10698: e083300e add r3, r3, lr

1069c: 23a02001 movcs r2, #1

106a0: 33a02000 movcc r2, #0

106a4: e0833002 add r3, r3, r2

106a8: e58d1004 str r1, [sp, #4]

106ac: e2842d05 add r2, r4, #320 ; 0x140

106b0: e1a01006 mov r1, r6

106b4: e58d0008 str r0, [sp, #8]

106b8: e1a00005 mov r0, r5

106bc: e58d300c str r3, [sp, #12]

106c0: ebfffda4 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

106c4: e59d3008 ldr r3, [sp, #8]

106c8: e5942148 ldr r2, [r4, #328] ; 0x148

106cc: e594114c ldr r1, [r4, #332] ; 0x14c

106d0: e0932002 adds r2, r3, r2

106d4: e59d300c ldr r3, [sp, #12]

106d8: e594c150 ldr ip, [r4, #336] ; 0x150

106dc: e59d0000 ldr r0, [sp]

106e0: e59de004 ldr lr, [sp, #4]

106e4: e0811003 add r1, r1, r3

106e8: e5943154 ldr r3, [r4, #340] ; 0x154

106ec: e58d2008 str r2, [sp, #8]

106f0: 23a02001 movcs r2, #1

106f4: 33a02000 movcc r2, #0

106f8: e090000c adds r0, r0, ip

106fc: e0811002 add r1, r1, r2

10700: e083300e add r3, r3, lr

10704: 23a02001 movcs r2, #1

10708: 33a02000 movcc r2, #0

1070c: e0833002 add r3, r3, r2

10710: e58d100c str r1, [sp, #12]

10714: e2842f56 add r2, r4, #344 ; 0x158

10718: e1a01005 mov r1, r5

1071c: e58d0000 str r0, [sp]

10720: e1a00006 mov r0, r6

10724: e58d3004 str r3, [sp, #4]

10728: ebfffd8a bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

1072c: e59d1000 ldr r1, [sp]

10730: e5943160 ldr r3, [r4, #352] ; 0x160

10734: e594e164 ldr lr, [r4, #356] ; 0x164

10738: e0918003 adds r8, r1, r3

1073c: e59d3004 ldr r3, [sp, #4]

10740: e59d2008 ldr r2, [sp, #8]

10744: e59d000c ldr r0, [sp, #12]

10748: e594c168 ldr ip, [r4, #360] ; 0x168

1074c: e08ee003 add lr, lr, r3

10750: e594316c ldr r3, [r4, #364] ; 0x16c

10754: e1a01006 mov r1, r6

10758: e0833000 add r3, r3, r0

1075c: e1a00005 mov r0, r5

10760: 23a05001 movcs r5, #1

10764: 33a05000 movcc r5, #0

10768: e092c00c adds ip, r2, ip

1076c: 23a02001 movcs r2, #1

10770: 33a02000 movcc r2, #0

10774: e0833002 add r3, r3, r2

10778: e08ee005 add lr, lr, r5

1077c: e2842e17 add r2, r4, #368 ; 0x170

10780: e58dc008 str ip, [sp, #8]

10784: e58d300c str r3, [sp, #12]

10788: e58d8000 str r8, [sp]

1078c: e58de004 str lr, [sp, #4]

10790: ebfffd70 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10794: e5941178 ldr r1, [r4, #376] ; 0x178

10798: e59d2008 ldr r2, [sp, #8]

1079c: e594317c ldr r3, [r4, #380] ; 0x17c

107a0: e59d000c ldr r0, [sp, #12]

107a4: e0922001 adds r2, r2, r1

107a8: e0833000 add r3, r3, r0

107ac: 23a0c001 movcs ip, #1

107b0: e1cd00d0 ldrd r0, [sp]

107b4: 33a0c000 movcc ip, #0

107b8: e587200c str r2, [r7, #12]

107bc: e5871000 str r1, [r7]

107c0: e083300c add r3, r3, ip

107c4: e9870009 stmib r7, {r0, r3}

107c8: e28dd010 add sp, sp, #16

107cc: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

107d0: e92d41f0 push {r4, r5, r6, r7, r8, lr}

107d4: e1a07001 mov r7, r1

107d8: e1a04000 mov r4, r0

107dc: e5901178 ldr r1, [r0, #376] ; 0x178

107e0: e5973008 ldr r3, [r7, #8]

107e4: e590017c ldr r0, [r0, #380] ; 0x17c

107e8: e597200c ldr r2, [r7, #12]

107ec: e0433000 sub r3, r3, r0

107f0: e8971001 ldm r7, {r0, ip}

107f4: e24dd010 sub sp, sp, #16

107f8: e0522001 subs r2, r2, r1

107fc: e1a0500d mov r5, sp

10800: e28d6008 add r6, sp, #8

10804: 33a01001 movcc r1, #1

10808: 23a01000 movcs r1, #0

1080c: e0433001 sub r3, r3, r1

10810: e58d2000 str r2, [sp]

10814: e1a01005 mov r1, r5

10818: e2842e17 add r2, r4, #368 ; 0x170

1081c: e58d000c str r0, [sp, #12]

10820: e1a00006 mov r0, r6

10824: e58d3004 str r3, [sp, #4]

10828: e58dc008 str ip, [sp, #8]

1082c: ebfffd49 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10830: e59d3000 ldr r3, [sp]

10834: e5942168 ldr r2, [r4, #360] ; 0x168

10838: e99d1002 ldmib sp, {r1, ip}

1083c: e0532002 subs r2, r3, r2

10840: e594316c ldr r3, [r4, #364] ; 0x16c

10844: e5940160 ldr r0, [r4, #352] ; 0x160

10848: e594e164 ldr lr, [r4, #356] ; 0x164

1084c: e0411003 sub r1, r1, r3

10850: e59d300c ldr r3, [sp, #12]

10854: e58d2000 str r2, [sp]

10858: 33a02001 movcc r2, #1

1085c: 23a02000 movcs r2, #0

10860: e05c0000 subs r0, ip, r0

10864: e0411002 sub r1, r1, r2

10868: e043300e sub r3, r3, lr

1086c: 33a02001 movcc r2, #1

10870: 23a02000 movcs r2, #0

10874: e0433002 sub r3, r3, r2

10878: e58d1004 str r1, [sp, #4]

1087c: e2842f56 add r2, r4, #344 ; 0x158

10880: e1a01006 mov r1, r6

10884: e58d0008 str r0, [sp, #8]

10888: e1a00005 mov r0, r5

1088c: e58d300c str r3, [sp, #12]

10890: ebfffd30 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10894: e59d3008 ldr r3, [sp, #8]

10898: e5942150 ldr r2, [r4, #336] ; 0x150

1089c: e59d100c ldr r1, [sp, #12]

108a0: e0532002 subs r2, r3, r2

108a4: e5943154 ldr r3, [r4, #340] ; 0x154

108a8: e594c148 ldr ip, [r4, #328] ; 0x148

108ac: e59d0000 ldr r0, [sp]

108b0: e594e14c ldr lr, [r4, #332] ; 0x14c

108b4: e0411003 sub r1, r1, r3

108b8: e59d3004 ldr r3, [sp, #4]

108bc: e58d2008 str r2, [sp, #8]

108c0: 33a02001 movcc r2, #1

108c4: 23a02000 movcs r2, #0

108c8: e050000c subs r0, r0, ip

108cc: e0411002 sub r1, r1, r2

108d0: e043300e sub r3, r3, lr

108d4: 33a02001 movcc r2, #1

108d8: 23a02000 movcs r2, #0

108dc: e0433002 sub r3, r3, r2

108e0: e58d100c str r1, [sp, #12]

108e4: e2842d05 add r2, r4, #320 ; 0x140

108e8: e1a01005 mov r1, r5

108ec: e58d0000 str r0, [sp]

108f0: e1a00006 mov r0, r6

108f4: e58d3004 str r3, [sp, #4]

108f8: ebfffd16 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

108fc: e59d3000 ldr r3, [sp]

10900: e5942138 ldr r2, [r4, #312] ; 0x138

10904: e59d1004 ldr r1, [sp, #4]

10908: e0532002 subs r2, r3, r2

1090c: e594313c ldr r3, [r4, #316] ; 0x13c

10910: e594c130 ldr ip, [r4, #304] ; 0x130

10914: e59d0008 ldr r0, [sp, #8]

10918: e594e134 ldr lr, [r4, #308] ; 0x134

1091c: e0411003 sub r1, r1, r3

10920: e59d300c ldr r3, [sp, #12]

10924: e58d2000 str r2, [sp]

10928: 33a02001 movcc r2, #1

1092c: 23a02000 movcs r2, #0

10930: e050000c subs r0, r0, ip

10934: e0411002 sub r1, r1, r2

10938: e043300e sub r3, r3, lr

1093c: 33a02001 movcc r2, #1

10940: 23a02000 movcs r2, #0

10944: e0433002 sub r3, r3, r2

10948: e58d1004 str r1, [sp, #4]

1094c: e2842f4a add r2, r4, #296 ; 0x128

10950: e1a01006 mov r1, r6

10954: e58d0008 str r0, [sp, #8]

10958: e1a00005 mov r0, r5

1095c: e58d300c str r3, [sp, #12]

10960: ebfffcfc bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10964: e59d3008 ldr r3, [sp, #8]

10968: e5942120 ldr r2, [r4, #288] ; 0x120

1096c: e59d100c ldr r1, [sp, #12]

10970: e0532002 subs r2, r3, r2

10974: e5943124 ldr r3, [r4, #292] ; 0x124

10978: e594c118 ldr ip, [r4, #280] ; 0x118

1097c: e59d0000 ldr r0, [sp]

10980: e594e11c ldr lr, [r4, #284] ; 0x11c

10984: e0411003 sub r1, r1, r3

10988: e59d3004 ldr r3, [sp, #4]

1098c: e58d2008 str r2, [sp, #8]

10990: 33a02001 movcc r2, #1

10994: 23a02000 movcs r2, #0

10998: e050000c subs r0, r0, ip

1099c: e0411002 sub r1, r1, r2

109a0: e043300e sub r3, r3, lr

109a4: 33a02001 movcc r2, #1

109a8: 23a02000 movcs r2, #0

109ac: e0433002 sub r3, r3, r2

109b0: e58d100c str r1, [sp, #12]

109b4: e2842e11 add r2, r4, #272 ; 0x110

109b8: e1a01005 mov r1, r5

109bc: e58d0000 str r0, [sp]

109c0: e1a00006 mov r0, r6

109c4: e58d3004 str r3, [sp, #4]

109c8: ebfffce2 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

109cc: e59d3000 ldr r3, [sp]

109d0: e5942108 ldr r2, [r4, #264] ; 0x108

109d4: e59d1004 ldr r1, [sp, #4]

109d8: e0532002 subs r2, r3, r2

109dc: e594310c ldr r3, [r4, #268] ; 0x10c

109e0: e594c100 ldr ip, [r4, #256] ; 0x100

109e4: e59d0008 ldr r0, [sp, #8]

109e8: e594e104 ldr lr, [r4, #260] ; 0x104

109ec: e0411003 sub r1, r1, r3

109f0: e59d300c ldr r3, [sp, #12]

109f4: e58d2000 str r2, [sp]

109f8: 33a02001 movcc r2, #1

109fc: 23a02000 movcs r2, #0

10a00: e050000c subs r0, r0, ip

10a04: e0411002 sub r1, r1, r2

10a08: e043300e sub r3, r3, lr

10a0c: 33a02001 movcc r2, #1

10a10: 23a02000 movcs r2, #0

10a14: e0433002 sub r3, r3, r2

10a18: e58d1004 str r1, [sp, #4]

10a1c: e28420f8 add r2, r4, #248 ; 0xf8

10a20: e1a01006 mov r1, r6

10a24: e58d0008 str r0, [sp, #8]

10a28: e1a00005 mov r0, r5

10a2c: e58d300c str r3, [sp, #12]

10a30: ebfffcc8 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10a34: e59d3008 ldr r3, [sp, #8]

10a38: e59420f0 ldr r2, [r4, #240] ; 0xf0

10a3c: e59d100c ldr r1, [sp, #12]

10a40: e0532002 subs r2, r3, r2

10a44: e59430f4 ldr r3, [r4, #244] ; 0xf4

10a48: e594c0e8 ldr ip, [r4, #232] ; 0xe8

10a4c: e59d0000 ldr r0, [sp]

10a50: e594e0ec ldr lr, [r4, #236] ; 0xec

10a54: e0411003 sub r1, r1, r3

10a58: e59d3004 ldr r3, [sp, #4]

10a5c: e58d2008 str r2, [sp, #8]

10a60: 33a02001 movcc r2, #1

10a64: 23a02000 movcs r2, #0

10a68: e050000c subs r0, r0, ip

10a6c: e0411002 sub r1, r1, r2

10a70: e043300e sub r3, r3, lr

10a74: 33a02001 movcc r2, #1

10a78: 23a02000 movcs r2, #0

10a7c: e0433002 sub r3, r3, r2

10a80: e58d100c str r1, [sp, #12]

10a84: e28420e0 add r2, r4, #224 ; 0xe0

10a88: e1a01005 mov r1, r5

10a8c: e58d0000 str r0, [sp]

10a90: e1a00006 mov r0, r6

10a94: e58d3004 str r3, [sp, #4]

10a98: ebfffcae bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10a9c: e59d3000 ldr r3, [sp]

10aa0: e59420d8 ldr r2, [r4, #216] ; 0xd8

10aa4: e59d1004 ldr r1, [sp, #4]

10aa8: e0532002 subs r2, r3, r2

10aac: e59430dc ldr r3, [r4, #220] ; 0xdc

10ab0: e594c0d0 ldr ip, [r4, #208] ; 0xd0

10ab4: e59d0008 ldr r0, [sp, #8]

10ab8: e594e0d4 ldr lr, [r4, #212] ; 0xd4

10abc: e0411003 sub r1, r1, r3

10ac0: e59d300c ldr r3, [sp, #12]

10ac4: e58d2000 str r2, [sp]

10ac8: 33a02001 movcc r2, #1

10acc: 23a02000 movcs r2, #0

10ad0: e050000c subs r0, r0, ip

10ad4: e0411002 sub r1, r1, r2

10ad8: e043300e sub r3, r3, lr

10adc: 33a02001 movcc r2, #1

10ae0: 23a02000 movcs r2, #0

10ae4: e0433002 sub r3, r3, r2

10ae8: e58d1004 str r1, [sp, #4]

10aec: e28420c8 add r2, r4, #200 ; 0xc8

10af0: e1a01006 mov r1, r6

10af4: e58d0008 str r0, [sp, #8]

10af8: e1a00005 mov r0, r5

10afc: e58d300c str r3, [sp, #12]

10b00: ebfffc94 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10b04: e59d3008 ldr r3, [sp, #8]

10b08: e59420c0 ldr r2, [r4, #192] ; 0xc0

10b0c: e59d100c ldr r1, [sp, #12]

10b10: e0532002 subs r2, r3, r2

10b14: e59430c4 ldr r3, [r4, #196] ; 0xc4

10b18: e594c0b8 ldr ip, [r4, #184] ; 0xb8

10b1c: e59d0000 ldr r0, [sp]

10b20: e594e0bc ldr lr, [r4, #188] ; 0xbc

10b24: e0411003 sub r1, r1, r3

10b28: e59d3004 ldr r3, [sp, #4]

10b2c: e58d2008 str r2, [sp, #8]

10b30: 33a02001 movcc r2, #1

10b34: 23a02000 movcs r2, #0

10b38: e050000c subs r0, r0, ip

10b3c: e0411002 sub r1, r1, r2

10b40: e043300e sub r3, r3, lr

10b44: 33a02001 movcc r2, #1

10b48: 23a02000 movcs r2, #0

10b4c: e0433002 sub r3, r3, r2

10b50: e58d100c str r1, [sp, #12]

10b54: e28420b0 add r2, r4, #176 ; 0xb0

10b58: e1a01005 mov r1, r5

10b5c: e58d0000 str r0, [sp]

10b60: e1a00006 mov r0, r6

10b64: e58d3004 str r3, [sp, #4]

10b68: ebfffc7a bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10b6c: e59d3000 ldr r3, [sp]

10b70: e59420a8 ldr r2, [r4, #168] ; 0xa8

10b74: e59d1004 ldr r1, [sp, #4]

10b78: e0532002 subs r2, r3, r2

10b7c: e59430ac ldr r3, [r4, #172] ; 0xac

10b80: e594c0a0 ldr ip, [r4, #160] ; 0xa0

10b84: e59d0008 ldr r0, [sp, #8]

10b88: e594e0a4 ldr lr, [r4, #164] ; 0xa4

10b8c: e0411003 sub r1, r1, r3

10b90: e59d300c ldr r3, [sp, #12]

10b94: e58d2000 str r2, [sp]

10b98: 33a02001 movcc r2, #1

10b9c: 23a02000 movcs r2, #0

10ba0: e050000c subs r0, r0, ip

10ba4: e0411002 sub r1, r1, r2

10ba8: e043300e sub r3, r3, lr

10bac: 33a02001 movcc r2, #1

10bb0: 23a02000 movcs r2, #0

10bb4: e0433002 sub r3, r3, r2

10bb8: e58d1004 str r1, [sp, #4]

10bbc: e2842098 add r2, r4, #152 ; 0x98

10bc0: e1a01006 mov r1, r6

10bc4: e58d0008 str r0, [sp, #8]

10bc8: e1a00005 mov r0, r5

10bcc: e58d300c str r3, [sp, #12]

10bd0: ebfffc60 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10bd4: e59d3008 ldr r3, [sp, #8]

10bd8: e5942090 ldr r2, [r4, #144] ; 0x90

10bdc: e59d100c ldr r1, [sp, #12]

10be0: e0532002 subs r2, r3, r2

10be4: e5943094 ldr r3, [r4, #148] ; 0x94

10be8: e594c088 ldr ip, [r4, #136] ; 0x88

10bec: e59d0000 ldr r0, [sp]

10bf0: e594e08c ldr lr, [r4, #140] ; 0x8c

10bf4: e0411003 sub r1, r1, r3

10bf8: e59d3004 ldr r3, [sp, #4]

10bfc: e58d2008 str r2, [sp, #8]

10c00: 33a02001 movcc r2, #1

10c04: 23a02000 movcs r2, #0

10c08: e050000c subs r0, r0, ip

10c0c: e0411002 sub r1, r1, r2

10c10: e043300e sub r3, r3, lr

10c14: 33a02001 movcc r2, #1

10c18: 23a02000 movcs r2, #0

10c1c: e0433002 sub r3, r3, r2

10c20: e58d100c str r1, [sp, #12]

10c24: e2842080 add r2, r4, #128 ; 0x80

10c28: e1a01005 mov r1, r5

10c2c: e58d0000 str r0, [sp]

10c30: e1a00006 mov r0, r6

10c34: e58d3004 str r3, [sp, #4]

10c38: ebfffc46 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10c3c: e59d3000 ldr r3, [sp]

10c40: e5942078 ldr r2, [r4, #120] ; 0x78

10c44: e59d1004 ldr r1, [sp, #4]

10c48: e0532002 subs r2, r3, r2

10c4c: e594307c ldr r3, [r4, #124] ; 0x7c

10c50: e594c070 ldr ip, [r4, #112] ; 0x70

10c54: e59d0008 ldr r0, [sp, #8]

10c58: e594e074 ldr lr, [r4, #116] ; 0x74

10c5c: e0411003 sub r1, r1, r3

10c60: e59d300c ldr r3, [sp, #12]

10c64: e58d2000 str r2, [sp]

10c68: 33a02001 movcc r2, #1

10c6c: 23a02000 movcs r2, #0

10c70: e050000c subs r0, r0, ip

10c74: e0411002 sub r1, r1, r2

10c78: e043300e sub r3, r3, lr

10c7c: 33a02001 movcc r2, #1

10c80: 23a02000 movcs r2, #0

10c84: e0433002 sub r3, r3, r2

10c88: e58d1004 str r1, [sp, #4]

10c8c: e2842068 add r2, r4, #104 ; 0x68

10c90: e1a01006 mov r1, r6

10c94: e58d0008 str r0, [sp, #8]

10c98: e1a00005 mov r0, r5

10c9c: e58d300c str r3, [sp, #12]

10ca0: ebfffc2c bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10ca4: e59d3008 ldr r3, [sp, #8]

10ca8: e5942060 ldr r2, [r4, #96] ; 0x60

10cac: e59d100c ldr r1, [sp, #12]

10cb0: e0532002 subs r2, r3, r2

10cb4: e5943064 ldr r3, [r4, #100] ; 0x64

10cb8: e594c058 ldr ip, [r4, #88] ; 0x58

10cbc: e59d0000 ldr r0, [sp]

10cc0: e594e05c ldr lr, [r4, #92] ; 0x5c

10cc4: e0411003 sub r1, r1, r3

10cc8: e59d3004 ldr r3, [sp, #4]

10ccc: e58d2008 str r2, [sp, #8]

10cd0: 33a02001 movcc r2, #1

10cd4: 23a02000 movcs r2, #0

10cd8: e050000c subs r0, r0, ip

10cdc: e0411002 sub r1, r1, r2

10ce0: e043300e sub r3, r3, lr

10ce4: 33a02001 movcc r2, #1

10ce8: 23a02000 movcs r2, #0

10cec: e0433002 sub r3, r3, r2

10cf0: e58d100c str r1, [sp, #12]

10cf4: e2842050 add r2, r4, #80 ; 0x50

10cf8: e1a01005 mov r1, r5

10cfc: e58d0000 str r0, [sp]

10d00: e1a00006 mov r0, r6

10d04: e58d3004 str r3, [sp, #4]

10d08: ebfffc12 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10d0c: e59d3000 ldr r3, [sp]

10d10: e5942048 ldr r2, [r4, #72] ; 0x48

10d14: e59d1004 ldr r1, [sp, #4]

10d18: e0532002 subs r2, r3, r2

10d1c: e594304c ldr r3, [r4, #76] ; 0x4c

10d20: e594c040 ldr ip, [r4, #64] ; 0x40

10d24: e59d0008 ldr r0, [sp, #8]

10d28: e594e044 ldr lr, [r4, #68] ; 0x44

10d2c: e0411003 sub r1, r1, r3

10d30: e59d300c ldr r3, [sp, #12]

10d34: e58d2000 str r2, [sp]

10d38: 33a02001 movcc r2, #1

10d3c: 23a02000 movcs r2, #0

10d40: e050000c subs r0, r0, ip

10d44: e0411002 sub r1, r1, r2

10d48: e043300e sub r3, r3, lr

10d4c: 33a02001 movcc r2, #1

10d50: 23a02000 movcs r2, #0

10d54: e0433002 sub r3, r3, r2

10d58: e58d1004 str r1, [sp, #4]

10d5c: e2842038 add r2, r4, #56 ; 0x38

10d60: e1a01006 mov r1, r6

10d64: e58d0008 str r0, [sp, #8]

10d68: e1a00005 mov r0, r5

10d6c: e58d300c str r3, [sp, #12]

10d70: ebfffbf8 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10d74: e59d3008 ldr r3, [sp, #8]

10d78: e5942030 ldr r2, [r4, #48] ; 0x30

10d7c: e59d100c ldr r1, [sp, #12]

10d80: e0532002 subs r2, r3, r2

10d84: e5943034 ldr r3, [r4, #52] ; 0x34

10d88: e594c028 ldr ip, [r4, #40] ; 0x28

10d8c: e59d0000 ldr r0, [sp]

10d90: e594e02c ldr lr, [r4, #44] ; 0x2c

10d94: e0411003 sub r1, r1, r3

10d98: e59d3004 ldr r3, [sp, #4]

10d9c: e58d2008 str r2, [sp, #8]

10da0: 33a02001 movcc r2, #1

10da4: 23a02000 movcs r2, #0

10da8: e050000c subs r0, r0, ip

10dac: e0411002 sub r1, r1, r2

10db0: e043300e sub r3, r3, lr

10db4: 33a02001 movcc r2, #1

10db8: 23a02000 movcs r2, #0

10dbc: e0433002 sub r3, r3, r2

10dc0: e58d100c str r1, [sp, #12]

10dc4: e2842020 add r2, r4, #32

10dc8: e1a01005 mov r1, r5

10dcc: e58d0000 str r0, [sp]

10dd0: e1a00006 mov r0, r6

10dd4: e58d3004 str r3, [sp, #4]

10dd8: ebfffbde bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10ddc: e59d1000 ldr r1, [sp]

10de0: e5943018 ldr r3, [r4, #24]

10de4: e59de004 ldr lr, [sp, #4]

10de8: e0518003 subs r8, r1, r3

10dec: e594301c ldr r3, [r4, #28]

10df0: e59d2008 ldr r2, [sp, #8]

10df4: e5940014 ldr r0, [r4, #20]

10df8: e594c010 ldr ip, [r4, #16]

10dfc: e04ee003 sub lr, lr, r3

10e00: e59d300c ldr r3, [sp, #12]

10e04: e1a01006 mov r1, r6

10e08: e0433000 sub r3, r3, r0

10e0c: e1a00005 mov r0, r5

10e10: 33a05001 movcc r5, #1

10e14: 23a05000 movcs r5, #0

10e18: e052c00c subs ip, r2, ip

10e1c: 33a02001 movcc r2, #1

10e20: 23a02000 movcs r2, #0

10e24: e0433002 sub r3, r3, r2

10e28: e04ee005 sub lr, lr, r5

10e2c: e2842008 add r2, r4, #8

10e30: e58dc008 str ip, [sp, #8]

10e34: e58d300c str r3, [sp, #12]

10e38: e58d8000 str r8, [sp]

10e3c: e58de004 str lr, [sp, #4]

10e40: ebfffbc4 bl fd58 <mcrypt\_mutex\_register@@Base+0xb5c4>

10e44: e5941000 ldr r1, [r4]

10e48: e59d2008 ldr r2, [sp, #8]

10e4c: e5940004 ldr r0, [r4, #4]

10e50: e59d300c ldr r3, [sp, #12]

10e54: e0522001 subs r2, r2, r1

10e58: e0433000 sub r3, r3, r0

10e5c: 33a0c001 movcc ip, #1

10e60: e1cd00d0 ldrd r0, [sp]

10e64: 23a0c000 movcs ip, #0

10e68: e587200c str r2, [r7, #12]

10e6c: e5871000 str r1, [r7]

10e70: e043300c sub r3, r3, ip

10e74: e9870009 stmib r7, {r0, r3}

10e78: e28dd010 add sp, sp, #16

10e7c: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

10e80: e3a00d06 mov r0, #384 ; 0x180

10e84: e12fff1e bx lr

10e88: e3a00010 mov r0, #16

10e8c: e12fff1e bx lr

10e90: e3a00001 mov r0, #1

10e94: e12fff1e bx lr

10e98: e3a00020 mov r0, #32

10e9c: e12fff1e bx lr

10ea0: e3a03003 mov r3, #3

10ea4: e5803000 str r3, [r0]

10ea8: e59f0004 ldr r0, [pc, #4] ; 10eb4 <mcrypt\_mutex\_register@@Base+0xc720>

10eac: e08f0000 add r0, pc, r0

10eb0: e12fff1e bx lr

10eb4: 0000f9c0 andeq pc, r0, r0, asr #19

10eb8: e59f0004 ldr r0, [pc, #4] ; 10ec4 <mcrypt\_mutex\_register@@Base+0xc730>

10ebc: e08f0000 add r0, pc, r0

10ec0: e12fff1e bx lr

10ec4: 0000f9bc ; <UNDEFINED> instruction: 0x0000f9bc

10ec8: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

10ecc: e24dd0e8 sub sp, sp, #232 ; 0xe8

10ed0: ebffffec bl 10e88 <mcrypt\_mutex\_register@@Base+0xc6f4>

10ed4: e1a06000 mov r6, r0

10ed8: ebffffee bl 10e98 <mcrypt\_mutex\_register@@Base+0xc704>

10edc: e1a01000 mov r1, r0

10ee0: e3a00001 mov r0, #1

10ee4: ebffc4d2 bl 2234 <calloc@plt>

10ee8: e2507000 subs r7, r0, #0

10eec: 0a00006f beq 110b0 <mcrypt\_mutex\_register@@Base+0xc91c>

10ef0: e2475001 sub r5, r7, #1

10ef4: e3a0400a mov r4, #10

10ef8: e2678001 rsb r8, r7, #1

10efc: ea000004 b 10f14 <mcrypt\_mutex\_register@@Base+0xc780>

10f00: e2742000 rsbs r2, r4, #0

10f04: e2844002 add r4, r4, #2

10f08: e6ef2072 uxtb r2, r2

10f0c: 52623000 rsbpl r3, r2, #0

10f10: e5e53001 strb r3, [r5, #1]!

10f14: ebffffdf bl 10e98 <mcrypt\_mutex\_register@@Base+0xc704>

10f18: e0882005 add r2, r8, r5

10f1c: e6ef3074 uxtb r3, r4

10f20: e1500002 cmp r0, r2

10f24: cafffff5 bgt 10f00 <mcrypt\_mutex\_register@@Base+0xc76c>

10f28: e3560000 cmp r6, #0

10f2c: da000039 ble 11018 <mcrypt\_mutex\_register@@Base+0xc884>

10f30: e1a0400d mov r4, sp

10f34: e1a02004 mov r2, r4

10f38: e3a03000 mov r3, #0

10f3c: e4c23001 strb r3, [r2], #1

10f40: e2833001 add r3, r3, #1

10f44: e1560003 cmp r6, r3

10f48: 1afffffb bne 10f3c <mcrypt\_mutex\_register@@Base+0xc7a8>

10f4c: ebffffcb bl 10e80 <mcrypt\_mutex\_register@@Base+0xc6ec>

10f50: ebffc4e7 bl 22f4 <malloc@plt>

10f54: e2509000 subs r9, r0, #0

10f58: 0a000050 beq 110a0 <mcrypt\_mutex\_register@@Base+0xc90c>

10f5c: e28d8010 add r8, sp, #16

10f60: e1a01004 mov r1, r4

10f64: e1a02006 mov r2, r6

10f68: e1a00008 mov r0, r8

10f6c: ebffc4c5 bl 2288 <memcpy@plt>

10f70: ebffffc8 bl 10e98 <mcrypt\_mutex\_register@@Base+0xc704>

10f74: e1a01007 mov r1, r7

10f78: e28da020 add sl, sp, #32

10f7c: e0886006 add r6, r8, r6

10f80: e1a05008 mov r5, r8

10f84: e1a0400a mov r4, sl

10f88: e1a02000 mov r2, r0

10f8c: e1a00009 mov r0, r9

10f90: ebfffc0a bl ffc0 <mcrypt\_mutex\_register@@Base+0xb82c>

10f94: e1a00007 mov r0, r7

10f98: ebffc4b7 bl 227c <free@plt>

10f9c: e1a01008 mov r1, r8

10fa0: e1a00009 mov r0, r9

10fa4: ebfffc5e bl 10124 <mcrypt\_mutex\_register@@Base+0xb990>

10fa8: e59f7108 ldr r7, [pc, #264] ; 110b8 <mcrypt\_mutex\_register@@Base+0xc924>

10fac: e08f7007 add r7, pc, r7

10fb0: e4d52001 ldrb r2, [r5], #1

10fb4: e1a00004 mov r0, r4

10fb8: e1a01007 mov r1, r7

10fbc: ebffc4ea bl 236c <sprintf@plt>

10fc0: e1560005 cmp r6, r5

10fc4: e2844002 add r4, r4, #2

10fc8: 1afffff8 bne 10fb0 <mcrypt\_mutex\_register@@Base+0xc81c>

10fcc: e59f40e8 ldr r4, [pc, #232] ; 110bc <mcrypt\_mutex\_register@@Base+0xc928>

10fd0: e1a0000a mov r0, sl

10fd4: e08f4004 add r4, pc, r4

10fd8: e1a01004 mov r1, r4

10fdc: ebffc49a bl 224c <strcmp@plt>

10fe0: e3500000 cmp r0, #0

10fe4: 1a00001c bne 1105c <mcrypt\_mutex\_register@@Base+0xc8c8>

10fe8: e1a01008 mov r1, r8

10fec: e1a00009 mov r0, r9

10ff0: ebfffdf6 bl 107d0 <mcrypt\_mutex\_register@@Base+0xc03c>

10ff4: e1a00009 mov r0, r9

10ff8: ebffc49f bl 227c <free@plt>

10ffc: e1a00008 mov r0, r8

11000: e1a0100d mov r1, sp

11004: ebffc490 bl 224c <strcmp@plt>

11008: e3500000 cmp r0, #0

1100c: 1a00001e bne 1108c <mcrypt\_mutex\_register@@Base+0xc8f8>

11010: e28dd0e8 add sp, sp, #232 ; 0xe8

11014: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

11018: ebffff98 bl 10e80 <mcrypt\_mutex\_register@@Base+0xc6ec>

1101c: ebffc4b4 bl 22f4 <malloc@plt>

11020: e2509000 subs r9, r0, #0

11024: 0a00001d beq 110a0 <mcrypt\_mutex\_register@@Base+0xc90c>

11028: ebffff9a bl 10e98 <mcrypt\_mutex\_register@@Base+0xc704>

1102c: e1a01007 mov r1, r7

11030: e28d8010 add r8, sp, #16

11034: e28da020 add sl, sp, #32

11038: e1a02000 mov r2, r0

1103c: e1a00009 mov r0, r9

11040: ebfffbde bl ffc0 <mcrypt\_mutex\_register@@Base+0xb82c>

11044: e1a00007 mov r0, r7

11048: ebffc48b bl 227c <free@plt>

1104c: e1a01008 mov r1, r8

11050: e1a00009 mov r0, r9

11054: ebfffc32 bl 10124 <mcrypt\_mutex\_register@@Base+0xb990>

11058: eaffffdb b 10fcc <mcrypt\_mutex\_register@@Base+0xc838>

1105c: e59f005c ldr r0, [pc, #92] ; 110c0 <mcrypt\_mutex\_register@@Base+0xc92c>

11060: e08f0000 add r0, pc, r0

11064: ebffc49f bl 22e8 <puts@plt>

11068: e59f0054 ldr r0, [pc, #84] ; 110c4 <mcrypt\_mutex\_register@@Base+0xc930>

1106c: e1a0200a mov r2, sl

11070: e1a01004 mov r1, r4

11074: e08f0000 add r0, pc, r0

11078: ebffc479 bl 2264 <printf@plt>

1107c: e1a00009 mov r0, r9

11080: ebffc47d bl 227c <free@plt>

11084: e3e00000 mvn r0, #0

11088: eaffffe0 b 11010 <mcrypt\_mutex\_register@@Base+0xc87c>

1108c: e59f0034 ldr r0, [pc, #52] ; 110c8 <mcrypt\_mutex\_register@@Base+0xc934>

11090: e08f0000 add r0, pc, r0

11094: ebffc493 bl 22e8 <puts@plt>

11098: e3e00000 mvn r0, #0

1109c: eaffffdb b 11010 <mcrypt\_mutex\_register@@Base+0xc87c>

110a0: e1a00007 mov r0, r7

110a4: ebffc474 bl 227c <free@plt>

110a8: e3e00000 mvn r0, #0

110ac: eaffffd7 b 11010 <mcrypt\_mutex\_register@@Base+0xc87c>

110b0: e3e00000 mvn r0, #0

110b4: eaffffd5 b 11010 <mcrypt\_mutex\_register@@Base+0xc87c>

110b8: 0000f6b4 ; <UNDEFINED> instruction: 0x0000f6b4

110bc: 0000f8ac andeq pc, r0, ip, lsr #17

110c0: 0000f61c andeq pc, r0, ip, lsl r6 ; <UNPREDICTABLE>

110c4: 0000f620 andeq pc, r0, r0, lsr #12

110c8: 0000f61c andeq pc, r0, ip, lsl r6 ; <UNPREDICTABLE>

110cc: e59f0000 ldr r0, [pc] ; 110d4 <mcrypt\_mutex\_register@@Base+0xc940>

110d0: e12fff1e bx lr

110d4: 01315731 teqeq r1, r1, lsr r7

110d8: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

110dc: e21090ff ands r9, r0, #255 ; 0xff

110e0: 03a07000 moveq r7, #0

110e4: 13a07001 movne r7, #1

110e8: e21180ff ands r8, r1, #255 ; 0xff

110ec: 03a07000 moveq r7, #0

110f0: e1a04420 lsr r4, r0, #8

110f4: e1a0c820 lsr ip, r0, #16

110f8: e1a03c20 lsr r3, r0, #24

110fc: e1a0e421 lsr lr, r1, #8

11100: e1a02821 lsr r2, r1, #16

11104: e1a01c21 lsr r1, r1, #24

11108: e3570000 cmp r7, #0

1110c: e6ef4074 uxtb r4, r4

11110: e6efc07c uxtb ip, ip

11114: e6ef5073 uxtb r5, r3

11118: e6efe07e uxtb lr, lr

1111c: e6ef2072 uxtb r2, r2

11120: e6ef6071 uxtb r6, r1

11124: 01a00007 moveq r0, r7

11128: 0a00000c beq 11160 <mcrypt\_mutex\_register@@Base+0xc9cc>

1112c: e59f70fc ldr r7, [pc, #252] ; 11230 <mcrypt\_mutex\_register@@Base+0xca9c>

11130: e59f00fc ldr r0, [pc, #252] ; 11234 <mcrypt\_mutex\_register@@Base+0xcaa0>

11134: e08f7007 add r7, pc, r7

11138: e0879009 add r9, r7, r9

1113c: e0878008 add r8, r7, r8

11140: e5d99100 ldrb r9, [r9, #256] ; 0x100

11144: e5d88100 ldrb r8, [r8, #256] ; 0x100

11148: e0898008 add r8, r9, r8

1114c: e0809890 umull r9, r0, r0, r8

11150: e1a003a0 lsr r0, r0, #7

11154: e0600400 rsb r0, r0, r0, lsl #8

11158: e0480000 sub r0, r8, r0

1115c: e7d70000 ldrb r0, [r7, r0]

11160: e3540000 cmp r4, #0

11164: 135e0000 cmpne lr, #0

11168: 0a00000d beq 111a4 <mcrypt\_mutex\_register@@Base+0xca10>

1116c: e59f80c4 ldr r8, [pc, #196] ; 11238 <mcrypt\_mutex\_register@@Base+0xcaa4>

11170: e59f70bc ldr r7, [pc, #188] ; 11234 <mcrypt\_mutex\_register@@Base+0xcaa0>

11174: e08f8008 add r8, pc, r8

11178: e0884004 add r4, r8, r4

1117c: e088e00e add lr, r8, lr

11180: e5d44100 ldrb r4, [r4, #256] ; 0x100

11184: e5dee100 ldrb lr, [lr, #256] ; 0x100

11188: e084400e add r4, r4, lr

1118c: e08e7497 umull r7, lr, r7, r4

11190: e1a0e3ae lsr lr, lr, #7

11194: e06ee40e rsb lr, lr, lr, lsl #8

11198: e044e00e sub lr, r4, lr

1119c: e7d8e00e ldrb lr, [r8, lr]

111a0: e020000e eor r0, r0, lr

111a4: e35c0000 cmp ip, #0

111a8: 13520000 cmpne r2, #0

111ac: 0a00000d beq 111e8 <mcrypt\_mutex\_register@@Base+0xca54>

111b0: e59f4084 ldr r4, [pc, #132] ; 1123c <mcrypt\_mutex\_register@@Base+0xcaa8>

111b4: e59fe078 ldr lr, [pc, #120] ; 11234 <mcrypt\_mutex\_register@@Base+0xcaa0>

111b8: e08f4004 add r4, pc, r4

111bc: e084c00c add ip, r4, ip

111c0: e0842002 add r2, r4, r2

111c4: e5dcc100 ldrb ip, [ip, #256] ; 0x100

111c8: e5d22100 ldrb r2, [r2, #256] ; 0x100

111cc: e08cc002 add ip, ip, r2

111d0: e082ec9e umull lr, r2, lr, ip

111d4: e1a023a2 lsr r2, r2, #7

111d8: e0622402 rsb r2, r2, r2, lsl #8

111dc: e04c2002 sub r2, ip, r2

111e0: e7d42002 ldrb r2, [r4, r2]

111e4: e0200002 eor r0, r0, r2

111e8: e3560000 cmp r6, #0

111ec: 13550000 cmpne r5, #0

111f0: 08bd83f0 popeq {r4, r5, r6, r7, r8, r9, pc}

111f4: e59fc044 ldr ip, [pc, #68] ; 11240 <mcrypt\_mutex\_register@@Base+0xcaac>

111f8: e59f2034 ldr r2, [pc, #52] ; 11234 <mcrypt\_mutex\_register@@Base+0xcaa0>

111fc: e08fc00c add ip, pc, ip

11200: e08c3003 add r3, ip, r3

11204: e08c1001 add r1, ip, r1

11208: e5d33100 ldrb r3, [r3, #256] ; 0x100

1120c: e5d11100 ldrb r1, [r1, #256] ; 0x100

11210: e0831001 add r1, r3, r1

11214: e0832192 umull r2, r3, r2, r1

11218: e1a033a3 lsr r3, r3, #7

1121c: e0633403 rsb r3, r3, r3, lsl #8

11220: e0413003 sub r3, r1, r3

11224: e7dc3003 ldrb r3, [ip, r3]

11228: e0200003 eor r0, r0, r3

1122c: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

11230: 00025468 andeq r5, r2, r8, ror #8

11234: 80808081 addhi r8, r0, r1, lsl #1

11238: 00025428 andeq r5, r2, r8, lsr #8

1123c: 000253e4 andeq r5, r2, r4, ror #7

11240: 000253a0 andeq r5, r2, r0, lsr #7

11244: e59fc71c ldr ip, [pc, #1820] ; 11968 <mcrypt\_mutex\_register@@Base+0xd1d4>

11248: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1124c: e08fc00c add ip, pc, ip

11250: e24dd04c sub sp, sp, #76 ; 0x4c

11254: e1a0e002 mov lr, r2

11258: e2823003 add r3, r2, #3

1125c: e35e0000 cmp lr, #0

11260: e58d2018 str r2, [sp, #24]

11264: e59c2200 ldr r2, [ip, #512] ; 0x200

11268: a1a0300e movge r3, lr

1126c: e3520000 cmp r2, #0

11270: e1cd01fc strd r0, [sp, #28]

11274: e1a0b143 asr fp, r3, #2

11278: 0a00010f beq 116bc <mcrypt\_mutex\_register@@Base+0xcf28>

1127c: e59d3018 ldr r3, [sp, #24]

11280: e59d201c ldr r2, [sp, #28]

11284: e353001b cmp r3, #27

11288: e3a03006 mov r3, #6

1128c: e582b000 str fp, [r2]

11290: e5823004 str r3, [r2, #4]

11294: da0000cc ble 115cc <mcrypt\_mutex\_register@@Base+0xce38>

11298: e28b3007 add r3, fp, #7

1129c: e28b2006 add r2, fp, #6

112a0: e0833083 add r3, r3, r3, lsl #1

112a4: e1a03083 lsl r3, r3, #1

112a8: e58d3010 str r3, [sp, #16]

112ac: e2433006 sub r3, r3, #6

112b0: e58d3004 str r3, [sp, #4]

112b4: e59d301c ldr r3, [sp, #28]

112b8: e59f56ac ldr r5, [pc, #1708] ; 1196c <mcrypt\_mutex\_register@@Base+0xd1d8>

112bc: e1a0c003 mov ip, r3

112c0: e3a09000 mov r9, #0

112c4: e5832008 str r2, [r3, #8]

112c8: e2898003 add r8, r9, #3

112cc: e2897001 add r7, r9, #1

112d0: e2891002 add r1, r9, #2

112d4: e2892005 add r2, r9, #5

112d8: e0803895 umull r3, r0, r5, r8

112dc: e0863195 umull r3, r6, r5, r1

112e0: e0843295 umull r3, r4, r5, r2

112e4: e08e3795 umull r3, lr, r5, r7

112e8: e2899004 add r9, r9, #4

112ec: e1a00120 lsr r0, r0, #2

112f0: e083a995 umull sl, r3, r5, r9

112f4: e0800080 add r0, r0, r0, lsl #1

112f8: e1a0e12e lsr lr, lr, #2

112fc: e1a06126 lsr r6, r6, #2

11300: e1a04124 lsr r4, r4, #2

11304: e1a03123 lsr r3, r3, #2

11308: e0480080 sub r0, r8, r0, lsl #1

1130c: e08ee08e add lr, lr, lr, lsl #1

11310: e0866086 add r6, r6, r6, lsl #1

11314: e0844084 add r4, r4, r4, lsl #1

11318: e0833083 add r3, r3, r3, lsl #1

1131c: e6ef0070 uxtb r0, r0

11320: e0493083 sub r3, r9, r3, lsl #1

11324: e047e08e sub lr, r7, lr, lsl #1

11328: e0411086 sub r1, r1, r6, lsl #1

1132c: e0422084 sub r2, r2, r4, lsl #1

11330: e3570006 cmp r7, #6

11334: e5cc000e strb r0, [ip, #14]

11338: e5cc0026 strb r0, [ip, #38] ; 0x26

1133c: e5cce00c strb lr, [ip, #12]

11340: e5cc100d strb r1, [ip, #13]

11344: e5cc2024 strb r2, [ip, #36] ; 0x24

11348: e5cc3025 strb r3, [ip, #37] ; 0x25

1134c: e1a09007 mov r9, r7

11350: e28cc003 add ip, ip, #3

11354: 1affffdb bne 112c8 <mcrypt\_mutex\_register@@Base+0xcb34>

11358: e59d3018 ldr r3, [sp, #24]

1135c: e58d7000 str r7, [sp]

11360: e3530003 cmp r3, #3

11364: da000178 ble 1194c <mcrypt\_mutex\_register@@Base+0xd1b8>

11368: e28d2024 add r2, sp, #36 ; 0x24

1136c: e59d1020 ldr r1, [sp, #32]

11370: e1a00002 mov r0, r2

11374: e3a03000 mov r3, #0

11378: e2833001 add r3, r3, #1

1137c: e491c004 ldr ip, [r1], #4

11380: e15b0003 cmp fp, r3

11384: e5a0c004 str ip, [r0, #4]!

11388: cafffffa bgt 11378 <mcrypt\_mutex\_register@@Base+0xcbe4>

1138c: e59d301c ldr r3, [sp, #28]

11390: e283303c add r3, r3, #60 ; 0x3c

11394: e58d3020 str r3, [sp, #32]

11398: e1a01003 mov r1, r3

1139c: e3a03000 mov r3, #0

113a0: e2833001 add r3, r3, #1

113a4: e5b20004 ldr r0, [r2, #4]!

113a8: e15b0003 cmp fp, r3

113ac: e4810004 str r0, [r1], #4

113b0: cafffffa bgt 113a0 <mcrypt\_mutex\_register@@Base+0xcc0c>

113b4: e59f45b4 ldr r4, [pc, #1460] ; 11970 <mcrypt\_mutex\_register@@Base+0xd1dc>

113b8: e59d201c ldr r2, [sp, #28]

113bc: e08f4004 add r4, pc, r4

113c0: e06b3f0b rsb r3, fp, fp, lsl #30

113c4: e1a0910b lsl r9, fp, #2

113c8: e59d6010 ldr r6, [sp, #16]

113cc: e284ab03 add sl, r4, #3072 ; 0xc00

113d0: e289c040 add ip, r9, #64 ; 0x40

113d4: e59f1598 ldr r1, [pc, #1432] ; 11974 <mcrypt\_mutex\_register@@Base+0xd1e0>

113d8: e0823103 add r3, r2, r3, lsl #2

113dc: e28aa004 add sl, sl, #4

113e0: e082c00c add ip, r2, ip

113e4: e2825050 add r5, r2, #80 ; 0x50

113e8: e046000b sub r0, r6, fp

113ec: e1a0e00b mov lr, fp

113f0: e08f1001 add r1, pc, r1

113f4: e283303c add r3, r3, #60 ; 0x3c

113f8: e58d1014 str r1, [sp, #20]

113fc: e58d300c str r3, [sp, #12]

11400: e51c1008 ldr r1, [ip, #-8]

11404: e59d3018 ldr r3, [sp, #24]

11408: e49a7004 ldr r7, [sl], #4

1140c: e1a01461 ror r1, r1, #8

11410: e353001b cmp r3, #27

11414: e1a08821 lsr r8, r1, #16

11418: e0843c21 add r3, r4, r1, lsr #24

1141c: e6e42071 uxtab r2, r4, r1

11420: e1a01421 lsr r1, r1, #8

11424: e6e48078 uxtab r8, r4, r8

11428: e6e41071 uxtab r1, r4, r1

1142c: e5d33204 ldrb r3, [r3, #516] ; 0x204

11430: e5d88204 ldrb r8, [r8, #516] ; 0x204

11434: e5d22204 ldrb r2, [r2, #516] ; 0x204

11438: e5d11204 ldrb r1, [r1, #516] ; 0x204

1143c: e1822c03 orr r2, r2, r3, lsl #24

11440: e5153014 ldr r3, [r5, #-20] ; 0xffffffec

11444: e1822808 orr r2, r2, r8, lsl #16

11448: e1822401 orr r2, r2, r1, lsl #8

1144c: e0233007 eor r3, r3, r7

11450: e0233002 eor r3, r3, r2

11454: e50c3004 str r3, [ip, #-4]

11458: ca000061 bgt 115e4 <mcrypt\_mutex\_register@@Base+0xce50>

1145c: e59d2018 ldr r2, [sp, #24]

11460: e3520007 cmp r2, #7

11464: da00000f ble 114a8 <mcrypt\_mutex\_register@@Base+0xcd14>

11468: e28e2001 add r2, lr, #1

1146c: e1520006 cmp r2, r6

11470: aa00000c bge 114a8 <mcrypt\_mutex\_register@@Base+0xcd14>

11474: e2458010 sub r8, r5, #16

11478: e1a0700c mov r7, ip

1147c: e3a01001 mov r1, #1

11480: ea000002 b 11490 <mcrypt\_mutex\_register@@Base+0xccfc>

11484: e1510000 cmp r1, r0

11488: 0a000006 beq 114a8 <mcrypt\_mutex\_register@@Base+0xcd14>

1148c: e5173004 ldr r3, [r7, #-4]

11490: e4982004 ldr r2, [r8], #4

11494: e2811001 add r1, r1, #1

11498: e0233002 eor r3, r3, r2

1149c: e15b0001 cmp fp, r1

114a0: e4873004 str r3, [r7], #4

114a4: cafffff6 bgt 11484 <mcrypt\_mutex\_register@@Base+0xccf0>

114a8: e08ee00b add lr, lr, fp

114ac: e15e0006 cmp lr, r6

114b0: e0855009 add r5, r5, r9

114b4: e08cc009 add ip, ip, r9

114b8: e040000b sub r0, r0, fp

114bc: baffffcf blt 11400 <mcrypt\_mutex\_register@@Base+0xcc6c>

114c0: e59d3010 ldr r3, [sp, #16]

114c4: e59d101c ldr r1, [sp, #28]

114c8: e2832081 add r2, r3, #129 ; 0x81

114cc: e59d3020 ldr r3, [sp, #32]

114d0: e0812102 add r2, r1, r2, lsl #2

114d4: e2810054 add r0, r1, #84 ; 0x54

114d8: e4931004 ldr r1, [r3], #4

114dc: e1500003 cmp r0, r3

114e0: e4821004 str r1, [r2], #4

114e4: 1afffffb bne 114d8 <mcrypt\_mutex\_register@@Base+0xcd44>

114e8: e59d3004 ldr r3, [sp, #4]

114ec: e59d201c ldr r2, [sp, #28]

114f0: e59fb480 ldr fp, [pc, #1152] ; 11978 <mcrypt\_mutex\_register@@Base+0xd1e4>

114f4: e0823083 add r3, r2, r3, lsl #1

114f8: e282806c add r8, r2, #108 ; 0x6c

114fc: e1a03083 lsl r3, r3, #1

11500: e2833e27 add r3, r3, #624 ; 0x270

11504: e58d3008 str r3, [sp, #8]

11508: e59d3008 ldr r3, [sp, #8]

1150c: e2486018 sub r6, r8, #24

11510: e0437008 sub r7, r3, r8

11514: e4965004 ldr r5, [r6], #4

11518: e1a0000b mov r0, fp

1151c: e1a01005 mov r1, r5

11520: ebfffeec bl 110d8 <mcrypt\_mutex\_register@@Base+0xc944>

11524: e1a01005 mov r1, r5

11528: e1a0a000 mov sl, r0

1152c: e59f0448 ldr r0, [pc, #1096] ; 1197c <mcrypt\_mutex\_register@@Base+0xd1e8>

11530: ebfffee8 bl 110d8 <mcrypt\_mutex\_register@@Base+0xc944>

11534: e1a01005 mov r1, r5

11538: e1a04000 mov r4, r0

1153c: e59f043c ldr r0, [pc, #1084] ; 11980 <mcrypt\_mutex\_register@@Base+0xd1ec>

11540: ebfffee4 bl 110d8 <mcrypt\_mutex\_register@@Base+0xc944>

11544: e1a01005 mov r1, r5

11548: e1a04804 lsl r4, r4, #16

1154c: e1844c0a orr r4, r4, sl, lsl #24

11550: e1a09000 mov r9, r0

11554: e59f0428 ldr r0, [pc, #1064] ; 11984 <mcrypt\_mutex\_register@@Base+0xd1f0>

11558: ebfffede bl 110d8 <mcrypt\_mutex\_register@@Base+0xc944>

1155c: e1580006 cmp r8, r6

11560: e1840000 orr r0, r4, r0

11564: e1800409 orr r0, r0, r9, lsl #8

11568: e4870004 str r0, [r7], #4

1156c: 1affffe8 bne 11514 <mcrypt\_mutex\_register@@Base+0xcd80>

11570: e59d3000 ldr r3, [sp]

11574: e59d2004 ldr r2, [sp, #4]

11578: e2833006 add r3, r3, #6

1157c: e1530002 cmp r3, r2

11580: e58d3000 str r3, [sp]

11584: e2888018 add r8, r8, #24

11588: baffffde blt 11508 <mcrypt\_mutex\_register@@Base+0xcd74>

1158c: e59d1004 ldr r1, [sp, #4]

11590: e59dc010 ldr ip, [sp, #16]

11594: e59d001c ldr r0, [sp, #28]

11598: e281308d add r3, r1, #141 ; 0x8d

1159c: e043300c sub r3, r3, ip

115a0: e0802101 add r2, r0, r1, lsl #2

115a4: e0803103 add r3, r0, r3, lsl #2

115a8: e2822038 add r2, r2, #56 ; 0x38

115ac: e2811001 add r1, r1, #1

115b0: e5b20004 ldr r0, [r2, #4]!

115b4: e151000c cmp r1, ip

115b8: e4830004 str r0, [r3], #4

115bc: bafffffa blt 115ac <mcrypt\_mutex\_register@@Base+0xce18>

115c0: e3a00000 mov r0, #0

115c4: e28dd04c add sp, sp, #76 ; 0x4c

115c8: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

115cc: e3a03048 mov r3, #72 ; 0x48

115d0: e58d3004 str r3, [sp, #4]

115d4: e3a0304e mov r3, #78 ; 0x4e

115d8: e58d3010 str r3, [sp, #16]

115dc: e3a0200c mov r2, #12

115e0: eaffff33 b 112b4 <mcrypt\_mutex\_register@@Base+0xcb20>

115e4: e59d700c ldr r7, [sp, #12]

115e8: e28e3001 add r3, lr, #1

115ec: e24c1004 sub r1, ip, #4

115f0: e28e8004 add r8, lr, #4

115f4: e58d0008 str r0, [sp, #8]

115f8: e1530006 cmp r3, r6

115fc: aa000006 bge 1161c <mcrypt\_mutex\_register@@Base+0xce88>

11600: e7972103 ldr r2, [r7, r3, lsl #2]

11604: e5910000 ldr r0, [r1]

11608: e2833001 add r3, r3, #1

1160c: e0222000 eor r2, r2, r0

11610: e1580003 cmp r8, r3

11614: e5a12004 str r2, [r1, #4]!

11618: 1afffff6 bne 115f8 <mcrypt\_mutex\_register@@Base+0xce64>

1161c: e1580006 cmp r8, r6

11620: e59d0008 ldr r0, [sp, #8]

11624: aa000013 bge 11678 <mcrypt\_mutex\_register@@Base+0xcee4>

11628: e59c2008 ldr r2, [ip, #8]

1162c: e5153004 ldr r3, [r5, #-4]

11630: e58d3008 str r3, [sp, #8]

11634: e59d3014 ldr r3, [sp, #20]

11638: e1a01822 lsr r1, r2, #16

1163c: e0837c22 add r7, r3, r2, lsr #24

11640: e6e38072 uxtab r8, r3, r2

11644: e1a02422 lsr r2, r2, #8

11648: e6e31071 uxtab r1, r3, r1

1164c: e6e32072 uxtab r2, r3, r2

11650: e5d77204 ldrb r7, [r7, #516] ; 0x204

11654: e5d83204 ldrb r3, [r8, #516] ; 0x204

11658: e5d11204 ldrb r1, [r1, #516] ; 0x204

1165c: e5d22204 ldrb r2, [r2, #516] ; 0x204

11660: e1833c07 orr r3, r3, r7, lsl #24

11664: e59d8008 ldr r8, [sp, #8]

11668: e1833801 orr r3, r3, r1, lsl #16

1166c: e1833402 orr r3, r3, r2, lsl #8

11670: e0288003 eor r8, r8, r3

11674: e58c800c str r8, [ip, #12]

11678: e28e3005 add r3, lr, #5

1167c: e1530006 cmp r3, r6

11680: aaffff88 bge 114a8 <mcrypt\_mutex\_register@@Base+0xcd14>

11684: e28c100c add r1, ip, #12

11688: e1a07005 mov r7, r5

1168c: e3a03005 mov r3, #5

11690: ea000001 b 1169c <mcrypt\_mutex\_register@@Base+0xcf08>

11694: e1530000 cmp r3, r0

11698: 0affff82 beq 114a8 <mcrypt\_mutex\_register@@Base+0xcd14>

1169c: e4972004 ldr r2, [r7], #4

116a0: e5918000 ldr r8, [r1]

116a4: e2833001 add r3, r3, #1

116a8: e0222008 eor r2, r2, r8

116ac: e15b0003 cmp fp, r3

116b0: e5a12004 str r2, [r1, #4]!

116b4: cafffff6 bgt 11694 <mcrypt\_mutex\_register@@Base+0xcf00>

116b8: eaffff7a b 114a8 <mcrypt\_mutex\_register@@Base+0xcd14>

116bc: e3a03001 mov r3, #1

116c0: e28c0c01 add r0, ip, #256 ; 0x100

116c4: e59f12bc ldr r1, [pc, #700] ; 11988 <mcrypt\_mutex\_register@@Base+0xd1f4>

116c8: e08ce003 add lr, ip, r3

116cc: e1c020b0 strh r2, [r0]

116d0: e1cc10b0 strh r1, [ip]

116d4: e5cc3103 strb r3, [ip, #259] ; 0x103

116d8: e08e0003 add r0, lr, r3

116dc: e28c40ff add r4, ip, #255 ; 0xff

116e0: e1a0100c mov r1, ip

116e4: e3a03003 mov r3, #3

116e8: ea000002 b 116f8 <mcrypt\_mutex\_register@@Base+0xcf64>

116ec: e3130080 tst r3, #128 ; 0x80

116f0: 13a0201b movne r2, #27

116f4: 03a02000 moveq r2, #0

116f8: e0222083 eor r2, r2, r3, lsl #1

116fc: e0233002 eor r3, r3, r2

11700: e1a0c000 mov ip, r0

11704: e6ef3073 uxtb r3, r3

11708: e0812003 add r2, r1, r3

1170c: e4c03001 strb r3, [r0], #1

11710: e154000c cmp r4, ip

11714: e040c00e sub ip, r0, lr

11718: e5c2c100 strb ip, [r2, #256] ; 0x100

1171c: 1afffff2 bne 116ec <mcrypt\_mutex\_register@@Base+0xcf58>

11720: e2818c01 add r8, r1, #256 ; 0x100

11724: e1a04008 mov r4, r8

11728: e2810f81 add r0, r1, #516 ; 0x204

1172c: e28880ff add r8, r8, #255 ; 0xff

11730: e2649001 rsb r9, r4, #1

11734: e2445c01 sub r5, r4, #256 ; 0x100

11738: e1a06000 mov r6, r0

1173c: e3a02063 mov r2, #99 ; 0x63

11740: e3a03000 mov r3, #0

11744: e5c12204 strb r2, [r1, #516] ; 0x204

11748: e5c13367 strb r3, [r1, #871] ; 0x367

1174c: e0891004 add r1, r9, r4

11750: e5f43001 ldrb r3, [r4, #1]!

11754: e26330ff rsb r3, r3, #255 ; 0xff

11758: e1580004 cmp r8, r4

1175c: e7d5e003 ldrb lr, [r5, r3]

11760: e1a0208e lsl r2, lr, #1

11764: e18223ae orr r2, r2, lr, lsr #7

11768: e6ef2072 uxtb r2, r2

1176c: e1a03082 lsl r3, r2, #1

11770: e18333a2 orr r3, r3, r2, lsr #7

11774: e6ef3073 uxtb r3, r3

11778: e1a0c083 lsl ip, r3, #1

1177c: e18cc3a3 orr ip, ip, r3, lsr #7

11780: e6efc07c uxtb ip, ip

11784: e1a0708c lsl r7, ip, #1

11788: e18773ac orr r7, r7, ip, lsr #7

1178c: e6af7077 sxtb r7, r7

11790: e02ee007 eor lr, lr, r7

11794: e022200e eor r2, r2, lr

11798: e0233002 eor r3, r3, r2

1179c: e6ef3073 uxtb r3, r3

117a0: e023300c eor r3, r3, ip

117a4: e2233063 eor r3, r3, #99 ; 0x63

117a8: e0852003 add r2, r5, r3

117ac: e5e63001 strb r3, [r6, #1]!

117b0: e5c21304 strb r1, [r2, #772] ; 0x304

117b4: 1affffe4 bne 1174c <mcrypt\_mutex\_register@@Base+0xcfb8>

117b8: e285cb03 add ip, r5, #3072 ; 0xc00

117bc: e3a03001 mov r3, #1

117c0: e28c2004 add r2, ip, #4

117c4: e5853c04 str r3, [r5, #3076] ; 0xc04

117c8: e28cc078 add ip, ip, #120 ; 0x78

117cc: e3a01000 mov r1, #0

117d0: ea000003 b 117e4 <mcrypt\_mutex\_register@@Base+0xd050>

117d4: e3130080 tst r3, #128 ; 0x80

117d8: 13a0101b movne r1, #27

117dc: 03a01000 moveq r1, #0

117e0: e5a23004 str r3, [r2, #4]!

117e4: e1a03083 lsl r3, r3, #1

117e8: e15c0002 cmp ip, r2

117ec: e6ef3073 uxtb r3, r3

117f0: e0233001 eor r3, r3, r1

117f4: 1afffff6 bne 117d4 <mcrypt\_mutex\_register@@Base+0xd040>

117f8: e59f618c ldr r6, [pc, #396] ; 1198c <mcrypt\_mutex\_register@@Base+0xd1f8>

117fc: e59f418c ldr r4, [pc, #396] ; 11990 <mcrypt\_mutex\_register@@Base+0xd1fc>

11800: e08f6006 add r6, pc, r6

11804: e286ec02 add lr, r6, #512 ; 0x200

11808: e5d6310b ldrb r3, [r6, #267] ; 0x10b

1180c: e2867c03 add r7, r6, #768 ; 0x300

11810: e59f917c ldr r9, [pc, #380] ; 11994 <mcrypt\_mutex\_register@@Base+0xd200>

11814: e58d3008 str r3, [sp, #8]

11818: e5d6310d ldrb r3, [r6, #269] ; 0x10d

1181c: e2868b01 add r8, r6, #1024 ; 0x400

11820: e08f4004 add r4, pc, r4

11824: e58d300c str r3, [sp, #12]

11828: e5d63109 ldrb r3, [r6, #265] ; 0x109

1182c: e28ee003 add lr, lr, #3

11830: e2877003 add r7, r7, #3

11834: e58d3010 str r3, [sp, #16]

11838: e5d6310e ldrb r3, [r6, #270] ; 0x10e

1183c: e2866b02 add r6, r6, #2048 ; 0x800

11840: e58db024 str fp, [sp, #36] ; 0x24

11844: e58d3014 str r3, [sp, #20]

11848: e28030ff add r3, r0, #255 ; 0xff

1184c: e58d3000 str r3, [sp]

11850: e5fe1001 ldrb r1, [lr, #1]!

11854: e5f70001 ldrb r0, [r7, #1]!

11858: e3110080 tst r1, #128 ; 0x80

1185c: e1a02081 lsl r2, r1, #1

11860: 13a0301b movne r3, #27

11864: 03a03000 moveq r3, #0

11868: e6ef2072 uxtb r2, r2

1186c: e0222003 eor r2, r2, r3

11870: e1a03401 lsl r3, r1, #8

11874: e021c002 eor ip, r1, r2

11878: e1833801 orr r3, r3, r1, lsl #16

1187c: e1832002 orr r2, r3, r2

11880: e1822c0c orr r2, r2, ip, lsl #24

11884: e3500000 cmp r0, #0

11888: e0845000 add r5, r4, r0

1188c: e1a0a000 mov sl, r0

11890: e5a62004 str r2, [r6, #4]!

11894: 0a000022 beq 11924 <mcrypt\_mutex\_register@@Base+0xd190>

11898: e5d50100 ldrb r0, [r5, #256] ; 0x100

1189c: e59d300c ldr r3, [sp, #12]

118a0: e080b003 add fp, r0, r3

118a4: e59d3008 ldr r3, [sp, #8]

118a8: e083a000 add sl, r3, r0

118ac: e0813b99 umull r3, r1, r9, fp

118b0: e0823a99 umull r3, r2, r9, sl

118b4: e59d3014 ldr r3, [sp, #20]

118b8: e58d2004 str r2, [sp, #4]

118bc: e0805003 add r5, r0, r3

118c0: e59d3010 ldr r3, [sp, #16]

118c4: e1a013a1 lsr r1, r1, #7

118c8: e0800003 add r0, r0, r3

118cc: e08c3599 umull r3, ip, r9, r5

118d0: e0832099 umull r2, r3, r9, r0

118d4: e59d2004 ldr r2, [sp, #4]

118d8: e0611401 rsb r1, r1, r1, lsl #8

118dc: e04b1001 sub r1, fp, r1

118e0: e1a023a2 lsr r2, r2, #7

118e4: e1a0c3ac lsr ip, ip, #7

118e8: e0622402 rsb r2, r2, r2, lsl #8

118ec: e1a033a3 lsr r3, r3, #7

118f0: e04a2002 sub r2, sl, r2

118f4: e7d41001 ldrb r1, [r4, r1]

118f8: e06cc40c rsb ip, ip, ip, lsl #8

118fc: e0633403 rsb r3, r3, r3, lsl #8

11900: e0403003 sub r3, r0, r3

11904: e7d42002 ldrb r2, [r4, r2]

11908: e045c00c sub ip, r5, ip

1190c: e7d43003 ldrb r3, [r4, r3]

11910: e7d4000c ldrb r0, [r4, ip]

11914: e1a01801 lsl r1, r1, #16

11918: e1811c02 orr r1, r1, r2, lsl #24

1191c: e1811000 orr r1, r1, r0

11920: e181a403 orr sl, r1, r3, lsl #8

11924: e59d3000 ldr r3, [sp]

11928: e5a8a004 str sl, [r8, #4]!

1192c: e15e0003 cmp lr, r3

11930: 1affffc6 bne 11850 <mcrypt\_mutex\_register@@Base+0xd0bc>

11934: e59f305c ldr r3, [pc, #92] ; 11998 <mcrypt\_mutex\_register@@Base+0xd204>

11938: e3a02001 mov r2, #1

1193c: e08f3003 add r3, pc, r3

11940: e59db024 ldr fp, [sp, #36] ; 0x24

11944: e5832200 str r2, [r3, #512] ; 0x200

11948: eafffe4b b 1127c <mcrypt\_mutex\_register@@Base+0xcae8>

1194c: e59d3010 ldr r3, [sp, #16]

11950: e15b0003 cmp fp, r3

11954: e59d301c ldr r3, [sp, #28]

11958: e283303c add r3, r3, #60 ; 0x3c

1195c: e58d3020 str r3, [sp, #32]

11960: aafffed6 bge 114c0 <mcrypt\_mutex\_register@@Base+0xcd2c>

11964: eafffe92 b 113b4 <mcrypt\_mutex\_register@@Base+0xcc20>

11968: 00025350 andeq r5, r2, r0, asr r3

1196c: aaaaaaab bge feabc420 <mcrypt\_mutex\_register@@Base+0xfeab7c8c>

11970: 000251e0 andeq r5, r2, r0, ror #3

11974: 000251ac andeq r5, r2, ip, lsr #3

11978: 0e090d0b cdpeq 13, 0, cr0, cr9, cr11, {0}

1197c: 0b0e090d bleq 393db8 <mcrypt\_mutex\_register@@Base+0x38f624>

11980: 0d0b0e09 stceq 14, cr0, [fp, #-36] ; 0xffffffdc

11984: 090d0b0e stmdbeq sp, {r1, r2, r3, r8, r9, fp}

11988: 00000301 andeq r0, r0, r1, lsl #6

1198c: 00024d9c muleq r2, ip, sp

11990: 00024d7c andeq r4, r2, ip, ror sp

11994: 80808081 addhi r8, r0, r1, lsl #1

11998: 00024c60 andeq r4, r2, r0, ror #24

1199c: e5903004 ldr r3, [r0, #4]

119a0: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

119a4: e24dd05c sub sp, sp, #92 ; 0x5c

119a8: e3530000 cmp r3, #0

119ac: e58d1014 str r1, [sp, #20]

119b0: e58d3004 str r3, [sp, #4]

119b4: da000086 ble 11bd4 <mcrypt\_mutex\_register@@Base+0xd440>

119b8: e1a03103 lsl r3, r3, #2

119bc: e1a02001 mov r2, r1

119c0: e0814003 add r4, r1, r3

119c4: e280c03c add ip, r0, #60 ; 0x3c

119c8: e28d1014 add r1, sp, #20

119cc: e58d300c str r3, [sp, #12]

119d0: e4923004 ldr r3, [r2], #4

119d4: e49ce004 ldr lr, [ip], #4

119d8: e1540002 cmp r4, r2

119dc: e023300e eor r3, r3, lr

119e0: e5a13004 str r3, [r1, #4]!

119e4: 1afffff9 bne 119d0 <mcrypt\_mutex\_register@@Base+0xd23c>

119e8: e5903008 ldr r3, [r0, #8]

119ec: e3530001 cmp r3, #1

119f0: e58d3008 str r3, [sp, #8]

119f4: da00007e ble 11bf4 <mcrypt\_mutex\_register@@Base+0xd460>

119f8: e59f1208 ldr r1, [pc, #520] ; 11c08 <mcrypt\_mutex\_register@@Base+0xd474>

119fc: e59d3004 ldr r3, [sp, #4]

11a00: e28db038 add fp, sp, #56 ; 0x38

11a04: e28d4018 add r4, sp, #24

11a08: e3a0a001 mov sl, #1

11a0c: e08f1001 add r1, pc, r1

11a10: e58d3000 str r3, [sp]

11a14: e58d0010 str r0, [sp, #16]

11a18: e59d3004 ldr r3, [sp, #4]

11a1c: e3530000 cmp r3, #0

11a20: da000026 ble 11ac0 <mcrypt\_mutex\_register@@Base+0xd32c>

11a24: e59d3000 ldr r3, [sp]

11a28: e59d2010 ldr r2, [sp, #16]

11a2c: e1a0e004 mov lr, r4

11a30: e0825103 add r5, r2, r3, lsl #2

11a34: e59d300c ldr r3, [sp, #12]

11a38: e2855038 add r5, r5, #56 ; 0x38

11a3c: e24b6004 sub r6, fp, #4

11a40: e0849003 add r9, r4, r3

11a44: e5d2700e ldrb r7, [r2, #14]

11a48: e5d2c00c ldrb ip, [r2, #12]

11a4c: e5d2000d ldrb r0, [r2, #13]

11a50: e49e3004 ldr r3, [lr], #4

11a54: e7947107 ldr r7, [r4, r7, lsl #2]

11a58: e084c10c add ip, r4, ip, lsl #2

11a5c: e0840100 add r0, r4, r0, lsl #2

11a60: e5dcc001 ldrb ip, [ip, #1]

11a64: e5d00002 ldrb r0, [r0, #2]

11a68: e1a07c27 lsr r7, r7, #24

11a6c: e6ef3073 uxtb r3, r3

11a70: e0817107 add r7, r1, r7, lsl #2

11a74: e0813103 add r3, r1, r3, lsl #2

11a78: e081c10c add ip, r1, ip, lsl #2

11a7c: e5977804 ldr r7, [r7, #2052] ; 0x804

11a80: e5933804 ldr r3, [r3, #2052] ; 0x804

11a84: e5b58004 ldr r8, [r5, #4]!

11a88: e0810100 add r0, r1, r0, lsl #2

11a8c: e59cc804 ldr ip, [ip, #2052] ; 0x804

11a90: e0233008 eor r3, r3, r8

11a94: e5900804 ldr r0, [r0, #2052] ; 0x804

11a98: e0233467 eor r3, r3, r7, ror #8

11a9c: e0233c6c eor r3, r3, ip, ror #24

11aa0: e0233860 eor r3, r3, r0, ror #16

11aa4: e159000e cmp r9, lr

11aa8: e5a63004 str r3, [r6, #4]!

11aac: e2822003 add r2, r2, #3

11ab0: 1affffe3 bne 11a44 <mcrypt\_mutex\_register@@Base+0xd2b0>

11ab4: e1cd20d0 ldrd r2, [sp]

11ab8: e0833002 add r3, r3, r2

11abc: e58d3000 str r3, [sp]

11ac0: e59d3008 ldr r3, [sp, #8]

11ac4: e28aa001 add sl, sl, #1

11ac8: e153000a cmp r3, sl

11acc: 11a03004 movne r3, r4

11ad0: 11a0400b movne r4, fp

11ad4: 11a0b003 movne fp, r3

11ad8: 1affffce bne 11a18 <mcrypt\_mutex\_register@@Base+0xd284>

11adc: e59d3004 ldr r3, [sp, #4]

11ae0: e59d0010 ldr r0, [sp, #16]

11ae4: e3530000 cmp r3, #0

11ae8: da000037 ble 11bcc <mcrypt\_mutex\_register@@Base+0xd438>

11aec: e59d3000 ldr r3, [sp]

11af0: e59f1114 ldr r1, [pc, #276] ; 11c0c <mcrypt\_mutex\_register@@Base+0xd478>

11af4: e2444004 sub r4, r4, #4

11af8: e0805103 add r5, r0, r3, lsl #2

11afc: e59d300c ldr r3, [sp, #12]

11b00: e08f1001 add r1, pc, r1

11b04: e2855038 add r5, r5, #56 ; 0x38

11b08: e08ba003 add sl, fp, r3

11b0c: e1a06004 mov r6, r4

11b10: e1a0e00b mov lr, fp

11b14: e1a02000 mov r2, r0

11b18: e5d2700c ldrb r7, [r2, #12]

11b1c: e5d2800e ldrb r8, [r2, #14]

11b20: e5d2c00d ldrb ip, [r2, #13]

11b24: e49e3004 ldr r3, [lr], #4

11b28: e08b7107 add r7, fp, r7, lsl #2

11b2c: e79b8108 ldr r8, [fp, r8, lsl #2]

11b30: e08bc10c add ip, fp, ip, lsl #2

11b34: e5d77001 ldrb r7, [r7, #1]

11b38: e5dcc002 ldrb ip, [ip, #2]

11b3c: e6e13073 uxtab r3, r1, r3

11b40: e0818c28 add r8, r1, r8, lsr #24

11b44: e0817007 add r7, r1, r7

11b48: e5d88204 ldrb r8, [r8, #516] ; 0x204

11b4c: e5d33204 ldrb r3, [r3, #516] ; 0x204

11b50: e081c00c add ip, r1, ip

11b54: e5d77204 ldrb r7, [r7, #516] ; 0x204

11b58: e5b59004 ldr r9, [r5, #4]!

11b5c: e5dcc204 ldrb ip, [ip, #516] ; 0x204

11b60: e0233009 eor r3, r3, r9

11b64: e0233468 eor r3, r3, r8, ror #8

11b68: e0233c67 eor r3, r3, r7, ror #24

11b6c: e023386c eor r3, r3, ip, ror #16

11b70: e15a000e cmp sl, lr

11b74: e5a63004 str r3, [r6, #4]!

11b78: e2822003 add r2, r2, #3

11b7c: 1affffe5 bne 11b18 <mcrypt\_mutex\_register@@Base+0xd384>

11b80: e3a02000 mov r2, #0

11b84: e59d1014 ldr r1, [sp, #20]

11b88: e24bb004 sub fp, fp, #4

11b8c: e1a0c002 mov ip, r2

11b90: e5b43004 ldr r3, [r4, #4]!

11b94: e2822001 add r2, r2, #1

11b98: e5c13000 strb r3, [r1]

11b9c: e1a05423 lsr r5, r3, #8

11ba0: e1a0e823 lsr lr, r3, #16

11ba4: e1a03c23 lsr r3, r3, #24

11ba8: e5c15001 strb r5, [r1, #1]

11bac: e5c1e002 strb lr, [r1, #2]

11bb0: e5c13003 strb r3, [r1, #3]

11bb4: e5903004 ldr r3, [r0, #4]

11bb8: e584c000 str ip, [r4]

11bbc: e1530002 cmp r3, r2

11bc0: e5abc004 str ip, [fp, #4]!

11bc4: e2811004 add r1, r1, #4

11bc8: cafffff0 bgt 11b90 <mcrypt\_mutex\_register@@Base+0xd3fc>

11bcc: e28dd05c add sp, sp, #92 ; 0x5c

11bd0: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

11bd4: e5903008 ldr r3, [r0, #8]

11bd8: e3530001 cmp r3, #1

11bdc: e58d3008 str r3, [sp, #8]

11be0: dafffff9 ble 11bcc <mcrypt\_mutex\_register@@Base+0xd438>

11be4: e59d3004 ldr r3, [sp, #4]

11be8: e1a03103 lsl r3, r3, #2

11bec: e58d300c str r3, [sp, #12]

11bf0: eaffff80 b 119f8 <mcrypt\_mutex\_register@@Base+0xd264>

11bf4: e59d3004 ldr r3, [sp, #4]

11bf8: e28d4038 add r4, sp, #56 ; 0x38

11bfc: e58d3000 str r3, [sp]

11c00: e28db018 add fp, sp, #24

11c04: eaffffb8 b 11aec <mcrypt\_mutex\_register@@Base+0xd358>

11c08: 00024b90 muleq r2, r0, fp

11c0c: 00024a9c muleq r2, ip, sl

11c10: e5903004 ldr r3, [r0, #4]

11c14: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

11c18: e24dd05c sub sp, sp, #92 ; 0x5c

11c1c: e3530000 cmp r3, #0

11c20: e58d1014 str r1, [sp, #20]

11c24: e58d3004 str r3, [sp, #4]

11c28: da000086 ble 11e48 <mcrypt\_mutex\_register@@Base+0xd6b4>

11c2c: e1a03103 lsl r3, r3, #2

11c30: e1a02001 mov r2, r1

11c34: e0814003 add r4, r1, r3

11c38: e280cf87 add ip, r0, #540 ; 0x21c

11c3c: e28d1014 add r1, sp, #20

11c40: e58d300c str r3, [sp, #12]

11c44: e4923004 ldr r3, [r2], #4

11c48: e49ce004 ldr lr, [ip], #4

11c4c: e1540002 cmp r4, r2

11c50: e023300e eor r3, r3, lr

11c54: e5a13004 str r3, [r1, #4]!

11c58: 1afffff9 bne 11c44 <mcrypt\_mutex\_register@@Base+0xd4b0>

11c5c: e5903008 ldr r3, [r0, #8]

11c60: e3530001 cmp r3, #1

11c64: e58d3008 str r3, [sp, #8]

11c68: da00007e ble 11e68 <mcrypt\_mutex\_register@@Base+0xd6d4>

11c6c: e59f1208 ldr r1, [pc, #520] ; 11e7c <mcrypt\_mutex\_register@@Base+0xd6e8>

11c70: e59d3004 ldr r3, [sp, #4]

11c74: e28db038 add fp, sp, #56 ; 0x38

11c78: e28d4018 add r4, sp, #24

11c7c: e3a0a001 mov sl, #1

11c80: e08f1001 add r1, pc, r1

11c84: e58d3000 str r3, [sp]

11c88: e58d0010 str r0, [sp, #16]

11c8c: e59d3004 ldr r3, [sp, #4]

11c90: e3530000 cmp r3, #0

11c94: da000026 ble 11d34 <mcrypt\_mutex\_register@@Base+0xd5a0>

11c98: e59d3000 ldr r3, [sp]

11c9c: e59d2010 ldr r2, [sp, #16]

11ca0: e1a0e004 mov lr, r4

11ca4: e0825103 add r5, r2, r3, lsl #2

11ca8: e59d300c ldr r3, [sp, #12]

11cac: e2855f86 add r5, r5, #536 ; 0x218

11cb0: e24b6004 sub r6, fp, #4

11cb4: e0849003 add r9, r4, r3

11cb8: e5d27026 ldrb r7, [r2, #38] ; 0x26

11cbc: e5d2c024 ldrb ip, [r2, #36] ; 0x24

11cc0: e5d20025 ldrb r0, [r2, #37] ; 0x25

11cc4: e49e3004 ldr r3, [lr], #4

11cc8: e7947107 ldr r7, [r4, r7, lsl #2]

11ccc: e084c10c add ip, r4, ip, lsl #2

11cd0: e0840100 add r0, r4, r0, lsl #2

11cd4: e5dcc001 ldrb ip, [ip, #1]

11cd8: e5d00002 ldrb r0, [r0, #2]

11cdc: e1a07c27 lsr r7, r7, #24

11ce0: e6ef3073 uxtb r3, r3

11ce4: e0817107 add r7, r1, r7, lsl #2

11ce8: e0813103 add r3, r1, r3, lsl #2

11cec: e081c10c add ip, r1, ip, lsl #2

11cf0: e5977404 ldr r7, [r7, #1028] ; 0x404

11cf4: e5933404 ldr r3, [r3, #1028] ; 0x404

11cf8: e5b58004 ldr r8, [r5, #4]!

11cfc: e0810100 add r0, r1, r0, lsl #2

11d00: e59cc404 ldr ip, [ip, #1028] ; 0x404

11d04: e0233008 eor r3, r3, r8

11d08: e5900404 ldr r0, [r0, #1028] ; 0x404

11d0c: e0233467 eor r3, r3, r7, ror #8

11d10: e0233c6c eor r3, r3, ip, ror #24

11d14: e0233860 eor r3, r3, r0, ror #16

11d18: e159000e cmp r9, lr

11d1c: e5a63004 str r3, [r6, #4]!

11d20: e2822003 add r2, r2, #3

11d24: 1affffe3 bne 11cb8 <mcrypt\_mutex\_register@@Base+0xd524>

11d28: e1cd20d0 ldrd r2, [sp]

11d2c: e0833002 add r3, r3, r2

11d30: e58d3000 str r3, [sp]

11d34: e59d3008 ldr r3, [sp, #8]

11d38: e28aa001 add sl, sl, #1

11d3c: e153000a cmp r3, sl

11d40: 11a03004 movne r3, r4

11d44: 11a0400b movne r4, fp

11d48: 11a0b003 movne fp, r3

11d4c: 1affffce bne 11c8c <mcrypt\_mutex\_register@@Base+0xd4f8>

11d50: e59d3004 ldr r3, [sp, #4]

11d54: e59d0010 ldr r0, [sp, #16]

11d58: e3530000 cmp r3, #0

11d5c: da000037 ble 11e40 <mcrypt\_mutex\_register@@Base+0xd6ac>

11d60: e59d3000 ldr r3, [sp]

11d64: e59f1114 ldr r1, [pc, #276] ; 11e80 <mcrypt\_mutex\_register@@Base+0xd6ec>

11d68: e2444004 sub r4, r4, #4

11d6c: e0805103 add r5, r0, r3, lsl #2

11d70: e59d300c ldr r3, [sp, #12]

11d74: e08f1001 add r1, pc, r1

11d78: e2855f86 add r5, r5, #536 ; 0x218

11d7c: e08ba003 add sl, fp, r3

11d80: e1a06004 mov r6, r4

11d84: e1a0e00b mov lr, fp

11d88: e1a02000 mov r2, r0

11d8c: e5d27024 ldrb r7, [r2, #36] ; 0x24

11d90: e5d28026 ldrb r8, [r2, #38] ; 0x26

11d94: e5d2c025 ldrb ip, [r2, #37] ; 0x25

11d98: e49e3004 ldr r3, [lr], #4

11d9c: e08b7107 add r7, fp, r7, lsl #2

11da0: e79b8108 ldr r8, [fp, r8, lsl #2]

11da4: e08bc10c add ip, fp, ip, lsl #2

11da8: e5d77001 ldrb r7, [r7, #1]

11dac: e5dcc002 ldrb ip, [ip, #2]

11db0: e6e13073 uxtab r3, r1, r3

11db4: e0818c28 add r8, r1, r8, lsr #24

11db8: e0817007 add r7, r1, r7

11dbc: e5d88304 ldrb r8, [r8, #772] ; 0x304

11dc0: e5d33304 ldrb r3, [r3, #772] ; 0x304

11dc4: e081c00c add ip, r1, ip

11dc8: e5d77304 ldrb r7, [r7, #772] ; 0x304

11dcc: e5b59004 ldr r9, [r5, #4]!

11dd0: e5dcc304 ldrb ip, [ip, #772] ; 0x304

11dd4: e0233009 eor r3, r3, r9

11dd8: e0233468 eor r3, r3, r8, ror #8

11ddc: e0233c67 eor r3, r3, r7, ror #24

11de0: e023386c eor r3, r3, ip, ror #16

11de4: e15a000e cmp sl, lr

11de8: e5a63004 str r3, [r6, #4]!

11dec: e2822003 add r2, r2, #3

11df0: 1affffe5 bne 11d8c <mcrypt\_mutex\_register@@Base+0xd5f8>

11df4: e3a02000 mov r2, #0

11df8: e59d1014 ldr r1, [sp, #20]

11dfc: e24bb004 sub fp, fp, #4

11e00: e1a0c002 mov ip, r2

11e04: e5b43004 ldr r3, [r4, #4]!

11e08: e2822001 add r2, r2, #1

11e0c: e5c13000 strb r3, [r1]

11e10: e1a05423 lsr r5, r3, #8

11e14: e1a0e823 lsr lr, r3, #16

11e18: e1a03c23 lsr r3, r3, #24

11e1c: e5c15001 strb r5, [r1, #1]

11e20: e5c1e002 strb lr, [r1, #2]

11e24: e5c13003 strb r3, [r1, #3]

11e28: e5903004 ldr r3, [r0, #4]

11e2c: e584c000 str ip, [r4]

11e30: e1530002 cmp r3, r2

11e34: e5abc004 str ip, [fp, #4]!

11e38: e2811004 add r1, r1, #4

11e3c: cafffff0 bgt 11e04 <mcrypt\_mutex\_register@@Base+0xd670>

11e40: e28dd05c add sp, sp, #92 ; 0x5c

11e44: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

11e48: e5903008 ldr r3, [r0, #8]

11e4c: e3530001 cmp r3, #1

11e50: e58d3008 str r3, [sp, #8]

11e54: dafffff9 ble 11e40 <mcrypt\_mutex\_register@@Base+0xd6ac>

11e58: e59d3004 ldr r3, [sp, #4]

11e5c: e1a03103 lsl r3, r3, #2

11e60: e58d300c str r3, [sp, #12]

11e64: eaffff80 b 11c6c <mcrypt\_mutex\_register@@Base+0xd4d8>

11e68: e59d3004 ldr r3, [sp, #4]

11e6c: e28d4038 add r4, sp, #56 ; 0x38

11e70: e58d3000 str r3, [sp]

11e74: e28db018 add fp, sp, #24

11e78: eaffffb8 b 11d60 <mcrypt\_mutex\_register@@Base+0xd5cc>

11e7c: 0002491c andeq r4, r2, ip, lsl r9

11e80: 00024828 andeq r4, r2, r8, lsr #16

11e84: e3a00fff mov r0, #1020 ; 0x3fc

11e88: e12fff1e bx lr

11e8c: e3a00018 mov r0, #24

11e90: e12fff1e bx lr

11e94: e3a00001 mov r0, #1

11e98: e12fff1e bx lr

11e9c: e3a00020 mov r0, #32

11ea0: e12fff1e bx lr

11ea4: e3a03003 mov r3, #3

11ea8: e5803000 str r3, [r0]

11eac: e59f0004 ldr r0, [pc, #4] ; 11eb8 <mcrypt\_mutex\_register@@Base+0xd724>

11eb0: e08f0000 add r0, pc, r0

11eb4: e12fff1e bx lr

11eb8: 0000e9f4 strdeq lr, [r0], -r4

11ebc: e59f0004 ldr r0, [pc, #4] ; 11ec8 <mcrypt\_mutex\_register@@Base+0xd734>

11ec0: e08f0000 add r0, pc, r0

11ec4: e12fff1e bx lr

11ec8: 0000e9f0 strdeq lr, [r0], -r0

11ecc: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

11ed0: e24ddf42 sub sp, sp, #264 ; 0x108

11ed4: ebffffec bl 11e8c <mcrypt\_mutex\_register@@Base+0xd6f8>

11ed8: e1a06000 mov r6, r0

11edc: ebffffee bl 11e9c <mcrypt\_mutex\_register@@Base+0xd708>

11ee0: e1a01000 mov r1, r0

11ee4: e3a00001 mov r0, #1

11ee8: ebffc0d1 bl 2234 <calloc@plt>

11eec: e2507000 subs r7, r0, #0

11ef0: 0a00006f beq 120b4 <mcrypt\_mutex\_register@@Base+0xd920>

11ef4: e2475001 sub r5, r7, #1

11ef8: e3a0400a mov r4, #10

11efc: e2678001 rsb r8, r7, #1

11f00: ea000004 b 11f18 <mcrypt\_mutex\_register@@Base+0xd784>

11f04: e2742000 rsbs r2, r4, #0

11f08: e2844002 add r4, r4, #2

11f0c: e6ef2072 uxtb r2, r2

11f10: 52623000 rsbpl r3, r2, #0

11f14: e5e53001 strb r3, [r5, #1]!

11f18: ebffffdf bl 11e9c <mcrypt\_mutex\_register@@Base+0xd708>

11f1c: e0882005 add r2, r8, r5

11f20: e6ef3074 uxtb r3, r4

11f24: e1500002 cmp r0, r2

11f28: cafffff5 bgt 11f04 <mcrypt\_mutex\_register@@Base+0xd770>

11f2c: e3560000 cmp r6, #0

11f30: da000039 ble 1201c <mcrypt\_mutex\_register@@Base+0xd888>

11f34: e1a0400d mov r4, sp

11f38: e1a02004 mov r2, r4

11f3c: e3a03000 mov r3, #0

11f40: e4c23001 strb r3, [r2], #1

11f44: e2833001 add r3, r3, #1

11f48: e1560003 cmp r6, r3

11f4c: 1afffffb bne 11f40 <mcrypt\_mutex\_register@@Base+0xd7ac>

11f50: ebffffcb bl 11e84 <mcrypt\_mutex\_register@@Base+0xd6f0>

11f54: ebffc0e6 bl 22f4 <malloc@plt>

11f58: e2509000 subs r9, r0, #0

11f5c: 0a000050 beq 120a4 <mcrypt\_mutex\_register@@Base+0xd910>

11f60: e28d8020 add r8, sp, #32

11f64: e1a01004 mov r1, r4

11f68: e1a02006 mov r2, r6

11f6c: e1a00008 mov r0, r8

11f70: ebffc0c4 bl 2288 <memcpy@plt>

11f74: ebffffc8 bl 11e9c <mcrypt\_mutex\_register@@Base+0xd708>

11f78: e1a01007 mov r1, r7

11f7c: e28da040 add sl, sp, #64 ; 0x40

11f80: e0886006 add r6, r8, r6

11f84: e1a05008 mov r5, r8

11f88: e1a0400a mov r4, sl

11f8c: e1a02000 mov r2, r0

11f90: e1a00009 mov r0, r9

11f94: ebfffcaa bl 11244 <mcrypt\_mutex\_register@@Base+0xcab0>

11f98: e1a00007 mov r0, r7

11f9c: ebffc0b6 bl 227c <free@plt>

11fa0: e1a01008 mov r1, r8

11fa4: e1a00009 mov r0, r9

11fa8: ebfffe7b bl 1199c <mcrypt\_mutex\_register@@Base+0xd208>

11fac: e59f7108 ldr r7, [pc, #264] ; 120bc <mcrypt\_mutex\_register@@Base+0xd928>

11fb0: e08f7007 add r7, pc, r7

11fb4: e4d52001 ldrb r2, [r5], #1

11fb8: e1a00004 mov r0, r4

11fbc: e1a01007 mov r1, r7

11fc0: ebffc0e9 bl 236c <sprintf@plt>

11fc4: e1560005 cmp r6, r5

11fc8: e2844002 add r4, r4, #2

11fcc: 1afffff8 bne 11fb4 <mcrypt\_mutex\_register@@Base+0xd820>

11fd0: e59f40e8 ldr r4, [pc, #232] ; 120c0 <mcrypt\_mutex\_register@@Base+0xd92c>

11fd4: e1a0000a mov r0, sl

11fd8: e08f4004 add r4, pc, r4

11fdc: e1a01004 mov r1, r4

11fe0: ebffc099 bl 224c <strcmp@plt>

11fe4: e3500000 cmp r0, #0

11fe8: 1a00001c bne 12060 <mcrypt\_mutex\_register@@Base+0xd8cc>

11fec: e1a01008 mov r1, r8

11ff0: e1a00009 mov r0, r9

11ff4: ebffff05 bl 11c10 <mcrypt\_mutex\_register@@Base+0xd47c>

11ff8: e1a00009 mov r0, r9

11ffc: ebffc09e bl 227c <free@plt>

12000: e1a00008 mov r0, r8

12004: e1a0100d mov r1, sp

12008: ebffc08f bl 224c <strcmp@plt>

1200c: e3500000 cmp r0, #0

12010: 1a00001e bne 12090 <mcrypt\_mutex\_register@@Base+0xd8fc>

12014: e28ddf42 add sp, sp, #264 ; 0x108

12018: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1201c: ebffff98 bl 11e84 <mcrypt\_mutex\_register@@Base+0xd6f0>

12020: ebffc0b3 bl 22f4 <malloc@plt>

12024: e2509000 subs r9, r0, #0

12028: 0a00001d beq 120a4 <mcrypt\_mutex\_register@@Base+0xd910>

1202c: ebffff9a bl 11e9c <mcrypt\_mutex\_register@@Base+0xd708>

12030: e1a01007 mov r1, r7

12034: e28d8020 add r8, sp, #32

12038: e28da040 add sl, sp, #64 ; 0x40

1203c: e1a02000 mov r2, r0

12040: e1a00009 mov r0, r9

12044: ebfffc7e bl 11244 <mcrypt\_mutex\_register@@Base+0xcab0>

12048: e1a00007 mov r0, r7

1204c: ebffc08a bl 227c <free@plt>

12050: e1a01008 mov r1, r8

12054: e1a00009 mov r0, r9

12058: ebfffe4f bl 1199c <mcrypt\_mutex\_register@@Base+0xd208>

1205c: eaffffdb b 11fd0 <mcrypt\_mutex\_register@@Base+0xd83c>

12060: e59f005c ldr r0, [pc, #92] ; 120c4 <mcrypt\_mutex\_register@@Base+0xd930>

12064: e08f0000 add r0, pc, r0

12068: ebffc09e bl 22e8 <puts@plt>

1206c: e59f0054 ldr r0, [pc, #84] ; 120c8 <mcrypt\_mutex\_register@@Base+0xd934>

12070: e1a0200a mov r2, sl

12074: e1a01004 mov r1, r4

12078: e08f0000 add r0, pc, r0

1207c: ebffc078 bl 2264 <printf@plt>

12080: e1a00009 mov r0, r9

12084: ebffc07c bl 227c <free@plt>

12088: e3e00000 mvn r0, #0

1208c: eaffffe0 b 12014 <mcrypt\_mutex\_register@@Base+0xd880>

12090: e59f0034 ldr r0, [pc, #52] ; 120cc <mcrypt\_mutex\_register@@Base+0xd938>

12094: e08f0000 add r0, pc, r0

12098: ebffc092 bl 22e8 <puts@plt>

1209c: e3e00000 mvn r0, #0

120a0: eaffffdb b 12014 <mcrypt\_mutex\_register@@Base+0xd880>

120a4: e1a00007 mov r0, r7

120a8: ebffc073 bl 227c <free@plt>

120ac: e3e00000 mvn r0, #0

120b0: eaffffd7 b 12014 <mcrypt\_mutex\_register@@Base+0xd880>

120b4: e3e00000 mvn r0, #0

120b8: eaffffd5 b 12014 <mcrypt\_mutex\_register@@Base+0xd880>

120bc: 0000e6b0 ; <UNDEFINED> instruction: 0x0000e6b0

120c0: 0000e8e8 andeq lr, r0, r8, ror #17

120c4: 0000e618 andeq lr, r0, r8, lsl r6

120c8: 0000e61c andeq lr, r0, ip, lsl r6

120cc: 0000e618 andeq lr, r0, r8, lsl r6

120d0: e59f0000 ldr r0, [pc] ; 120d8 <mcrypt\_mutex\_register@@Base+0xd944>

120d4: e12fff1e bx lr

120d8: 01315731 teqeq r1, r1, lsr r7

120dc: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

120e0: e24dd028 sub sp, sp, #40 ; 0x28

120e4: e1a04002 mov r4, r2

120e8: e28d5004 add r5, sp, #4

120ec: e1a08000 mov r8, r0

120f0: e1a06001 mov r6, r1

120f4: e1a00005 mov r0, r5

120f8: e3a02024 mov r2, #36 ; 0x24

120fc: e3a01000 mov r1, #0

12100: e59f9164 ldr r9, [pc, #356] ; 1226c <mcrypt\_mutex\_register@@Base+0xdad8>

12104: ebffc08f bl 2348 <memset@plt>

12108: e1b03124 lsrs r3, r4, #2

1210c: e085e004 add lr, r5, r4

12110: e08f9009 add r9, pc, r9

12114: 0a00004d beq 12250 <mcrypt\_mutex\_register@@Base+0xdabc>

12118: e0863103 add r3, r6, r3, lsl #2

1211c: e1a02005 mov r2, r5

12120: e5331004 ldr r1, [r3, #-4]!

12124: e1530006 cmp r3, r6

12128: e4821004 str r1, [r2], #4

1212c: 1afffffb bne 12120 <mcrypt\_mutex\_register@@Base+0xd98c>

12130: e3a03000 mov r3, #0

12134: e5884210 str r4, [r8, #528] ; 0x210

12138: e7c53004 strb r3, [r5, r4]

1213c: e2483001 sub r3, r8, #1

12140: e1a00005 mov r0, r5

12144: e3a01000 mov r1, #0

12148: e5d0c000 ldrb ip, [r0]

1214c: e2832002 add r2, r3, #2

12150: e0422008 sub r2, r2, r8

12154: e021100c eor r1, r1, ip

12158: e5ce1000 strb r1, [lr]

1215c: e4d0c001 ldrb ip, [r0], #1

12160: e1520004 cmp r2, r4

12164: e2833001 add r3, r3, #1

12168: e5c3c000 strb ip, [r3]

1216c: 3afffff5 bcc 12148 <mcrypt\_mutex\_register@@Base+0xd9b4>

12170: e59f30f8 ldr r3, [pc, #248] ; 12270 <mcrypt\_mutex\_register@@Base+0xdadc>

12174: e3e0700f mvn r7, #15

12178: e2856001 add r6, r5, #1

1217c: e799c003 ldr ip, [r9, r3]

12180: e0477008 sub r7, r7, r8

12184: e288e01f add lr, r8, #31

12188: e0866004 add r6, r6, r4

1218c: e3a09000 mov r9, #0

12190: e1a03005 mov r3, r5

12194: e5d31000 ldrb r1, [r3]

12198: e1a022a1 lsr r2, r1, #5

1219c: e1822181 orr r2, r2, r1, lsl #3

121a0: e4c32001 strb r2, [r3], #1

121a4: e1560003 cmp r6, r3

121a8: 1afffff9 bne 12194 <mcrypt\_mutex\_register@@Base+0xda00>

121ac: e359000f cmp r9, #15

121b0: e2899001 add r9, r9, #1

121b4: e24e2010 sub r2, lr, #16

121b8: e1a03009 mov r3, r9

121bc: e2878024 add r8, r7, #36 ; 0x24

121c0: 9a000013 bls 12214 <mcrypt\_mutex\_register@@Base+0xda80>

121c4: e0880002 add r0, r8, r2

121c8: e28d1028 add r1, sp, #40 ; 0x28

121cc: e0811003 add r1, r1, r3

121d0: e6ef0070 uxtb r0, r0

121d4: e551a024 ldrb sl, [r1, #-36] ; 0xffffffdc

121d8: e7dc1000 ldrb r1, [ip, r0]

121dc: e1540003 cmp r4, r3

121e0: e2833001 add r3, r3, #1

121e4: e081100a add r1, r1, sl

121e8: e5e21001 strb r1, [r2, #1]!

121ec: 03a03000 moveq r3, #0

121f0: e15e0002 cmp lr, r2

121f4: 1afffff2 bne 121c4 <mcrypt\_mutex\_register@@Base+0xda30>

121f8: e1590004 cmp r9, r4

121fc: e28ee010 add lr, lr, #16

12200: e2877001 add r7, r7, #1

12204: 3affffe1 bcc 12190 <mcrypt\_mutex\_register@@Base+0xd9fc>

12208: e3a00000 mov r0, #0

1220c: e28dd028 add sp, sp, #40 ; 0x28

12210: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

12214: e0880002 add r0, r8, r2

12218: e28d1028 add r1, sp, #40 ; 0x28

1221c: e0811003 add r1, r1, r3

12220: e6ef0070 uxtb r0, r0

12224: e551a024 ldrb sl, [r1, #-36] ; 0xffffffdc

12228: e7dc1000 ldrb r1, [ip, r0]

1222c: e1540003 cmp r4, r3

12230: e2833001 add r3, r3, #1

12234: 03a03000 moveq r3, #0

12238: e7dc1001 ldrb r1, [ip, r1]

1223c: e081100a add r1, r1, sl

12240: e5e21001 strb r1, [r2, #1]!

12244: e15e0002 cmp lr, r2

12248: 1afffff1 bne 12214 <mcrypt\_mutex\_register@@Base+0xda80>

1224c: eaffffe9 b 121f8 <mcrypt\_mutex\_register@@Base+0xda64>

12250: e3540000 cmp r4, #0

12254: e5884210 str r4, [r8, #528] ; 0x210

12258: e7c53004 strb r3, [r5, r4]

1225c: 1affffb6 bne 1213c <mcrypt\_mutex\_register@@Base+0xd9a8>

12260: e3a00000 mov r0, #0

12264: e28dd028 add sp, sp, #40 ; 0x28

12268: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1226c: 00021ee8 andeq r1, r2, r8, ror #29

12270: 000000a0 andeq r0, r0, r0, lsr #1

12274: e59f3500 ldr r3, [pc, #1280] ; 1277c <mcrypt\_mutex\_register@@Base+0xdfe8>

12278: e59f2500 ldr r2, [pc, #1280] ; 12780 <mcrypt\_mutex\_register@@Base+0xdfec>

1227c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

12280: e08f3003 add r3, pc, r3

12284: e5d15000 ldrb r5, [r1]

12288: e5d0c000 ldrb ip, [r0]

1228c: e793e002 ldr lr, [r3, r2]

12290: e5d14010 ldrb r4, [r1, #16]

12294: e02cc005 eor ip, ip, r5

12298: e59f24e4 ldr r2, [pc, #1252] ; 12784 <mcrypt\_mutex\_register@@Base+0xdff0>

1229c: e7de600c ldrb r6, [lr, ip]

122a0: e5d0c001 ldrb ip, [r0, #1]

122a4: e5d08002 ldrb r8, [r0, #2]

122a8: e0866004 add r6, r6, r4

122ac: e5d07003 ldrb r7, [r0, #3]

122b0: e6ef6076 uxtb r6, r6

122b4: e5c06000 strb r6, [r0]

122b8: e7932002 ldr r2, [r3, r2]

122bc: e5d14001 ldrb r4, [r1, #1]

122c0: e082c00c add ip, r2, ip

122c4: e5d1a011 ldrb sl, [r1, #17]

122c8: e0828008 add r8, r2, r8

122cc: e7dc9004 ldrb r9, [ip, r4]

122d0: e24dd02c sub sp, sp, #44 ; 0x2c

122d4: e5d05004 ldrb r5, [r0, #4]

122d8: e029900a eor r9, r9, sl

122dc: e5c09001 strb r9, [r0, #1]

122e0: e5d1a002 ldrb sl, [r1, #2]

122e4: e5d1c012 ldrb ip, [r1, #18]

122e8: e0869009 add r9, r6, r9

122ec: e5d04005 ldrb r4, [r0, #5]

122f0: e7d8800a ldrb r8, [r8, sl]

122f4: e6ef9079 uxtb r9, r9

122f8: e0866009 add r6, r6, r9

122fc: e02cc008 eor ip, ip, r8

12300: e5c0c002 strb ip, [r0, #2]

12304: e5d18003 ldrb r8, [r1, #3]

12308: e5d1a013 ldrb sl, [r1, #19]

1230c: e6ef6076 uxtb r6, r6

12310: e0277008 eor r7, r7, r8

12314: e58d6004 str r6, [sp, #4]

12318: e7de8007 ldrb r8, [lr, r7]

1231c: e0824004 add r4, r2, r4

12320: e088800a add r8, r8, sl

12324: e6ef8078 uxtb r8, r8

12328: e5c08003 strb r8, [r0, #3]

1232c: e5d16004 ldrb r6, [r1, #4]

12330: e5d13014 ldrb r3, [r1, #20]

12334: e08c8008 add r8, ip, r8

12338: e0255006 eor r5, r5, r6

1233c: e6ef8078 uxtb r8, r8

12340: e7de5005 ldrb r5, [lr, r5]

12344: e08cc008 add ip, ip, r8

12348: e0853003 add r3, r5, r3

1234c: e6ef607c uxtb r6, ip

12350: e6ef3073 uxtb r3, r3

12354: e5c03004 strb r3, [r0, #4]

12358: e5d15005 ldrb r5, [r1, #5]

1235c: e5d1c015 ldrb ip, [r1, #21]

12360: e0899006 add r9, r9, r6

12364: e7d45005 ldrb r5, [r4, r5]

12368: e6ef4079 uxtb r4, r9

1236c: e0867004 add r7, r6, r4

12370: e58d4014 str r4, [sp, #20]

12374: e025500c eor r5, r5, ip

12378: e5d04006 ldrb r4, [r0, #6]

1237c: e5c05005 strb r5, [r0, #5]

12380: e5d16006 ldrb r6, [r1, #6]

12384: e0824004 add r4, r2, r4

12388: e5d1c016 ldrb ip, [r1, #22]

1238c: e5d09007 ldrb r9, [r0, #7]

12390: e7d4b006 ldrb fp, [r4, r6]

12394: e58d7020 str r7, [sp, #32]

12398: e5d07008 ldrb r7, [r0, #8]

1239c: e02bb00c eor fp, fp, ip

123a0: e5c0b006 strb fp, [r0, #6]

123a4: e5d14007 ldrb r4, [r1, #7]

123a8: e5d1c017 ldrb ip, [r1, #23]

123ac: e5d06009 ldrb r6, [r0, #9]

123b0: e0299004 eor r9, r9, r4

123b4: e5d0400a ldrb r4, [r0, #10]

123b8: e7dea009 ldrb sl, [lr, r9]

123bc: e5d0900b ldrb r9, [r0, #11]

123c0: e0826006 add r6, r2, r6

123c4: e08aa00c add sl, sl, ip

123c8: e58d900c str r9, [sp, #12]

123cc: e6efa07a uxtb sl, sl

123d0: e5c0a007 strb sl, [r0, #7]

123d4: e5d19008 ldrb r9, [r1, #8]

123d8: e5d0c00c ldrb ip, [r0, #12]

123dc: e0824004 add r4, r2, r4

123e0: e0277009 eor r7, r7, r9

123e4: e58dc01c str ip, [sp, #28]

123e8: e7de7007 ldrb r7, [lr, r7]

123ec: e5d1c018 ldrb ip, [r1, #24]

123f0: e0835005 add r5, r3, r5

123f4: e08ba00a add sl, fp, sl

123f8: e087c00c add ip, r7, ip

123fc: e6ef5075 uxtb r5, r5

12400: e6efc07c uxtb ip, ip

12404: e5c0c008 strb ip, [r0, #8]

12408: e5d19009 ldrb r9, [r1, #9]

1240c: e5d17019 ldrb r7, [r1, #25]

12410: e6efa07a uxtb sl, sl

12414: e0833005 add r3, r3, r5

12418: e7d66009 ldrb r6, [r6, r9]

1241c: e08bb00a add fp, fp, sl

12420: e6ef3073 uxtb r3, r3

12424: e0277006 eor r7, r7, r6

12428: e5c07009 strb r7, [r0, #9]

1242c: e5d1600a ldrb r6, [r1, #10]

12430: e08c7007 add r7, ip, r7

12434: e5d1901a ldrb r9, [r1, #26]

12438: e6efb07b uxtb fp, fp

1243c: e7d44006 ldrb r4, [r4, r6]

12440: e6ef6077 uxtb r6, r7

12444: e08cc006 add ip, ip, r6

12448: e0299004 eor r9, r9, r4

1244c: e58d6008 str r6, [sp, #8]

12450: e6efc07c uxtb ip, ip

12454: e5c0900a strb r9, [r0, #10]

12458: e5d1400b ldrb r4, [r1, #11]

1245c: e1a0700c mov r7, ip

12460: e59dc00c ldr ip, [sp, #12]

12464: e5d1601b ldrb r6, [r1, #27]

12468: e024400c eor r4, r4, ip

1246c: e085500b add r5, r5, fp

12470: e7de4004 ldrb r4, [lr, r4]

12474: e59dc004 ldr ip, [sp, #4]

12478: e0888003 add r8, r8, r3

1247c: e0844006 add r4, r4, r6

12480: e6ef8078 uxtb r8, r8

12484: e6ef4074 uxtb r4, r4

12488: e5c0400b strb r4, [r0, #11]

1248c: e08ca00a add sl, ip, sl

12490: e6efc075 uxtb ip, r5

12494: e5d1500c ldrb r5, [r1, #12]

12498: e0836008 add r6, r3, r8

1249c: e59d301c ldr r3, [sp, #28]

124a0: e58d6024 str r6, [sp, #36] ; 0x24

124a4: e0255003 eor r5, r5, r3

124a8: e5d0600d ldrb r6, [r0, #13]

124ac: e7de3005 ldrb r3, [lr, r5]

124b0: e5d1501c ldrb r5, [r1, #28]

124b4: e58d8018 str r8, [sp, #24]

124b8: e0826006 add r6, r2, r6

124bc: e0835005 add r5, r3, r5

124c0: e6efa07a uxtb sl, sl

124c4: e6ef5075 uxtb r5, r5

124c8: e5c0500c strb r5, [r0, #12]

124cc: e5d1800d ldrb r8, [r1, #13]

124d0: e5d1301d ldrb r3, [r1, #29]

124d4: e58da010 str sl, [sp, #16]

124d8: e5d0a00e ldrb sl, [r0, #14]

124dc: e7d66008 ldrb r6, [r6, r8]

124e0: e5d0800f ldrb r8, [r0, #15]

124e4: e082200a add r2, r2, sl

124e8: e0233006 eor r3, r3, r6

124ec: e5c0300d strb r3, [r0, #13]

124f0: e5d1a00e ldrb sl, [r1, #14]

124f4: e5d1601e ldrb r6, [r1, #30]

124f8: e0894004 add r4, r9, r4

124fc: e0853003 add r3, r5, r3

12500: e7d2a00a ldrb sl, [r2, sl]

12504: e6ef4074 uxtb r4, r4

12508: e0899004 add r9, r9, r4

1250c: e026600a eor r6, r6, sl

12510: e5c0600e strb r6, [r0, #14]

12514: e5d1a00f ldrb sl, [r1, #15]

12518: e5d1101f ldrb r1, [r1, #31]

1251c: e6ef3073 uxtb r3, r3

12520: e028800a eor r8, r8, sl

12524: e0852003 add r2, r5, r3

12528: e7dee008 ldrb lr, [lr, r8]

1252c: e59d5008 ldr r5, [sp, #8]

12530: e6ef9079 uxtb r9, r9

12534: e08ee001 add lr, lr, r1

12538: e086e00e add lr, r6, lr

1253c: e58d700c str r7, [sp, #12]

12540: e58dc01c str ip, [sp, #28]

12544: e0874004 add r4, r7, r4

12548: e0857009 add r7, r5, r9

1254c: e59d5004 ldr r5, [sp, #4]

12550: e59da010 ldr sl, [sp, #16]

12554: e6efe07e uxtb lr, lr

12558: e086100e add r1, r6, lr

1255c: e085600a add r6, r5, sl

12560: e5dd5020 ldrb r5, [sp, #32]

12564: e6ef7077 uxtb r7, r7

12568: e6ef1071 uxtb r1, r1

1256c: e58d5008 str r5, [sp, #8]

12570: e5dd5024 ldrb r5, [sp, #36] ; 0x24

12574: e0899007 add r9, r9, r7

12578: e08bb00c add fp, fp, ip

1257c: e1a08005 mov r8, r5

12580: e0833001 add r3, r3, r1

12584: e6ef5076 uxtb r5, r6

12588: e58d5004 str r5, [sp, #4]

1258c: e6ef2072 uxtb r2, r2

12590: e59d5014 ldr r5, [sp, #20]

12594: e59dc00c ldr ip, [sp, #12]

12598: e6ef6079 uxtb r6, r9

1259c: e082e00e add lr, r2, lr

125a0: e6ef3073 uxtb r3, r3

125a4: e6efb07b uxtb fp, fp

125a8: e58d800c str r8, [sp, #12]

125ac: e58d6010 str r6, [sp, #16]

125b0: e085900b add r9, r5, fp

125b4: e6ef4074 uxtb r4, r4

125b8: e59d5004 ldr r5, [sp, #4]

125bc: e0811003 add r1, r1, r3

125c0: e08aa008 add sl, sl, r8

125c4: e0863003 add r3, r6, r3

125c8: e59d8018 ldr r8, [sp, #24]

125cc: e59d6008 ldr r6, [sp, #8]

125d0: e6efe07e uxtb lr, lr

125d4: e08cc004 add ip, ip, r4

125d8: e082200e add r2, r2, lr

125dc: e0858008 add r8, r5, r8

125e0: e086e00e add lr, r6, lr

125e4: e59d501c ldr r5, [sp, #28]

125e8: e6ef6073 uxtb r6, r3

125ec: e58d6014 str r6, [sp, #20]

125f0: e6efc07c uxtb ip, ip

125f4: e59d600c ldr r6, [sp, #12]

125f8: e085500c add r5, r5, ip

125fc: e6ef9079 uxtb r9, r9

12600: e6efa07a uxtb sl, sl

12604: e086300a add r3, r6, sl

12608: e08bb009 add fp, fp, r9

1260c: e59d6004 ldr r6, [sp, #4]

12610: e6ef8078 uxtb r8, r8

12614: e6ef5075 uxtb r5, r5

12618: e0866008 add r6, r6, r8

1261c: e08cc005 add ip, ip, r5

12620: e6efb07b uxtb fp, fp

12624: e58d6004 str r6, [sp, #4]

12628: e58dc018 str ip, [sp, #24]

1262c: e59d6014 ldr r6, [sp, #20]

12630: e59dc010 ldr ip, [sp, #16]

12634: e58db00c str fp, [sp, #12]

12638: e6efb073 uxtb fp, r3

1263c: e5dd3004 ldrb r3, [sp, #4]

12640: e08cc006 add ip, ip, r6

12644: e59d6008 ldr r6, [sp, #8]

12648: e58d3004 str r3, [sp, #4]

1264c: e6efe07e uxtb lr, lr

12650: e5dd3018 ldrb r3, [sp, #24]

12654: e086600e add r6, r6, lr

12658: e6ef2072 uxtb r2, r2

1265c: e0844002 add r4, r4, r2

12660: e58d3010 str r3, [sp, #16]

12664: e6ef3076 uxtb r3, r6

12668: e59d600c ldr r6, [sp, #12]

1266c: e6ef4074 uxtb r4, r4

12670: e58d3008 str r3, [sp, #8]

12674: e0822004 add r2, r2, r4

12678: e59d3004 ldr r3, [sp, #4]

1267c: e0864004 add r4, r6, r4

12680: e59d6014 ldr r6, [sp, #20]

12684: e6ef4074 uxtb r4, r4

12688: e0833006 add r3, r3, r6

1268c: e59d6008 ldr r6, [sp, #8]

12690: e6ef3073 uxtb r3, r3

12694: e0865005 add r5, r6, r5

12698: e59d6010 ldr r6, [sp, #16]

1269c: e6ef5075 uxtb r5, r5

126a0: e08ee006 add lr, lr, r6

126a4: e59d600c ldr r6, [sp, #12]

126a8: e6ef1071 uxtb r1, r1

126ac: e0866004 add r6, r6, r4

126b0: e58d600c str r6, [sp, #12]

126b4: e59d6004 ldr r6, [sp, #4]

126b8: e0877001 add r7, r7, r1

126bc: e0866003 add r6, r6, r3

126c0: e58d6014 str r6, [sp, #20]

126c4: e59d6008 ldr r6, [sp, #8]

126c8: e6efe07e uxtb lr, lr

126cc: e0866005 add r6, r6, r5

126d0: e58d6004 str r6, [sp, #4]

126d4: e59d6010 ldr r6, [sp, #16]

126d8: e6ef7077 uxtb r7, r7

126dc: e086600e add r6, r6, lr

126e0: e0811007 add r1, r1, r7

126e4: e6ef2072 uxtb r2, r2

126e8: e58d6008 str r6, [sp, #8]

126ec: e0888002 add r8, r8, r2

126f0: e59d600c ldr r6, [sp, #12]

126f4: e6ef1071 uxtb r1, r1

126f8: e6efc07c uxtb ip, ip

126fc: e6ef8078 uxtb r8, r8

12700: e08aa001 add sl, sl, r1

12704: e08b7007 add r7, fp, r7

12708: e089900c add r9, r9, ip

1270c: e5c06006 strb r6, [r0, #6]

12710: e59d6014 ldr r6, [sp, #20]

12714: e0822008 add r2, r2, r8

12718: e6efa07a uxtb sl, sl

1271c: e6ef7077 uxtb r7, r7

12720: e6ef9079 uxtb r9, r9

12724: e5c0200e strb r2, [r0, #14]

12728: e5c06004 strb r6, [r0, #4]

1272c: e59d2004 ldr r2, [sp, #4]

12730: e59d6008 ldr r6, [sp, #8]

12734: e081100a add r1, r1, sl

12738: e08bb007 add fp, fp, r7

1273c: e08cc009 add ip, ip, r9

12740: e5c01000 strb r1, [r0]

12744: e5c0a001 strb sl, [r0, #1]

12748: e5c0b008 strb fp, [r0, #8]

1274c: e5c0c00c strb ip, [r0, #12]

12750: e5c07009 strb r7, [r0, #9]

12754: e5c0900d strb r9, [r0, #13]

12758: e5c0800f strb r8, [r0, #15]

1275c: e5c0200a strb r2, [r0, #10]

12760: e5c04007 strb r4, [r0, #7]

12764: e5c0500b strb r5, [r0, #11]

12768: e5c03005 strb r3, [r0, #5]

1276c: e5c06002 strb r6, [r0, #2]

12770: e5c0e003 strb lr, [r0, #3]

12774: e28dd02c add sp, sp, #44 ; 0x2c

12778: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1277c: 00021d78 andeq r1, r2, r8, ror sp

12780: 000000a0 andeq r0, r0, r0, lsr #1

12784: 000000a4 andeq r0, r0, r4, lsr #1

12788: e5d0200c ldrb r2, [r0, #12]

1278c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

12790: e24dd06c sub sp, sp, #108 ; 0x6c

12794: e5d04006 ldrb r4, [r0, #6]

12798: e58d2028 str r2, [sp, #40] ; 0x28

1279c: e5d09000 ldrb r9, [r0]

127a0: e5d02009 ldrb r2, [r0, #9]

127a4: e58d4030 str r4, [sp, #48] ; 0x30

127a8: e5d04005 ldrb r4, [r0, #5]

127ac: e5d0800b ldrb r8, [r0, #11]

127b0: e5d0a00a ldrb sl, [r0, #10]

127b4: e5d0b00e ldrb fp, [r0, #14]

127b8: e5d0c00d ldrb ip, [r0, #13]

127bc: e5d07001 ldrb r7, [r0, #1]

127c0: e5d0300f ldrb r3, [r0, #15]

127c4: e5d0e007 ldrb lr, [r0, #7]

127c8: e58d902c str r9, [sp, #44] ; 0x2c

127cc: e58d2010 str r2, [sp, #16]

127d0: e5d09008 ldrb r9, [r0, #8]

127d4: e5d02004 ldrb r2, [r0, #4]

127d8: e58d4004 str r4, [sp, #4]

127dc: e59d4028 ldr r4, [sp, #40] ; 0x28

127e0: e5d05003 ldrb r5, [r0, #3]

127e4: e5d06002 ldrb r6, [r0, #2]

127e8: e58d9024 str r9, [sp, #36] ; 0x24

127ec: e58d2040 str r2, [sp, #64] ; 0x40

127f0: e58da050 str sl, [sp, #80] ; 0x50

127f4: e048200a sub r2, r8, sl

127f8: e58db054 str fp, [sp, #84] ; 0x54

127fc: e043a00b sub sl, r3, fp

12800: e58dc03c str ip, [sp, #60] ; 0x3c

12804: e58de01c str lr, [sp, #28]

12808: e58d7020 str r7, [sp, #32]

1280c: e58d3018 str r3, [sp, #24]

12810: e04c4004 sub r4, ip, r4

12814: e59dc030 ldr ip, [sp, #48] ; 0x30

12818: e59d302c ldr r3, [sp, #44] ; 0x2c

1281c: e04ee00c sub lr, lr, ip

12820: e047b003 sub fp, r7, r3

12824: e59dc010 ldr ip, [sp, #16]

12828: e59d7024 ldr r7, [sp, #36] ; 0x24

1282c: e0459006 sub r9, r5, r6

12830: e59d3040 ldr r3, [sp, #64] ; 0x40

12834: e04cc007 sub ip, ip, r7

12838: e59d7004 ldr r7, [sp, #4]

1283c: e6ef9079 uxtb r9, r9

12840: e0477003 sub r7, r7, r3

12844: e6ef2072 uxtb r2, r2

12848: e0853009 add r3, r5, r9

1284c: e58d804c str r8, [sp, #76] ; 0x4c

12850: e0888002 add r8, r8, r2

12854: e6ef3073 uxtb r3, r3

12858: e58d300c str r3, [sp, #12]

1285c: e6ef8078 uxtb r8, r8

12860: e58d8014 str r8, [sp, #20]

12864: e59d800c ldr r8, [sp, #12]

12868: e59d3018 ldr r3, [sp, #24]

1286c: e0882002 add r2, r8, r2

12870: e6efa07a uxtb sl, sl

12874: e083300a add r3, r3, sl

12878: e6ef2072 uxtb r2, r2

1287c: e58d2048 str r2, [sp, #72] ; 0x48

12880: e59d201c ldr r2, [sp, #28]

12884: e6efe07e uxtb lr, lr

12888: e6ef3073 uxtb r3, r3

1288c: e58d3008 str r3, [sp, #8]

12890: e0466005 sub r6, r6, r5

12894: e082300e add r3, r2, lr

12898: e59d5010 ldr r5, [sp, #16]

1289c: e6efc07c uxtb ip, ip

128a0: e6ef2073 uxtb r2, r3

128a4: e085300c add r3, r5, ip

128a8: e58d2034 str r2, [sp, #52] ; 0x34

128ac: e082a00a add sl, r2, sl

128b0: e59d2020 ldr r2, [sp, #32]

128b4: e58d6058 str r6, [sp, #88] ; 0x58

128b8: e6efb07b uxtb fp, fp

128bc: e6ef6073 uxtb r6, r3

128c0: e082200b add r2, r2, fp

128c4: e086b00b add fp, r6, fp

128c8: e58d6038 str r6, [sp, #56] ; 0x38

128cc: e6ef507b uxtb r5, fp

128d0: e59d6004 ldr r6, [sp, #4]

128d4: e59db008 ldr fp, [sp, #8]

128d8: e6ef7077 uxtb r7, r7

128dc: e59d304c ldr r3, [sp, #76] ; 0x4c

128e0: e0866007 add r6, r6, r7

128e4: e08b7007 add r7, fp, r7

128e8: e59db050 ldr fp, [sp, #80] ; 0x50

128ec: e59d8014 ldr r8, [sp, #20]

128f0: e58d5044 str r5, [sp, #68] ; 0x44

128f4: e04b5003 sub r5, fp, r3

128f8: e59d3018 ldr r3, [sp, #24]

128fc: e59db054 ldr fp, [sp, #84] ; 0x54

12900: e0889009 add r9, r8, r9

12904: e04bb003 sub fp, fp, r3

12908: e58db050 str fp, [sp, #80] ; 0x50

1290c: e59db014 ldr fp, [sp, #20]

12910: e6ef9079 uxtb r9, r9

12914: e58d5064 str r5, [sp, #100] ; 0x64

12918: e08b3009 add r3, fp, r9

1291c: e59d503c ldr r5, [sp, #60] ; 0x3c

12920: e59db028 ldr fp, [sp, #40] ; 0x28

12924: e6ef3073 uxtb r3, r3

12928: e04bb005 sub fp, fp, r5

1292c: e59d803c ldr r8, [sp, #60] ; 0x3c

12930: e58db054 str fp, [sp, #84] ; 0x54

12934: e58d3018 str r3, [sp, #24]

12938: e59d5048 ldr r5, [sp, #72] ; 0x48

1293c: e59d300c ldr r3, [sp, #12]

12940: e59db030 ldr fp, [sp, #48] ; 0x30

12944: e0835005 add r5, r3, r5

12948: e59d301c ldr r3, [sp, #28]

1294c: e6ef5075 uxtb r5, r5

12950: e04bb003 sub fp, fp, r3

12954: e59d3034 ldr r3, [sp, #52] ; 0x34

12958: e6efa07a uxtb sl, sl

1295c: e58db05c str fp, [sp, #92] ; 0x5c

12960: e6ef4074 uxtb r4, r4

12964: e083b00a add fp, r3, sl

12968: e085a00a add sl, r5, sl

1296c: e0888004 add r8, r8, r4

12970: e6efa07a uxtb sl, sl

12974: e59d3020 ldr r3, [sp, #32]

12978: e6ef8078 uxtb r8, r8

1297c: e58da01c str sl, [sp, #28]

12980: e59da02c ldr sl, [sp, #44] ; 0x2c

12984: e088e00e add lr, r8, lr

12988: e04aa003 sub sl, sl, r3

1298c: e58da04c str sl, [sp, #76] ; 0x4c

12990: e6efe07e uxtb lr, lr

12994: e59da018 ldr sl, [sp, #24]

12998: e088300e add r3, r8, lr

1299c: e6efb07b uxtb fp, fp

129a0: e08ae00e add lr, sl, lr

129a4: e59da048 ldr sl, [sp, #72] ; 0x48

129a8: e6ef3073 uxtb r3, r3

129ac: e08b9009 add r9, fp, r9

129b0: e58d503c str r5, [sp, #60] ; 0x3c

129b4: e083500a add r5, r3, sl

129b8: e6ef9079 uxtb r9, r9

129bc: e58d9028 str r9, [sp, #40] ; 0x28

129c0: e6efa075 uxtb sl, r5

129c4: e59d9024 ldr r9, [sp, #36] ; 0x24

129c8: e59d5010 ldr r5, [sp, #16]

129cc: e6efe07e uxtb lr, lr

129d0: e58de020 str lr, [sp, #32]

129d4: e049e005 sub lr, r9, r5

129d8: e58de060 str lr, [sp, #96] ; 0x60

129dc: e59d9038 ldr r9, [sp, #56] ; 0x38

129e0: e59de044 ldr lr, [sp, #68] ; 0x44

129e4: e6ef2072 uxtb r2, r2

129e8: e089500e add r5, r9, lr

129ec: e59d9040 ldr r9, [sp, #64] ; 0x40

129f0: e59de004 ldr lr, [sp, #4]

129f4: e082c00c add ip, r2, ip

129f8: e049e00e sub lr, r9, lr

129fc: e6ef9075 uxtb r9, r5

12a00: e58d9004 str r9, [sp, #4]

12a04: e59d5014 ldr r5, [sp, #20]

12a08: e59d9058 ldr r9, [sp, #88] ; 0x58

12a0c: e6efc07c uxtb ip, ip

12a10: e0499005 sub r9, r9, r5

12a14: e59d5008 ldr r5, [sp, #8]

12a18: e6ef7077 uxtb r7, r7

12a1c: e58da02c str sl, [sp, #44] ; 0x2c

12a20: e58d9048 str r9, [sp, #72] ; 0x48

12a24: e082a00c add sl, r2, ip

12a28: e0859007 add r9, r5, r7

12a2c: e6ef6076 uxtb r6, r6

12a30: e6efa07a uxtb sl, sl

12a34: e6ef9079 uxtb r9, r9

12a38: e0864004 add r4, r6, r4

12a3c: e08a7007 add r7, sl, r7

12a40: e089c00c add ip, r9, ip

12a44: e6ef4074 uxtb r4, r4

12a48: e6ef7077 uxtb r7, r7

12a4c: e6efc07c uxtb ip, ip

12a50: e58d7030 str r7, [sp, #48] ; 0x30

12a54: e58dc024 str ip, [sp, #36] ; 0x24

12a58: e59d7004 ldr r7, [sp, #4]

12a5c: e086c004 add ip, r6, r4

12a60: e59d5044 ldr r5, [sp, #68] ; 0x44

12a64: e6efc07c uxtb ip, ip

12a68: e0874004 add r4, r7, r4

12a6c: e08c5005 add r5, ip, r5

12a70: e6ef4074 uxtb r4, r4

12a74: e58d4014 str r4, [sp, #20]

12a78: e6ef4075 uxtb r4, r5

12a7c: e58d4010 str r4, [sp, #16]

12a80: e59d5064 ldr r5, [sp, #100] ; 0x64

12a84: e59d400c ldr r4, [sp, #12]

12a88: e59d7050 ldr r7, [sp, #80] ; 0x50

12a8c: e0454004 sub r4, r5, r4

12a90: e59d5034 ldr r5, [sp, #52] ; 0x34

12a94: e0475005 sub r5, r7, r5

12a98: e59d705c ldr r7, [sp, #92] ; 0x5c

12a9c: e0478008 sub r8, r7, r8

12aa0: e58d8044 str r8, [sp, #68] ; 0x44

12aa4: e59d7038 ldr r7, [sp, #56] ; 0x38

12aa8: e59d804c ldr r8, [sp, #76] ; 0x4c

12aac: e0488007 sub r8, r8, r7

12ab0: e59d7008 ldr r7, [sp, #8]

12ab4: e58d8034 str r8, [sp, #52] ; 0x34

12ab8: e04ee007 sub lr, lr, r7

12abc: e59d8060 ldr r8, [sp, #96] ; 0x60

12ac0: e59d7054 ldr r7, [sp, #84] ; 0x54

12ac4: e0482002 sub r2, r8, r2

12ac8: e0478006 sub r8, r7, r6

12acc: e58d8040 str r8, [sp, #64] ; 0x40

12ad0: e59d601c ldr r6, [sp, #28]

12ad4: e59d803c ldr r8, [sp, #60] ; 0x3c

12ad8: e04ee00a sub lr, lr, sl

12adc: e0887006 add r7, r8, r6

12ae0: e59d6028 ldr r6, [sp, #40] ; 0x28

12ae4: e0455008 sub r5, r5, r8

12ae8: e08b6006 add r6, fp, r6

12aec: e59d8048 ldr r8, [sp, #72] ; 0x48

12af0: e6ef7077 uxtb r7, r7

12af4: e58d7008 str r7, [sp, #8]

12af8: e048b00b sub fp, r8, fp

12afc: e59d702c ldr r7, [sp, #44] ; 0x2c

12b00: e6ef6076 uxtb r6, r6

12b04: e58d600c str r6, [sp, #12]

12b08: e58d5038 str r5, [sp, #56] ; 0x38

12b0c: e59d6018 ldr r6, [sp, #24]

12b10: e59d5020 ldr r5, [sp, #32]

12b14: e58db03c str fp, [sp, #60] ; 0x3c

12b18: e59d8044 ldr r8, [sp, #68] ; 0x44

12b1c: e59db030 ldr fp, [sp, #48] ; 0x30

12b20: e0837007 add r7, r3, r7

12b24: e0443003 sub r3, r4, r3

12b28: e58d3018 str r3, [sp, #24]

12b2c: e59d4024 ldr r4, [sp, #36] ; 0x24

12b30: e0865005 add r5, r6, r5

12b34: e0488006 sub r8, r8, r6

12b38: e08a600b add r6, sl, fp

12b3c: e59da010 ldr sl, [sp, #16]

12b40: e59db004 ldr fp, [sp, #4]

12b44: e59d3034 ldr r3, [sp, #52] ; 0x34

12b48: e0894004 add r4, r9, r4

12b4c: e08ca00a add sl, ip, sl

12b50: e0422009 sub r2, r2, r9

12b54: e043c00c sub ip, r3, ip

12b58: e59d9014 ldr r9, [sp, #20]

12b5c: e59d3040 ldr r3, [sp, #64] ; 0x40

12b60: e6ef7077 uxtb r7, r7

12b64: e08b9009 add r9, fp, r9

12b68: e043b00b sub fp, r3, fp

12b6c: e59d3030 ldr r3, [sp, #48] ; 0x30

12b70: e6ef6076 uxtb r6, r6

12b74: e04ee007 sub lr, lr, r7

12b78: e0837087 add r7, r3, r7, lsl #1

12b7c: e59d3020 ldr r3, [sp, #32]

12b80: e6ef5075 uxtb r5, r5

12b84: e0833086 add r3, r3, r6, lsl #1

12b88: e58d3030 str r3, [sp, #48] ; 0x30

12b8c: e59d3024 ldr r3, [sp, #36] ; 0x24

12b90: e6ef4074 uxtb r4, r4

12b94: e0488006 sub r8, r8, r6

12b98: e0422005 sub r2, r2, r5

12b9c: e59d602c ldr r6, [sp, #44] ; 0x2c

12ba0: e0835085 add r5, r3, r5, lsl #1

12ba4: e59d3018 ldr r3, [sp, #24]

12ba8: e6efa07a uxtb sl, sl

12bac: e0433004 sub r3, r3, r4

12bb0: e0864084 add r4, r6, r4, lsl #1

12bb4: e59d6008 ldr r6, [sp, #8]

12bb8: e6ef9079 uxtb r9, r9

12bbc: e04cc006 sub ip, ip, r6

12bc0: e58dc004 str ip, [sp, #4]

12bc4: e59dc010 ldr ip, [sp, #16]

12bc8: e6ef8078 uxtb r8, r8

12bcc: e08cc086 add ip, ip, r6, lsl #1

12bd0: e58dc008 str ip, [sp, #8]

12bd4: e59d601c ldr r6, [sp, #28]

12bd8: e59dc038 ldr ip, [sp, #56] ; 0x38

12bdc: e6ef5075 uxtb r5, r5

12be0: e04cc00a sub ip, ip, sl

12be4: e086a08a add sl, r6, sl, lsl #1

12be8: e59d600c ldr r6, [sp, #12]

12bec: e58dc010 str ip, [sp, #16]

12bf0: e59dc014 ldr ip, [sp, #20]

12bf4: e04bb006 sub fp, fp, r6

12bf8: e08c6086 add r6, ip, r6, lsl #1

12bfc: e59dc03c ldr ip, [sp, #60] ; 0x3c

12c00: e58d6018 str r6, [sp, #24]

12c04: e04cc009 sub ip, ip, r9

12c08: e58dc020 str ip, [sp, #32]

12c0c: e59fc2a4 ldr ip, [pc, #676] ; 12eb8 <mcrypt\_mutex\_register@@Base+0xe724>

12c10: e59d6028 ldr r6, [sp, #40] ; 0x28

12c14: e58dc028 str ip, [sp, #40] ; 0x28

12c18: e5ddc030 ldrb ip, [sp, #48] ; 0x30

12c1c: e6efa07a uxtb sl, sl

12c20: e0869089 add r9, r6, r9, lsl #1

12c24: e58dc01c str ip, [sp, #28]

12c28: e5ddc004 ldrb ip, [sp, #4]

12c2c: e59f6288 ldr r6, [pc, #648] ; 12ebc <mcrypt\_mutex\_register@@Base+0xe728>

12c30: e6efe07e uxtb lr, lr

12c34: e58dc004 str ip, [sp, #4]

12c38: e5ddc008 ldrb ip, [sp, #8]

12c3c: e6ef7077 uxtb r7, r7

12c40: e6ef2072 uxtb r2, r2

12c44: e58dc008 str ip, [sp, #8]

12c48: e5ddc010 ldrb ip, [sp, #16]

12c4c: e6ef3073 uxtb r3, r3

12c50: e6ef4074 uxtb r4, r4

12c54: e58dc00c str ip, [sp, #12]

12c58: e58da010 str sl, [sp, #16]

12c5c: e6efa07b uxtb sl, fp

12c60: e5ddb018 ldrb fp, [sp, #24]

12c64: e58da014 str sl, [sp, #20]

12c68: e59dc01c ldr ip, [sp, #28]

12c6c: e58db018 str fp, [sp, #24]

12c70: e59db004 ldr fp, [sp, #4]

12c74: e5dda020 ldrb sl, [sp, #32]

12c78: e5c0b00e strb fp, [r0, #14]

12c7c: e59db014 ldr fp, [sp, #20]

12c80: e5c0c007 strb ip, [r0, #7]

12c84: e59dc008 ldr ip, [sp, #8]

12c88: e5c08006 strb r8, [r0, #6]

12c8c: e5c05005 strb r5, [r0, #5]

12c90: e5c0b00a strb fp, [r0, #10]

12c94: e58d8020 str r8, [sp, #32]

12c98: e58d5024 str r5, [sp, #36] ; 0x24

12c9c: e59db018 ldr fp, [sp, #24]

12ca0: e59d500c ldr r5, [sp, #12]

12ca4: e59d8010 ldr r8, [sp, #16]

12ca8: e5c0c00f strb ip, [r0, #15]

12cac: e6ef9079 uxtb r9, r9

12cb0: e59dc028 ldr ip, [sp, #40] ; 0x28

12cb4: e5c0e000 strb lr, [r0]

12cb8: e5c07001 strb r7, [r0, #1]

12cbc: e5c02004 strb r2, [r0, #4]

12cc0: e5c03002 strb r3, [r0, #2]

12cc4: e5c04003 strb r4, [r0, #3]

12cc8: e5c0500c strb r5, [r0, #12]

12ccc: e5c0a008 strb sl, [r0, #8]

12cd0: e5c09009 strb r9, [r0, #9]

12cd4: e08f6006 add r6, pc, r6

12cd8: e5c0800d strb r8, [r0, #13]

12cdc: e5c0b00b strb fp, [r0, #11]

12ce0: e5d1b010 ldrb fp, [r1, #16]

12ce4: e796c00c ldr ip, [r6, ip]

12ce8: e5d18000 ldrb r8, [r1]

12cec: e04ee00b sub lr, lr, fp

12cf0: e08ce00e add lr, ip, lr

12cf4: e59f51c4 ldr r5, [pc, #452] ; 12ec0 <mcrypt\_mutex\_register@@Base+0xe72c>

12cf8: e5dee100 ldrb lr, [lr, #256] ; 0x100

12cfc: e028800e eor r8, r8, lr

12d00: e5c08000 strb r8, [r0]

12d04: e5d1e011 ldrb lr, [r1, #17]

12d08: e7968005 ldr r8, [r6, r5]

12d0c: e5d15001 ldrb r5, [r1, #1]

12d10: e027700e eor r7, r7, lr

12d14: e7d8e007 ldrb lr, [r8, r7]

12d18: e04e5005 sub r5, lr, r5

12d1c: e5c05001 strb r5, [r0, #1]

12d20: e5d15012 ldrb r5, [r1, #18]

12d24: e5d1e002 ldrb lr, [r1, #2]

12d28: e0233005 eor r3, r3, r5

12d2c: e7d83003 ldrb r3, [r8, r3]

12d30: e043e00e sub lr, r3, lr

12d34: e5c0e002 strb lr, [r0, #2]

12d38: e5d1e013 ldrb lr, [r1, #19]

12d3c: e5d13003 ldrb r3, [r1, #3]

12d40: e044400e sub r4, r4, lr

12d44: e08c4004 add r4, ip, r4

12d48: e5d4e100 ldrb lr, [r4, #256] ; 0x100

12d4c: e023300e eor r3, r3, lr

12d50: e5c03003 strb r3, [r0, #3]

12d54: e5d1e014 ldrb lr, [r1, #20]

12d58: e5d13004 ldrb r3, [r1, #4]

12d5c: e042200e sub r2, r2, lr

12d60: e08c2002 add r2, ip, r2

12d64: e59de020 ldr lr, [sp, #32]

12d68: e5d22100 ldrb r2, [r2, #256] ; 0x100

12d6c: e0233002 eor r3, r3, r2

12d70: e5c03004 strb r3, [r0, #4]

12d74: e5d15015 ldrb r5, [r1, #21]

12d78: e59d2024 ldr r2, [sp, #36] ; 0x24

12d7c: e5d13005 ldrb r3, [r1, #5]

12d80: e0255002 eor r5, r5, r2

12d84: e7d82005 ldrb r2, [r8, r5]

12d88: e0423003 sub r3, r2, r3

12d8c: e5c03005 strb r3, [r0, #5]

12d90: e5d12016 ldrb r2, [r1, #22]

12d94: e5d13006 ldrb r3, [r1, #6]

12d98: e022200e eor r2, r2, lr

12d9c: e7d82002 ldrb r2, [r8, r2]

12da0: e0423003 sub r3, r2, r3

12da4: e5c03006 strb r3, [r0, #6]

12da8: e5d16017 ldrb r6, [r1, #23]

12dac: e59d301c ldr r3, [sp, #28]

12db0: e0436006 sub r6, r3, r6

12db4: e08c6006 add r6, ip, r6

12db8: e5d63100 ldrb r3, [r6, #256] ; 0x100

12dbc: e5d12007 ldrb r2, [r1, #7]

12dc0: e59de018 ldr lr, [sp, #24]

12dc4: e59d500c ldr r5, [sp, #12]

12dc8: e0233002 eor r3, r3, r2

12dcc: e5c03007 strb r3, [r0, #7]

12dd0: e5d12018 ldrb r2, [r1, #24]

12dd4: e5d13008 ldrb r3, [r1, #8]

12dd8: e04aa002 sub sl, sl, r2

12ddc: e08ca00a add sl, ip, sl

12de0: e5da2100 ldrb r2, [sl, #256] ; 0x100

12de4: e59da014 ldr sl, [sp, #20]

12de8: e0233002 eor r3, r3, r2

12dec: e5c03008 strb r3, [r0, #8]

12df0: e5d13019 ldrb r3, [r1, #25]

12df4: e5d12009 ldrb r2, [r1, #9]

12df8: e0299003 eor r9, r9, r3

12dfc: e7d83009 ldrb r3, [r8, r9]

12e00: e0433002 sub r3, r3, r2

12e04: e5c03009 strb r3, [r0, #9]

12e08: e5d1b01a ldrb fp, [r1, #26]

12e0c: e5d1200a ldrb r2, [r1, #10]

12e10: e02bb00a eor fp, fp, sl

12e14: e7d8300b ldrb r3, [r8, fp]

12e18: e0433002 sub r3, r3, r2

12e1c: e5c0300a strb r3, [r0, #10]

12e20: e5d1301b ldrb r3, [r1, #27]

12e24: e5d1200b ldrb r2, [r1, #11]

12e28: e04e3003 sub r3, lr, r3

12e2c: e08c3003 add r3, ip, r3

12e30: e5d33100 ldrb r3, [r3, #256] ; 0x100

12e34: e0233002 eor r3, r3, r2

12e38: e5c0300b strb r3, [r0, #11]

12e3c: e5d1301c ldrb r3, [r1, #28]

12e40: e5d1200c ldrb r2, [r1, #12]

12e44: e0453003 sub r3, r5, r3

12e48: e08c3003 add r3, ip, r3

12e4c: e5d33100 ldrb r3, [r3, #256] ; 0x100

12e50: e0233002 eor r3, r3, r2

12e54: e5c0300c strb r3, [r0, #12]

12e58: e5d1a01d ldrb sl, [r1, #29]

12e5c: e59d3010 ldr r3, [sp, #16]

12e60: e5d1200d ldrb r2, [r1, #13]

12e64: e02aa003 eor sl, sl, r3

12e68: e7d8300a ldrb r3, [r8, sl]

12e6c: e0433002 sub r3, r3, r2

12e70: e5c0300d strb r3, [r0, #13]

12e74: e5d1301e ldrb r3, [r1, #30]

12e78: e5d1200e ldrb r2, [r1, #14]

12e7c: e59db004 ldr fp, [sp, #4]

12e80: e023300b eor r3, r3, fp

12e84: e7d83003 ldrb r3, [r8, r3]

12e88: e0433002 sub r3, r3, r2

12e8c: e5c0300e strb r3, [r0, #14]

12e90: e5d1301f ldrb r3, [r1, #31]

12e94: e59d2008 ldr r2, [sp, #8]

12e98: e0423003 sub r3, r2, r3

12e9c: e08cc003 add ip, ip, r3

12ea0: e5d1200f ldrb r2, [r1, #15]

12ea4: e5dc3100 ldrb r3, [ip, #256] ; 0x100

12ea8: e0233002 eor r3, r3, r2

12eac: e5c0300f strb r3, [r0, #15]

12eb0: e28dd06c add sp, sp, #108 ; 0x6c

12eb4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

12eb8: 000000a4 andeq r0, r0, r4, lsr #1

12ebc: 00021324 andeq r1, r2, r4, lsr #6

12ec0: 000000a0 andeq r0, r0, r0, lsr #1

12ec4: e92d40f0 push {r4, r5, r6, r7, lr}

12ec8: e1a06001 mov r6, r1

12ecc: e1a04000 mov r4, r0

12ed0: e1c100d0 ldrd r0, [r1]

12ed4: e24dd044 sub sp, sp, #68 ; 0x44

12ed8: e1c620d8 ldrd r2, [r6, #8]

12edc: e1a0500d mov r5, sp

12ee0: e58d000c str r0, [sp, #12]

12ee4: e58d1008 str r1, [sp, #8]

12ee8: e1a00005 mov r0, r5

12eec: e1a01004 mov r1, r4

12ef0: e58d2004 str r2, [sp, #4]

12ef4: e58d3000 str r3, [sp]

12ef8: ebfffcdd bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12efc: e2841020 add r1, r4, #32

12f00: e1a00005 mov r0, r5

12f04: ebfffcda bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f08: e2841040 add r1, r4, #64 ; 0x40

12f0c: e1a00005 mov r0, r5

12f10: ebfffcd7 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f14: e2841060 add r1, r4, #96 ; 0x60

12f18: e1a00005 mov r0, r5

12f1c: ebfffcd4 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f20: e2841080 add r1, r4, #128 ; 0x80

12f24: e1a00005 mov r0, r5

12f28: ebfffcd1 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f2c: e28410a0 add r1, r4, #160 ; 0xa0

12f30: e1a00005 mov r0, r5

12f34: ebfffcce bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f38: e28410c0 add r1, r4, #192 ; 0xc0

12f3c: e1a00005 mov r0, r5

12f40: ebfffccb bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f44: e28410e0 add r1, r4, #224 ; 0xe0

12f48: e1a00005 mov r0, r5

12f4c: ebfffcc8 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

12f50: e5942210 ldr r2, [r4, #528] ; 0x210

12f54: e3520010 cmp r2, #16

12f58: 8a000048 bhi 13080 <mcrypt\_mutex\_register@@Base+0xe8ec>

12f5c: e0843202 add r3, r4, r2, lsl #4

12f60: e7d41202 ldrb r1, [r4, r2, lsl #4]

12f64: e5dde000 ldrb lr, [sp]

12f68: e5dd4001 ldrb r4, [sp, #1]

12f6c: e5d32002 ldrb r2, [r3, #2]

12f70: e5dd0002 ldrb r0, [sp, #2]

12f74: e5d3c001 ldrb ip, [r3, #1]

12f78: e5dd5004 ldrb r5, [sp, #4]

12f7c: e02ee001 eor lr, lr, r1

12f80: e084c00c add ip, r4, ip

12f84: e5d31003 ldrb r1, [r3, #3]

12f88: e5dd4003 ldrb r4, [sp, #3]

12f8c: e0800002 add r0, r0, r2

12f90: e5d32004 ldrb r2, [r3, #4]

12f94: e0211004 eor r1, r1, r4

12f98: e5cde000 strb lr, [sp]

12f9c: e5d34005 ldrb r4, [r3, #5]

12fa0: e0222005 eor r2, r2, r5

12fa4: e5d3e006 ldrb lr, [r3, #6]

12fa8: e5dd5005 ldrb r5, [sp, #5]

12fac: e5cdc001 strb ip, [sp, #1]

12fb0: e5ddc006 ldrb ip, [sp, #6]

12fb4: e5d37007 ldrb r7, [r3, #7]

12fb8: e0854004 add r4, r5, r4

12fbc: e5cd0002 strb r0, [sp, #2]

12fc0: e5d35008 ldrb r5, [r3, #8]

12fc4: e5dd0008 ldrb r0, [sp, #8]

12fc8: e08ce00e add lr, ip, lr

12fcc: e5ddc007 ldrb ip, [sp, #7]

12fd0: e5cd1003 strb r1, [sp, #3]

12fd4: e5cd2004 strb r2, [sp, #4]

12fd8: e5dd1009 ldrb r1, [sp, #9]

12fdc: e02cc007 eor ip, ip, r7

12fe0: e0200005 eor r0, r0, r5

12fe4: e5d37009 ldrb r7, [r3, #9]

12fe8: e5d3500a ldrb r5, [r3, #10]

12fec: e5dd200a ldrb r2, [sp, #10]

12ff0: e0811007 add r1, r1, r7

12ff4: e5cd4005 strb r4, [sp, #5]

12ff8: e0822005 add r2, r2, r5

12ffc: e5cde006 strb lr, [sp, #6]

13000: e5cdc007 strb ip, [sp, #7]

13004: e5cd0008 strb r0, [sp, #8]

13008: e5cd1009 strb r1, [sp, #9]

1300c: e5cd200a strb r2, [sp, #10]

13010: e5d3100b ldrb r1, [r3, #11]

13014: e5d3200c ldrb r2, [r3, #12]

13018: e5ddc00b ldrb ip, [sp, #11]

1301c: e5dd000c ldrb r0, [sp, #12]

13020: e5d3500d ldrb r5, [r3, #13]

13024: e5d3400e ldrb r4, [r3, #14]

13028: e5d3e00f ldrb lr, [r3, #15]

1302c: e02cc001 eor ip, ip, r1

13030: e0200002 eor r0, r0, r2

13034: e5dd100d ldrb r1, [sp, #13]

13038: e5dd200e ldrb r2, [sp, #14]

1303c: e5dd300f ldrb r3, [sp, #15]

13040: e0811005 add r1, r1, r5

13044: e0822004 add r2, r2, r4

13048: e023300e eor r3, r3, lr

1304c: e5cd200e strb r2, [sp, #14]

13050: e5cd300f strb r3, [sp, #15]

13054: e5cdc00b strb ip, [sp, #11]

13058: e5cd000c strb r0, [sp, #12]

1305c: e5cd100d strb r1, [sp, #13]

13060: e1cd00d8 ldrd r0, [sp, #8]

13064: e1cd20d0 ldrd r2, [sp]

13068: e5860004 str r0, [r6, #4]

1306c: e5861000 str r1, [r6]

13070: e586200c str r2, [r6, #12]

13074: e5863008 str r3, [r6, #8]

13078: e28dd044 add sp, sp, #68 ; 0x44

1307c: e8bd80f0 pop {r4, r5, r6, r7, pc}

13080: e2841c01 add r1, r4, #256 ; 0x100

13084: e1a00005 mov r0, r5

13088: ebfffc79 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

1308c: e2841e12 add r1, r4, #288 ; 0x120

13090: e1a00005 mov r0, r5

13094: ebfffc76 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

13098: e2841d05 add r1, r4, #320 ; 0x140

1309c: e1a00005 mov r0, r5

130a0: ebfffc73 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

130a4: e2841e16 add r1, r4, #352 ; 0x160

130a8: e1a00005 mov r0, r5

130ac: ebfffc70 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

130b0: e5942210 ldr r2, [r4, #528] ; 0x210

130b4: e3520018 cmp r2, #24

130b8: 9affffa7 bls 12f5c <mcrypt\_mutex\_register@@Base+0xe7c8>

130bc: e2841d06 add r1, r4, #384 ; 0x180

130c0: e1a00005 mov r0, r5

130c4: ebfffc6a bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

130c8: e2841e1a add r1, r4, #416 ; 0x1a0

130cc: e1a00005 mov r0, r5

130d0: ebfffc67 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

130d4: e2841d07 add r1, r4, #448 ; 0x1c0

130d8: e1a00005 mov r0, r5

130dc: ebfffc64 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

130e0: e1a00005 mov r0, r5

130e4: e2841e1e add r1, r4, #480 ; 0x1e0

130e8: ebfffc61 bl 12274 <mcrypt\_mutex\_register@@Base+0xdae0>

130ec: e5942210 ldr r2, [r4, #528] ; 0x210

130f0: eaffff99 b 12f5c <mcrypt\_mutex\_register@@Base+0xe7c8>

130f4: e5902210 ldr r2, [r0, #528] ; 0x210

130f8: e92d40f0 push {r4, r5, r6, r7, lr}

130fc: e1a06001 mov r6, r1

13100: e0803202 add r3, r0, r2, lsl #4

13104: e591100c ldr r1, [r1, #12]

13108: e7d0e202 ldrb lr, [r0, r2, lsl #4]

1310c: e1a04000 mov r4, r0

13110: e5d30002 ldrb r0, [r3, #2]

13114: e5d37001 ldrb r7, [r3, #1]

13118: e5d35003 ldrb r5, [r3, #3]

1311c: e24dd044 sub sp, sp, #68 ; 0x44

13120: e0600821 rsb r0, r0, r1, lsr #16

13124: e0255c21 eor r5, r5, r1, lsr #24

13128: e02ee001 eor lr, lr, r1

1312c: e5d3c004 ldrb ip, [r3, #4]

13130: e0677421 rsb r7, r7, r1, lsr #8

13134: e5cd0002 strb r0, [sp, #2]

13138: e5961008 ldr r1, [r6, #8]

1313c: e5d30005 ldrb r0, [r3, #5]

13140: e5cd5003 strb r5, [sp, #3]

13144: e5cde000 strb lr, [sp]

13148: e5d35006 ldrb r5, [r3, #6]

1314c: e5d3e007 ldrb lr, [r3, #7]

13150: e02cc001 eor ip, ip, r1

13154: e0600421 rsb r0, r0, r1, lsr #8

13158: e0655821 rsb r5, r5, r1, lsr #16

1315c: e02eec21 eor lr, lr, r1, lsr #24

13160: e5cdc004 strb ip, [sp, #4]

13164: e5961004 ldr r1, [r6, #4]

13168: e5d3c008 ldrb ip, [r3, #8]

1316c: e5cd0005 strb r0, [sp, #5]

13170: e5d30009 ldrb r0, [r3, #9]

13174: e5cd5006 strb r5, [sp, #6]

13178: e5cde007 strb lr, [sp, #7]

1317c: e5d3500a ldrb r5, [r3, #10]

13180: e5d3e00b ldrb lr, [r3, #11]

13184: e02cc001 eor ip, ip, r1

13188: e0600421 rsb r0, r0, r1, lsr #8

1318c: e0655821 rsb r5, r5, r1, lsr #16

13190: e5cdc008 strb ip, [sp, #8]

13194: e02eec21 eor lr, lr, r1, lsr #24

13198: e5d3c00c ldrb ip, [r3, #12]

1319c: e5961000 ldr r1, [r6]

131a0: e5cd0009 strb r0, [sp, #9]

131a4: e5d3000d ldrb r0, [r3, #13]

131a8: e02cc001 eor ip, ip, r1

131ac: e5cd500a strb r5, [sp, #10]

131b0: e0600421 rsb r0, r0, r1, lsr #8

131b4: e5cd7001 strb r7, [sp, #1]

131b8: e5cde00b strb lr, [sp, #11]

131bc: e5cdc00c strb ip, [sp, #12]

131c0: e5cd000d strb r0, [sp, #13]

131c4: e5d3000e ldrb r0, [r3, #14]

131c8: e5d3300f ldrb r3, [r3, #15]

131cc: e3520018 cmp r2, #24

131d0: e0600821 rsb r0, r0, r1, lsr #16

131d4: e0231c21 eor r1, r3, r1, lsr #24

131d8: e5cd000e strb r0, [sp, #14]

131dc: e5cd100f strb r1, [sp, #15]

131e0: e1a0500d mov r5, sp

131e4: 8a000021 bhi 13270 <mcrypt\_mutex\_register@@Base+0xeadc>

131e8: e3520010 cmp r2, #16

131ec: 8a00002e bhi 132ac <mcrypt\_mutex\_register@@Base+0xeb18>

131f0: e28410e0 add r1, r4, #224 ; 0xe0

131f4: e1a00005 mov r0, r5

131f8: ebfffd62 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

131fc: e28410c0 add r1, r4, #192 ; 0xc0

13200: e1a00005 mov r0, r5

13204: ebfffd5f bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13208: e28410a0 add r1, r4, #160 ; 0xa0

1320c: e1a00005 mov r0, r5

13210: ebfffd5c bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13214: e2841080 add r1, r4, #128 ; 0x80

13218: e1a00005 mov r0, r5

1321c: ebfffd59 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13220: e2841060 add r1, r4, #96 ; 0x60

13224: e1a00005 mov r0, r5

13228: ebfffd56 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

1322c: e2841040 add r1, r4, #64 ; 0x40

13230: e1a00005 mov r0, r5

13234: ebfffd53 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13238: e2841020 add r1, r4, #32

1323c: e1a00005 mov r0, r5

13240: ebfffd50 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13244: e1a01004 mov r1, r4

13248: e1a00005 mov r0, r5

1324c: ebfffd4d bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13250: e1cd00d0 ldrd r0, [sp]

13254: e1cd20d8 ldrd r2, [sp, #8]

13258: e586000c str r0, [r6, #12]

1325c: e5861008 str r1, [r6, #8]

13260: e5862004 str r2, [r6, #4]

13264: e5863000 str r3, [r6]

13268: e28dd044 add sp, sp, #68 ; 0x44

1326c: e8bd80f0 pop {r4, r5, r6, r7, pc}

13270: e1a00005 mov r0, r5

13274: e2841e1e add r1, r4, #480 ; 0x1e0

13278: ebfffd42 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

1327c: e1a00005 mov r0, r5

13280: e2841d07 add r1, r4, #448 ; 0x1c0

13284: ebfffd3f bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13288: e1a00005 mov r0, r5

1328c: e2841e1a add r1, r4, #416 ; 0x1a0

13290: ebfffd3c bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

13294: e1a00005 mov r0, r5

13298: e2841d06 add r1, r4, #384 ; 0x180

1329c: ebfffd39 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

132a0: e5942210 ldr r2, [r4, #528] ; 0x210

132a4: e3520010 cmp r2, #16

132a8: 9affffd0 bls 131f0 <mcrypt\_mutex\_register@@Base+0xea5c>

132ac: e2841e16 add r1, r4, #352 ; 0x160

132b0: e1a00005 mov r0, r5

132b4: ebfffd33 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

132b8: e2841d05 add r1, r4, #320 ; 0x140

132bc: e1a00005 mov r0, r5

132c0: ebfffd30 bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

132c4: e2841e12 add r1, r4, #288 ; 0x120

132c8: e1a00005 mov r0, r5

132cc: ebfffd2d bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

132d0: e2841c01 add r1, r4, #256 ; 0x100

132d4: e1a00005 mov r0, r5

132d8: ebfffd2a bl 12788 <mcrypt\_mutex\_register@@Base+0xdff4>

132dc: eaffffc3 b 131f0 <mcrypt\_mutex\_register@@Base+0xea5c>

132e0: e3a00f85 mov r0, #532 ; 0x214

132e4: e12fff1e bx lr

132e8: e3a00010 mov r0, #16

132ec: e12fff1e bx lr

132f0: e3a00001 mov r0, #1

132f4: e12fff1e bx lr

132f8: e3a00020 mov r0, #32

132fc: e12fff1e bx lr

13300: e3a03003 mov r3, #3

13304: e5803000 str r3, [r0]

13308: e59f0004 ldr r0, [pc, #4] ; 13314 <mcrypt\_mutex\_register@@Base+0xeb80>

1330c: e08f0000 add r0, pc, r0

13310: e12fff1e bx lr

13314: 0000d5e8 andeq sp, r0, r8, ror #11

13318: e59f0004 ldr r0, [pc, #4] ; 13324 <mcrypt\_mutex\_register@@Base+0xeb90>

1331c: e08f0000 add r0, pc, r0

13320: e12fff1e bx lr

13324: 0000d5e4 andeq sp, r0, r4, ror #11

13328: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

1332c: e24dd0e8 sub sp, sp, #232 ; 0xe8

13330: ebffffec bl 132e8 <mcrypt\_mutex\_register@@Base+0xeb54>

13334: e1a06000 mov r6, r0

13338: ebffffee bl 132f8 <mcrypt\_mutex\_register@@Base+0xeb64>

1333c: e1a01000 mov r1, r0

13340: e3a00001 mov r0, #1

13344: ebffbbba bl 2234 <calloc@plt>

13348: e2507000 subs r7, r0, #0

1334c: 0a000064 beq 134e4 <mcrypt\_mutex\_register@@Base+0xed50>

13350: e2475001 sub r5, r7, #1

13354: e3a0400a mov r4, #10

13358: e2678001 rsb r8, r7, #1

1335c: ea000004 b 13374 <mcrypt\_mutex\_register@@Base+0xebe0>

13360: e2742000 rsbs r2, r4, #0

13364: e2844002 add r4, r4, #2

13368: e6ef2072 uxtb r2, r2

1336c: 52623000 rsbpl r3, r2, #0

13370: e5e53001 strb r3, [r5, #1]!

13374: ebffffdf bl 132f8 <mcrypt\_mutex\_register@@Base+0xeb64>

13378: e0882005 add r2, r8, r5

1337c: e6ef3074 uxtb r3, r4

13380: e1500002 cmp r0, r2

13384: cafffff5 bgt 13360 <mcrypt\_mutex\_register@@Base+0xebcc>

13388: e3560000 cmp r6, #0

1338c: da000039 ble 13478 <mcrypt\_mutex\_register@@Base+0xece4>

13390: e1a0400d mov r4, sp

13394: e1a02004 mov r2, r4

13398: e3a03000 mov r3, #0

1339c: e4c23001 strb r3, [r2], #1

133a0: e2833001 add r3, r3, #1

133a4: e1560003 cmp r6, r3

133a8: 1afffffb bne 1339c <mcrypt\_mutex\_register@@Base+0xec08>

133ac: ebffffcb bl 132e0 <mcrypt\_mutex\_register@@Base+0xeb4c>

133b0: ebffbbcf bl 22f4 <malloc@plt>

133b4: e2509000 subs r9, r0, #0

133b8: 0a000049 beq 134e4 <mcrypt\_mutex\_register@@Base+0xed50>

133bc: e28d8010 add r8, sp, #16

133c0: e1a01004 mov r1, r4

133c4: e1a02006 mov r2, r6

133c8: e1a00008 mov r0, r8

133cc: ebffbbad bl 2288 <memcpy@plt>

133d0: ebffffc8 bl 132f8 <mcrypt\_mutex\_register@@Base+0xeb64>

133d4: e1a01007 mov r1, r7

133d8: e28da020 add sl, sp, #32

133dc: e0886006 add r6, r8, r6

133e0: e1a05008 mov r5, r8

133e4: e1a0400a mov r4, sl

133e8: e1a02000 mov r2, r0

133ec: e1a00009 mov r0, r9

133f0: ebfffb39 bl 120dc <mcrypt\_mutex\_register@@Base+0xd948>

133f4: e1a00007 mov r0, r7

133f8: ebffbb9f bl 227c <free@plt>

133fc: e1a01008 mov r1, r8

13400: e1a00009 mov r0, r9

13404: ebfffeae bl 12ec4 <mcrypt\_mutex\_register@@Base+0xe730>

13408: e59f70f0 ldr r7, [pc, #240] ; 13500 <mcrypt\_mutex\_register@@Base+0xed6c>

1340c: e08f7007 add r7, pc, r7

13410: e4d52001 ldrb r2, [r5], #1

13414: e1a00004 mov r0, r4

13418: e1a01007 mov r1, r7

1341c: ebffbbd2 bl 236c <sprintf@plt>

13420: e1550006 cmp r5, r6

13424: e2844002 add r4, r4, #2

13428: 1afffff8 bne 13410 <mcrypt\_mutex\_register@@Base+0xec7c>

1342c: e59f40d0 ldr r4, [pc, #208] ; 13504 <mcrypt\_mutex\_register@@Base+0xed70>

13430: e1a0000a mov r0, sl

13434: e08f4004 add r4, pc, r4

13438: e1a01004 mov r1, r4

1343c: ebffbb82 bl 224c <strcmp@plt>

13440: e3500000 cmp r0, #0

13444: 1a00001c bne 134bc <mcrypt\_mutex\_register@@Base+0xed28>

13448: e1a01008 mov r1, r8

1344c: e1a00009 mov r0, r9

13450: ebffff27 bl 130f4 <mcrypt\_mutex\_register@@Base+0xe960>

13454: e1a00009 mov r0, r9

13458: ebffbb87 bl 227c <free@plt>

1345c: e1a00008 mov r0, r8

13460: e1a0100d mov r1, sp

13464: ebffbb78 bl 224c <strcmp@plt>

13468: e3500000 cmp r0, #0

1346c: 1a00001e bne 134ec <mcrypt\_mutex\_register@@Base+0xed58>

13470: e28dd0e8 add sp, sp, #232 ; 0xe8

13474: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

13478: ebffff98 bl 132e0 <mcrypt\_mutex\_register@@Base+0xeb4c>

1347c: ebffbb9c bl 22f4 <malloc@plt>

13480: e2509000 subs r9, r0, #0

13484: 0a000016 beq 134e4 <mcrypt\_mutex\_register@@Base+0xed50>

13488: ebffff9a bl 132f8 <mcrypt\_mutex\_register@@Base+0xeb64>

1348c: e1a01007 mov r1, r7

13490: e28d8010 add r8, sp, #16

13494: e28da020 add sl, sp, #32

13498: e1a02000 mov r2, r0

1349c: e1a00009 mov r0, r9

134a0: ebfffb0d bl 120dc <mcrypt\_mutex\_register@@Base+0xd948>

134a4: e1a00007 mov r0, r7

134a8: ebffbb73 bl 227c <free@plt>

134ac: e1a01008 mov r1, r8

134b0: e1a00009 mov r0, r9

134b4: ebfffe82 bl 12ec4 <mcrypt\_mutex\_register@@Base+0xe730>

134b8: eaffffdb b 1342c <mcrypt\_mutex\_register@@Base+0xec98>

134bc: e59f0044 ldr r0, [pc, #68] ; 13508 <mcrypt\_mutex\_register@@Base+0xed74>

134c0: e08f0000 add r0, pc, r0

134c4: ebffbb87 bl 22e8 <puts@plt>

134c8: e59f003c ldr r0, [pc, #60] ; 1350c <mcrypt\_mutex\_register@@Base+0xed78>

134cc: e1a0200a mov r2, sl

134d0: e1a01004 mov r1, r4

134d4: e08f0000 add r0, pc, r0

134d8: ebffbb61 bl 2264 <printf@plt>

134dc: e1a00009 mov r0, r9

134e0: ebffbb65 bl 227c <free@plt>

134e4: e3e00000 mvn r0, #0

134e8: eaffffe0 b 13470 <mcrypt\_mutex\_register@@Base+0xecdc>

134ec: e59f001c ldr r0, [pc, #28] ; 13510 <mcrypt\_mutex\_register@@Base+0xed7c>

134f0: e08f0000 add r0, pc, r0

134f4: ebffbb7b bl 22e8 <puts@plt>

134f8: e3e00000 mvn r0, #0

134fc: eaffffdb b 13470 <mcrypt\_mutex\_register@@Base+0xecdc>

13500: 0000d254 andeq sp, r0, r4, asr r2

13504: 0000d4d4 ldrdeq sp, [r0], -r4

13508: 0000d1bc ; <UNDEFINED> instruction: 0x0000d1bc

1350c: 0000d1c0 andeq sp, r0, r0, asr #3

13510: 0000d1bc ; <UNDEFINED> instruction: 0x0000d1bc

13514: e59f0000 ldr r0, [pc] ; 1351c <mcrypt\_mutex\_register@@Base+0xed88>

13518: e12fff1e bx lr

1351c: 01315731 teqeq r1, r1, lsr r7

13520: e3520020 cmp r2, #32

13524: 1a000044 bne 1363c <mcrypt\_mutex\_register@@Base+0xeea8>

13528: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

1352c: e1a02000 mov r2, r0

13530: e591e000 ldr lr, [r1]

13534: e591c00c ldr ip, [r1, #12]

13538: e5915004 ldr r5, [r1, #4]

1353c: e5914008 ldr r4, [r1, #8]

13540: e59f80fc ldr r8, [pc, #252] ; 13644 <mcrypt\_mutex\_register@@Base+0xeeb0>

13544: e580e000 str lr, [r0]

13548: e5805004 str r5, [r0, #4]

1354c: e5804008 str r4, [r0, #8]

13550: e08f8008 add r8, pc, r8

13554: e1a0300c mov r3, ip

13558: e1a0700e mov r7, lr

1355c: e1a01000 mov r1, r0

13560: e2806fff add r6, r0, #1020 ; 0x3fc

13564: e5a2c00c str ip, [r2, #12]!

13568: ea000000 b 13570 <mcrypt\_mutex\_register@@Base+0xeddc>

1356c: e512700c ldr r7, [r2, #-12]

13570: e0873003 add r3, r7, r3

13574: e2037007 and r7, r3, #7

13578: e7987107 ldr r7, [r8, r7, lsl #2]

1357c: e02731a3 eor r3, r7, r3, lsr #3

13580: e5a23004 str r3, [r2, #4]!

13584: e1520006 cmp r2, r6

13588: 1afffff7 bne 1356c <mcrypt\_mutex\_register@@Base+0xedd8>

1358c: e2407004 sub r7, r0, #4

13590: e1a03007 mov r3, r7

13594: e2809058 add r9, r0, #88 ; 0x58

13598: e5b32004 ldr r2, [r3, #4]!

1359c: e1590003 cmp r9, r3

135a0: e5938164 ldr r8, [r3, #356] ; 0x164

135a4: e0822008 add r2, r2, r8

135a8: e5832000 str r2, [r3]

135ac: 1afffff9 bne 13598 <mcrypt\_mutex\_register@@Base+0xee04>

135b0: e59080ec ldr r8, [r0, #236] ; 0xec

135b4: e5903084 ldr r3, [r0, #132] ; 0x84

135b8: e3c88502 bic r8, r8, #8388608 ; 0x800000

135bc: e3888401 orr r8, r8, #16777216 ; 0x1000000

135c0: e3888001 orr r8, r8, #1

135c4: e5b72004 ldr r2, [r7, #4]!

135c8: e3c33502 bic r3, r3, #8388608 ; 0x800000

135cc: e0833008 add r3, r3, r8

135d0: e3c224ff bic r2, r2, #-16777216 ; 0xff000000

135d4: e0222003 eor r2, r2, r3

135d8: e1570006 cmp r7, r6

135dc: e5872000 str r2, [r7]

135e0: 1afffff7 bne 135c4 <mcrypt\_mutex\_register@@Base+0xee30>

135e4: e5906000 ldr r6, [r0]

135e8: e6ef3073 uxtb r3, r3

135ec: e3a02000 mov r2, #0

135f0: e5806400 str r6, [r0, #1024] ; 0x400

135f4: e0236002 eor r6, r3, r2

135f8: e2822001 add r2, r2, #1

135fc: e7d06106 ldrb r6, [r0, r6, lsl #2]

13600: e3520c01 cmp r2, #256 ; 0x100

13604: e0233006 eor r3, r3, r6

13608: e7906103 ldr r6, [r0, r3, lsl #2]

1360c: e5816000 str r6, [r1]

13610: e5b16004 ldr r6, [r1, #4]!

13614: e7806103 str r6, [r0, r3, lsl #2]

13618: 1afffff5 bne 135f4 <mcrypt\_mutex\_register@@Base+0xee60>

1361c: e3a03000 mov r3, #0

13620: e580e404 str lr, [r0, #1028] ; 0x404

13624: e5805408 str r5, [r0, #1032] ; 0x408

13628: e580440c str r4, [r0, #1036] ; 0x40c

1362c: e580c410 str ip, [r0, #1040] ; 0x410

13630: e5803414 str r3, [r0, #1044] ; 0x414

13634: e1a00003 mov r0, r3

13638: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

1363c: e3e00000 mvn r0, #0

13640: e12fff1e bx lr

13644: 0000d3dc ldrdeq sp, [r0], -ip

13648: e3520000 cmp r2, #0

1364c: 012fff1e bxeq lr

13650: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

13654: e24dd00c sub sp, sp, #12

13658: e590e410 ldr lr, [r0, #1040] ; 0x410

1365c: e5906404 ldr r6, [r0, #1028] ; 0x404

13660: e58de004 str lr, [sp, #4]

13664: e5905408 ldr r5, [r0, #1032] ; 0x408

13668: e590440c ldr r4, [r0, #1036] ; 0x40c

1366c: da000027 ble 13710 <mcrypt\_mutex\_register@@Base+0xef7c>

13670: e2411001 sub r1, r1, #1

13674: e2807e41 add r7, r0, #1040 ; 0x410

13678: e0812002 add r2, r1, r2

1367c: e2877008 add r7, r7, #8

13680: e3a08000 mov r8, #0

13684: e590c414 ldr ip, [r0, #1044] ; 0x414

13688: e28d3008 add r3, sp, #8

1368c: e083c00c add ip, r3, ip

13690: e5f19001 ldrb r9, [r1, #1]!

13694: e55c3004 ldrb r3, [ip, #-4]

13698: e0233009 eor r3, r3, r9

1369c: e5c13000 strb r3, [r1]

136a0: e590c414 ldr ip, [r0, #1044] ; 0x414

136a4: e7c7300c strb r3, [r7, ip]

136a8: e5903414 ldr r3, [r0, #1044] ; 0x414

136ac: e2833001 add r3, r3, #1

136b0: e3530004 cmp r3, #4

136b4: 15803414 strne r3, [r0, #1044] ; 0x414

136b8: 1a000012 bne 13708 <mcrypt\_mutex\_register@@Base+0xef74>

136bc: e5903418 ldr r3, [r0, #1048] ; 0x418

136c0: e5808414 str r8, [r0, #1044] ; 0x414

136c4: e0866003 add r6, r6, r3

136c8: e6ef3076 uxtb r3, r6

136cc: e7903103 ldr r3, [r0, r3, lsl #2]

136d0: e0236426 eor r6, r3, r6, lsr #8

136d4: e0865005 add r5, r6, r5

136d8: e6ef3075 uxtb r3, r5

136dc: e7903103 ldr r3, [r0, r3, lsl #2]

136e0: e0235425 eor r5, r3, r5, lsr #8

136e4: e0854004 add r4, r5, r4

136e8: e6ef3074 uxtb r3, r4

136ec: e7903103 ldr r3, [r0, r3, lsl #2]

136f0: e0234424 eor r4, r3, r4, lsr #8

136f4: e084e00e add lr, r4, lr

136f8: e6ef307e uxtb r3, lr

136fc: e7903103 ldr r3, [r0, r3, lsl #2]

13700: e023e42e eor lr, r3, lr, lsr #8

13704: e58de004 str lr, [sp, #4]

13708: e1510002 cmp r1, r2

1370c: 1affffdc bne 13684 <mcrypt\_mutex\_register@@Base+0xeef0>

13710: e5806404 str r6, [r0, #1028] ; 0x404

13714: e5805408 str r5, [r0, #1032] ; 0x408

13718: e580440c str r4, [r0, #1036] ; 0x40c

1371c: e580e410 str lr, [r0, #1040] ; 0x410

13720: e28dd00c add sp, sp, #12

13724: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

13728: e3520000 cmp r2, #0

1372c: 012fff1e bxeq lr

13730: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

13734: e24dd00c sub sp, sp, #12

13738: e590e410 ldr lr, [r0, #1040] ; 0x410

1373c: e5906404 ldr r6, [r0, #1028] ; 0x404

13740: e58de004 str lr, [sp, #4]

13744: e5905408 ldr r5, [r0, #1032] ; 0x408

13748: e590440c ldr r4, [r0, #1036] ; 0x40c

1374c: da000028 ble 137f4 <mcrypt\_mutex\_register@@Base+0xf060>

13750: e2411001 sub r1, r1, #1

13754: e2807e41 add r7, r0, #1040 ; 0x410

13758: e0812002 add r2, r1, r2

1375c: e2877008 add r7, r7, #8

13760: e3a08000 mov r8, #0

13764: e5f1c001 ldrb ip, [r1, #1]!

13768: e5903414 ldr r3, [r0, #1044] ; 0x414

1376c: e7c7c003 strb ip, [r7, r3]

13770: e590c414 ldr ip, [r0, #1044] ; 0x414

13774: e28d3008 add r3, sp, #8

13778: e083c00c add ip, r3, ip

1377c: e5d19000 ldrb r9, [r1]

13780: e55c3004 ldrb r3, [ip, #-4]

13784: e0233009 eor r3, r3, r9

13788: e5c13000 strb r3, [r1]

1378c: e5903414 ldr r3, [r0, #1044] ; 0x414

13790: e2833001 add r3, r3, #1

13794: e3530004 cmp r3, #4

13798: 15803414 strne r3, [r0, #1044] ; 0x414

1379c: 1a000012 bne 137ec <mcrypt\_mutex\_register@@Base+0xf058>

137a0: e5903418 ldr r3, [r0, #1048] ; 0x418

137a4: e5808414 str r8, [r0, #1044] ; 0x414

137a8: e0866003 add r6, r6, r3

137ac: e6ef3076 uxtb r3, r6

137b0: e7903103 ldr r3, [r0, r3, lsl #2]

137b4: e0236426 eor r6, r3, r6, lsr #8

137b8: e0865005 add r5, r6, r5

137bc: e6ef3075 uxtb r3, r5

137c0: e7903103 ldr r3, [r0, r3, lsl #2]

137c4: e0235425 eor r5, r3, r5, lsr #8

137c8: e0854004 add r4, r5, r4

137cc: e6ef3074 uxtb r3, r4

137d0: e7903103 ldr r3, [r0, r3, lsl #2]

137d4: e0234424 eor r4, r3, r4, lsr #8

137d8: e084e00e add lr, r4, lr

137dc: e6ef307e uxtb r3, lr

137e0: e7903103 ldr r3, [r0, r3, lsl #2]

137e4: e023e42e eor lr, r3, lr, lsr #8

137e8: e58de004 str lr, [sp, #4]

137ec: e1510002 cmp r1, r2

137f0: 1affffdb bne 13764 <mcrypt\_mutex\_register@@Base+0xefd0>

137f4: e5806404 str r6, [r0, #1028] ; 0x404

137f8: e5805408 str r5, [r0, #1032] ; 0x408

137fc: e580440c str r4, [r0, #1036] ; 0x40c

13800: e580e410 str lr, [r0, #1040] ; 0x410

13804: e28dd00c add sp, sp, #12

13808: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

1380c: e59f0000 ldr r0, [pc] ; 13814 <mcrypt\_mutex\_register@@Base+0xf080>

13810: e12fff1e bx lr

13814: 00000444 andeq r0, r0, r4, asr #8

13818: e3a00001 mov r0, #1

1381c: e12fff1e bx lr

13820: e3a00000 mov r0, #0

13824: e12fff1e bx lr

13828: e3a00000 mov r0, #0

1382c: e12fff1e bx lr

13830: e3a00020 mov r0, #32

13834: e12fff1e bx lr

13838: e59f3010 ldr r3, [pc, #16] ; 13850 <mcrypt\_mutex\_register@@Base+0xf0bc>

1383c: e3a02001 mov r2, #1

13840: e08f3003 add r3, pc, r3

13844: e5802000 str r2, [r0]

13848: e2830028 add r0, r3, #40 ; 0x28

1384c: e12fff1e bx lr

13850: 0000d0ec andeq sp, r0, ip, ror #1

13854: e59f0004 ldr r0, [pc, #4] ; 13860 <mcrypt\_mutex\_register@@Base+0xf0cc>

13858: e08f0000 add r0, pc, r0

1385c: e12fff1e bx lr

13860: 0000d100 andeq sp, r0, r0, lsl #2

13864: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

13868: e24ddf4d sub sp, sp, #308 ; 0x134

1386c: ebffffef bl 13830 <mcrypt\_mutex\_register@@Base+0xf09c>

13870: e3a0400a mov r4, #10

13874: e1a01000 mov r1, r0

13878: e3a00001 mov r0, #1

1387c: ebffba6c bl 2234 <calloc@plt>

13880: e1a08000 mov r8, r0

13884: e2405001 sub r5, r0, #1

13888: e2606001 rsb r6, r0, #1

1388c: ea000001 b 13898 <mcrypt\_mutex\_register@@Base+0xf104>

13890: e5e54001 strb r4, [r5, #1]!

13894: e6ef4072 uxtb r4, r2

13898: ebffffe4 bl 13830 <mcrypt\_mutex\_register@@Base+0xf09c>

1389c: e0863005 add r3, r6, r5

138a0: e2842005 add r2, r4, #5

138a4: e1500003 cmp r0, r3

138a8: cafffff8 bgt 13890 <mcrypt\_mutex\_register@@Base+0xf0fc>

138ac: e28db010 add fp, sp, #16

138b0: e24b3001 sub r3, fp, #1

138b4: e2630005 rsb r0, r3, #5

138b8: e28d103a add r1, sp, #58 ; 0x3a

138bc: e0802003 add r2, r0, r3

138c0: e5e32001 strb r2, [r3, #1]!

138c4: e1530001 cmp r3, r1

138c8: 1afffffb bne 138bc <mcrypt\_mutex\_register@@Base+0xf128>

138cc: ebffffce bl 1380c <mcrypt\_mutex\_register@@Base+0xf078>

138d0: ebffba87 bl 22f4 <malloc@plt>

138d4: e28d5068 add r5, sp, #104 ; 0x68

138d8: e59f7164 ldr r7, [pc, #356] ; 13a44 <mcrypt\_mutex\_register@@Base+0xf2b0>

138dc: e28d403b add r4, sp, #59 ; 0x3b

138e0: e08f7007 add r7, pc, r7

138e4: e28d6066 add r6, sp, #102 ; 0x66

138e8: e1a0a000 mov sl, r0

138ec: ebffffc6 bl 1380c <mcrypt\_mutex\_register@@Base+0xf078>

138f0: ebffba7f bl 22f4 <malloc@plt>

138f4: e1a0e00b mov lr, fp

138f8: e28dc03c add ip, sp, #60 ; 0x3c

138fc: e58dc008 str ip, [sp, #8]

13900: e58d500c str r5, [sp, #12]

13904: e1a09000 mov r9, r0

13908: e8be000f ldm lr!, {r0, r1, r2, r3}

1390c: e8ac000f stmia ip!, {r0, r1, r2, r3}

13910: e8be000f ldm lr!, {r0, r1, r2, r3}

13914: e8ac000f stmia ip!, {r0, r1, r2, r3}

13918: e89e0007 ldm lr, {r0, r1, r2}

1391c: e1a03822 lsr r3, r2, #16

13920: e8ac0003 stmia ip!, {r0, r1}

13924: e0cc20b2 strh r2, [ip], #2

13928: e5cc3000 strb r3, [ip]

1392c: ebffffbf bl 13830 <mcrypt\_mutex\_register@@Base+0xf09c>

13930: e3a03000 mov r3, #0

13934: e58d3000 str r3, [sp]

13938: e1a01008 mov r1, r8

1393c: e1a02000 mov r2, r0

13940: e1a0000a mov r0, sl

13944: ebfffef5 bl 13520 <mcrypt\_mutex\_register@@Base+0xed8c>

13948: e1a0000a mov r0, sl

1394c: e59d1008 ldr r1, [sp, #8]

13950: e3a0202b mov r2, #43 ; 0x2b

13954: ebffff3b bl 13648 <mcrypt\_mutex\_register@@Base+0xeeb4>

13958: e1a0000a mov r0, sl

1395c: ebffba46 bl 227c <free@plt>

13960: e5f42001 ldrb r2, [r4, #1]!

13964: e1a00005 mov r0, r5

13968: e1a01007 mov r1, r7

1396c: ebffba7e bl 236c <sprintf@plt>

13970: e1540006 cmp r4, r6

13974: e2855002 add r5, r5, #2

13978: 1afffff8 bne 13960 <mcrypt\_mutex\_register@@Base+0xf1cc>

1397c: e59f50c4 ldr r5, [pc, #196] ; 13a48 <mcrypt\_mutex\_register@@Base+0xf2b4>

13980: e59d000c ldr r0, [sp, #12]

13984: e08f5005 add r5, pc, r5

13988: e1a01005 mov r1, r5

1398c: ebffba2e bl 224c <strcmp@plt>

13990: e2504000 subs r4, r0, #0

13994: 1a000017 bne 139f8 <mcrypt\_mutex\_register@@Base+0xf264>

13998: ebffffa4 bl 13830 <mcrypt\_mutex\_register@@Base+0xf09c>

1399c: e1a03004 mov r3, r4

139a0: e1a01008 mov r1, r8

139a4: e58d4000 str r4, [sp]

139a8: e1a02000 mov r2, r0

139ac: e1a00009 mov r0, r9

139b0: ebfffeda bl 13520 <mcrypt\_mutex\_register@@Base+0xed8c>

139b4: e1a00008 mov r0, r8

139b8: ebffba2f bl 227c <free@plt>

139bc: e59d4008 ldr r4, [sp, #8]

139c0: e3a0202b mov r2, #43 ; 0x2b

139c4: e1a01004 mov r1, r4

139c8: e1a00009 mov r0, r9

139cc: ebffff55 bl 13728 <mcrypt\_mutex\_register@@Base+0xef94>

139d0: e1a00009 mov r0, r9

139d4: ebffba28 bl 227c <free@plt>

139d8: e1a0100b mov r1, fp

139dc: e1a00004 mov r0, r4

139e0: e3a0202b mov r2, #43 ; 0x2b

139e4: ebffba2a bl 2294 <memcmp@plt>

139e8: e3500000 cmp r0, #0

139ec: 1a00000f bne 13a30 <mcrypt\_mutex\_register@@Base+0xf29c>

139f0: e28ddf4d add sp, sp, #308 ; 0x134

139f4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

139f8: e59f004c ldr r0, [pc, #76] ; 13a4c <mcrypt\_mutex\_register@@Base+0xf2b8>

139fc: e08f0000 add r0, pc, r0

13a00: ebffba38 bl 22e8 <puts@plt>

13a04: e59f0044 ldr r0, [pc, #68] ; 13a50 <mcrypt\_mutex\_register@@Base+0xf2bc>

13a08: e59d200c ldr r2, [sp, #12]

13a0c: e1a01005 mov r1, r5

13a10: e08f0000 add r0, pc, r0

13a14: ebffba12 bl 2264 <printf@plt>

13a18: e1a0000a mov r0, sl

13a1c: ebffba16 bl 227c <free@plt>

13a20: e1a00009 mov r0, r9

13a24: ebffba14 bl 227c <free@plt>

13a28: e3e00000 mvn r0, #0

13a2c: eaffffef b 139f0 <mcrypt\_mutex\_register@@Base+0xf25c>

13a30: e59f001c ldr r0, [pc, #28] ; 13a54 <mcrypt\_mutex\_register@@Base+0xf2c0>

13a34: e08f0000 add r0, pc, r0

13a38: ebffba2a bl 22e8 <puts@plt>

13a3c: e3e00000 mvn r0, #0

13a40: eaffffea b 139f0 <mcrypt\_mutex\_register@@Base+0xf25c>

13a44: 0000cd80 andeq ip, r0, r0, lsl #27

13a48: 0000cfdc ldrdeq ip, [r0], -ip ; <UNPREDICTABLE>

13a4c: 0000cc80 andeq ip, r0, r0, lsl #25

13a50: 0000cc84 andeq ip, r0, r4, lsl #25

13a54: 0000cc78 andeq ip, r0, r8, ror ip

13a58: e59f0000 ldr r0, [pc] ; 13a60 <mcrypt\_mutex\_register@@Base+0xf2cc>

13a5c: e12fff1e bx lr

13a60: 01315731 teqeq r1, r1, lsr r7

13a64: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

13a68: e280eeff add lr, r0, #4080 ; 0xff0

13a6c: e2806a01 add r6, r0, #4096 ; 0x1000

13a70: e8910018 ldm r1, {r3, r4}

13a74: e28ee00c add lr, lr, #12

13a78: e286503c add r5, r6, #60 ; 0x3c

13a7c: e5be2004 ldr r2, [lr, #4]!

13a80: e0222003 eor r2, r2, r3

13a84: e155000e cmp r5, lr

13a88: e1a03822 lsr r3, r2, #16

13a8c: e1a0c422 lsr ip, r2, #8

13a90: e6ef3073 uxtb r3, r3

13a94: e1a08c22 lsr r8, r2, #24

13a98: e2833c01 add r3, r3, #256 ; 0x100

13a9c: e6efc07c uxtb ip, ip

13aa0: e28ccc02 add ip, ip, #512 ; 0x200

13aa4: e6ef7072 uxtb r7, r2

13aa8: e7909108 ldr r9, [r0, r8, lsl #2]

13aac: e7903103 ldr r3, [r0, r3, lsl #2]

13ab0: e2877c03 add r7, r7, #768 ; 0x300

13ab4: e790810c ldr r8, [r0, ip, lsl #2]

13ab8: e0833009 add r3, r3, r9

13abc: e790c107 ldr ip, [r0, r7, lsl #2]

13ac0: e0233008 eor r3, r3, r8

13ac4: e083300c add r3, r3, ip

13ac8: e0233004 eor r3, r3, r4

13acc: e1a04002 mov r4, r2

13ad0: 1affffe9 bne 13a7c <mcrypt\_mutex\_register@@Base+0xf2e8>

13ad4: e5960040 ldr r0, [r6, #64] ; 0x40

13ad8: e5962044 ldr r2, [r6, #68] ; 0x44

13adc: e0233000 eor r3, r3, r0

13ae0: e0244002 eor r4, r4, r2

13ae4: e5813004 str r3, [r1, #4]

13ae8: e5814000 str r4, [r1]

13aec: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

13af0: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

13af4: e24ddd41 sub sp, sp, #4160 ; 0x1040

13af8: e59f4200 ldr r4, [pc, #512] ; 13d00 <mcrypt\_mutex\_register@@Base+0xf56c>

13afc: e24dd01c sub sp, sp, #28

13b00: e08f4004 add r4, pc, r4

13b04: e28d5058 add r5, sp, #88 ; 0x58

13b08: e28dae45 add sl, sp, #1104 ; 0x450

13b0c: e1a07000 mov r7, r0

13b10: e28aa008 add sl, sl, #8

13b14: e1a08001 mov r8, r1

13b18: e1a06002 mov r6, r2

13b1c: e1a01004 mov r1, r4

13b20: e3a02b01 mov r2, #1024 ; 0x400

13b24: e1a00005 mov r0, r5

13b28: e28d9e85 add r9, sp, #2128 ; 0x850

13b2c: ebffb9d5 bl 2288 <memcpy@plt>

13b30: e2899008 add r9, r9, #8

13b34: e2841b01 add r1, r4, #1024 ; 0x400

13b38: e3a02b01 mov r2, #1024 ; 0x400

13b3c: e1a0000a mov r0, sl

13b40: ebffb9d0 bl 2288 <memcpy@plt>

13b44: e2841b02 add r1, r4, #2048 ; 0x800

13b48: e3a02b01 mov r2, #1024 ; 0x400

13b4c: e1a00009 mov r0, r9

13b50: ebffb9cc bl 2288 <memcpy@plt>

13b54: e2841b03 add r1, r4, #3072 ; 0xc00

13b58: e28d4ec5 add r4, sp, #3152 ; 0xc50

13b5c: e2844008 add r4, r4, #8

13b60: e3a02b01 mov r2, #1024 ; 0x400

13b64: e1a00004 mov r0, r4

13b68: ebffb9c6 bl 2288 <memcpy@plt>

13b6c: e59f1190 ldr r1, [pc, #400] ; 13d04 <mcrypt\_mutex\_register@@Base+0xf570>

13b70: e3a02048 mov r2, #72 ; 0x48

13b74: e08f1001 add r1, pc, r1

13b78: e2411eff sub r1, r1, #4080 ; 0xff0

13b7c: e2411008 sub r1, r1, #8

13b80: e2450048 sub r0, r5, #72 ; 0x48

13b84: ebffb9bf bl 2288 <memcpy@plt>

13b88: e285bfff add fp, r5, #1020 ; 0x3fc

13b8c: e24ac004 sub ip, sl, #4

13b90: e2490004 sub r0, r9, #4

13b94: e2441004 sub r1, r4, #4

13b98: e2452004 sub r2, r5, #4

13b9c: e2473004 sub r3, r7, #4

13ba0: e5b2a004 ldr sl, [r2, #4]!

13ba4: e5bc9004 ldr r9, [ip, #4]!

13ba8: e5b04004 ldr r4, [r0, #4]!

13bac: e5b1e004 ldr lr, [r1, #4]!

13bb0: e152000b cmp r2, fp

13bb4: e5a3a004 str sl, [r3, #4]!

13bb8: e5839400 str r9, [r3, #1024] ; 0x400

13bbc: e5834800 str r4, [r3, #2048] ; 0x800

13bc0: e583ec00 str lr, [r3, #3072] ; 0xc00

13bc4: 1afffff5 bne 13ba0 <mcrypt\_mutex\_register@@Base+0xf40c>

13bc8: e287beff add fp, r7, #4080 ; 0xff0

13bcc: e28bb00c add fp, fp, #12

13bd0: e2873d41 add r3, r7, #4160 ; 0x1040

13bd4: e1a0100b mov r1, fp

13bd8: e28d0010 add r0, sp, #16

13bdc: e58d3004 str r3, [sp, #4]

13be0: e283a004 add sl, r3, #4

13be4: e2873a01 add r3, r7, #4096 ; 0x1000

13be8: e58d3000 str r3, [sp]

13bec: e490c004 ldr ip, [r0], #4

13bf0: e5a1c004 str ip, [r1, #4]!

13bf4: e15a0001 cmp sl, r1

13bf8: 1afffffb bne 13bec <mcrypt\_mutex\_register@@Base+0xf458>

13bfc: e3a09000 mov r9, #0

13c00: e1a00009 mov r0, r9

13c04: e1a01006 mov r1, r6

13c08: eb002044 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

13c0c: e2890001 add r0, r9, #1

13c10: e7d84001 ldrb r4, [r8, r1]

13c14: e1a01006 mov r1, r6

13c18: eb002040 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

13c1c: e2890002 add r0, r9, #2

13c20: e1a04404 lsl r4, r4, #8

13c24: e7d8c001 ldrb ip, [r8, r1]

13c28: e1a01006 mov r1, r6

13c2c: e184400c orr r4, r4, ip

13c30: eb00203a bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

13c34: e1a04404 lsl r4, r4, #8

13c38: e2890003 add r0, r9, #3

13c3c: e7d8c001 ldrb ip, [r8, r1]

13c40: e1a01006 mov r1, r6

13c44: e18c4004 orr r4, ip, r4

13c48: eb002034 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

13c4c: e1a04404 lsl r4, r4, #8

13c50: e5bbc004 ldr ip, [fp, #4]!

13c54: e2890004 add r0, r9, #4

13c58: e7d8e001 ldrb lr, [r8, r1]

13c5c: e1a01006 mov r1, r6

13c60: e18e4004 orr r4, lr, r4

13c64: e024400c eor r4, r4, ip

13c68: e58b4000 str r4, [fp]

13c6c: eb00202b bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

13c70: e15a000b cmp sl, fp

13c74: e6bf9071 sxth r9, r1

13c78: 1affffe0 bne 13c00 <mcrypt\_mutex\_register@@Base+0xf46c>

13c7c: e59d3004 ldr r3, [sp, #4]

13c80: e59da000 ldr sl, [sp]

13c84: e28d8008 add r8, sp, #8

13c88: e283b008 add fp, r3, #8

13c8c: e3a03000 mov r3, #0

13c90: e5053050 str r3, [r5, #-80] ; 0xffffffb0

13c94: e505304c str r3, [r5, #-76] ; 0xffffffb4

13c98: e1a01008 mov r1, r8

13c9c: e1a00007 mov r0, r7

13ca0: ebffff6f bl 13a64 <mcrypt\_mutex\_register@@Base+0xf2d0>

13ca4: e14525d0 ldrd r2, [r5, #-80] ; 0xffffffb0

13ca8: e28aa008 add sl, sl, #8

13cac: e14a20f8 strd r2, [sl, #-8]

13cb0: e15b000a cmp fp, sl

13cb4: 1afffff7 bne 13c98 <mcrypt\_mutex\_register@@Base+0xf504>

13cb8: e2876b01 add r6, r7, #1024 ; 0x400

13cbc: e2879b05 add r9, r7, #5120 ; 0x1400

13cc0: e2464b01 sub r4, r6, #1024 ; 0x400

13cc4: e1a01008 mov r1, r8

13cc8: e1a00007 mov r0, r7

13ccc: ebffff64 bl 13a64 <mcrypt\_mutex\_register@@Base+0xf2d0>

13cd0: e14525d0 ldrd r2, [r5, #-80] ; 0xffffffb0

13cd4: e2844008 add r4, r4, #8

13cd8: e14420f8 strd r2, [r4, #-8]

13cdc: e1560004 cmp r6, r4

13ce0: 1afffff7 bne 13cc4 <mcrypt\_mutex\_register@@Base+0xf530>

13ce4: e2866b01 add r6, r6, #1024 ; 0x400

13ce8: e1590006 cmp r9, r6

13cec: 1afffff3 bne 13cc0 <mcrypt\_mutex\_register@@Base+0xf52c>

13cf0: e3a00000 mov r0, #0

13cf4: e28ddd41 add sp, sp, #4160 ; 0x1040

13cf8: e28dd01c add sp, sp, #28

13cfc: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

13d00: 0000ceb8 ; <UNDEFINED> instruction: 0x0000ceb8

13d04: 0000ee3c andeq lr, r0, ip, lsr lr

13d08: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

13d0c: e280eeff add lr, r0, #4080 ; 0xff0

13d10: e2806a01 add r6, r0, #4096 ; 0x1000

13d14: e8910018 ldm r1, {r3, r4}

13d18: e28ee00c add lr, lr, #12

13d1c: e286503c add r5, r6, #60 ; 0x3c

13d20: e5be2004 ldr r2, [lr, #4]!

13d24: e0222003 eor r2, r2, r3

13d28: e155000e cmp r5, lr

13d2c: e1a03822 lsr r3, r2, #16

13d30: e1a0c422 lsr ip, r2, #8

13d34: e6ef3073 uxtb r3, r3

13d38: e1a08c22 lsr r8, r2, #24

13d3c: e2833c01 add r3, r3, #256 ; 0x100

13d40: e6efc07c uxtb ip, ip

13d44: e28ccc02 add ip, ip, #512 ; 0x200

13d48: e6ef7072 uxtb r7, r2

13d4c: e7909108 ldr r9, [r0, r8, lsl #2]

13d50: e7903103 ldr r3, [r0, r3, lsl #2]

13d54: e2877c03 add r7, r7, #768 ; 0x300

13d58: e790810c ldr r8, [r0, ip, lsl #2]

13d5c: e0833009 add r3, r3, r9

13d60: e790c107 ldr ip, [r0, r7, lsl #2]

13d64: e0233008 eor r3, r3, r8

13d68: e083300c add r3, r3, ip

13d6c: e0233004 eor r3, r3, r4

13d70: e1a04002 mov r4, r2

13d74: 1affffe9 bne 13d20 <mcrypt\_mutex\_register@@Base+0xf58c>

13d78: e5960040 ldr r0, [r6, #64] ; 0x40

13d7c: e5962044 ldr r2, [r6, #68] ; 0x44

13d80: e0233000 eor r3, r3, r0

13d84: e0244002 eor r4, r4, r2

13d88: e5813004 str r3, [r1, #4]

13d8c: e5814000 str r4, [r1]

13d90: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

13d94: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

13d98: e280ed41 add lr, r0, #4160 ; 0x1040

13d9c: e2806a01 add r6, r0, #4096 ; 0x1000

13da0: e8910018 ldm r1, {r3, r4}

13da4: e28ee008 add lr, lr, #8

13da8: e2865008 add r5, r6, #8

13dac: e53e2004 ldr r2, [lr, #-4]!

13db0: e0222003 eor r2, r2, r3

13db4: e155000e cmp r5, lr

13db8: e1a03822 lsr r3, r2, #16

13dbc: e1a0c422 lsr ip, r2, #8

13dc0: e6ef3073 uxtb r3, r3

13dc4: e1a08c22 lsr r8, r2, #24

13dc8: e2833c01 add r3, r3, #256 ; 0x100

13dcc: e6efc07c uxtb ip, ip

13dd0: e28ccc02 add ip, ip, #512 ; 0x200

13dd4: e6ef7072 uxtb r7, r2

13dd8: e7909108 ldr r9, [r0, r8, lsl #2]

13ddc: e7903103 ldr r3, [r0, r3, lsl #2]

13de0: e2877c03 add r7, r7, #768 ; 0x300

13de4: e790810c ldr r8, [r0, ip, lsl #2]

13de8: e0833009 add r3, r3, r9

13dec: e790c107 ldr ip, [r0, r7, lsl #2]

13df0: e0233008 eor r3, r3, r8

13df4: e083300c add r3, r3, ip

13df8: e0233004 eor r3, r3, r4

13dfc: e1a04002 mov r4, r2

13e00: 1affffe9 bne 13dac <mcrypt\_mutex\_register@@Base+0xf618>

13e04: e5960004 ldr r0, [r6, #4]

13e08: e5962000 ldr r2, [r6]

13e0c: e0233000 eor r3, r3, r0

13e10: e0244002 eor r4, r4, r2

13e14: e5813004 str r3, [r1, #4]

13e18: e5814000 str r4, [r1]

13e1c: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

13e20: e92d4010 push {r4, lr}

13e24: e6bf2072 sxth r2, r2

13e28: ebffff30 bl 13af0 <mcrypt\_mutex\_register@@Base+0xf35c>

13e2c: e3a00000 mov r0, #0

13e30: e8bd8010 pop {r4, pc}

13e34: e59f0000 ldr r0, [pc] ; 13e3c <mcrypt\_mutex\_register@@Base+0xf6a8>

13e38: e12fff1e bx lr

13e3c: 00001048 andeq r1, r0, r8, asr #32

13e40: e3a00008 mov r0, #8

13e44: e12fff1e bx lr

13e48: e3a00001 mov r0, #1

13e4c: e12fff1e bx lr

13e50: e3a00038 mov r0, #56 ; 0x38

13e54: e12fff1e bx lr

13e58: e3a03000 mov r3, #0

13e5c: e5803000 str r3, [r0]

13e60: e1a00003 mov r0, r3

13e64: e12fff1e bx lr

13e68: e59f0004 ldr r0, [pc, #4] ; 13e74 <mcrypt\_mutex\_register@@Base+0xf6e0>

13e6c: e08f0000 add r0, pc, r0

13e70: e12fff1e bx lr

13e74: 0000db94 muleq r0, r4, fp

13e78: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

13e7c: e24dd0e8 sub sp, sp, #232 ; 0xe8

13e80: ebffffee bl 13e40 <mcrypt\_mutex\_register@@Base+0xf6ac>

13e84: e1a06000 mov r6, r0

13e88: ebfffff0 bl 13e50 <mcrypt\_mutex\_register@@Base+0xf6bc>

13e8c: e1a01000 mov r1, r0

13e90: e3a00001 mov r0, #1

13e94: ebffb8e6 bl 2234 <calloc@plt>

13e98: e2507000 subs r7, r0, #0

13e9c: 12475001 subne r5, r7, #1

13ea0: 13a0400a movne r4, #10

13ea4: 12678001 rsbne r8, r7, #1

13ea8: 1a000005 bne 13ec4 <mcrypt\_mutex\_register@@Base+0xf730>

13eac: ea000060 b 14034 <mcrypt\_mutex\_register@@Base+0xf8a0>

13eb0: e2742000 rsbs r2, r4, #0

13eb4: e2844002 add r4, r4, #2

13eb8: e6ef2072 uxtb r2, r2

13ebc: 52623000 rsbpl r3, r2, #0

13ec0: e5e53001 strb r3, [r5, #1]!

13ec4: ebffffe1 bl 13e50 <mcrypt\_mutex\_register@@Base+0xf6bc>

13ec8: e0882005 add r2, r8, r5

13ecc: e6ef3074 uxtb r3, r4

13ed0: e1500002 cmp r0, r2

13ed4: cafffff5 bgt 13eb0 <mcrypt\_mutex\_register@@Base+0xf71c>

13ed8: e3560000 cmp r6, #0

13edc: da000039 ble 13fc8 <mcrypt\_mutex\_register@@Base+0xf834>

13ee0: e1a0400d mov r4, sp

13ee4: e1a02004 mov r2, r4

13ee8: e3a03000 mov r3, #0

13eec: e4c23001 strb r3, [r2], #1

13ef0: e2833001 add r3, r3, #1

13ef4: e1560003 cmp r6, r3

13ef8: 1afffffb bne 13eec <mcrypt\_mutex\_register@@Base+0xf758>

13efc: ebffffcc bl 13e34 <mcrypt\_mutex\_register@@Base+0xf6a0>

13f00: ebffb8fb bl 22f4 <malloc@plt>

13f04: e2509000 subs r9, r0, #0

13f08: 0a000049 beq 14034 <mcrypt\_mutex\_register@@Base+0xf8a0>

13f0c: e28d8010 add r8, sp, #16

13f10: e1a01004 mov r1, r4

13f14: e1a02006 mov r2, r6

13f18: e1a00008 mov r0, r8

13f1c: ebffb8d9 bl 2288 <memcpy@plt>

13f20: ebffffca bl 13e50 <mcrypt\_mutex\_register@@Base+0xf6bc>

13f24: e1a01007 mov r1, r7

13f28: e28da020 add sl, sp, #32

13f2c: e0886006 add r6, r8, r6

13f30: e1a05008 mov r5, r8

13f34: e1a0400a mov r4, sl

13f38: e1a02000 mov r2, r0

13f3c: e1a00009 mov r0, r9

13f40: ebffffb6 bl 13e20 <mcrypt\_mutex\_register@@Base+0xf68c>

13f44: e1a00007 mov r0, r7

13f48: ebffb8cb bl 227c <free@plt>

13f4c: e1a01008 mov r1, r8

13f50: e1a00009 mov r0, r9

13f54: ebffff6b bl 13d08 <mcrypt\_mutex\_register@@Base+0xf574>

13f58: e59f70f0 ldr r7, [pc, #240] ; 14050 <mcrypt\_mutex\_register@@Base+0xf8bc>

13f5c: e08f7007 add r7, pc, r7

13f60: e4d52001 ldrb r2, [r5], #1

13f64: e1a00004 mov r0, r4

13f68: e1a01007 mov r1, r7

13f6c: ebffb8fe bl 236c <sprintf@plt>

13f70: e1560005 cmp r6, r5

13f74: e2844002 add r4, r4, #2

13f78: 1afffff8 bne 13f60 <mcrypt\_mutex\_register@@Base+0xf7cc>

13f7c: e59f40d0 ldr r4, [pc, #208] ; 14054 <mcrypt\_mutex\_register@@Base+0xf8c0>

13f80: e1a0000a mov r0, sl

13f84: e08f4004 add r4, pc, r4

13f88: e1a01004 mov r1, r4

13f8c: ebffb8ae bl 224c <strcmp@plt>

13f90: e3500000 cmp r0, #0

13f94: 1a00001c bne 1400c <mcrypt\_mutex\_register@@Base+0xf878>

13f98: e1a01008 mov r1, r8

13f9c: e1a00009 mov r0, r9

13fa0: ebffff7b bl 13d94 <mcrypt\_mutex\_register@@Base+0xf600>

13fa4: e1a00009 mov r0, r9

13fa8: ebffb8b3 bl 227c <free@plt>

13fac: e1a00008 mov r0, r8

13fb0: e1a0100d mov r1, sp

13fb4: ebffb8a4 bl 224c <strcmp@plt>

13fb8: e3500000 cmp r0, #0

13fbc: 1a00001e bne 1403c <mcrypt\_mutex\_register@@Base+0xf8a8>

13fc0: e28dd0e8 add sp, sp, #232 ; 0xe8

13fc4: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

13fc8: ebffff99 bl 13e34 <mcrypt\_mutex\_register@@Base+0xf6a0>

13fcc: ebffb8c8 bl 22f4 <malloc@plt>

13fd0: e2509000 subs r9, r0, #0

13fd4: 0a000016 beq 14034 <mcrypt\_mutex\_register@@Base+0xf8a0>

13fd8: ebffff9c bl 13e50 <mcrypt\_mutex\_register@@Base+0xf6bc>

13fdc: e1a01007 mov r1, r7

13fe0: e28d8010 add r8, sp, #16

13fe4: e28da020 add sl, sp, #32

13fe8: e1a02000 mov r2, r0

13fec: e1a00009 mov r0, r9

13ff0: ebffff8a bl 13e20 <mcrypt\_mutex\_register@@Base+0xf68c>

13ff4: e1a00007 mov r0, r7

13ff8: ebffb89f bl 227c <free@plt>

13ffc: e1a01008 mov r1, r8

14000: e1a00009 mov r0, r9

14004: ebffff3f bl 13d08 <mcrypt\_mutex\_register@@Base+0xf574>

14008: eaffffdb b 13f7c <mcrypt\_mutex\_register@@Base+0xf7e8>

1400c: e59f0044 ldr r0, [pc, #68] ; 14058 <mcrypt\_mutex\_register@@Base+0xf8c4>

14010: e08f0000 add r0, pc, r0

14014: ebffb8b3 bl 22e8 <puts@plt>

14018: e59f003c ldr r0, [pc, #60] ; 1405c <mcrypt\_mutex\_register@@Base+0xf8c8>

1401c: e1a0200a mov r2, sl

14020: e1a01004 mov r1, r4

14024: e08f0000 add r0, pc, r0

14028: ebffb88d bl 2264 <printf@plt>

1402c: e1a00009 mov r0, r9

14030: ebffb891 bl 227c <free@plt>

14034: e3e00000 mvn r0, #0

14038: eaffffe0 b 13fc0 <mcrypt\_mutex\_register@@Base+0xf82c>

1403c: e59f001c ldr r0, [pc, #28] ; 14060 <mcrypt\_mutex\_register@@Base+0xf8cc>

14040: e08f0000 add r0, pc, r0

14044: ebffb8a7 bl 22e8 <puts@plt>

14048: e3e00000 mvn r0, #0

1404c: eaffffdb b 13fc0 <mcrypt\_mutex\_register@@Base+0xf82c>

14050: 0000c704 andeq ip, r0, r4, lsl #14

14054: 0000da88 andeq sp, r0, r8, lsl #21

14058: 0000c66c andeq ip, r0, ip, ror #12

1405c: 0000c670 andeq ip, r0, r0, ror r6

14060: 0000c66c andeq ip, r0, ip, ror #12

14064: e59f0000 ldr r0, [pc] ; 1406c <mcrypt\_mutex\_register@@Base+0xf8d8>

14068: e12fff1e bx lr

1406c: 01315731 teqeq r1, r1, lsr r7

14070: e92d40f0 push {r4, r5, r6, r7, lr}

14074: e5d2c007 ldrb ip, [r2, #7]

14078: e5d23006 ldrb r3, [r2, #6]

1407c: e5d2e005 ldrb lr, [r2, #5]

14080: e02ccfe1 eor ip, ip, r1, ror #31

14084: e02331a1 eor r3, r3, r1, lsr #3

14088: e5d25004 ldrb r5, [r2, #4]

1408c: e02ee3a1 eor lr, lr, r1, lsr #7

14090: e20cc03f and ip, ip, #63 ; 0x3f

14094: e203303f and r3, r3, #63 ; 0x3f

14098: e20e403f and r4, lr, #63 ; 0x3f

1409c: e0803103 add r3, r0, r3, lsl #2

140a0: e080c10c add ip, r0, ip, lsl #2

140a4: e02555a1 eor r5, r5, r1, lsr #11

140a8: e5d2e003 ldrb lr, [r2, #3]

140ac: e0804104 add r4, r0, r4, lsl #2

140b0: e5937680 ldr r7, [r3, #1664] ; 0x680

140b4: e205603f and r6, r5, #63 ; 0x3f

140b8: e59cc780 ldr ip, [ip, #1920] ; 0x780

140bc: e5d25002 ldrb r5, [r2, #2]

140c0: e5943580 ldr r3, [r4, #1408] ; 0x580

140c4: e5d24001 ldrb r4, [r2, #1]

140c8: e02ee7a1 eor lr, lr, r1, lsr #15

140cc: e18cc007 orr ip, ip, r7

140d0: e5d27000 ldrb r7, [r2]

140d4: e1a02281 lsl r2, r1, #5

140d8: e02559a1 eor r5, r5, r1, lsr #19

140dc: e20ee03f and lr, lr, #63 ; 0x3f

140e0: e2022020 and r2, r2, #32

140e4: e0806106 add r6, r0, r6, lsl #2

140e8: e0244ba1 eor r4, r4, r1, lsr #23

140ec: e1822da1 orr r2, r2, r1, lsr #27

140f0: e205503f and r5, r5, #63 ; 0x3f

140f4: e080e10e add lr, r0, lr, lsl #2

140f8: e204403f and r4, r4, #63 ; 0x3f

140fc: e0222007 eor r2, r2, r7

14100: e0805105 add r5, r0, r5, lsl #2

14104: e5966480 ldr r6, [r6, #1152] ; 0x480

14108: e202203f and r2, r2, #63 ; 0x3f

1410c: e0801104 add r1, r0, r4, lsl #2

14110: e18cc003 orr ip, ip, r3

14114: e59ee380 ldr lr, [lr, #896] ; 0x380

14118: e18c3006 orr r3, ip, r6

1411c: e0800102 add r0, r0, r2, lsl #2

14120: e595c280 ldr ip, [r5, #640] ; 0x280

14124: e183300e orr r3, r3, lr

14128: e5912180 ldr r2, [r1, #384] ; 0x180

1412c: e183300c orr r3, r3, ip

14130: e5900080 ldr r0, [r0, #128] ; 0x80

14134: e1833002 orr r3, r3, r2

14138: e1830000 orr r0, r3, r0

1413c: e8bd80f0 pop {r4, r5, r6, r7, pc}

14140: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

14144: e3a0a000 mov sl, #0

14148: e24dd00c sub sp, sp, #12

1414c: e1a0900a mov r9, sl

14150: e1a0800a mov r8, sl

14154: e1a0700a mov r7, sl

14158: e1a0600a mov r6, sl

1415c: e1a0500a mov r5, sl

14160: e1a0400a mov r4, sl

14164: e3a0c010 mov ip, #16

14168: e2403001 sub r3, r0, #1

1416c: e58d3004 str r3, [sp, #4]

14170: e58da000 str sl, [sp]

14174: e582a000 str sl, [r2]

14178: e582a004 str sl, [r2, #4]

1417c: e59d0004 ldr r0, [sp, #4]

14180: e5f03001 ldrb r3, [r0, #1]!

14184: e58d0004 str r0, [sp, #4]

14188: e203000f and r0, r3, #15

1418c: e2800e11 add r0, r0, #272 ; 0x110

14190: e08c3223 add r3, ip, r3, lsr #4

14194: e080000c add r0, r0, ip

14198: e2833c01 add r3, r3, #256 ; 0x100

1419c: e7d1b180 ldrb fp, [r1, r0, lsl #3]

141a0: e7d1e183 ldrb lr, [r1, r3, lsl #3]

141a4: e0810180 add r0, r1, r0, lsl #3

141a8: e0813183 add r3, r1, r3, lsl #3

141ac: e18ee00b orr lr, lr, fp

141b0: e18ea00a orr sl, lr, sl

141b4: e5c2a000 strb sl, [r2]

141b8: e5d0b001 ldrb fp, [r0, #1]

141bc: e5d3e001 ldrb lr, [r3, #1]

141c0: e28cc020 add ip, ip, #32

141c4: e35c0e11 cmp ip, #272 ; 0x110

141c8: e18ee00b orr lr, lr, fp

141cc: e18e9009 orr r9, lr, r9

141d0: e5c29001 strb r9, [r2, #1]

141d4: e5d0b002 ldrb fp, [r0, #2]

141d8: e5d3e002 ldrb lr, [r3, #2]

141dc: e18ee00b orr lr, lr, fp

141e0: e18e8008 orr r8, lr, r8

141e4: e5c28002 strb r8, [r2, #2]

141e8: e5d0b003 ldrb fp, [r0, #3]

141ec: e5d3e003 ldrb lr, [r3, #3]

141f0: e18ee00b orr lr, lr, fp

141f4: e18e7007 orr r7, lr, r7

141f8: e5c27003 strb r7, [r2, #3]

141fc: e5d0b004 ldrb fp, [r0, #4]

14200: e5d3e004 ldrb lr, [r3, #4]

14204: e18ee00b orr lr, lr, fp

14208: e18e6006 orr r6, lr, r6

1420c: e5c26004 strb r6, [r2, #4]

14210: e5d0b005 ldrb fp, [r0, #5]

14214: e5d3e005 ldrb lr, [r3, #5]

14218: e18ee00b orr lr, lr, fp

1421c: e18e5005 orr r5, lr, r5

14220: e5c25005 strb r5, [r2, #5]

14224: e5d3e006 ldrb lr, [r3, #6]

14228: e5d0b006 ldrb fp, [r0, #6]

1422c: e18ee00b orr lr, lr, fp

14230: e18e4004 orr r4, lr, r4

14234: e5c24006 strb r4, [r2, #6]

14238: e5d00007 ldrb r0, [r0, #7]

1423c: e5d33007 ldrb r3, [r3, #7]

14240: e1833000 orr r3, r3, r0

14244: e59d0000 ldr r0, [sp]

14248: e1833000 orr r3, r3, r0

1424c: e58d3000 str r3, [sp]

14250: e5c23007 strb r3, [r2, #7]

14254: 1affffc8 bne 1417c <mcrypt\_mutex\_register@@Base+0xf9e8>

14258: e28dd00c add sp, sp, #12

1425c: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

14260: e3a03000 mov r3, #0

14264: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

14268: e24dd00c sub sp, sp, #12

1426c: e5823000 str r3, [r2]

14270: e5823004 str r3, [r2, #4]

14274: e5d29000 ldrb r9, [r2]

14278: e5d23007 ldrb r3, [r2, #7]

1427c: e5d28001 ldrb r8, [r2, #1]

14280: e5d27002 ldrb r7, [r2, #2]

14284: e5d26003 ldrb r6, [r2, #3]

14288: e5d25004 ldrb r5, [r2, #4]

1428c: e5d24005 ldrb r4, [r2, #5]

14290: e5d2e006 ldrb lr, [r2, #6]

14294: e3a0c010 mov ip, #16

14298: e58d3000 str r3, [sp]

1429c: e2403001 sub r3, r0, #1

142a0: e58d3004 str r3, [sp, #4]

142a4: e59d0004 ldr r0, [sp, #4]

142a8: e5f03001 ldrb r3, [r0, #1]!

142ac: e58d0004 str r0, [sp, #4]

142b0: e203000f and r0, r3, #15

142b4: e2800e21 add r0, r0, #528 ; 0x210

142b8: e08c3223 add r3, ip, r3, lsr #4

142bc: e080000c add r0, r0, ip

142c0: e2833c02 add r3, r3, #512 ; 0x200

142c4: e7d1b180 ldrb fp, [r1, r0, lsl #3]

142c8: e7d1a183 ldrb sl, [r1, r3, lsl #3]

142cc: e0810180 add r0, r1, r0, lsl #3

142d0: e0813183 add r3, r1, r3, lsl #3

142d4: e18aa00b orr sl, sl, fp

142d8: e18a9009 orr r9, sl, r9

142dc: e5c29000 strb r9, [r2]

142e0: e5d0b001 ldrb fp, [r0, #1]

142e4: e5d3a001 ldrb sl, [r3, #1]

142e8: e28cc020 add ip, ip, #32

142ec: e35c0e11 cmp ip, #272 ; 0x110

142f0: e18aa00b orr sl, sl, fp

142f4: e18a8008 orr r8, sl, r8

142f8: e5c28001 strb r8, [r2, #1]

142fc: e5d0b002 ldrb fp, [r0, #2]

14300: e5d3a002 ldrb sl, [r3, #2]

14304: e18aa00b orr sl, sl, fp

14308: e18a7007 orr r7, sl, r7

1430c: e5c27002 strb r7, [r2, #2]

14310: e5d0b003 ldrb fp, [r0, #3]

14314: e5d3a003 ldrb sl, [r3, #3]

14318: e18aa00b orr sl, sl, fp

1431c: e18a6006 orr r6, sl, r6

14320: e5c26003 strb r6, [r2, #3]

14324: e5d0b004 ldrb fp, [r0, #4]

14328: e5d3a004 ldrb sl, [r3, #4]

1432c: e18aa00b orr sl, sl, fp

14330: e18a5005 orr r5, sl, r5

14334: e5c25004 strb r5, [r2, #4]

14338: e5d0b005 ldrb fp, [r0, #5]

1433c: e5d3a005 ldrb sl, [r3, #5]

14340: e18aa00b orr sl, sl, fp

14344: e18a4004 orr r4, sl, r4

14348: e5c24005 strb r4, [r2, #5]

1434c: e5d3a006 ldrb sl, [r3, #6]

14350: e5d0b006 ldrb fp, [r0, #6]

14354: e18aa00b orr sl, sl, fp

14358: e18ae00e orr lr, sl, lr

1435c: e5c2e006 strb lr, [r2, #6]

14360: e5d00007 ldrb r0, [r0, #7]

14364: e5d33007 ldrb r3, [r3, #7]

14368: e1833000 orr r3, r3, r0

1436c: e59d0000 ldr r0, [sp]

14370: e1833000 orr r3, r3, r0

14374: e58d3000 str r3, [sp]

14378: e5c23007 strb r3, [r2, #7]

1437c: 1affffc8 bne 142a4 <mcrypt\_mutex\_register@@Base+0xfb10>

14380: e28dd00c add sp, sp, #12

14384: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

14388: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1438c: e24dd084 sub sp, sp, #132 ; 0x84

14390: e28d3048 add r3, sp, #72 ; 0x48

14394: e1a04003 mov r4, r3

14398: e1a06001 mov r6, r1

1439c: e3a02d62 mov r2, #6272 ; 0x1880

143a0: e3a01000 mov r1, #0

143a4: e58d3008 str r3, [sp, #8]

143a8: e1a05000 mov r5, r0

143ac: ebffb7e5 bl 2348 <memset@plt>

143b0: e1a0c004 mov ip, r4

143b4: e3a00000 mov r0, #0

143b8: e59f13bc ldr r1, [pc, #956] ; 1477c <mcrypt\_mutex\_register@@Base+0xffe8>

143bc: e3a03010 mov r3, #16

143c0: e08f1001 add r1, pc, r1

143c4: e3a02000 mov r2, #0

143c8: ea000003 b 143dc <mcrypt\_mutex\_register@@Base+0xfc48>

143cc: e2822001 add r2, r2, #1

143d0: e3520020 cmp r2, #32

143d4: 0a000004 beq 143ec <mcrypt\_mutex\_register@@Base+0xfc58>

143d8: e5f13001 ldrb r3, [r1, #1]!

143dc: e2433001 sub r3, r3, #1

143e0: e1530000 cmp r3, r0

143e4: 1afffff8 bne 143cc <mcrypt\_mutex\_register@@Base+0xfc38>

143e8: e5cc2000 strb r2, [ip]

143ec: e2800001 add r0, r0, #1

143f0: e3500020 cmp r0, #32

143f4: e28cc001 add ip, ip, #1

143f8: 1affffee bne 143b8 <mcrypt\_mutex\_register@@Base+0xfc24>

143fc: e59f337c ldr r3, [pc, #892] ; 14780 <mcrypt\_mutex\_register@@Base+0xffec>

14400: e59f937c ldr r9, [pc, #892] ; 14784 <mcrypt\_mutex\_register@@Base+0xfff0>

14404: e08f3003 add r3, pc, r3

14408: e2833020 add r3, r3, #32

1440c: e58d600c str r6, [sp, #12]

14410: e285b07c add fp, r5, #124 ; 0x7c

14414: e3a0c00e mov ip, #14

14418: e3a0a000 mov sl, #0

1441c: e3a08008 mov r8, #8

14420: e3a07102 mov r7, #-2147483648 ; 0x80000000

14424: e08f9009 add r9, pc, r9

14428: e1a06003 mov r6, r3

1442c: e58d5004 str r5, [sp, #4]

14430: e1a0e00b mov lr, fp

14434: e3a01000 mov r1, #0

14438: e089530a add r5, r9, sl, lsl #6

1443c: e3a02000 mov r2, #0

14440: e1a03002 mov r3, r2

14444: e01c0358 ands r0, ip, r8, asr r3

14448: 17d40003 ldrbne r0, [r4, r3]

1444c: e2833001 add r3, r3, #1

14450: 11822037 orrne r2, r2, r7, lsr r0

14454: e3530004 cmp r3, #4

14458: 1afffff9 bne 14444 <mcrypt\_mutex\_register@@Base+0xfcb0>

1445c: e2811001 add r1, r1, #1

14460: e3510040 cmp r1, #64 ; 0x40

14464: e5ae2004 str r2, [lr, #4]!

14468: 0a000008 beq 14490 <mcrypt\_mutex\_register@@Base+0xfcfc>

1446c: e1a00d81 lsl r0, r1, #27

14470: e2012020 and r2, r1, #32

14474: e1a03201 lsl r3, r1, #4

14478: e1822e20 orr r2, r2, r0, lsr #28

1447c: e2033010 and r3, r3, #16

14480: e1823003 orr r3, r2, r3

14484: e0853003 add r3, r5, r3

14488: e5d3c020 ldrb ip, [r3, #32]

1448c: eaffffea b 1443c <mcrypt\_mutex\_register@@Base+0xfca8>

14490: e28aa001 add sl, sl, #1

14494: e35a0008 cmp sl, #8

14498: e2844004 add r4, r4, #4

1449c: e28bbc01 add fp, fp, #256 ; 0x100

144a0: 17d6c30a ldrbne ip, [r6, sl, lsl #6]

144a4: 1affffe1 bne 14430 <mcrypt\_mutex\_register@@Base+0xfc9c>

144a8: e59d5004 ldr r5, [sp, #4]

144ac: e59f82d4 ldr r8, [pc, #724] ; 14788 <mcrypt\_mutex\_register@@Base+0xfff4>

144b0: e3a02b02 mov r2, #2048 ; 0x800

144b4: e3a01000 mov r1, #0

144b8: e2850d22 add r0, r5, #2176 ; 0x880

144bc: e08f8008 add r8, pc, r8

144c0: e59d600c ldr r6, [sp, #12]

144c4: ebffb79f bl 2348 <memset@plt>

144c8: e59fe2bc ldr lr, [pc, #700] ; 1478c <mcrypt\_mutex\_register@@Base+0xfff8>

144cc: e288be25 add fp, r8, #592 ; 0x250

144d0: e3a07000 mov r7, #0

144d4: e2888e29 add r8, r8, #656 ; 0x290

144d8: e1e0900b mvn r9, fp

144dc: e08fe00e add lr, pc, lr

144e0: e3a00000 mov r0, #0

144e4: e1a0c207 lsl ip, r7, #4

144e8: e08c1000 add r1, ip, r0

144ec: e28b2001 add r2, fp, #1

144f0: e0851181 add r1, r5, r1, lsl #3

144f4: e3a0303a mov r3, #58 ; 0x3a

144f8: ea000000 b 14500 <mcrypt\_mutex\_register@@Base+0xfd6c>

144fc: e4d23001 ldrb r3, [r2], #1

14500: e2433001 sub r3, r3, #1

14504: e203a003 and sl, r3, #3

14508: e1570143 cmp r7, r3, asr #2

1450c: e08ea10a add sl, lr, sl, lsl #2

14510: e0893002 add r3, r9, r2

14514: 1a000008 bne 1453c <mcrypt\_mutex\_register@@Base+0xfda8>

14518: e59aa220 ldr sl, [sl, #544] ; 0x220

1451c: e2034007 and r4, r3, #7

14520: e110000a tst r0, sl

14524: e08131c3 add r3, r1, r3, asr #3

14528: e08e4104 add r4, lr, r4, lsl #2

1452c: 15d3a880 ldrbne sl, [r3, #2176] ; 0x880

14530: 15944230 ldrne r4, [r4, #560] ; 0x230

14534: 1184400a orrne r4, r4, sl

14538: 15c34880 strbne r4, [r3, #2176] ; 0x880

1453c: e1580002 cmp r8, r2

14540: 1affffed bne 144fc <mcrypt\_mutex\_register@@Base+0xfd68>

14544: e2800001 add r0, r0, #1

14548: e3500010 cmp r0, #16

1454c: 1affffe5 bne 144e8 <mcrypt\_mutex\_register@@Base+0xfd54>

14550: e2877001 add r7, r7, #1

14554: e3570010 cmp r7, #16

14558: 1affffe0 bne 144e0 <mcrypt\_mutex\_register@@Base+0xfd4c>

1455c: e3a02b02 mov r2, #2048 ; 0x800

14560: e3a01000 mov r1, #0

14564: e2850d42 add r0, r5, #4224 ; 0x1080

14568: ebffb776 bl 2348 <memset@plt>

1456c: e59f821c ldr r8, [pc, #540] ; 14790 <mcrypt\_mutex\_register@@Base+0xfffc>

14570: e59f421c ldr r4, [pc, #540] ; 14794 <mcrypt\_mutex\_register@@Base+0x10000>

14574: e08f8008 add r8, pc, r8

14578: e288be29 add fp, r8, #656 ; 0x290

1457c: e3a07000 mov r7, #0

14580: e2888e2d add r8, r8, #720 ; 0x2d0

14584: e1e0900b mvn r9, fp

14588: e08f4004 add r4, pc, r4

1458c: e3a00000 mov r0, #0

14590: e1a0c207 lsl ip, r7, #4

14594: e08c1000 add r1, ip, r0

14598: e28b2001 add r2, fp, #1

1459c: e0851181 add r1, r5, r1, lsl #3

145a0: e3a03028 mov r3, #40 ; 0x28

145a4: ea000000 b 145ac <mcrypt\_mutex\_register@@Base+0xfe18>

145a8: e4d23001 ldrb r3, [r2], #1

145ac: e2433001 sub r3, r3, #1

145b0: e203a003 and sl, r3, #3

145b4: e1570143 cmp r7, r3, asr #2

145b8: e084a10a add sl, r4, sl, lsl #2

145bc: e0893002 add r3, r9, r2

145c0: 1a000009 bne 145ec <mcrypt\_mutex\_register@@Base+0xfe58>

145c4: e59aa220 ldr sl, [sl, #544] ; 0x220

145c8: e081e1c3 add lr, r1, r3, asr #3

145cc: e2033007 and r3, r3, #7

145d0: e110000a tst r0, sl

145d4: e28eed42 add lr, lr, #4224 ; 0x1080

145d8: e0843103 add r3, r4, r3, lsl #2

145dc: 15dea000 ldrbne sl, [lr]

145e0: 15933230 ldrne r3, [r3, #560] ; 0x230

145e4: 1183300a orrne r3, r3, sl

145e8: 15ce3000 strbne r3, [lr]

145ec: e1580002 cmp r8, r2

145f0: 1affffec bne 145a8 <mcrypt\_mutex\_register@@Base+0xfe14>

145f4: e2800001 add r0, r0, #1

145f8: e3500010 cmp r0, #16

145fc: 1affffe4 bne 14594 <mcrypt\_mutex\_register@@Base+0xfe00>

14600: e2877001 add r7, r7, #1

14604: e3570010 cmp r7, #16

14608: 1affffdf bne 1458c <mcrypt\_mutex\_register@@Base+0xfdf8>

1460c: e59f0184 ldr r0, [pc, #388] ; 14798 <mcrypt\_mutex\_register@@Base+0x10004>

14610: e59fe184 ldr lr, [pc, #388] ; 1479c <mcrypt\_mutex\_register@@Base+0x10008>

14614: e08f0000 add r0, pc, r0

14618: e28d100f add r1, sp, #15

1461c: e2800e2d add r0, r0, #720 ; 0x2d0

14620: e28dc047 add ip, sp, #71 ; 0x47

14624: e3a02080 mov r2, #128 ; 0x80

14628: e3a03038 mov r3, #56 ; 0x38

1462c: e08fe00e add lr, pc, lr

14630: ea000004 b 14648 <mcrypt\_mutex\_register@@Base+0xfeb4>

14634: e5f03001 ldrb r3, [r0, #1]!

14638: e2433001 sub r3, r3, #1

1463c: e2032007 and r2, r3, #7

14640: e08e2102 add r2, lr, r2, lsl #2

14644: e5922230 ldr r2, [r2, #560] ; 0x230

14648: e7d631c3 ldrb r3, [r6, r3, asr #3]

1464c: e1130002 tst r3, r2

14650: 13a03001 movne r3, #1

14654: 03a03000 moveq r3, #0

14658: e5e13001 strb r3, [r1, #1]!

1465c: e15c0001 cmp ip, r1

14660: 1afffff3 bne 14634 <mcrypt\_mutex\_register@@Base+0xfea0>

14664: e59f8134 ldr r8, [pc, #308] ; 147a0 <mcrypt\_mutex\_register@@Base+0x1000c>

14668: e59f6134 ldr r6, [pc, #308] ; 147a4 <mcrypt\_mutex\_register@@Base+0x10010>

1466c: e08f8008 add r8, pc, r8

14670: e2889fc6 add r9, r8, #792 ; 0x318

14674: e59fe12c ldr lr, [pc, #300] ; 147a8 <mcrypt\_mutex\_register@@Base+0x10014>

14678: e1a0a008 mov sl, r8

1467c: e3a03001 mov r3, #1

14680: e2888fc2 add r8, r8, #776 ; 0x308

14684: e1e04009 mvn r4, r9

14688: e3a07000 mov r7, #0

1468c: e08f6006 add r6, pc, r6

14690: e28d2010 add r2, sp, #16

14694: e58d200c str r2, [sp, #12]

14698: e59d2008 ldr r2, [sp, #8]

1469c: e0420003 sub r0, r2, r3

146a0: e59d200c ldr r2, [sp, #12]

146a4: e082c003 add ip, r2, r3

146a8: e3a02000 mov r2, #0

146ac: ea00000a b 146dc <mcrypt\_mutex\_register@@Base+0xff48>

146b0: e3530037 cmp r3, #55 ; 0x37

146b4: da000000 ble 146bc <mcrypt\_mutex\_register@@Base+0xff28>

146b8: e243101c sub r1, r3, #28

146bc: e28db080 add fp, sp, #128 ; 0x80

146c0: e08b1001 add r1, fp, r1

146c4: e3520037 cmp r2, #55 ; 0x37

146c8: e5511070 ldrb r1, [r1, #-112] ; 0xffffff90

146cc: e7c01003 strb r1, [r0, r3]

146d0: 0a000009 beq 146fc <mcrypt\_mutex\_register@@Base+0xff68>

146d4: e2822001 add r2, r2, #1

146d8: e2833001 add r3, r3, #1

146dc: e352001b cmp r2, #27

146e0: e1a01003 mov r1, r3

146e4: 8afffff1 bhi 146b0 <mcrypt\_mutex\_register@@Base+0xff1c>

146e8: e353001b cmp r3, #27

146ec: cafffff1 bgt 146b8 <mcrypt\_mutex\_register@@Base+0xff24>

146f0: e7dc1002 ldrb r1, [ip, r2]

146f4: e7c01003 strb r1, [r0, r3]

146f8: eafffff5 b 146d4 <mcrypt\_mutex\_register@@Base+0xff40>

146fc: e2892001 add r2, r9, #1

14700: e28acfd2 add ip, sl, #840 ; 0x348

14704: e3a0300e mov r3, #14

14708: e0850187 add r0, r5, r7, lsl #3

1470c: e58d5004 str r5, [sp, #4]

14710: ea000000 b 14718 <mcrypt\_mutex\_register@@Base+0xff84>

14714: e4d23001 ldrb r3, [r2], #1

14718: e28d1080 add r1, sp, #128 ; 0x80

1471c: e0813003 add r3, r1, r3

14720: e0841002 add r1, r4, r2

14724: e553b039 ldrb fp, [r3, #-57] ; 0xffffffc7

14728: e0c3519e smull r5, r3, lr, r1

1472c: e0433fc1 sub r3, r3, r1, asr #31

14730: e35b0000 cmp fp, #0

14734: e083b083 add fp, r3, r3, lsl #1

14738: 0a000005 beq 14754 <mcrypt\_mutex\_register@@Base+0xffc0>

1473c: e041108b sub r1, r1, fp, lsl #1

14740: e7d0b003 ldrb fp, [r0, r3]

14744: e0861101 add r1, r6, r1, lsl #2

14748: e5911230 ldr r1, [r1, #560] ; 0x230

1474c: e18bb141 orr fp, fp, r1, asr #2

14750: e7c0b003 strb fp, [r0, r3]

14754: e15c0002 cmp ip, r2

14758: 1affffed bne 14714 <mcrypt\_mutex\_register@@Base+0xff80>

1475c: e2877001 add r7, r7, #1

14760: e3570010 cmp r7, #16

14764: e59d5004 ldr r5, [sp, #4]

14768: 15f83001 ldrbne r3, [r8, #1]!

1476c: 1affffc9 bne 14698 <mcrypt\_mutex\_register@@Base+0xff04>

14770: e3a00000 mov r0, #0

14774: e28dd084 add sp, sp, #132 ; 0x84

14778: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1477c: 0000d660 andeq sp, r0, r0, ror #12

14780: 0000d61c andeq sp, r0, ip, lsl r6

14784: 0000d5fc strdeq sp, [r0], -ip

14788: 0000d564 andeq sp, r0, r4, ror #10

1478c: 0000d544 andeq sp, r0, r4, asr #10

14790: 0000d4ac andeq sp, r0, ip, lsr #9

14794: 0000d498 muleq r0, r8, r4

14798: 0000d40c andeq sp, r0, ip, lsl #8

1479c: 0000d3f4 strdeq sp, [r0], -r4

147a0: 0000d3b4 ; <UNDEFINED> instruction: 0x0000d3b4

147a4: 0000d394 muleq r0, r4, r3

147a8: 2aaaaaab bcs feabf25c <mcrypt\_mutex\_register@@Base+0xfeabaac8>

147ac: e92d41f0 push {r4, r5, r6, r7, r8, lr}

147b0: e24dd008 sub sp, sp, #8

147b4: e1a07001 mov r7, r1

147b8: e1a0600d mov r6, sp

147bc: e1a04000 mov r4, r0

147c0: e1a01000 mov r1, r0

147c4: e1a02006 mov r2, r6

147c8: e1a00007 mov r0, r7

147cc: ebfffe5b bl 14140 <mcrypt\_mutex\_register@@Base+0xf9ac>

147d0: e59d5004 ldr r5, [sp, #4]

147d4: e1a02004 mov r2, r4

147d8: e6bf5f35 rev r5, r5

147dc: e1a01005 mov r1, r5

147e0: e1a00004 mov r0, r4

147e4: ebfffe21 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

147e8: e59d8000 ldr r8, [sp]

147ec: e2842008 add r2, r4, #8

147f0: e6bf8f38 rev r8, r8

147f4: e0288000 eor r8, r8, r0

147f8: e1a01008 mov r1, r8

147fc: e1a00004 mov r0, r4

14800: ebfffe1a bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14804: e2842010 add r2, r4, #16

14808: e0255000 eor r5, r5, r0

1480c: e1a01005 mov r1, r5

14810: e1a00004 mov r0, r4

14814: ebfffe15 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14818: e2842018 add r2, r4, #24

1481c: e0288000 eor r8, r8, r0

14820: e1a01008 mov r1, r8

14824: e1a00004 mov r0, r4

14828: ebfffe10 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

1482c: e2842020 add r2, r4, #32

14830: e0255000 eor r5, r5, r0

14834: e1a01005 mov r1, r5

14838: e1a00004 mov r0, r4

1483c: ebfffe0b bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14840: e2842028 add r2, r4, #40 ; 0x28

14844: e0288000 eor r8, r8, r0

14848: e1a01008 mov r1, r8

1484c: e1a00004 mov r0, r4

14850: ebfffe06 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14854: e2842030 add r2, r4, #48 ; 0x30

14858: e0255000 eor r5, r5, r0

1485c: e1a01005 mov r1, r5

14860: e1a00004 mov r0, r4

14864: ebfffe01 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14868: e2842038 add r2, r4, #56 ; 0x38

1486c: e0288000 eor r8, r8, r0

14870: e1a01008 mov r1, r8

14874: e1a00004 mov r0, r4

14878: ebfffdfc bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

1487c: e2842040 add r2, r4, #64 ; 0x40

14880: e0255000 eor r5, r5, r0

14884: e1a01005 mov r1, r5

14888: e1a00004 mov r0, r4

1488c: ebfffdf7 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14890: e2842048 add r2, r4, #72 ; 0x48

14894: e0288000 eor r8, r8, r0

14898: e1a01008 mov r1, r8

1489c: e1a00004 mov r0, r4

148a0: ebfffdf2 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

148a4: e2842050 add r2, r4, #80 ; 0x50

148a8: e0255000 eor r5, r5, r0

148ac: e1a01005 mov r1, r5

148b0: e1a00004 mov r0, r4

148b4: ebfffded bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

148b8: e2842058 add r2, r4, #88 ; 0x58

148bc: e0288000 eor r8, r8, r0

148c0: e1a01008 mov r1, r8

148c4: e1a00004 mov r0, r4

148c8: ebfffde8 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

148cc: e2842060 add r2, r4, #96 ; 0x60

148d0: e0255000 eor r5, r5, r0

148d4: e1a01005 mov r1, r5

148d8: e1a00004 mov r0, r4

148dc: ebfffde3 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

148e0: e2842068 add r2, r4, #104 ; 0x68

148e4: e0288000 eor r8, r8, r0

148e8: e1a01008 mov r1, r8

148ec: e1a00004 mov r0, r4

148f0: ebfffdde bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

148f4: e2842070 add r2, r4, #112 ; 0x70

148f8: e0255000 eor r5, r5, r0

148fc: e1a01005 mov r1, r5

14900: e1a00004 mov r0, r4

14904: ebfffdd9 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14908: e2842078 add r2, r4, #120 ; 0x78

1490c: e0288000 eor r8, r8, r0

14910: e1a01008 mov r1, r8

14914: e1a00004 mov r0, r4

14918: ebfffdd4 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

1491c: e6bf8f38 rev r8, r8

14920: e1a02007 mov r2, r7

14924: e1a01004 mov r1, r4

14928: e58d8004 str r8, [sp, #4]

1492c: e0255000 eor r5, r5, r0

14930: e6bf5f35 rev r5, r5

14934: e1a00006 mov r0, r6

14938: e58d5000 str r5, [sp]

1493c: ebfffe47 bl 14260 <mcrypt\_mutex\_register@@Base+0xfacc>

14940: e28dd008 add sp, sp, #8

14944: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

14948: e92d41f0 push {r4, r5, r6, r7, r8, lr}

1494c: e24dd008 sub sp, sp, #8

14950: e1a07001 mov r7, r1

14954: e1a0600d mov r6, sp

14958: e1a04000 mov r4, r0

1495c: e1a01000 mov r1, r0

14960: e1a02006 mov r2, r6

14964: e1a00007 mov r0, r7

14968: ebfffdf4 bl 14140 <mcrypt\_mutex\_register@@Base+0xf9ac>

1496c: e59d5004 ldr r5, [sp, #4]

14970: e2842078 add r2, r4, #120 ; 0x78

14974: e6bf5f35 rev r5, r5

14978: e1a01005 mov r1, r5

1497c: e1a00004 mov r0, r4

14980: ebfffdba bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14984: e59d8000 ldr r8, [sp]

14988: e2842070 add r2, r4, #112 ; 0x70

1498c: e6bf8f38 rev r8, r8

14990: e0288000 eor r8, r8, r0

14994: e1a01008 mov r1, r8

14998: e1a00004 mov r0, r4

1499c: ebfffdb3 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

149a0: e2842068 add r2, r4, #104 ; 0x68

149a4: e0255000 eor r5, r5, r0

149a8: e1a01005 mov r1, r5

149ac: e1a00004 mov r0, r4

149b0: ebfffdae bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

149b4: e2842060 add r2, r4, #96 ; 0x60

149b8: e0288000 eor r8, r8, r0

149bc: e1a01008 mov r1, r8

149c0: e1a00004 mov r0, r4

149c4: ebfffda9 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

149c8: e2842058 add r2, r4, #88 ; 0x58

149cc: e0255000 eor r5, r5, r0

149d0: e1a01005 mov r1, r5

149d4: e1a00004 mov r0, r4

149d8: ebfffda4 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

149dc: e2842050 add r2, r4, #80 ; 0x50

149e0: e0288000 eor r8, r8, r0

149e4: e1a01008 mov r1, r8

149e8: e1a00004 mov r0, r4

149ec: ebfffd9f bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

149f0: e2842048 add r2, r4, #72 ; 0x48

149f4: e0255000 eor r5, r5, r0

149f8: e1a01005 mov r1, r5

149fc: e1a00004 mov r0, r4

14a00: ebfffd9a bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a04: e2842040 add r2, r4, #64 ; 0x40

14a08: e0288000 eor r8, r8, r0

14a0c: e1a01008 mov r1, r8

14a10: e1a00004 mov r0, r4

14a14: ebfffd95 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a18: e2842038 add r2, r4, #56 ; 0x38

14a1c: e0255000 eor r5, r5, r0

14a20: e1a01005 mov r1, r5

14a24: e1a00004 mov r0, r4

14a28: ebfffd90 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a2c: e2842030 add r2, r4, #48 ; 0x30

14a30: e0288000 eor r8, r8, r0

14a34: e1a01008 mov r1, r8

14a38: e1a00004 mov r0, r4

14a3c: ebfffd8b bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a40: e2842028 add r2, r4, #40 ; 0x28

14a44: e0255000 eor r5, r5, r0

14a48: e1a01005 mov r1, r5

14a4c: e1a00004 mov r0, r4

14a50: ebfffd86 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a54: e2842020 add r2, r4, #32

14a58: e0288000 eor r8, r8, r0

14a5c: e1a01008 mov r1, r8

14a60: e1a00004 mov r0, r4

14a64: ebfffd81 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a68: e2842018 add r2, r4, #24

14a6c: e0255000 eor r5, r5, r0

14a70: e1a01005 mov r1, r5

14a74: e1a00004 mov r0, r4

14a78: ebfffd7c bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a7c: e2842010 add r2, r4, #16

14a80: e0288000 eor r8, r8, r0

14a84: e1a01008 mov r1, r8

14a88: e1a00004 mov r0, r4

14a8c: ebfffd77 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14a90: e2842008 add r2, r4, #8

14a94: e0255000 eor r5, r5, r0

14a98: e1a01005 mov r1, r5

14a9c: e1a00004 mov r0, r4

14aa0: ebfffd72 bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14aa4: e1a02004 mov r2, r4

14aa8: e0288000 eor r8, r8, r0

14aac: e1a01008 mov r1, r8

14ab0: e1a00004 mov r0, r4

14ab4: ebfffd6d bl 14070 <mcrypt\_mutex\_register@@Base+0xf8dc>

14ab8: e6bf8f38 rev r8, r8

14abc: e1a02007 mov r2, r7

14ac0: e1a01004 mov r1, r4

14ac4: e58d8004 str r8, [sp, #4]

14ac8: e0255000 eor r5, r5, r0

14acc: e6bf5f35 rev r5, r5

14ad0: e1a00006 mov r0, r6

14ad4: e58d5000 str r5, [sp]

14ad8: ebfffde0 bl 14260 <mcrypt\_mutex\_register@@Base+0xfacc>

14adc: e28dd008 add sp, sp, #8

14ae0: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

14ae4: e3a00d62 mov r0, #6272 ; 0x1880

14ae8: e12fff1e bx lr

14aec: e3a00008 mov r0, #8

14af0: e12fff1e bx lr

14af4: e3a00001 mov r0, #1

14af8: e12fff1e bx lr

14afc: e3a00008 mov r0, #8

14b00: e12fff1e bx lr

14b04: e59f3010 ldr r3, [pc, #16] ; 14b1c <mcrypt\_mutex\_register@@Base+0x10388>

14b08: e3a02001 mov r2, #1

14b0c: e08f3003 add r3, pc, r3

14b10: e5802000 str r2, [r0]

14b14: e2830fd2 add r0, r3, #840 ; 0x348

14b18: e12fff1e bx lr

14b1c: 0000cf14 andeq ip, r0, r4, lsl pc

14b20: e59f0004 ldr r0, [pc, #4] ; 14b2c <mcrypt\_mutex\_register@@Base+0x10398>

14b24: e08f0000 add r0, pc, r0

14b28: e12fff1e bx lr

14b2c: 0000d248 andeq sp, r0, r8, asr #4

14b30: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

14b34: e24dd0e8 sub sp, sp, #232 ; 0xe8

14b38: ebffffeb bl 14aec <mcrypt\_mutex\_register@@Base+0x10358>

14b3c: e1a06000 mov r6, r0

14b40: ebffffed bl 14afc <mcrypt\_mutex\_register@@Base+0x10368>

14b44: e1a01000 mov r1, r0

14b48: e3a00001 mov r0, #1

14b4c: ebffb5b8 bl 2234 <calloc@plt>

14b50: e2507000 subs r7, r0, #0

14b54: 0a00006f beq 14d18 <mcrypt\_mutex\_register@@Base+0x10584>

14b58: e2475001 sub r5, r7, #1

14b5c: e3a0400a mov r4, #10

14b60: e2678001 rsb r8, r7, #1

14b64: ea000004 b 14b7c <mcrypt\_mutex\_register@@Base+0x103e8>

14b68: e2742000 rsbs r2, r4, #0

14b6c: e2844002 add r4, r4, #2

14b70: e6ef2072 uxtb r2, r2

14b74: 52623000 rsbpl r3, r2, #0

14b78: e5e53001 strb r3, [r5, #1]!

14b7c: ebffffde bl 14afc <mcrypt\_mutex\_register@@Base+0x10368>

14b80: e0882005 add r2, r8, r5

14b84: e6ef3074 uxtb r3, r4

14b88: e1500002 cmp r0, r2

14b8c: cafffff5 bgt 14b68 <mcrypt\_mutex\_register@@Base+0x103d4>

14b90: e3560000 cmp r6, #0

14b94: da000039 ble 14c80 <mcrypt\_mutex\_register@@Base+0x104ec>

14b98: e1a0400d mov r4, sp

14b9c: e1a02004 mov r2, r4

14ba0: e3a03000 mov r3, #0

14ba4: e4c23001 strb r3, [r2], #1

14ba8: e2833001 add r3, r3, #1

14bac: e1560003 cmp r6, r3

14bb0: 1afffffb bne 14ba4 <mcrypt\_mutex\_register@@Base+0x10410>

14bb4: ebffffca bl 14ae4 <mcrypt\_mutex\_register@@Base+0x10350>

14bb8: ebffb5cd bl 22f4 <malloc@plt>

14bbc: e2509000 subs r9, r0, #0

14bc0: 0a000050 beq 14d08 <mcrypt\_mutex\_register@@Base+0x10574>

14bc4: e28d8010 add r8, sp, #16

14bc8: e1a01004 mov r1, r4

14bcc: e1a02006 mov r2, r6

14bd0: e1a00008 mov r0, r8

14bd4: ebffb5ab bl 2288 <memcpy@plt>

14bd8: ebffffc7 bl 14afc <mcrypt\_mutex\_register@@Base+0x10368>

14bdc: e1a01007 mov r1, r7

14be0: e28da020 add sl, sp, #32

14be4: e0886006 add r6, r8, r6

14be8: e1a05008 mov r5, r8

14bec: e1a0400a mov r4, sl

14bf0: e1a02000 mov r2, r0

14bf4: e1a00009 mov r0, r9

14bf8: ebfffde2 bl 14388 <mcrypt\_mutex\_register@@Base+0xfbf4>

14bfc: e1a00007 mov r0, r7

14c00: ebffb59d bl 227c <free@plt>

14c04: e1a01008 mov r1, r8

14c08: e1a00009 mov r0, r9

14c0c: ebfffee6 bl 147ac <mcrypt\_mutex\_register@@Base+0x10018>

14c10: e59f7108 ldr r7, [pc, #264] ; 14d20 <mcrypt\_mutex\_register@@Base+0x1058c>

14c14: e08f7007 add r7, pc, r7

14c18: e4d52001 ldrb r2, [r5], #1

14c1c: e1a00004 mov r0, r4

14c20: e1a01007 mov r1, r7

14c24: ebffb5d0 bl 236c <sprintf@plt>

14c28: e1560005 cmp r6, r5

14c2c: e2844002 add r4, r4, #2

14c30: 1afffff8 bne 14c18 <mcrypt\_mutex\_register@@Base+0x10484>

14c34: e59f40e8 ldr r4, [pc, #232] ; 14d24 <mcrypt\_mutex\_register@@Base+0x10590>

14c38: e1a0000a mov r0, sl

14c3c: e08f4004 add r4, pc, r4

14c40: e1a01004 mov r1, r4

14c44: ebffb580 bl 224c <strcmp@plt>

14c48: e3500000 cmp r0, #0

14c4c: 1a00001c bne 14cc4 <mcrypt\_mutex\_register@@Base+0x10530>

14c50: e1a01008 mov r1, r8

14c54: e1a00009 mov r0, r9

14c58: ebffff3a bl 14948 <mcrypt\_mutex\_register@@Base+0x101b4>

14c5c: e1a00009 mov r0, r9

14c60: ebffb585 bl 227c <free@plt>

14c64: e1a00008 mov r0, r8

14c68: e1a0100d mov r1, sp

14c6c: ebffb576 bl 224c <strcmp@plt>

14c70: e3500000 cmp r0, #0

14c74: 1a00001e bne 14cf4 <mcrypt\_mutex\_register@@Base+0x10560>

14c78: e28dd0e8 add sp, sp, #232 ; 0xe8

14c7c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

14c80: ebffff97 bl 14ae4 <mcrypt\_mutex\_register@@Base+0x10350>

14c84: ebffb59a bl 22f4 <malloc@plt>

14c88: e2509000 subs r9, r0, #0

14c8c: 0a00001d beq 14d08 <mcrypt\_mutex\_register@@Base+0x10574>

14c90: ebffff99 bl 14afc <mcrypt\_mutex\_register@@Base+0x10368>

14c94: e1a01007 mov r1, r7

14c98: e28d8010 add r8, sp, #16

14c9c: e28da020 add sl, sp, #32

14ca0: e1a02000 mov r2, r0

14ca4: e1a00009 mov r0, r9

14ca8: ebfffdb6 bl 14388 <mcrypt\_mutex\_register@@Base+0xfbf4>

14cac: e1a00007 mov r0, r7

14cb0: ebffb571 bl 227c <free@plt>

14cb4: e1a01008 mov r1, r8

14cb8: e1a00009 mov r0, r9

14cbc: ebfffeba bl 147ac <mcrypt\_mutex\_register@@Base+0x10018>

14cc0: eaffffdb b 14c34 <mcrypt\_mutex\_register@@Base+0x104a0>

14cc4: e59f005c ldr r0, [pc, #92] ; 14d28 <mcrypt\_mutex\_register@@Base+0x10594>

14cc8: e08f0000 add r0, pc, r0

14ccc: ebffb585 bl 22e8 <puts@plt>

14cd0: e59f0054 ldr r0, [pc, #84] ; 14d2c <mcrypt\_mutex\_register@@Base+0x10598>

14cd4: e1a0200a mov r2, sl

14cd8: e1a01004 mov r1, r4

14cdc: e08f0000 add r0, pc, r0

14ce0: ebffb55f bl 2264 <printf@plt>

14ce4: e1a00009 mov r0, r9

14ce8: ebffb563 bl 227c <free@plt>

14cec: e3e00000 mvn r0, #0

14cf0: eaffffe0 b 14c78 <mcrypt\_mutex\_register@@Base+0x104e4>

14cf4: e59f0034 ldr r0, [pc, #52] ; 14d30 <mcrypt\_mutex\_register@@Base+0x1059c>

14cf8: e08f0000 add r0, pc, r0

14cfc: ebffb579 bl 22e8 <puts@plt>

14d00: e3e00000 mvn r0, #0

14d04: eaffffdb b 14c78 <mcrypt\_mutex\_register@@Base+0x104e4>

14d08: e1a00007 mov r0, r7

14d0c: ebffb55a bl 227c <free@plt>

14d10: e3e00000 mvn r0, #0

14d14: eaffffd7 b 14c78 <mcrypt\_mutex\_register@@Base+0x104e4>

14d18: e3e00000 mvn r0, #0

14d1c: eaffffd5 b 14c78 <mcrypt\_mutex\_register@@Base+0x104e4>

14d20: 0000ba4c andeq fp, r0, ip, asr #20

14d24: 0000d134 andeq sp, r0, r4, lsr r1

14d28: 0000b9b4 ; <UNDEFINED> instruction: 0x0000b9b4

14d2c: 0000b9b8 ; <UNDEFINED> instruction: 0x0000b9b8

14d30: 0000b9b4 ; <UNDEFINED> instruction: 0x0000b9b4

14d34: e59f0000 ldr r0, [pc] ; 14d3c <mcrypt\_mutex\_register@@Base+0x105a8>

14d38: e12fff1e bx lr

14d3c: 01315731 teqeq r1, r1, lsr r7

14d40: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

14d44: e21090ff ands r9, r0, #255 ; 0xff

14d48: 03a07000 moveq r7, #0

14d4c: 13a07001 movne r7, #1

14d50: e21180ff ands r8, r1, #255 ; 0xff

14d54: 03a07000 moveq r7, #0

14d58: e1a04420 lsr r4, r0, #8

14d5c: e1a0c820 lsr ip, r0, #16

14d60: e1a03c20 lsr r3, r0, #24

14d64: e1a0e421 lsr lr, r1, #8

14d68: e1a02821 lsr r2, r1, #16

14d6c: e1a01c21 lsr r1, r1, #24

14d70: e3570000 cmp r7, #0

14d74: e6ef4074 uxtb r4, r4

14d78: e6efc07c uxtb ip, ip

14d7c: e6ef5073 uxtb r5, r3

14d80: e6efe07e uxtb lr, lr

14d84: e6ef2072 uxtb r2, r2

14d88: e6ef6071 uxtb r6, r1

14d8c: 01a00007 moveq r0, r7

14d90: 0a00000c beq 14dc8 <mcrypt\_mutex\_register@@Base+0x10634>

14d94: e59f70fc ldr r7, [pc, #252] ; 14e98 <mcrypt\_mutex\_register@@Base+0x10704>

14d98: e59f00fc ldr r0, [pc, #252] ; 14e9c <mcrypt\_mutex\_register@@Base+0x10708>

14d9c: e08f7007 add r7, pc, r7

14da0: e0879009 add r9, r7, r9

14da4: e0878008 add r8, r7, r8

14da8: e5d99100 ldrb r9, [r9, #256] ; 0x100

14dac: e5d88100 ldrb r8, [r8, #256] ; 0x100

14db0: e0898008 add r8, r9, r8

14db4: e0809890 umull r9, r0, r0, r8

14db8: e1a003a0 lsr r0, r0, #7

14dbc: e0600400 rsb r0, r0, r0, lsl #8

14dc0: e0480000 sub r0, r8, r0

14dc4: e7d70000 ldrb r0, [r7, r0]

14dc8: e3540000 cmp r4, #0

14dcc: 135e0000 cmpne lr, #0

14dd0: 0a00000d beq 14e0c <mcrypt\_mutex\_register@@Base+0x10678>

14dd4: e59f80c4 ldr r8, [pc, #196] ; 14ea0 <mcrypt\_mutex\_register@@Base+0x1070c>

14dd8: e59f70bc ldr r7, [pc, #188] ; 14e9c <mcrypt\_mutex\_register@@Base+0x10708>

14ddc: e08f8008 add r8, pc, r8

14de0: e0884004 add r4, r8, r4

14de4: e088e00e add lr, r8, lr

14de8: e5d44100 ldrb r4, [r4, #256] ; 0x100

14dec: e5dee100 ldrb lr, [lr, #256] ; 0x100

14df0: e084400e add r4, r4, lr

14df4: e08e7497 umull r7, lr, r7, r4

14df8: e1a0e3ae lsr lr, lr, #7

14dfc: e06ee40e rsb lr, lr, lr, lsl #8

14e00: e044e00e sub lr, r4, lr

14e04: e7d8e00e ldrb lr, [r8, lr]

14e08: e020000e eor r0, r0, lr

14e0c: e35c0000 cmp ip, #0

14e10: 13520000 cmpne r2, #0

14e14: 0a00000d beq 14e50 <mcrypt\_mutex\_register@@Base+0x106bc>

14e18: e59f4084 ldr r4, [pc, #132] ; 14ea4 <mcrypt\_mutex\_register@@Base+0x10710>

14e1c: e59fe078 ldr lr, [pc, #120] ; 14e9c <mcrypt\_mutex\_register@@Base+0x10708>

14e20: e08f4004 add r4, pc, r4

14e24: e084c00c add ip, r4, ip

14e28: e0842002 add r2, r4, r2

14e2c: e5dcc100 ldrb ip, [ip, #256] ; 0x100

14e30: e5d22100 ldrb r2, [r2, #256] ; 0x100

14e34: e08cc002 add ip, ip, r2

14e38: e082ec9e umull lr, r2, lr, ip

14e3c: e1a023a2 lsr r2, r2, #7

14e40: e0622402 rsb r2, r2, r2, lsl #8

14e44: e04c2002 sub r2, ip, r2

14e48: e7d42002 ldrb r2, [r4, r2]

14e4c: e0200002 eor r0, r0, r2

14e50: e3560000 cmp r6, #0

14e54: 13550000 cmpne r5, #0

14e58: 08bd83f0 popeq {r4, r5, r6, r7, r8, r9, pc}

14e5c: e59fc044 ldr ip, [pc, #68] ; 14ea8 <mcrypt\_mutex\_register@@Base+0x10714>

14e60: e59f2034 ldr r2, [pc, #52] ; 14e9c <mcrypt\_mutex\_register@@Base+0x10708>

14e64: e08fc00c add ip, pc, ip

14e68: e08c3003 add r3, ip, r3

14e6c: e08c1001 add r1, ip, r1

14e70: e5d33100 ldrb r3, [r3, #256] ; 0x100

14e74: e5d11100 ldrb r1, [r1, #256] ; 0x100

14e78: e0831001 add r1, r3, r1

14e7c: e0832192 umull r2, r3, r2, r1

14e80: e1a033a3 lsr r3, r3, #7

14e84: e0633403 rsb r3, r3, r3, lsl #8

14e88: e0413003 sub r3, r1, r3

14e8c: e7dc3003 ldrb r3, [ip, r3]

14e90: e0200003 eor r0, r0, r3

14e94: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

14e98: 0002247c andeq r2, r2, ip, ror r4

14e9c: 80808081 addhi r8, r0, r1, lsl #1

14ea0: 0002243c andeq r2, r2, ip, lsr r4

14ea4: 000223f8 strdeq r2, [r2], -r8

14ea8: 000223b4 ; <UNDEFINED> instruction: 0x000223b4

14eac: e59fc6dc ldr ip, [pc, #1756] ; 15590 <mcrypt\_mutex\_register@@Base+0x10dfc>

14eb0: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

14eb4: e08fc00c add ip, pc, ip

14eb8: e24dd04c sub sp, sp, #76 ; 0x4c

14ebc: e1a0e002 mov lr, r2

14ec0: e2823003 add r3, r2, #3

14ec4: e35e0000 cmp lr, #0

14ec8: e58d2018 str r2, [sp, #24]

14ecc: e59c2200 ldr r2, [ip, #512] ; 0x200

14ed0: a1a0300e movge r3, lr

14ed4: e3520000 cmp r2, #0

14ed8: e1cd01fc strd r0, [sp, #28]

14edc: e1a0b143 asr fp, r3, #2

14ee0: 0a0000ff beq 152e4 <mcrypt\_mutex\_register@@Base+0x10b50>

14ee4: e59d3018 ldr r3, [sp, #24]

14ee8: e59d201c ldr r2, [sp, #28]

14eec: e3530023 cmp r3, #35 ; 0x23

14ef0: e3a03008 mov r3, #8

14ef4: e582b000 str fp, [r2]

14ef8: e5823004 str r3, [r2, #4]

14efc: da0000bc ble 151f4 <mcrypt\_mutex\_register@@Base+0x10a60>

14f00: e28b3007 add r3, fp, #7

14f04: e28b1006 add r1, fp, #6

14f08: e1a03183 lsl r3, r3, #3

14f0c: e58d3008 str r3, [sp, #8]

14f10: e2433008 sub r3, r3, #8

14f14: e58d3010 str r3, [sp, #16]

14f18: e59d001c ldr r0, [sp, #28]

14f1c: e3a02000 mov r2, #0

14f20: e1a03000 mov r3, r0

14f24: e5801008 str r1, [r0, #8]

14f28: e2820001 add r0, r2, #1

14f2c: e2824003 add r4, r2, #3

14f30: e2821004 add r1, r2, #4

14f34: e282e007 add lr, r2, #7

14f38: e282c005 add ip, r2, #5

14f3c: e1a02000 mov r2, r0

14f40: e2011007 and r1, r1, #7

14f44: e2000007 and r0, r0, #7

14f48: e2044007 and r4, r4, #7

14f4c: e20ee007 and lr, lr, #7

14f50: e20cc007 and ip, ip, #7

14f54: e3520008 cmp r2, #8

14f58: e5c3000c strb r0, [r3, #12]

14f5c: e5c3400d strb r4, [r3, #13]

14f60: e5c3100e strb r1, [r3, #14]

14f64: e5c31026 strb r1, [r3, #38] ; 0x26

14f68: e5c3e024 strb lr, [r3, #36] ; 0x24

14f6c: e5c3c025 strb ip, [r3, #37] ; 0x25

14f70: e2833003 add r3, r3, #3

14f74: 1affffeb bne 14f28 <mcrypt\_mutex\_register@@Base+0x10794>

14f78: e59d3018 ldr r3, [sp, #24]

14f7c: e3530003 cmp r3, #3

14f80: da00017b ble 15574 <mcrypt\_mutex\_register@@Base+0x10de0>

14f84: e28d2024 add r2, sp, #36 ; 0x24

14f88: e59d1020 ldr r1, [sp, #32]

14f8c: e1a00002 mov r0, r2

14f90: e3a03000 mov r3, #0

14f94: e2833001 add r3, r3, #1

14f98: e491c004 ldr ip, [r1], #4

14f9c: e15b0003 cmp fp, r3

14fa0: e5a0c004 str ip, [r0, #4]!

14fa4: cafffffa bgt 14f94 <mcrypt\_mutex\_register@@Base+0x10800>

14fa8: e59d301c ldr r3, [sp, #28]

14fac: e283303c add r3, r3, #60 ; 0x3c

14fb0: e58d3014 str r3, [sp, #20]

14fb4: e1a01003 mov r1, r3

14fb8: e3a03000 mov r3, #0

14fbc: e2833001 add r3, r3, #1

14fc0: e5b20004 ldr r0, [r2, #4]!

14fc4: e15b0003 cmp fp, r3

14fc8: e4810004 str r0, [r1], #4

14fcc: cafffffa bgt 14fbc <mcrypt\_mutex\_register@@Base+0x10828>

14fd0: e59f45bc ldr r4, [pc, #1468] ; 15594 <mcrypt\_mutex\_register@@Base+0x10e00>

14fd4: e59d201c ldr r2, [sp, #28]

14fd8: e08f4004 add r4, pc, r4

14fdc: e06b3f0b rsb r3, fp, fp, lsl #30

14fe0: e1a0610b lsl r6, fp, #2

14fe4: e59d7008 ldr r7, [sp, #8]

14fe8: e284ab03 add sl, r4, #3072 ; 0xc00

14fec: e286c040 add ip, r6, #64 ; 0x40

14ff0: e59f15a0 ldr r1, [pc, #1440] ; 15598 <mcrypt\_mutex\_register@@Base+0x10e04>

14ff4: e0823103 add r3, r2, r3, lsl #2

14ff8: e28aa004 add sl, sl, #4

14ffc: e082c00c add ip, r2, ip

15000: e2825050 add r5, r2, #80 ; 0x50

15004: e047000b sub r0, r7, fp

15008: e1a0e00b mov lr, fp

1500c: e08f1001 add r1, pc, r1

15010: e283303c add r3, r3, #60 ; 0x3c

15014: e58d100c str r1, [sp, #12]

15018: e58d3004 str r3, [sp, #4]

1501c: e51c1008 ldr r1, [ip, #-8]

15020: e59d3018 ldr r3, [sp, #24]

15024: e49a8004 ldr r8, [sl], #4

15028: e1a01461 ror r1, r1, #8

1502c: e353001b cmp r3, #27

15030: e1a09821 lsr r9, r1, #16

15034: e0843c21 add r3, r4, r1, lsr #24

15038: e6e42071 uxtab r2, r4, r1

1503c: e1a01421 lsr r1, r1, #8

15040: e6e49079 uxtab r9, r4, r9

15044: e6e41071 uxtab r1, r4, r1

15048: e5d33204 ldrb r3, [r3, #516] ; 0x204

1504c: e5d99204 ldrb r9, [r9, #516] ; 0x204

15050: e5d22204 ldrb r2, [r2, #516] ; 0x204

15054: e5d11204 ldrb r1, [r1, #516] ; 0x204

15058: e1822c03 orr r2, r2, r3, lsl #24

1505c: e5153014 ldr r3, [r5, #-20] ; 0xffffffec

15060: e1822809 orr r2, r2, r9, lsl #16

15064: e1822401 orr r2, r2, r1, lsl #8

15068: e0233008 eor r3, r3, r8

1506c: e0233002 eor r3, r3, r2

15070: e50c3004 str r3, [ip, #-4]

15074: ca000064 bgt 1520c <mcrypt\_mutex\_register@@Base+0x10a78>

15078: e59d2018 ldr r2, [sp, #24]

1507c: e3520007 cmp r2, #7

15080: da00000f ble 150c4 <mcrypt\_mutex\_register@@Base+0x10930>

15084: e28e2001 add r2, lr, #1

15088: e1520007 cmp r2, r7

1508c: aa00000c bge 150c4 <mcrypt\_mutex\_register@@Base+0x10930>

15090: e2459010 sub r9, r5, #16

15094: e1a0800c mov r8, ip

15098: e3a01001 mov r1, #1

1509c: ea000002 b 150ac <mcrypt\_mutex\_register@@Base+0x10918>

150a0: e1510000 cmp r1, r0

150a4: 0a000006 beq 150c4 <mcrypt\_mutex\_register@@Base+0x10930>

150a8: e5183004 ldr r3, [r8, #-4]

150ac: e4992004 ldr r2, [r9], #4

150b0: e2811001 add r1, r1, #1

150b4: e0233002 eor r3, r3, r2

150b8: e15b0001 cmp fp, r1

150bc: e4883004 str r3, [r8], #4

150c0: cafffff6 bgt 150a0 <mcrypt\_mutex\_register@@Base+0x1090c>

150c4: e08ee00b add lr, lr, fp

150c8: e15e0007 cmp lr, r7

150cc: e0855006 add r5, r5, r6

150d0: e08cc006 add ip, ip, r6

150d4: e040000b sub r0, r0, fp

150d8: baffffcf blt 1501c <mcrypt\_mutex\_register@@Base+0x10888>

150dc: e59d3008 ldr r3, [sp, #8]

150e0: e59d101c ldr r1, [sp, #28]

150e4: e0812103 add r2, r1, r3, lsl #2

150e8: e59d3014 ldr r3, [sp, #20]

150ec: e2822f7e add r2, r2, #504 ; 0x1f8

150f0: e281005c add r0, r1, #92 ; 0x5c

150f4: e4931004 ldr r1, [r3], #4

150f8: e1500003 cmp r0, r3

150fc: e5a21004 str r1, [r2, #4]!

15100: 1afffffb bne 150f4 <mcrypt\_mutex\_register@@Base+0x10960>

15104: e59d3010 ldr r3, [sp, #16]

15108: e59d001c ldr r0, [sp, #28]

1510c: e2432009 sub r2, r3, #9

15110: e0803083 add r3, r0, r3, lsl #1

15114: e1a021a2 lsr r2, r2, #3

15118: e59fb47c ldr fp, [pc, #1148] ; 1559c <mcrypt\_mutex\_register@@Base+0x10e08>

1511c: e2801098 add r1, r0, #152 ; 0x98

15120: e1a03083 lsl r3, r3, #1

15124: e2808078 add r8, r0, #120 ; 0x78

15128: e0812282 add r2, r1, r2, lsl #5

1512c: e2833e27 add r3, r3, #624 ; 0x270

15130: e58d2000 str r2, [sp]

15134: e58d3004 str r3, [sp, #4]

15138: e59d3004 ldr r3, [sp, #4]

1513c: e2486020 sub r6, r8, #32

15140: e043a008 sub sl, r3, r8

15144: e5b65004 ldr r5, [r6, #4]!

15148: e1a0000b mov r0, fp

1514c: e1a01005 mov r1, r5

15150: ebfffefa bl 14d40 <mcrypt\_mutex\_register@@Base+0x105ac>

15154: e1a01005 mov r1, r5

15158: e1a09000 mov r9, r0

1515c: e59f043c ldr r0, [pc, #1084] ; 155a0 <mcrypt\_mutex\_register@@Base+0x10e0c>

15160: ebfffef6 bl 14d40 <mcrypt\_mutex\_register@@Base+0x105ac>

15164: e1a01005 mov r1, r5

15168: e1a04000 mov r4, r0

1516c: e59f0430 ldr r0, [pc, #1072] ; 155a4 <mcrypt\_mutex\_register@@Base+0x10e10>

15170: ebfffef2 bl 14d40 <mcrypt\_mutex\_register@@Base+0x105ac>

15174: e1a01005 mov r1, r5

15178: e1a04804 lsl r4, r4, #16

1517c: e1844c09 orr r4, r4, r9, lsl #24

15180: e1a07000 mov r7, r0

15184: e59f041c ldr r0, [pc, #1052] ; 155a8 <mcrypt\_mutex\_register@@Base+0x10e14>

15188: ebfffeec bl 14d40 <mcrypt\_mutex\_register@@Base+0x105ac>

1518c: e1580006 cmp r8, r6

15190: e1840000 orr r0, r4, r0

15194: e1800407 orr r0, r0, r7, lsl #8

15198: e5aa0004 str r0, [sl, #4]!

1519c: 1affffe8 bne 15144 <mcrypt\_mutex\_register@@Base+0x109b0>

151a0: e59d3000 ldr r3, [sp]

151a4: e2888020 add r8, r8, #32

151a8: e1530008 cmp r3, r8

151ac: 1affffe1 bne 15138 <mcrypt\_mutex\_register@@Base+0x109a4>

151b0: e59d1010 ldr r1, [sp, #16]

151b4: e59dc008 ldr ip, [sp, #8]

151b8: e281308f add r3, r1, #143 ; 0x8f

151bc: e59d001c ldr r0, [sp, #28]

151c0: e043300c sub r3, r3, ip

151c4: e281200f add r2, r1, #15

151c8: e0803103 add r3, r0, r3, lsl #2

151cc: e2433004 sub r3, r3, #4

151d0: e0802102 add r2, r0, r2, lsl #2

151d4: e2811001 add r1, r1, #1

151d8: e4920004 ldr r0, [r2], #4

151dc: e151000c cmp r1, ip

151e0: e5a30004 str r0, [r3, #4]!

151e4: bafffffa blt 151d4 <mcrypt\_mutex\_register@@Base+0x10a40>

151e8: e3a00000 mov r0, #0

151ec: e28dd04c add sp, sp, #76 ; 0x4c

151f0: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

151f4: e3a03070 mov r3, #112 ; 0x70

151f8: e58d3010 str r3, [sp, #16]

151fc: e3a03078 mov r3, #120 ; 0x78

15200: e58d3008 str r3, [sp, #8]

15204: e3a0100e mov r1, #14

15208: eaffff42 b 14f18 <mcrypt\_mutex\_register@@Base+0x10784>

1520c: e59d8004 ldr r8, [sp, #4]

15210: e28e3001 add r3, lr, #1

15214: e24c1004 sub r1, ip, #4

15218: e28e9004 add r9, lr, #4

1521c: e58d0000 str r0, [sp]

15220: e1530007 cmp r3, r7

15224: aa000006 bge 15244 <mcrypt\_mutex\_register@@Base+0x10ab0>

15228: e7982103 ldr r2, [r8, r3, lsl #2]

1522c: e5910000 ldr r0, [r1]

15230: e2833001 add r3, r3, #1

15234: e0222000 eor r2, r2, r0

15238: e1590003 cmp r9, r3

1523c: e5a12004 str r2, [r1, #4]!

15240: 1afffff6 bne 15220 <mcrypt\_mutex\_register@@Base+0x10a8c>

15244: e1590007 cmp r9, r7

15248: e59d0000 ldr r0, [sp]

1524c: aa000013 bge 152a0 <mcrypt\_mutex\_register@@Base+0x10b0c>

15250: e59c2008 ldr r2, [ip, #8]

15254: e5153004 ldr r3, [r5, #-4]

15258: e58d3000 str r3, [sp]

1525c: e59d300c ldr r3, [sp, #12]

15260: e1a01822 lsr r1, r2, #16

15264: e0838c22 add r8, r3, r2, lsr #24

15268: e6e39072 uxtab r9, r3, r2

1526c: e1a02422 lsr r2, r2, #8

15270: e6e31071 uxtab r1, r3, r1

15274: e6e32072 uxtab r2, r3, r2

15278: e5d88204 ldrb r8, [r8, #516] ; 0x204

1527c: e5d93204 ldrb r3, [r9, #516] ; 0x204

15280: e5d11204 ldrb r1, [r1, #516] ; 0x204

15284: e5d22204 ldrb r2, [r2, #516] ; 0x204

15288: e1833c08 orr r3, r3, r8, lsl #24

1528c: e59d9000 ldr r9, [sp]

15290: e1833801 orr r3, r3, r1, lsl #16

15294: e1833402 orr r3, r3, r2, lsl #8

15298: e0299003 eor r9, r9, r3

1529c: e58c900c str r9, [ip, #12]

152a0: e28e3005 add r3, lr, #5

152a4: e1530007 cmp r3, r7

152a8: aaffff85 bge 150c4 <mcrypt\_mutex\_register@@Base+0x10930>

152ac: e28c100c add r1, ip, #12

152b0: e1a08005 mov r8, r5

152b4: e3a03005 mov r3, #5

152b8: ea000001 b 152c4 <mcrypt\_mutex\_register@@Base+0x10b30>

152bc: e1530000 cmp r3, r0

152c0: 0affff7f beq 150c4 <mcrypt\_mutex\_register@@Base+0x10930>

152c4: e4982004 ldr r2, [r8], #4

152c8: e5919000 ldr r9, [r1]

152cc: e2833001 add r3, r3, #1

152d0: e0222009 eor r2, r2, r9

152d4: e15b0003 cmp fp, r3

152d8: e5a12004 str r2, [r1, #4]!

152dc: cafffff6 bgt 152bc <mcrypt\_mutex\_register@@Base+0x10b28>

152e0: eaffff77 b 150c4 <mcrypt\_mutex\_register@@Base+0x10930>

152e4: e3a03001 mov r3, #1

152e8: e28c0c01 add r0, ip, #256 ; 0x100

152ec: e59f12b8 ldr r1, [pc, #696] ; 155ac <mcrypt\_mutex\_register@@Base+0x10e18>

152f0: e08ce003 add lr, ip, r3

152f4: e1c020b0 strh r2, [r0]

152f8: e1cc10b0 strh r1, [ip]

152fc: e5cc3103 strb r3, [ip, #259] ; 0x103

15300: e08e0003 add r0, lr, r3

15304: e28c40ff add r4, ip, #255 ; 0xff

15308: e1a0100c mov r1, ip

1530c: e3a03003 mov r3, #3

15310: ea000002 b 15320 <mcrypt\_mutex\_register@@Base+0x10b8c>

15314: e3130080 tst r3, #128 ; 0x80

15318: 13a0201b movne r2, #27

1531c: 03a02000 moveq r2, #0

15320: e0222083 eor r2, r2, r3, lsl #1

15324: e0233002 eor r3, r3, r2

15328: e1a0c000 mov ip, r0

1532c: e6ef3073 uxtb r3, r3

15330: e0812003 add r2, r1, r3

15334: e4c03001 strb r3, [r0], #1

15338: e154000c cmp r4, ip

1533c: e040c00e sub ip, r0, lr

15340: e5c2c100 strb ip, [r2, #256] ; 0x100

15344: 1afffff2 bne 15314 <mcrypt\_mutex\_register@@Base+0x10b80>

15348: e2818c01 add r8, r1, #256 ; 0x100

1534c: e1a04008 mov r4, r8

15350: e2810f81 add r0, r1, #516 ; 0x204

15354: e28880ff add r8, r8, #255 ; 0xff

15358: e2649001 rsb r9, r4, #1

1535c: e2445c01 sub r5, r4, #256 ; 0x100

15360: e1a06000 mov r6, r0

15364: e3a02063 mov r2, #99 ; 0x63

15368: e3a03000 mov r3, #0

1536c: e5c12204 strb r2, [r1, #516] ; 0x204

15370: e5c13367 strb r3, [r1, #871] ; 0x367

15374: e0891004 add r1, r9, r4

15378: e5f43001 ldrb r3, [r4, #1]!

1537c: e26330ff rsb r3, r3, #255 ; 0xff

15380: e1580004 cmp r8, r4

15384: e7d5e003 ldrb lr, [r5, r3]

15388: e1a0208e lsl r2, lr, #1

1538c: e18223ae orr r2, r2, lr, lsr #7

15390: e6ef2072 uxtb r2, r2

15394: e1a03082 lsl r3, r2, #1

15398: e18333a2 orr r3, r3, r2, lsr #7

1539c: e6ef3073 uxtb r3, r3

153a0: e1a0c083 lsl ip, r3, #1

153a4: e18cc3a3 orr ip, ip, r3, lsr #7

153a8: e6efc07c uxtb ip, ip

153ac: e1a0708c lsl r7, ip, #1

153b0: e18773ac orr r7, r7, ip, lsr #7

153b4: e6af7077 sxtb r7, r7

153b8: e02ee007 eor lr, lr, r7

153bc: e022200e eor r2, r2, lr

153c0: e0233002 eor r3, r3, r2

153c4: e6ef3073 uxtb r3, r3

153c8: e023300c eor r3, r3, ip

153cc: e2233063 eor r3, r3, #99 ; 0x63

153d0: e0852003 add r2, r5, r3

153d4: e5e63001 strb r3, [r6, #1]!

153d8: e5c21304 strb r1, [r2, #772] ; 0x304

153dc: 1affffe4 bne 15374 <mcrypt\_mutex\_register@@Base+0x10be0>

153e0: e285cb03 add ip, r5, #3072 ; 0xc00

153e4: e3a03001 mov r3, #1

153e8: e28c2004 add r2, ip, #4

153ec: e5853c04 str r3, [r5, #3076] ; 0xc04

153f0: e28cc078 add ip, ip, #120 ; 0x78

153f4: e3a01000 mov r1, #0

153f8: ea000003 b 1540c <mcrypt\_mutex\_register@@Base+0x10c78>

153fc: e3130080 tst r3, #128 ; 0x80

15400: 13a0101b movne r1, #27

15404: 03a01000 moveq r1, #0

15408: e5a23004 str r3, [r2, #4]!

1540c: e1a03083 lsl r3, r3, #1

15410: e15c0002 cmp ip, r2

15414: e6ef3073 uxtb r3, r3

15418: e0233001 eor r3, r3, r1

1541c: 1afffff6 bne 153fc <mcrypt\_mutex\_register@@Base+0x10c68>

15420: e59f6188 ldr r6, [pc, #392] ; 155b0 <mcrypt\_mutex\_register@@Base+0x10e1c>

15424: e59f4188 ldr r4, [pc, #392] ; 155b4 <mcrypt\_mutex\_register@@Base+0x10e20>

15428: e08f6006 add r6, pc, r6

1542c: e286ec02 add lr, r6, #512 ; 0x200

15430: e5d6310b ldrb r3, [r6, #267] ; 0x10b

15434: e2867c03 add r7, r6, #768 ; 0x300

15438: e59f9178 ldr r9, [pc, #376] ; 155b8 <mcrypt\_mutex\_register@@Base+0x10e24>

1543c: e58d3008 str r3, [sp, #8]

15440: e5d6310d ldrb r3, [r6, #269] ; 0x10d

15444: e2868b01 add r8, r6, #1024 ; 0x400

15448: e08f4004 add r4, pc, r4

1544c: e58d300c str r3, [sp, #12]

15450: e5d63109 ldrb r3, [r6, #265] ; 0x109

15454: e28ee003 add lr, lr, #3

15458: e2877003 add r7, r7, #3

1545c: e58d3010 str r3, [sp, #16]

15460: e5d6310e ldrb r3, [r6, #270] ; 0x10e

15464: e2866b02 add r6, r6, #2048 ; 0x800

15468: e58db024 str fp, [sp, #36] ; 0x24

1546c: e58d3014 str r3, [sp, #20]

15470: e28030ff add r3, r0, #255 ; 0xff

15474: e58d3000 str r3, [sp]

15478: e5fe1001 ldrb r1, [lr, #1]!

1547c: e5f70001 ldrb r0, [r7, #1]!

15480: e3110080 tst r1, #128 ; 0x80

15484: e1a02081 lsl r2, r1, #1

15488: 13a0301b movne r3, #27

1548c: 03a03000 moveq r3, #0

15490: e6ef2072 uxtb r2, r2

15494: e0222003 eor r2, r2, r3

15498: e1a03401 lsl r3, r1, #8

1549c: e021c002 eor ip, r1, r2

154a0: e1833801 orr r3, r3, r1, lsl #16

154a4: e1832002 orr r2, r3, r2

154a8: e1822c0c orr r2, r2, ip, lsl #24

154ac: e3500000 cmp r0, #0

154b0: e0845000 add r5, r4, r0

154b4: e1a0a000 mov sl, r0

154b8: e5a62004 str r2, [r6, #4]!

154bc: 0a000022 beq 1554c <mcrypt\_mutex\_register@@Base+0x10db8>

154c0: e5d50100 ldrb r0, [r5, #256] ; 0x100

154c4: e59d300c ldr r3, [sp, #12]

154c8: e080b003 add fp, r0, r3

154cc: e59d3008 ldr r3, [sp, #8]

154d0: e083a000 add sl, r3, r0

154d4: e0813b99 umull r3, r1, r9, fp

154d8: e0823a99 umull r3, r2, r9, sl

154dc: e59d3014 ldr r3, [sp, #20]

154e0: e58d2004 str r2, [sp, #4]

154e4: e0805003 add r5, r0, r3

154e8: e59d3010 ldr r3, [sp, #16]

154ec: e1a013a1 lsr r1, r1, #7

154f0: e0800003 add r0, r0, r3

154f4: e08c3599 umull r3, ip, r9, r5

154f8: e0832099 umull r2, r3, r9, r0

154fc: e59d2004 ldr r2, [sp, #4]

15500: e0611401 rsb r1, r1, r1, lsl #8

15504: e04b1001 sub r1, fp, r1

15508: e1a023a2 lsr r2, r2, #7

1550c: e1a0c3ac lsr ip, ip, #7

15510: e0622402 rsb r2, r2, r2, lsl #8

15514: e1a033a3 lsr r3, r3, #7

15518: e04a2002 sub r2, sl, r2

1551c: e7d41001 ldrb r1, [r4, r1]

15520: e06cc40c rsb ip, ip, ip, lsl #8

15524: e0633403 rsb r3, r3, r3, lsl #8

15528: e0403003 sub r3, r0, r3

1552c: e7d42002 ldrb r2, [r4, r2]

15530: e045c00c sub ip, r5, ip

15534: e7d43003 ldrb r3, [r4, r3]

15538: e7d4000c ldrb r0, [r4, ip]

1553c: e1a01801 lsl r1, r1, #16

15540: e1811c02 orr r1, r1, r2, lsl #24

15544: e1811000 orr r1, r1, r0

15548: e181a403 orr sl, r1, r3, lsl #8

1554c: e59d3000 ldr r3, [sp]

15550: e5a8a004 str sl, [r8, #4]!

15554: e153000e cmp r3, lr

15558: 1affffc6 bne 15478 <mcrypt\_mutex\_register@@Base+0x10ce4>

1555c: e59f3058 ldr r3, [pc, #88] ; 155bc <mcrypt\_mutex\_register@@Base+0x10e28>

15560: e3a02001 mov r2, #1

15564: e08f3003 add r3, pc, r3

15568: e59db024 ldr fp, [sp, #36] ; 0x24

1556c: e5832200 str r2, [r3, #512] ; 0x200

15570: eafffe5b b 14ee4 <mcrypt\_mutex\_register@@Base+0x10750>

15574: e59d3008 ldr r3, [sp, #8]

15578: e15b0003 cmp fp, r3

1557c: e59d301c ldr r3, [sp, #28]

15580: e283303c add r3, r3, #60 ; 0x3c

15584: e58d3014 str r3, [sp, #20]

15588: aafffed3 bge 150dc <mcrypt\_mutex\_register@@Base+0x10948>

1558c: eafffe8f b 14fd0 <mcrypt\_mutex\_register@@Base+0x1083c>

15590: 00022364 andeq r2, r2, r4, ror #6

15594: 00022240 andeq r2, r2, r0, asr #4

15598: 0002220c andeq r2, r2, ip, lsl #4

1559c: 0e090d0b cdpeq 13, 0, cr0, cr9, cr11, {0}

155a0: 0b0e090d bleq 3979dc <mcrypt\_mutex\_register@@Base+0x393248>

155a4: 0d0b0e09 stceq 14, cr0, [fp, #-36] ; 0xffffffdc

155a8: 090d0b0e stmdbeq sp, {r1, r2, r3, r8, r9, fp}

155ac: 00000301 andeq r0, r0, r1, lsl #6

155b0: 00021df0 strdeq r1, [r2], -r0

155b4: 00021dd0 ldrdeq r1, [r2], -r0

155b8: 80808081 addhi r8, r0, r1, lsl #1

155bc: 00021cb4 ; <UNDEFINED> instruction: 0x00021cb4

155c0: e5903004 ldr r3, [r0, #4]

155c4: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

155c8: e24dd05c sub sp, sp, #92 ; 0x5c

155cc: e3530000 cmp r3, #0

155d0: e58d1014 str r1, [sp, #20]

155d4: e58d3004 str r3, [sp, #4]

155d8: da000086 ble 157f8 <mcrypt\_mutex\_register@@Base+0x11064>

155dc: e1a03103 lsl r3, r3, #2

155e0: e1a02001 mov r2, r1

155e4: e0814003 add r4, r1, r3

155e8: e280c03c add ip, r0, #60 ; 0x3c

155ec: e28d1014 add r1, sp, #20

155f0: e58d300c str r3, [sp, #12]

155f4: e4923004 ldr r3, [r2], #4

155f8: e49ce004 ldr lr, [ip], #4

155fc: e1540002 cmp r4, r2

15600: e023300e eor r3, r3, lr

15604: e5a13004 str r3, [r1, #4]!

15608: 1afffff9 bne 155f4 <mcrypt\_mutex\_register@@Base+0x10e60>

1560c: e5903008 ldr r3, [r0, #8]

15610: e3530001 cmp r3, #1

15614: e58d3008 str r3, [sp, #8]

15618: da00007e ble 15818 <mcrypt\_mutex\_register@@Base+0x11084>

1561c: e59f1208 ldr r1, [pc, #520] ; 1582c <mcrypt\_mutex\_register@@Base+0x11098>

15620: e59d3004 ldr r3, [sp, #4]

15624: e28db038 add fp, sp, #56 ; 0x38

15628: e28d4018 add r4, sp, #24

1562c: e3a0a001 mov sl, #1

15630: e08f1001 add r1, pc, r1

15634: e58d3000 str r3, [sp]

15638: e58d0010 str r0, [sp, #16]

1563c: e59d3004 ldr r3, [sp, #4]

15640: e3530000 cmp r3, #0

15644: da000026 ble 156e4 <mcrypt\_mutex\_register@@Base+0x10f50>

15648: e59d3000 ldr r3, [sp]

1564c: e59d2010 ldr r2, [sp, #16]

15650: e1a0e004 mov lr, r4

15654: e0825103 add r5, r2, r3, lsl #2

15658: e59d300c ldr r3, [sp, #12]

1565c: e2855038 add r5, r5, #56 ; 0x38

15660: e24b6004 sub r6, fp, #4

15664: e0849003 add r9, r4, r3

15668: e5d2700e ldrb r7, [r2, #14]

1566c: e5d2c00c ldrb ip, [r2, #12]

15670: e5d2000d ldrb r0, [r2, #13]

15674: e49e3004 ldr r3, [lr], #4

15678: e7947107 ldr r7, [r4, r7, lsl #2]

1567c: e084c10c add ip, r4, ip, lsl #2

15680: e0840100 add r0, r4, r0, lsl #2

15684: e5dcc001 ldrb ip, [ip, #1]

15688: e5d00002 ldrb r0, [r0, #2]

1568c: e1a07c27 lsr r7, r7, #24

15690: e6ef3073 uxtb r3, r3

15694: e0817107 add r7, r1, r7, lsl #2

15698: e0813103 add r3, r1, r3, lsl #2

1569c: e081c10c add ip, r1, ip, lsl #2

156a0: e5977804 ldr r7, [r7, #2052] ; 0x804

156a4: e5933804 ldr r3, [r3, #2052] ; 0x804

156a8: e5b58004 ldr r8, [r5, #4]!

156ac: e0810100 add r0, r1, r0, lsl #2

156b0: e59cc804 ldr ip, [ip, #2052] ; 0x804

156b4: e0233008 eor r3, r3, r8

156b8: e5900804 ldr r0, [r0, #2052] ; 0x804

156bc: e0233467 eor r3, r3, r7, ror #8

156c0: e0233c6c eor r3, r3, ip, ror #24

156c4: e0233860 eor r3, r3, r0, ror #16

156c8: e159000e cmp r9, lr

156cc: e5a63004 str r3, [r6, #4]!

156d0: e2822003 add r2, r2, #3

156d4: 1affffe3 bne 15668 <mcrypt\_mutex\_register@@Base+0x10ed4>

156d8: e1cd20d0 ldrd r2, [sp]

156dc: e0833002 add r3, r3, r2

156e0: e58d3000 str r3, [sp]

156e4: e59d3008 ldr r3, [sp, #8]

156e8: e28aa001 add sl, sl, #1

156ec: e153000a cmp r3, sl

156f0: 11a03004 movne r3, r4

156f4: 11a0400b movne r4, fp

156f8: 11a0b003 movne fp, r3

156fc: 1affffce bne 1563c <mcrypt\_mutex\_register@@Base+0x10ea8>

15700: e59d3004 ldr r3, [sp, #4]

15704: e59d0010 ldr r0, [sp, #16]

15708: e3530000 cmp r3, #0

1570c: da000037 ble 157f0 <mcrypt\_mutex\_register@@Base+0x1105c>

15710: e59d3000 ldr r3, [sp]

15714: e59f1114 ldr r1, [pc, #276] ; 15830 <mcrypt\_mutex\_register@@Base+0x1109c>

15718: e2444004 sub r4, r4, #4

1571c: e0805103 add r5, r0, r3, lsl #2

15720: e59d300c ldr r3, [sp, #12]

15724: e08f1001 add r1, pc, r1

15728: e2855038 add r5, r5, #56 ; 0x38

1572c: e08ba003 add sl, fp, r3

15730: e1a06004 mov r6, r4

15734: e1a0e00b mov lr, fp

15738: e1a02000 mov r2, r0

1573c: e5d2700c ldrb r7, [r2, #12]

15740: e5d2800e ldrb r8, [r2, #14]

15744: e5d2c00d ldrb ip, [r2, #13]

15748: e49e3004 ldr r3, [lr], #4

1574c: e08b7107 add r7, fp, r7, lsl #2

15750: e79b8108 ldr r8, [fp, r8, lsl #2]

15754: e08bc10c add ip, fp, ip, lsl #2

15758: e5d77001 ldrb r7, [r7, #1]

1575c: e5dcc002 ldrb ip, [ip, #2]

15760: e6e13073 uxtab r3, r1, r3

15764: e0818c28 add r8, r1, r8, lsr #24

15768: e0817007 add r7, r1, r7

1576c: e5d88204 ldrb r8, [r8, #516] ; 0x204

15770: e5d33204 ldrb r3, [r3, #516] ; 0x204

15774: e081c00c add ip, r1, ip

15778: e5d77204 ldrb r7, [r7, #516] ; 0x204

1577c: e5b59004 ldr r9, [r5, #4]!

15780: e5dcc204 ldrb ip, [ip, #516] ; 0x204

15784: e0233009 eor r3, r3, r9

15788: e0233468 eor r3, r3, r8, ror #8

1578c: e0233c67 eor r3, r3, r7, ror #24

15790: e023386c eor r3, r3, ip, ror #16

15794: e15a000e cmp sl, lr

15798: e5a63004 str r3, [r6, #4]!

1579c: e2822003 add r2, r2, #3

157a0: 1affffe5 bne 1573c <mcrypt\_mutex\_register@@Base+0x10fa8>

157a4: e3a02000 mov r2, #0

157a8: e59d1014 ldr r1, [sp, #20]

157ac: e24bb004 sub fp, fp, #4

157b0: e1a0c002 mov ip, r2

157b4: e5b43004 ldr r3, [r4, #4]!

157b8: e2822001 add r2, r2, #1

157bc: e5c13000 strb r3, [r1]

157c0: e1a05423 lsr r5, r3, #8

157c4: e1a0e823 lsr lr, r3, #16

157c8: e1a03c23 lsr r3, r3, #24

157cc: e5c15001 strb r5, [r1, #1]

157d0: e5c1e002 strb lr, [r1, #2]

157d4: e5c13003 strb r3, [r1, #3]

157d8: e5903004 ldr r3, [r0, #4]

157dc: e584c000 str ip, [r4]

157e0: e1530002 cmp r3, r2

157e4: e5abc004 str ip, [fp, #4]!

157e8: e2811004 add r1, r1, #4

157ec: cafffff0 bgt 157b4 <mcrypt\_mutex\_register@@Base+0x11020>

157f0: e28dd05c add sp, sp, #92 ; 0x5c

157f4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

157f8: e5903008 ldr r3, [r0, #8]

157fc: e3530001 cmp r3, #1

15800: e58d3008 str r3, [sp, #8]

15804: dafffff9 ble 157f0 <mcrypt\_mutex\_register@@Base+0x1105c>

15808: e59d3004 ldr r3, [sp, #4]

1580c: e1a03103 lsl r3, r3, #2

15810: e58d300c str r3, [sp, #12]

15814: eaffff80 b 1561c <mcrypt\_mutex\_register@@Base+0x10e88>

15818: e59d3004 ldr r3, [sp, #4]

1581c: e28d4038 add r4, sp, #56 ; 0x38

15820: e58d3000 str r3, [sp]

15824: e28db018 add fp, sp, #24

15828: eaffffb8 b 15710 <mcrypt\_mutex\_register@@Base+0x10f7c>

1582c: 00021be8 andeq r1, r2, r8, ror #23

15830: 00021af4 strdeq r1, [r2], -r4

15834: e5903004 ldr r3, [r0, #4]

15838: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1583c: e24dd05c sub sp, sp, #92 ; 0x5c

15840: e3530000 cmp r3, #0

15844: e58d1014 str r1, [sp, #20]

15848: e58d3004 str r3, [sp, #4]

1584c: da000086 ble 15a6c <mcrypt\_mutex\_register@@Base+0x112d8>

15850: e1a03103 lsl r3, r3, #2

15854: e1a02001 mov r2, r1

15858: e0814003 add r4, r1, r3

1585c: e280cf87 add ip, r0, #540 ; 0x21c

15860: e28d1014 add r1, sp, #20

15864: e58d300c str r3, [sp, #12]

15868: e4923004 ldr r3, [r2], #4

1586c: e49ce004 ldr lr, [ip], #4

15870: e1540002 cmp r4, r2

15874: e023300e eor r3, r3, lr

15878: e5a13004 str r3, [r1, #4]!

1587c: 1afffff9 bne 15868 <mcrypt\_mutex\_register@@Base+0x110d4>

15880: e5903008 ldr r3, [r0, #8]

15884: e3530001 cmp r3, #1

15888: e58d3008 str r3, [sp, #8]

1588c: da00007e ble 15a8c <mcrypt\_mutex\_register@@Base+0x112f8>

15890: e59f1208 ldr r1, [pc, #520] ; 15aa0 <mcrypt\_mutex\_register@@Base+0x1130c>

15894: e59d3004 ldr r3, [sp, #4]

15898: e28db038 add fp, sp, #56 ; 0x38

1589c: e28d4018 add r4, sp, #24

158a0: e3a0a001 mov sl, #1

158a4: e08f1001 add r1, pc, r1

158a8: e58d3000 str r3, [sp]

158ac: e58d0010 str r0, [sp, #16]

158b0: e59d3004 ldr r3, [sp, #4]

158b4: e3530000 cmp r3, #0

158b8: da000026 ble 15958 <mcrypt\_mutex\_register@@Base+0x111c4>

158bc: e59d3000 ldr r3, [sp]

158c0: e59d2010 ldr r2, [sp, #16]

158c4: e1a0e004 mov lr, r4

158c8: e0825103 add r5, r2, r3, lsl #2

158cc: e59d300c ldr r3, [sp, #12]

158d0: e2855f86 add r5, r5, #536 ; 0x218

158d4: e24b6004 sub r6, fp, #4

158d8: e0849003 add r9, r4, r3

158dc: e5d27026 ldrb r7, [r2, #38] ; 0x26

158e0: e5d2c024 ldrb ip, [r2, #36] ; 0x24

158e4: e5d20025 ldrb r0, [r2, #37] ; 0x25

158e8: e49e3004 ldr r3, [lr], #4

158ec: e7947107 ldr r7, [r4, r7, lsl #2]

158f0: e084c10c add ip, r4, ip, lsl #2

158f4: e0840100 add r0, r4, r0, lsl #2

158f8: e5dcc001 ldrb ip, [ip, #1]

158fc: e5d00002 ldrb r0, [r0, #2]

15900: e1a07c27 lsr r7, r7, #24

15904: e6ef3073 uxtb r3, r3

15908: e0817107 add r7, r1, r7, lsl #2

1590c: e0813103 add r3, r1, r3, lsl #2

15910: e081c10c add ip, r1, ip, lsl #2

15914: e5977404 ldr r7, [r7, #1028] ; 0x404

15918: e5933404 ldr r3, [r3, #1028] ; 0x404

1591c: e5b58004 ldr r8, [r5, #4]!

15920: e0810100 add r0, r1, r0, lsl #2

15924: e59cc404 ldr ip, [ip, #1028] ; 0x404

15928: e0233008 eor r3, r3, r8

1592c: e5900404 ldr r0, [r0, #1028] ; 0x404

15930: e0233467 eor r3, r3, r7, ror #8

15934: e0233c6c eor r3, r3, ip, ror #24

15938: e0233860 eor r3, r3, r0, ror #16

1593c: e159000e cmp r9, lr

15940: e5a63004 str r3, [r6, #4]!

15944: e2822003 add r2, r2, #3

15948: 1affffe3 bne 158dc <mcrypt\_mutex\_register@@Base+0x11148>

1594c: e1cd20d0 ldrd r2, [sp]

15950: e0833002 add r3, r3, r2

15954: e58d3000 str r3, [sp]

15958: e59d3008 ldr r3, [sp, #8]

1595c: e28aa001 add sl, sl, #1

15960: e153000a cmp r3, sl

15964: 11a03004 movne r3, r4

15968: 11a0400b movne r4, fp

1596c: 11a0b003 movne fp, r3

15970: 1affffce bne 158b0 <mcrypt\_mutex\_register@@Base+0x1111c>

15974: e59d3004 ldr r3, [sp, #4]

15978: e59d0010 ldr r0, [sp, #16]

1597c: e3530000 cmp r3, #0

15980: da000037 ble 15a64 <mcrypt\_mutex\_register@@Base+0x112d0>

15984: e59d3000 ldr r3, [sp]

15988: e59f1114 ldr r1, [pc, #276] ; 15aa4 <mcrypt\_mutex\_register@@Base+0x11310>

1598c: e2444004 sub r4, r4, #4

15990: e0805103 add r5, r0, r3, lsl #2

15994: e59d300c ldr r3, [sp, #12]

15998: e08f1001 add r1, pc, r1

1599c: e2855f86 add r5, r5, #536 ; 0x218

159a0: e08ba003 add sl, fp, r3

159a4: e1a06004 mov r6, r4

159a8: e1a0e00b mov lr, fp

159ac: e1a02000 mov r2, r0

159b0: e5d27024 ldrb r7, [r2, #36] ; 0x24

159b4: e5d28026 ldrb r8, [r2, #38] ; 0x26

159b8: e5d2c025 ldrb ip, [r2, #37] ; 0x25

159bc: e49e3004 ldr r3, [lr], #4

159c0: e08b7107 add r7, fp, r7, lsl #2

159c4: e79b8108 ldr r8, [fp, r8, lsl #2]

159c8: e08bc10c add ip, fp, ip, lsl #2

159cc: e5d77001 ldrb r7, [r7, #1]

159d0: e5dcc002 ldrb ip, [ip, #2]

159d4: e6e13073 uxtab r3, r1, r3

159d8: e0818c28 add r8, r1, r8, lsr #24

159dc: e0817007 add r7, r1, r7

159e0: e5d88304 ldrb r8, [r8, #772] ; 0x304

159e4: e5d33304 ldrb r3, [r3, #772] ; 0x304

159e8: e081c00c add ip, r1, ip

159ec: e5d77304 ldrb r7, [r7, #772] ; 0x304

159f0: e5b59004 ldr r9, [r5, #4]!

159f4: e5dcc304 ldrb ip, [ip, #772] ; 0x304

159f8: e0233009 eor r3, r3, r9

159fc: e0233468 eor r3, r3, r8, ror #8

15a00: e0233c67 eor r3, r3, r7, ror #24

15a04: e023386c eor r3, r3, ip, ror #16

15a08: e15a000e cmp sl, lr

15a0c: e5a63004 str r3, [r6, #4]!

15a10: e2822003 add r2, r2, #3

15a14: 1affffe5 bne 159b0 <mcrypt\_mutex\_register@@Base+0x1121c>

15a18: e3a02000 mov r2, #0

15a1c: e59d1014 ldr r1, [sp, #20]

15a20: e24bb004 sub fp, fp, #4

15a24: e1a0c002 mov ip, r2

15a28: e5b43004 ldr r3, [r4, #4]!

15a2c: e2822001 add r2, r2, #1

15a30: e5c13000 strb r3, [r1]

15a34: e1a05423 lsr r5, r3, #8

15a38: e1a0e823 lsr lr, r3, #16

15a3c: e1a03c23 lsr r3, r3, #24

15a40: e5c15001 strb r5, [r1, #1]

15a44: e5c1e002 strb lr, [r1, #2]

15a48: e5c13003 strb r3, [r1, #3]

15a4c: e5903004 ldr r3, [r0, #4]

15a50: e584c000 str ip, [r4]

15a54: e1530002 cmp r3, r2

15a58: e5abc004 str ip, [fp, #4]!

15a5c: e2811004 add r1, r1, #4

15a60: cafffff0 bgt 15a28 <mcrypt\_mutex\_register@@Base+0x11294>

15a64: e28dd05c add sp, sp, #92 ; 0x5c

15a68: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

15a6c: e5903008 ldr r3, [r0, #8]

15a70: e3530001 cmp r3, #1

15a74: e58d3008 str r3, [sp, #8]

15a78: dafffff9 ble 15a64 <mcrypt\_mutex\_register@@Base+0x112d0>

15a7c: e59d3004 ldr r3, [sp, #4]

15a80: e1a03103 lsl r3, r3, #2

15a84: e58d300c str r3, [sp, #12]

15a88: eaffff80 b 15890 <mcrypt\_mutex\_register@@Base+0x110fc>

15a8c: e59d3004 ldr r3, [sp, #4]

15a90: e28d4038 add r4, sp, #56 ; 0x38

15a94: e58d3000 str r3, [sp]

15a98: e28db018 add fp, sp, #24

15a9c: eaffffb8 b 15984 <mcrypt\_mutex\_register@@Base+0x111f0>

15aa0: 00021974 andeq r1, r2, r4, ror r9

15aa4: 00021880 andeq r1, r2, r0, lsl #17

15aa8: e3a00fff mov r0, #1020 ; 0x3fc

15aac: e12fff1e bx lr

15ab0: e3a00020 mov r0, #32

15ab4: e12fff1e bx lr

15ab8: e3a00001 mov r0, #1

15abc: e12fff1e bx lr

15ac0: e3a00020 mov r0, #32

15ac4: e12fff1e bx lr

15ac8: e3a03003 mov r3, #3

15acc: e5803000 str r3, [r0]

15ad0: e59f0004 ldr r0, [pc, #4] ; 15adc <mcrypt\_mutex\_register@@Base+0x11348>

15ad4: e08f0000 add r0, pc, r0

15ad8: e12fff1e bx lr

15adc: 0000c2b0 ; <UNDEFINED> instruction: 0x0000c2b0

15ae0: e59f0004 ldr r0, [pc, #4] ; 15aec <mcrypt\_mutex\_register@@Base+0x11358>

15ae4: e08f0000 add r0, pc, r0

15ae8: e12fff1e bx lr

15aec: 0000c2ac andeq ip, r0, ip, lsr #5

15af0: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

15af4: e24ddf42 sub sp, sp, #264 ; 0x108

15af8: ebffffec bl 15ab0 <mcrypt\_mutex\_register@@Base+0x1131c>

15afc: e1a06000 mov r6, r0

15b00: ebffffee bl 15ac0 <mcrypt\_mutex\_register@@Base+0x1132c>

15b04: e1a01000 mov r1, r0

15b08: e3a00001 mov r0, #1

15b0c: ebffb1c8 bl 2234 <calloc@plt>

15b10: e2507000 subs r7, r0, #0

15b14: 0a00006f beq 15cd8 <mcrypt\_mutex\_register@@Base+0x11544>

15b18: e2475001 sub r5, r7, #1

15b1c: e3a0400a mov r4, #10

15b20: e2678001 rsb r8, r7, #1

15b24: ea000004 b 15b3c <mcrypt\_mutex\_register@@Base+0x113a8>

15b28: e2742000 rsbs r2, r4, #0

15b2c: e2844002 add r4, r4, #2

15b30: e6ef2072 uxtb r2, r2

15b34: 52623000 rsbpl r3, r2, #0

15b38: e5e53001 strb r3, [r5, #1]!

15b3c: ebffffdf bl 15ac0 <mcrypt\_mutex\_register@@Base+0x1132c>

15b40: e0882005 add r2, r8, r5

15b44: e6ef3074 uxtb r3, r4

15b48: e1500002 cmp r0, r2

15b4c: cafffff5 bgt 15b28 <mcrypt\_mutex\_register@@Base+0x11394>

15b50: e3560000 cmp r6, #0

15b54: da000039 ble 15c40 <mcrypt\_mutex\_register@@Base+0x114ac>

15b58: e1a0400d mov r4, sp

15b5c: e1a02004 mov r2, r4

15b60: e3a03000 mov r3, #0

15b64: e4c23001 strb r3, [r2], #1

15b68: e2833001 add r3, r3, #1

15b6c: e1560003 cmp r6, r3

15b70: 1afffffb bne 15b64 <mcrypt\_mutex\_register@@Base+0x113d0>

15b74: ebffffcb bl 15aa8 <mcrypt\_mutex\_register@@Base+0x11314>

15b78: ebffb1dd bl 22f4 <malloc@plt>

15b7c: e2509000 subs r9, r0, #0

15b80: 0a000050 beq 15cc8 <mcrypt\_mutex\_register@@Base+0x11534>

15b84: e28d8020 add r8, sp, #32

15b88: e1a01004 mov r1, r4

15b8c: e1a02006 mov r2, r6

15b90: e1a00008 mov r0, r8

15b94: ebffb1bb bl 2288 <memcpy@plt>

15b98: ebffffc8 bl 15ac0 <mcrypt\_mutex\_register@@Base+0x1132c>

15b9c: e1a01007 mov r1, r7

15ba0: e28da040 add sl, sp, #64 ; 0x40

15ba4: e0886006 add r6, r8, r6

15ba8: e1a05008 mov r5, r8

15bac: e1a0400a mov r4, sl

15bb0: e1a02000 mov r2, r0

15bb4: e1a00009 mov r0, r9

15bb8: ebfffcbb bl 14eac <mcrypt\_mutex\_register@@Base+0x10718>

15bbc: e1a00007 mov r0, r7

15bc0: ebffb1ad bl 227c <free@plt>

15bc4: e1a01008 mov r1, r8

15bc8: e1a00009 mov r0, r9

15bcc: ebfffe7b bl 155c0 <mcrypt\_mutex\_register@@Base+0x10e2c>

15bd0: e59f7108 ldr r7, [pc, #264] ; 15ce0 <mcrypt\_mutex\_register@@Base+0x1154c>

15bd4: e08f7007 add r7, pc, r7

15bd8: e4d52001 ldrb r2, [r5], #1

15bdc: e1a00004 mov r0, r4

15be0: e1a01007 mov r1, r7

15be4: ebffb1e0 bl 236c <sprintf@plt>

15be8: e1560005 cmp r6, r5

15bec: e2844002 add r4, r4, #2

15bf0: 1afffff8 bne 15bd8 <mcrypt\_mutex\_register@@Base+0x11444>

15bf4: e59f40e8 ldr r4, [pc, #232] ; 15ce4 <mcrypt\_mutex\_register@@Base+0x11550>

15bf8: e1a0000a mov r0, sl

15bfc: e08f4004 add r4, pc, r4

15c00: e1a01004 mov r1, r4

15c04: ebffb190 bl 224c <strcmp@plt>

15c08: e3500000 cmp r0, #0

15c0c: 1a00001c bne 15c84 <mcrypt\_mutex\_register@@Base+0x114f0>

15c10: e1a01008 mov r1, r8

15c14: e1a00009 mov r0, r9

15c18: ebffff05 bl 15834 <mcrypt\_mutex\_register@@Base+0x110a0>

15c1c: e1a00009 mov r0, r9

15c20: ebffb195 bl 227c <free@plt>

15c24: e1a00008 mov r0, r8

15c28: e1a0100d mov r1, sp

15c2c: ebffb186 bl 224c <strcmp@plt>

15c30: e3500000 cmp r0, #0

15c34: 1a00001e bne 15cb4 <mcrypt\_mutex\_register@@Base+0x11520>

15c38: e28ddf42 add sp, sp, #264 ; 0x108

15c3c: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

15c40: ebffff98 bl 15aa8 <mcrypt\_mutex\_register@@Base+0x11314>

15c44: ebffb1aa bl 22f4 <malloc@plt>

15c48: e2509000 subs r9, r0, #0

15c4c: 0a00001d beq 15cc8 <mcrypt\_mutex\_register@@Base+0x11534>

15c50: ebffff9a bl 15ac0 <mcrypt\_mutex\_register@@Base+0x1132c>

15c54: e1a01007 mov r1, r7

15c58: e28d8020 add r8, sp, #32

15c5c: e28da040 add sl, sp, #64 ; 0x40

15c60: e1a02000 mov r2, r0

15c64: e1a00009 mov r0, r9

15c68: ebfffc8f bl 14eac <mcrypt\_mutex\_register@@Base+0x10718>

15c6c: e1a00007 mov r0, r7

15c70: ebffb181 bl 227c <free@plt>

15c74: e1a01008 mov r1, r8

15c78: e1a00009 mov r0, r9

15c7c: ebfffe4f bl 155c0 <mcrypt\_mutex\_register@@Base+0x10e2c>

15c80: eaffffdb b 15bf4 <mcrypt\_mutex\_register@@Base+0x11460>

15c84: e59f005c ldr r0, [pc, #92] ; 15ce8 <mcrypt\_mutex\_register@@Base+0x11554>

15c88: e08f0000 add r0, pc, r0

15c8c: ebffb195 bl 22e8 <puts@plt>

15c90: e59f0054 ldr r0, [pc, #84] ; 15cec <mcrypt\_mutex\_register@@Base+0x11558>

15c94: e1a0200a mov r2, sl

15c98: e1a01004 mov r1, r4

15c9c: e08f0000 add r0, pc, r0

15ca0: ebffb16f bl 2264 <printf@plt>

15ca4: e1a00009 mov r0, r9

15ca8: ebffb173 bl 227c <free@plt>

15cac: e3e00000 mvn r0, #0

15cb0: eaffffe0 b 15c38 <mcrypt\_mutex\_register@@Base+0x114a4>

15cb4: e59f0034 ldr r0, [pc, #52] ; 15cf0 <mcrypt\_mutex\_register@@Base+0x1155c>

15cb8: e08f0000 add r0, pc, r0

15cbc: ebffb189 bl 22e8 <puts@plt>

15cc0: e3e00000 mvn r0, #0

15cc4: eaffffdb b 15c38 <mcrypt\_mutex\_register@@Base+0x114a4>

15cc8: e1a00007 mov r0, r7

15ccc: ebffb16a bl 227c <free@plt>

15cd0: e3e00000 mvn r0, #0

15cd4: eaffffd7 b 15c38 <mcrypt\_mutex\_register@@Base+0x114a4>

15cd8: e3e00000 mvn r0, #0

15cdc: eaffffd5 b 15c38 <mcrypt\_mutex\_register@@Base+0x114a4>

15ce0: 0000aa8c andeq sl, r0, ip, lsl #21

15ce4: 0000c1a4 andeq ip, r0, r4, lsr #3

15ce8: 0000a9f4 strdeq sl, [r0], -r4

15cec: 0000a9f8 strdeq sl, [r0], -r8

15cf0: 0000a9f4 strdeq sl, [r0], -r4

15cf4: e59f0000 ldr r0, [pc] ; 15cfc <mcrypt\_mutex\_register@@Base+0x11568>

15cf8: e12fff1e bx lr

15cfc: 01315731 teqeq r1, r1, lsr r7

15d00: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

15d04: e1a06182 lsl r6, r2, #3

15d08: e3560c01 cmp r6, #256 ; 0x100

15d0c: 8a000351 bhi 16a58 <mcrypt\_mutex\_register@@Base+0x122c4>

15d10: e286301f add r3, r6, #31

15d14: e1b032a3 lsrs r3, r3, #5

15d18: 0a000349 beq 16a44 <mcrypt\_mutex\_register@@Base+0x122b0>

15d1c: e1a04103 lsl r4, r3, #2

15d20: e2402004 sub r2, r0, #4

15d24: e0815004 add r5, r1, r4

15d28: e1a0c002 mov ip, r2

15d2c: e491e004 ldr lr, [r1], #4

15d30: e1550001 cmp r5, r1

15d34: e5ace004 str lr, [ip, #4]!

15d38: 1afffffb bne 15d2c <mcrypt\_mutex\_register@@Base+0x11598>

15d3c: e3560c01 cmp r6, #256 ; 0x100

15d40: 0a000010 beq 15d88 <mcrypt\_mutex\_register@@Base+0x115f4>

15d44: e3530008 cmp r3, #8

15d48: 0a000005 beq 15d64 <mcrypt\_mutex\_register@@Base+0x115d0>

15d4c: e0801004 add r1, r0, r4

15d50: e3a0c000 mov ip, #0

15d54: e2833001 add r3, r3, #1

15d58: e3530008 cmp r3, #8

15d5c: e481c004 str ip, [r1], #4

15d60: 1afffffb bne 15d54 <mcrypt\_mutex\_register@@Base+0x115c0>

15d64: e206101f and r1, r6, #31

15d68: e3a03001 mov r3, #1

15d6c: e1a062a6 lsr r6, r6, #5

15d70: e1a01113 lsl r1, r3, r1

15d74: e790c106 ldr ip, [r0, r6, lsl #2]

15d78: e2413001 sub r3, r1, #1

15d7c: e003300c and r3, r3, ip

15d80: e1833001 orr r3, r3, r1

15d84: e7803106 str r3, [r0, r6, lsl #2]

15d88: e59f4cd0 ldr r4, [pc, #3280] ; 16a60 <mcrypt\_mutex\_register@@Base+0x122cc>

15d8c: e3a01000 mov r1, #0

15d90: e5b23004 ldr r3, [r2, #4]!

15d94: e592c00c ldr ip, [r2, #12]

15d98: e592e014 ldr lr, [r2, #20]

15d9c: e023300c eor r3, r3, ip

15da0: e0233001 eor r3, r3, r1

15da4: e592c01c ldr ip, [r2, #28]

15da8: e023300e eor r3, r3, lr

15dac: e023300c eor r3, r3, ip

15db0: e0233004 eor r3, r3, r4

15db4: e2811001 add r1, r1, #1

15db8: e1a03ae3 ror r3, r3, #21

15dbc: e3510084 cmp r1, #132 ; 0x84

15dc0: e5823020 str r3, [r2, #32]

15dc4: 1afffff1 bne 15d90 <mcrypt\_mutex\_register@@Base+0x115fc>

15dc8: e590a020 ldr sl, [r0, #32]

15dcc: e5905028 ldr r5, [r0, #40] ; 0x28

15dd0: e590202c ldr r2, [r0, #44] ; 0x2c

15dd4: e590c024 ldr ip, [r0, #36] ; 0x24

15dd8: e02ae005 eor lr, sl, r5

15ddc: e02ee002 eor lr, lr, r2

15de0: e00a400e and r4, sl, lr

15de4: e18c1002 orr r1, ip, r2

15de8: e0247001 eor r7, r4, r1

15dec: e0244002 eor r4, r4, r2

15df0: e18a2002 orr r2, sl, r2

15df4: e022200c eor r2, r2, ip

15df8: e00cc004 and ip, ip, r4

15dfc: e02ee00c eor lr, lr, ip

15e00: e590c030 ldr ip, [r0, #48] ; 0x30

15e04: e5903038 ldr r3, [r0, #56] ; 0x38

15e08: e590b034 ldr fp, [r0, #52] ; 0x34

15e0c: e590603c ldr r6, [r0, #60] ; 0x3c

15e10: e18aa00e orr sl, sl, lr

15e14: e0077005 and r7, r7, r5

15e18: e00aa004 and sl, sl, r4

15e1c: e1e0c00c mvn ip, ip

15e20: e0277002 eor r7, r7, r2

15e24: e022200e eor r2, r2, lr

15e28: e003500c and r5, r3, ip

15e2c: e021100a eor r1, r1, sl

15e30: e022200a eor r2, r2, sl

15e34: e02ba006 eor sl, fp, r6

15e38: e025500a eor r5, r5, sl

15e3c: e5807020 str r7, [r0, #32]

15e40: e0237005 eor r7, r3, r5

15e44: e007700b and r7, r7, fp

15e48: e023300c eor r3, r3, ip

15e4c: e5908040 ldr r8, [r0, #64] ; 0x40

15e50: e5801024 str r1, [r0, #36] ; 0x24

15e54: e186c00c orr ip, r6, ip

15e58: e5901044 ldr r1, [r0, #68] ; 0x44

15e5c: e1866007 orr r6, r6, r7

15e60: e0277003 eor r7, r7, r3

15e64: e1853003 orr r3, r5, r3

15e68: e0066003 and r6, r6, r3

15e6c: e5903030 ldr r3, [r0, #48] ; 0x30

15e70: e0281001 eor r1, r8, r1

15e74: e5909050 ldr r9, [r0, #80] ; 0x50

15e78: e590405c ldr r4, [r0, #92] ; 0x5c

15e7c: e0266003 eor r6, r6, r3

15e80: e1e01001 mvn r1, r1

15e84: e5903048 ldr r3, [r0, #72] ; 0x48

15e88: e580e028 str lr, [r0, #40] ; 0x28

15e8c: e1888001 orr r8, r8, r1

15e90: e590e054 ldr lr, [r0, #84] ; 0x54

15e94: e0288003 eor r8, r8, r3

15e98: e580202c str r2, [r0, #44] ; 0x2c

15e9c: e0293004 eor r3, r9, r4

15ea0: e5902058 ldr r2, [r0, #88] ; 0x58

15ea4: e5805030 str r5, [r0, #48] ; 0x30

15ea8: e00e5003 and r5, lr, r3

15eac: e0255009 eor r5, r5, r9

15eb0: e0233002 eor r3, r3, r2

15eb4: e02cc00a eor ip, ip, sl

15eb8: e590a04c ldr sl, [r0, #76] ; 0x4c

15ebc: e02ee003 eor lr, lr, r3

15ec0: e1822005 orr r2, r2, r5

15ec4: e02cc007 eor ip, ip, r7

15ec8: e0044009 and r4, r4, r9

15ecc: e5909044 ldr r9, [r0, #68] ; 0x44

15ed0: e024400e eor r4, r4, lr

15ed4: e02cc006 eor ip, ip, r6

15ed8: e02ee002 eor lr, lr, r2

15edc: e5806038 str r6, [r0, #56] ; 0x38

15ee0: e18a2001 orr r2, sl, r1

15ee4: e0236005 eor r6, r3, r5

15ee8: e02aa008 eor sl, sl, r8

15eec: e0066004 and r6, r6, r4

15ef0: e0222009 eor r2, r2, r9

15ef4: e021100a eor r1, r1, sl

15ef8: e0255006 eor r5, r5, r6

15efc: e0233006 eor r3, r3, r6

15f00: e0086002 and r6, r8, r2

15f04: e0222008 eor r2, r2, r8

15f08: e0266001 eor r6, r6, r1

15f0c: e0011002 and r1, r1, r2

15f10: e0288001 eor r8, r8, r1

15f14: e0222006 eor r2, r2, r6

15f18: e1e05005 mvn r5, r5

15f1c: e580703c str r7, [r0, #60] ; 0x3c

15f20: e580c034 str ip, [r0, #52] ; 0x34

15f24: e580a048 str sl, [r0, #72] ; 0x48

15f28: e580604c str r6, [r0, #76] ; 0x4c

15f2c: e5802044 str r2, [r0, #68] ; 0x44

15f30: e5808040 str r8, [r0, #64] ; 0x40

15f34: e580e058 str lr, [r0, #88] ; 0x58

15f38: e580405c str r4, [r0, #92] ; 0x5c

15f3c: e5805050 str r5, [r0, #80] ; 0x50

15f40: e5907068 ldr r7, [r0, #104] ; 0x68

15f44: e590e064 ldr lr, [r0, #100] ; 0x64

15f48: e590a06c ldr sl, [r0, #108] ; 0x6c

15f4c: e1e06007 mvn r6, r7

15f50: e5908060 ldr r8, [r0, #96] ; 0x60

15f54: e18e5006 orr r5, lr, r6

15f58: e1e03003 mvn r3, r3

15f5c: e5901070 ldr r1, [r0, #112] ; 0x70

15f60: e590b07c ldr fp, [r0, #124] ; 0x7c

15f64: e5803054 str r3, [r0, #84] ; 0x54

15f68: e02a3005 eor r3, sl, r5

15f6c: e1866003 orr r6, r6, r3

15f70: e0033008 and r3, r3, r8

15f74: e021200b eor r2, r1, fp

15f78: e027700e eor r7, r7, lr

15f7c: e02e4003 eor r4, lr, r3

15f80: e1e0c001 mvn ip, r1

15f84: e5901078 ldr r1, [r0, #120] ; 0x78

15f88: e028800a eor r8, r8, sl

15f8c: e18cc002 orr ip, ip, r2

15f90: e1844007 orr r4, r4, r7

15f94: e02cc001 eor ip, ip, r1

15f98: e0244008 eor r4, r4, r8

15f9c: e5901074 ldr r1, [r0, #116] ; 0x74

15fa0: e0277003 eor r7, r7, r3

15fa4: e0233004 eor r3, r3, r4

15fa8: e00aa005 and sl, sl, r5

15fac: e5804064 str r4, [r0, #100] ; 0x64

15fb0: e0266003 eor r6, r6, r3

15fb4: e021400c eor r4, r1, ip

15fb8: e0033007 and r3, r3, r7

15fbc: e5909080 ldr r9, [r0, #128] ; 0x80

15fc0: e023300a eor r3, r3, sl

15fc4: e0211002 eor r1, r1, r2

15fc8: e1822004 orr r2, r2, r4

15fcc: e590a088 ldr sl, [r0, #136] ; 0x88

15fd0: e0266003 eor r6, r6, r3

15fd4: e02bb002 eor fp, fp, r2

15fd8: e5803068 str r3, [r0, #104] ; 0x68

15fdc: e5903084 ldr r3, [r0, #132] ; 0x84

15fe0: e1e02009 mvn r2, r9

15fe4: e5804074 str r4, [r0, #116] ; 0x74

15fe8: e02c400b eor r4, ip, fp

15fec: e00bb00c and fp, fp, ip

15ff0: e580706c str r7, [r0, #108] ; 0x6c

15ff4: e5806060 str r6, [r0, #96] ; 0x60

15ff8: e0297003 eor r7, r9, r3

15ffc: e022600a eor r6, r2, sl

16000: e590308c ldr r3, [r0, #140] ; 0x8c

16004: e02ba001 eor sl, fp, r1

16008: e0011004 and r1, r1, r4

1600c: e5905090 ldr r5, [r0, #144] ; 0x90

16010: e590e09c ldr lr, [r0, #156] ; 0x9c

16014: e02cc001 eor ip, ip, r1

16018: e0299003 eor r9, r9, r3

1601c: e1e0c00c mvn ip, ip

16020: e025100e eor r1, r5, lr

16024: e5908094 ldr r8, [r0, #148] ; 0x94

16028: e1ce3005 bic r3, lr, r5

1602c: e580c07c str ip, [r0, #124] ; 0x7c

16030: e590e098 ldr lr, [r0, #152] ; 0x98

16034: e187c009 orr ip, r7, r9

16038: e026600c eor r6, r6, ip

1603c: e023300e eor r3, r3, lr

16040: e1822006 orr r2, r2, r6

16044: e1e0e008 mvn lr, r8

16048: e0299002 eor r9, r9, r2

1604c: e181200e orr r2, r1, lr

16050: e0222003 eor r2, r2, r3

16054: e1888003 orr r8, r8, r3

16058: e02ee001 eor lr, lr, r1

1605c: e590c08c ldr ip, [r0, #140] ; 0x8c

16060: e0233005 eor r3, r3, r5

16064: e0211008 eor r1, r1, r8

16068: e0055002 and r5, r5, r2

1606c: e008800e and r8, r8, lr

16070: e0255008 eor r5, r5, r8

16074: e024400a eor r4, r4, sl

16078: e00cc006 and ip, ip, r6

1607c: e5804070 str r4, [r0, #112] ; 0x70

16080: e0278006 eor r8, r7, r6

16084: e5904084 ldr r4, [r0, #132] ; 0x84

16088: e00ee005 and lr, lr, r5

1608c: e028800c eor r8, r8, ip

16090: e02ee003 eor lr, lr, r3

16094: e187300c orr r3, r7, ip

16098: e024400c eor r4, r4, ip

1609c: e0233009 eor r3, r3, r9

160a0: e0099008 and r9, r9, r8

160a4: e0299004 eor r9, r9, r4

160a8: e580a078 str sl, [r0, #120] ; 0x78

160ac: e5806080 str r6, [r0, #128] ; 0x80

160b0: e580908c str r9, [r0, #140] ; 0x8c

160b4: e5803088 str r3, [r0, #136] ; 0x88

160b8: e5808084 str r8, [r0, #132] ; 0x84

160bc: e580109c str r1, [r0, #156] ; 0x9c

160c0: e580e094 str lr, [r0, #148] ; 0x94

160c4: e590a0a0 ldr sl, [r0, #160] ; 0xa0

160c8: e5805098 str r5, [r0, #152] ; 0x98

160cc: e59050a8 ldr r5, [r0, #168] ; 0xa8

160d0: e5802090 str r2, [r0, #144] ; 0x90

160d4: e59020ac ldr r2, [r0, #172] ; 0xac

160d8: e590c0a4 ldr ip, [r0, #164] ; 0xa4

160dc: e02ae005 eor lr, sl, r5

160e0: e02ee002 eor lr, lr, r2

160e4: e00a400e and r4, sl, lr

160e8: e18c1002 orr r1, ip, r2

160ec: e0247001 eor r7, r4, r1

160f0: e0244002 eor r4, r4, r2

160f4: e18a2002 orr r2, sl, r2

160f8: e022200c eor r2, r2, ip

160fc: e00cc004 and ip, ip, r4

16100: e02ee00c eor lr, lr, ip

16104: e590c0b0 ldr ip, [r0, #176] ; 0xb0

16108: e59030b8 ldr r3, [r0, #184] ; 0xb8

1610c: e590b0b4 ldr fp, [r0, #180] ; 0xb4

16110: e59060bc ldr r6, [r0, #188] ; 0xbc

16114: e18aa00e orr sl, sl, lr

16118: e0077005 and r7, r7, r5

1611c: e00aa004 and sl, sl, r4

16120: e1e0c00c mvn ip, ip

16124: e0277002 eor r7, r7, r2

16128: e022200e eor r2, r2, lr

1612c: e003500c and r5, r3, ip

16130: e021100a eor r1, r1, sl

16134: e022200a eor r2, r2, sl

16138: e02ba006 eor sl, fp, r6

1613c: e025500a eor r5, r5, sl

16140: e58070a0 str r7, [r0, #160] ; 0xa0

16144: e0237005 eor r7, r3, r5

16148: e007700b and r7, r7, fp

1614c: e023300c eor r3, r3, ip

16150: e59080c0 ldr r8, [r0, #192] ; 0xc0

16154: e58010a4 str r1, [r0, #164] ; 0xa4

16158: e186c00c orr ip, r6, ip

1615c: e59010c4 ldr r1, [r0, #196] ; 0xc4

16160: e1866007 orr r6, r6, r7

16164: e0277003 eor r7, r7, r3

16168: e1853003 orr r3, r5, r3

1616c: e0066003 and r6, r6, r3

16170: e59030b0 ldr r3, [r0, #176] ; 0xb0

16174: e0281001 eor r1, r8, r1

16178: e59090d0 ldr r9, [r0, #208] ; 0xd0

1617c: e59040dc ldr r4, [r0, #220] ; 0xdc

16180: e0266003 eor r6, r6, r3

16184: e1e01001 mvn r1, r1

16188: e59030c8 ldr r3, [r0, #200] ; 0xc8

1618c: e580e0a8 str lr, [r0, #168] ; 0xa8

16190: e1888001 orr r8, r8, r1

16194: e590e0d4 ldr lr, [r0, #212] ; 0xd4

16198: e0288003 eor r8, r8, r3

1619c: e58020ac str r2, [r0, #172] ; 0xac

161a0: e0293004 eor r3, r9, r4

161a4: e59020d8 ldr r2, [r0, #216] ; 0xd8

161a8: e58050b0 str r5, [r0, #176] ; 0xb0

161ac: e00e5003 and r5, lr, r3

161b0: e0255009 eor r5, r5, r9

161b4: e0233002 eor r3, r3, r2

161b8: e02cc00a eor ip, ip, sl

161bc: e590a0cc ldr sl, [r0, #204] ; 0xcc

161c0: e02ee003 eor lr, lr, r3

161c4: e1822005 orr r2, r2, r5

161c8: e02cc007 eor ip, ip, r7

161cc: e0044009 and r4, r4, r9

161d0: e59090c4 ldr r9, [r0, #196] ; 0xc4

161d4: e024400e eor r4, r4, lr

161d8: e02cc006 eor ip, ip, r6

161dc: e02ee002 eor lr, lr, r2

161e0: e58060b8 str r6, [r0, #184] ; 0xb8

161e4: e18a2001 orr r2, sl, r1

161e8: e0236005 eor r6, r3, r5

161ec: e02aa008 eor sl, sl, r8

161f0: e0066004 and r6, r6, r4

161f4: e0222009 eor r2, r2, r9

161f8: e021100a eor r1, r1, sl

161fc: e0255006 eor r5, r5, r6

16200: e0233006 eor r3, r3, r6

16204: e0086002 and r6, r8, r2

16208: e0222008 eor r2, r2, r8

1620c: e0266001 eor r6, r6, r1

16210: e0011002 and r1, r1, r2

16214: e0288001 eor r8, r8, r1

16218: e0222006 eor r2, r2, r6

1621c: e58070bc str r7, [r0, #188] ; 0xbc

16220: e580c0b4 str ip, [r0, #180] ; 0xb4

16224: e580a0c8 str sl, [r0, #200] ; 0xc8

16228: e58060cc str r6, [r0, #204] ; 0xcc

1622c: e58020c4 str r2, [r0, #196] ; 0xc4

16230: e58080c0 str r8, [r0, #192] ; 0xc0

16234: e580e0d8 str lr, [r0, #216] ; 0xd8

16238: e59070e8 ldr r7, [r0, #232] ; 0xe8

1623c: e590e0e4 ldr lr, [r0, #228] ; 0xe4

16240: e590a0ec ldr sl, [r0, #236] ; 0xec

16244: e1e06007 mvn r6, r7

16248: e1e05005 mvn r5, r5

1624c: e59080e0 ldr r8, [r0, #224] ; 0xe0

16250: e58050d0 str r5, [r0, #208] ; 0xd0

16254: e1e03003 mvn r3, r3

16258: e18e5006 orr r5, lr, r6

1625c: e59010f0 ldr r1, [r0, #240] ; 0xf0

16260: e590b0fc ldr fp, [r0, #252] ; 0xfc

16264: e58030d4 str r3, [r0, #212] ; 0xd4

16268: e02a3005 eor r3, sl, r5

1626c: e1866003 orr r6, r6, r3

16270: e0033008 and r3, r3, r8

16274: e021200b eor r2, r1, fp

16278: e58040dc str r4, [r0, #220] ; 0xdc

1627c: e027700e eor r7, r7, lr

16280: e02e4003 eor r4, lr, r3

16284: e1e0c001 mvn ip, r1

16288: e59010f8 ldr r1, [r0, #248] ; 0xf8

1628c: e028800a eor r8, r8, sl

16290: e18cc002 orr ip, ip, r2

16294: e1844007 orr r4, r4, r7

16298: e02cc001 eor ip, ip, r1

1629c: e0244008 eor r4, r4, r8

162a0: e59010f4 ldr r1, [r0, #244] ; 0xf4

162a4: e0277003 eor r7, r7, r3

162a8: e0233004 eor r3, r3, r4

162ac: e00aa005 and sl, sl, r5

162b0: e58040e4 str r4, [r0, #228] ; 0xe4

162b4: e0266003 eor r6, r6, r3

162b8: e021400c eor r4, r1, ip

162bc: e0033007 and r3, r3, r7

162c0: e5909100 ldr r9, [r0, #256] ; 0x100

162c4: e023300a eor r3, r3, sl

162c8: e0211002 eor r1, r1, r2

162cc: e1822004 orr r2, r2, r4

162d0: e590a108 ldr sl, [r0, #264] ; 0x108

162d4: e0266003 eor r6, r6, r3

162d8: e02bb002 eor fp, fp, r2

162dc: e58030e8 str r3, [r0, #232] ; 0xe8

162e0: e5903104 ldr r3, [r0, #260] ; 0x104

162e4: e1e02009 mvn r2, r9

162e8: e58040f4 str r4, [r0, #244] ; 0xf4

162ec: e02c400b eor r4, ip, fp

162f0: e00bb00c and fp, fp, ip

162f4: e58070ec str r7, [r0, #236] ; 0xec

162f8: e58060e0 str r6, [r0, #224] ; 0xe0

162fc: e0297003 eor r7, r9, r3

16300: e022600a eor r6, r2, sl

16304: e590310c ldr r3, [r0, #268] ; 0x10c

16308: e02ba001 eor sl, fp, r1

1630c: e0011004 and r1, r1, r4

16310: e5905110 ldr r5, [r0, #272] ; 0x110

16314: e590e11c ldr lr, [r0, #284] ; 0x11c

16318: e02cc001 eor ip, ip, r1

1631c: e0299003 eor r9, r9, r3

16320: e1e0c00c mvn ip, ip

16324: e025100e eor r1, r5, lr

16328: e5908114 ldr r8, [r0, #276] ; 0x114

1632c: e1ce3005 bic r3, lr, r5

16330: e580c0fc str ip, [r0, #252] ; 0xfc

16334: e590e118 ldr lr, [r0, #280] ; 0x118

16338: e187c009 orr ip, r7, r9

1633c: e026600c eor r6, r6, ip

16340: e023300e eor r3, r3, lr

16344: e1822006 orr r2, r2, r6

16348: e1e0e008 mvn lr, r8

1634c: e0299002 eor r9, r9, r2

16350: e181200e orr r2, r1, lr

16354: e0222003 eor r2, r2, r3

16358: e1888003 orr r8, r8, r3

1635c: e02ee001 eor lr, lr, r1

16360: e590c10c ldr ip, [r0, #268] ; 0x10c

16364: e0233005 eor r3, r3, r5

16368: e0211008 eor r1, r1, r8

1636c: e0055002 and r5, r5, r2

16370: e008800e and r8, r8, lr

16374: e0255008 eor r5, r5, r8

16378: e024400a eor r4, r4, sl

1637c: e00cc006 and ip, ip, r6

16380: e58040f0 str r4, [r0, #240] ; 0xf0

16384: e0278006 eor r8, r7, r6

16388: e5904104 ldr r4, [r0, #260] ; 0x104

1638c: e00ee005 and lr, lr, r5

16390: e028800c eor r8, r8, ip

16394: e02ee003 eor lr, lr, r3

16398: e187300c orr r3, r7, ip

1639c: e024400c eor r4, r4, ip

163a0: e0233009 eor r3, r3, r9

163a4: e0099008 and r9, r9, r8

163a8: e0299004 eor r9, r9, r4

163ac: e580a0f8 str sl, [r0, #248] ; 0xf8

163b0: e5806100 str r6, [r0, #256] ; 0x100

163b4: e580910c str r9, [r0, #268] ; 0x10c

163b8: e5803108 str r3, [r0, #264] ; 0x108

163bc: e5808104 str r8, [r0, #260] ; 0x104

163c0: e590a120 ldr sl, [r0, #288] ; 0x120

163c4: e5805118 str r5, [r0, #280] ; 0x118

163c8: e5905128 ldr r5, [r0, #296] ; 0x128

163cc: e5802110 str r2, [r0, #272] ; 0x110

163d0: e590212c ldr r2, [r0, #300] ; 0x12c

163d4: e590c124 ldr ip, [r0, #292] ; 0x124

163d8: e580e114 str lr, [r0, #276] ; 0x114

163dc: e02ae005 eor lr, sl, r5

163e0: e02ee002 eor lr, lr, r2

163e4: e00a400e and r4, sl, lr

163e8: e580111c str r1, [r0, #284] ; 0x11c

163ec: e18c1002 orr r1, ip, r2

163f0: e0247001 eor r7, r4, r1

163f4: e0244002 eor r4, r4, r2

163f8: e18a2002 orr r2, sl, r2

163fc: e022200c eor r2, r2, ip

16400: e00cc004 and ip, ip, r4

16404: e02ee00c eor lr, lr, ip

16408: e590c130 ldr ip, [r0, #304] ; 0x130

1640c: e5903138 ldr r3, [r0, #312] ; 0x138

16410: e590b134 ldr fp, [r0, #308] ; 0x134

16414: e590613c ldr r6, [r0, #316] ; 0x13c

16418: e18aa00e orr sl, sl, lr

1641c: e0077005 and r7, r7, r5

16420: e00aa004 and sl, sl, r4

16424: e1e0c00c mvn ip, ip

16428: e0277002 eor r7, r7, r2

1642c: e022200e eor r2, r2, lr

16430: e003500c and r5, r3, ip

16434: e021100a eor r1, r1, sl

16438: e022200a eor r2, r2, sl

1643c: e02ba006 eor sl, fp, r6

16440: e025500a eor r5, r5, sl

16444: e5807120 str r7, [r0, #288] ; 0x120

16448: e0237005 eor r7, r3, r5

1644c: e007700b and r7, r7, fp

16450: e023300c eor r3, r3, ip

16454: e5908140 ldr r8, [r0, #320] ; 0x140

16458: e5801124 str r1, [r0, #292] ; 0x124

1645c: e186c00c orr ip, r6, ip

16460: e5901144 ldr r1, [r0, #324] ; 0x144

16464: e1866007 orr r6, r6, r7

16468: e0277003 eor r7, r7, r3

1646c: e1853003 orr r3, r5, r3

16470: e0066003 and r6, r6, r3

16474: e5903130 ldr r3, [r0, #304] ; 0x130

16478: e0281001 eor r1, r8, r1

1647c: e5909150 ldr r9, [r0, #336] ; 0x150

16480: e590415c ldr r4, [r0, #348] ; 0x15c

16484: e0266003 eor r6, r6, r3

16488: e1e01001 mvn r1, r1

1648c: e5903148 ldr r3, [r0, #328] ; 0x148

16490: e580e128 str lr, [r0, #296] ; 0x128

16494: e1888001 orr r8, r8, r1

16498: e590e154 ldr lr, [r0, #340] ; 0x154

1649c: e0288003 eor r8, r8, r3

164a0: e580212c str r2, [r0, #300] ; 0x12c

164a4: e0293004 eor r3, r9, r4

164a8: e5902158 ldr r2, [r0, #344] ; 0x158

164ac: e5805130 str r5, [r0, #304] ; 0x130

164b0: e00e5003 and r5, lr, r3

164b4: e0255009 eor r5, r5, r9

164b8: e0233002 eor r3, r3, r2

164bc: e02cc00a eor ip, ip, sl

164c0: e590a14c ldr sl, [r0, #332] ; 0x14c

164c4: e02ee003 eor lr, lr, r3

164c8: e1822005 orr r2, r2, r5

164cc: e02cc007 eor ip, ip, r7

164d0: e0044009 and r4, r4, r9

164d4: e5909144 ldr r9, [r0, #324] ; 0x144

164d8: e024400e eor r4, r4, lr

164dc: e02cc006 eor ip, ip, r6

164e0: e02ee002 eor lr, lr, r2

164e4: e5806138 str r6, [r0, #312] ; 0x138

164e8: e18a2001 orr r2, sl, r1

164ec: e0236005 eor r6, r3, r5

164f0: e02aa008 eor sl, sl, r8

164f4: e0066004 and r6, r6, r4

164f8: e0222009 eor r2, r2, r9

164fc: e021100a eor r1, r1, sl

16500: e0255006 eor r5, r5, r6

16504: e0233006 eor r3, r3, r6

16508: e0086002 and r6, r8, r2

1650c: e0266001 eor r6, r6, r1

16510: e0222008 eor r2, r2, r8

16514: e0011002 and r1, r1, r2

16518: e0222006 eor r2, r2, r6

1651c: e580713c str r7, [r0, #316] ; 0x13c

16520: e580c134 str ip, [r0, #308] ; 0x134

16524: e580a148 str sl, [r0, #328] ; 0x148

16528: e580614c str r6, [r0, #332] ; 0x14c

1652c: e5802144 str r2, [r0, #324] ; 0x144

16530: e5907168 ldr r7, [r0, #360] ; 0x168

16534: e580e158 str lr, [r0, #344] ; 0x158

16538: e590e164 ldr lr, [r0, #356] ; 0x164

1653c: e590a16c ldr sl, [r0, #364] ; 0x16c

16540: e0288001 eor r8, r8, r1

16544: e1e06007 mvn r6, r7

16548: e1e05005 mvn r5, r5

1654c: e5808140 str r8, [r0, #320] ; 0x140

16550: e5805150 str r5, [r0, #336] ; 0x150

16554: e5908160 ldr r8, [r0, #352] ; 0x160

16558: e18e5006 orr r5, lr, r6

1655c: e1e03003 mvn r3, r3

16560: e5901170 ldr r1, [r0, #368] ; 0x170

16564: e590b17c ldr fp, [r0, #380] ; 0x17c

16568: e5803154 str r3, [r0, #340] ; 0x154

1656c: e02a3005 eor r3, sl, r5

16570: e1866003 orr r6, r6, r3

16574: e0033008 and r3, r3, r8

16578: e021200b eor r2, r1, fp

1657c: e580415c str r4, [r0, #348] ; 0x15c

16580: e027700e eor r7, r7, lr

16584: e02e4003 eor r4, lr, r3

16588: e1e0c001 mvn ip, r1

1658c: e5901178 ldr r1, [r0, #376] ; 0x178

16590: e028800a eor r8, r8, sl

16594: e18cc002 orr ip, ip, r2

16598: e1844007 orr r4, r4, r7

1659c: e02cc001 eor ip, ip, r1

165a0: e0244008 eor r4, r4, r8

165a4: e5901174 ldr r1, [r0, #372] ; 0x174

165a8: e0277003 eor r7, r7, r3

165ac: e0233004 eor r3, r3, r4

165b0: e00aa005 and sl, sl, r5

165b4: e5804164 str r4, [r0, #356] ; 0x164

165b8: e0266003 eor r6, r6, r3

165bc: e021400c eor r4, r1, ip

165c0: e0033007 and r3, r3, r7

165c4: e5909180 ldr r9, [r0, #384] ; 0x180

165c8: e023300a eor r3, r3, sl

165cc: e0211002 eor r1, r1, r2

165d0: e1822004 orr r2, r2, r4

165d4: e590a188 ldr sl, [r0, #392] ; 0x188

165d8: e0266003 eor r6, r6, r3

165dc: e02bb002 eor fp, fp, r2

165e0: e5803168 str r3, [r0, #360] ; 0x168

165e4: e5903184 ldr r3, [r0, #388] ; 0x184

165e8: e1e02009 mvn r2, r9

165ec: e5804174 str r4, [r0, #372] ; 0x174

165f0: e02c400b eor r4, ip, fp

165f4: e00bb00c and fp, fp, ip

165f8: e580716c str r7, [r0, #364] ; 0x16c

165fc: e5806160 str r6, [r0, #352] ; 0x160

16600: e0297003 eor r7, r9, r3

16604: e022600a eor r6, r2, sl

16608: e590318c ldr r3, [r0, #396] ; 0x18c

1660c: e02ba001 eor sl, fp, r1

16610: e0011004 and r1, r1, r4

16614: e5905190 ldr r5, [r0, #400] ; 0x190

16618: e590e19c ldr lr, [r0, #412] ; 0x19c

1661c: e02cc001 eor ip, ip, r1

16620: e0299003 eor r9, r9, r3

16624: e1e0c00c mvn ip, ip

16628: e025100e eor r1, r5, lr

1662c: e5908194 ldr r8, [r0, #404] ; 0x194

16630: e1ce3005 bic r3, lr, r5

16634: e580c17c str ip, [r0, #380] ; 0x17c

16638: e590e198 ldr lr, [r0, #408] ; 0x198

1663c: e187c009 orr ip, r7, r9

16640: e026600c eor r6, r6, ip

16644: e023300e eor r3, r3, lr

16648: e1822006 orr r2, r2, r6

1664c: e1e0e008 mvn lr, r8

16650: e0299002 eor r9, r9, r2

16654: e181200e orr r2, r1, lr

16658: e0222003 eor r2, r2, r3

1665c: e1888003 orr r8, r8, r3

16660: e02ee001 eor lr, lr, r1

16664: e590c18c ldr ip, [r0, #396] ; 0x18c

16668: e0233005 eor r3, r3, r5

1666c: e0211008 eor r1, r1, r8

16670: e0055002 and r5, r5, r2

16674: e008800e and r8, r8, lr

16678: e0255008 eor r5, r5, r8

1667c: e024400a eor r4, r4, sl

16680: e00cc006 and ip, ip, r6

16684: e5804170 str r4, [r0, #368] ; 0x170

16688: e0278006 eor r8, r7, r6

1668c: e5904184 ldr r4, [r0, #388] ; 0x184

16690: e00ee005 and lr, lr, r5

16694: e028800c eor r8, r8, ip

16698: e02ee003 eor lr, lr, r3

1669c: e187300c orr r3, r7, ip

166a0: e024400c eor r4, r4, ip

166a4: e0233009 eor r3, r3, r9

166a8: e0099008 and r9, r9, r8

166ac: e0299004 eor r9, r9, r4

166b0: e580a178 str sl, [r0, #376] ; 0x178

166b4: e5806180 str r6, [r0, #384] ; 0x180

166b8: e580918c str r9, [r0, #396] ; 0x18c

166bc: e590a1a0 ldr sl, [r0, #416] ; 0x1a0

166c0: e5805198 str r5, [r0, #408] ; 0x198

166c4: e59051a8 ldr r5, [r0, #424] ; 0x1a8

166c8: e5802190 str r2, [r0, #400] ; 0x190

166cc: e59021ac ldr r2, [r0, #428] ; 0x1ac

166d0: e590c1a4 ldr ip, [r0, #420] ; 0x1a4

166d4: e580e194 str lr, [r0, #404] ; 0x194

166d8: e02ae005 eor lr, sl, r5

166dc: e02ee002 eor lr, lr, r2

166e0: e00a400e and r4, sl, lr

166e4: e580119c str r1, [r0, #412] ; 0x19c

166e8: e18c1002 orr r1, ip, r2

166ec: e0247001 eor r7, r4, r1

166f0: e0244002 eor r4, r4, r2

166f4: e18a2002 orr r2, sl, r2

166f8: e022200c eor r2, r2, ip

166fc: e00cc004 and ip, ip, r4

16700: e02ee00c eor lr, lr, ip

16704: e590c1b0 ldr ip, [r0, #432] ; 0x1b0

16708: e590b1b4 ldr fp, [r0, #436] ; 0x1b4

1670c: e59061bc ldr r6, [r0, #444] ; 0x1bc

16710: e5803188 str r3, [r0, #392] ; 0x188

16714: e18aa00e orr sl, sl, lr

16718: e59031b8 ldr r3, [r0, #440] ; 0x1b8

1671c: e0077005 and r7, r7, r5

16720: e00aa004 and sl, sl, r4

16724: e1e0c00c mvn ip, ip

16728: e0277002 eor r7, r7, r2

1672c: e022200e eor r2, r2, lr

16730: e003500c and r5, r3, ip

16734: e021100a eor r1, r1, sl

16738: e022200a eor r2, r2, sl

1673c: e02ba006 eor sl, fp, r6

16740: e025500a eor r5, r5, sl

16744: e58071a0 str r7, [r0, #416] ; 0x1a0

16748: e0237005 eor r7, r3, r5

1674c: e007700b and r7, r7, fp

16750: e023300c eor r3, r3, ip

16754: e5808184 str r8, [r0, #388] ; 0x184

16758: e58011a4 str r1, [r0, #420] ; 0x1a4

1675c: e59081c0 ldr r8, [r0, #448] ; 0x1c0

16760: e59011c4 ldr r1, [r0, #452] ; 0x1c4

16764: e186c00c orr ip, r6, ip

16768: e1866007 orr r6, r6, r7

1676c: e0277003 eor r7, r7, r3

16770: e1853003 orr r3, r5, r3

16774: e0066003 and r6, r6, r3

16778: e59031b0 ldr r3, [r0, #432] ; 0x1b0

1677c: e0281001 eor r1, r8, r1

16780: e59091d0 ldr r9, [r0, #464] ; 0x1d0

16784: e59041dc ldr r4, [r0, #476] ; 0x1dc

16788: e0266003 eor r6, r6, r3

1678c: e1e01001 mvn r1, r1

16790: e59031c8 ldr r3, [r0, #456] ; 0x1c8

16794: e580e1a8 str lr, [r0, #424] ; 0x1a8

16798: e1888001 orr r8, r8, r1

1679c: e590e1d4 ldr lr, [r0, #468] ; 0x1d4

167a0: e0288003 eor r8, r8, r3

167a4: e58021ac str r2, [r0, #428] ; 0x1ac

167a8: e0293004 eor r3, r9, r4

167ac: e59021d8 ldr r2, [r0, #472] ; 0x1d8

167b0: e58051b0 str r5, [r0, #432] ; 0x1b0

167b4: e00e5003 and r5, lr, r3

167b8: e0255009 eor r5, r5, r9

167bc: e0233002 eor r3, r3, r2

167c0: e02cc00a eor ip, ip, sl

167c4: e590a1cc ldr sl, [r0, #460] ; 0x1cc

167c8: e02ee003 eor lr, lr, r3

167cc: e1822005 orr r2, r2, r5

167d0: e02cc007 eor ip, ip, r7

167d4: e0044009 and r4, r4, r9

167d8: e59091c4 ldr r9, [r0, #452] ; 0x1c4

167dc: e024400e eor r4, r4, lr

167e0: e02cc006 eor ip, ip, r6

167e4: e02ee002 eor lr, lr, r2

167e8: e58061b8 str r6, [r0, #440] ; 0x1b8

167ec: e18a2001 orr r2, sl, r1

167f0: e0236005 eor r6, r3, r5

167f4: e02aa008 eor sl, sl, r8

167f8: e0066004 and r6, r6, r4

167fc: e0222009 eor r2, r2, r9

16800: e021100a eor r1, r1, sl

16804: e58071bc str r7, [r0, #444] ; 0x1bc

16808: e580c1b4 str ip, [r0, #436] ; 0x1b4

1680c: e0255006 eor r5, r5, r6

16810: e580a1c8 str sl, [r0, #456] ; 0x1c8

16814: e0233006 eor r3, r3, r6

16818: e59071e8 ldr r7, [r0, #488] ; 0x1e8

1681c: e0086002 and r6, r8, r2

16820: e0222008 eor r2, r2, r8

16824: e0266001 eor r6, r6, r1

16828: e580e1d8 str lr, [r0, #472] ; 0x1d8

1682c: e0011002 and r1, r1, r2

16830: e590e1e4 ldr lr, [r0, #484] ; 0x1e4

16834: e590a1ec ldr sl, [r0, #492] ; 0x1ec

16838: e0222006 eor r2, r2, r6

1683c: e0288001 eor r8, r8, r1

16840: e58061cc str r6, [r0, #460] ; 0x1cc

16844: e1e05005 mvn r5, r5

16848: e1e06007 mvn r6, r7

1684c: e58081c0 str r8, [r0, #448] ; 0x1c0

16850: e58051d0 str r5, [r0, #464] ; 0x1d0

16854: e59081e0 ldr r8, [r0, #480] ; 0x1e0

16858: e18e5006 orr r5, lr, r6

1685c: e1e03003 mvn r3, r3

16860: e59011f0 ldr r1, [r0, #496] ; 0x1f0

16864: e590b1fc ldr fp, [r0, #508] ; 0x1fc

16868: e58031d4 str r3, [r0, #468] ; 0x1d4

1686c: e02a3005 eor r3, sl, r5

16870: e1866003 orr r6, r6, r3

16874: e0033008 and r3, r3, r8

16878: e58021c4 str r2, [r0, #452] ; 0x1c4

1687c: e58041dc str r4, [r0, #476] ; 0x1dc

16880: e021200b eor r2, r1, fp

16884: e027700e eor r7, r7, lr

16888: e02e4003 eor r4, lr, r3

1688c: e1e0c001 mvn ip, r1

16890: e59011f8 ldr r1, [r0, #504] ; 0x1f8

16894: e028800a eor r8, r8, sl

16898: e18cc002 orr ip, ip, r2

1689c: e1844007 orr r4, r4, r7

168a0: e02cc001 eor ip, ip, r1

168a4: e0244008 eor r4, r4, r8

168a8: e59011f4 ldr r1, [r0, #500] ; 0x1f4

168ac: e0277003 eor r7, r7, r3

168b0: e0233004 eor r3, r3, r4

168b4: e00aa005 and sl, sl, r5

168b8: e58041e4 str r4, [r0, #484] ; 0x1e4

168bc: e0266003 eor r6, r6, r3

168c0: e021400c eor r4, r1, ip

168c4: e0033007 and r3, r3, r7

168c8: e5909200 ldr r9, [r0, #512] ; 0x200

168cc: e023300a eor r3, r3, sl

168d0: e0211002 eor r1, r1, r2

168d4: e1822004 orr r2, r2, r4

168d8: e590a208 ldr sl, [r0, #520] ; 0x208

168dc: e0266003 eor r6, r6, r3

168e0: e02bb002 eor fp, fp, r2

168e4: e58031e8 str r3, [r0, #488] ; 0x1e8

168e8: e5903204 ldr r3, [r0, #516] ; 0x204

168ec: e1e02009 mvn r2, r9

168f0: e58041f4 str r4, [r0, #500] ; 0x1f4

168f4: e02c400b eor r4, ip, fp

168f8: e00bb00c and fp, fp, ip

168fc: e58071ec str r7, [r0, #492] ; 0x1ec

16900: e58061e0 str r6, [r0, #480] ; 0x1e0

16904: e0297003 eor r7, r9, r3

16908: e022600a eor r6, r2, sl

1690c: e590320c ldr r3, [r0, #524] ; 0x20c

16910: e02ba001 eor sl, fp, r1

16914: e0011004 and r1, r1, r4

16918: e5905210 ldr r5, [r0, #528] ; 0x210

1691c: e590e21c ldr lr, [r0, #540] ; 0x21c

16920: e02cc001 eor ip, ip, r1

16924: e0299003 eor r9, r9, r3

16928: e1e0c00c mvn ip, ip

1692c: e025100e eor r1, r5, lr

16930: e5908214 ldr r8, [r0, #532] ; 0x214

16934: e1ce3005 bic r3, lr, r5

16938: e580c1fc str ip, [r0, #508] ; 0x1fc

1693c: e590e218 ldr lr, [r0, #536] ; 0x218

16940: e187c009 orr ip, r7, r9

16944: e026600c eor r6, r6, ip

16948: e023300e eor r3, r3, lr

1694c: e1822006 orr r2, r2, r6

16950: e1e0e008 mvn lr, r8

16954: e0299002 eor r9, r9, r2

16958: e181200e orr r2, r1, lr

1695c: e0222003 eor r2, r2, r3

16960: e1888003 orr r8, r8, r3

16964: e02ee001 eor lr, lr, r1

16968: e590c20c ldr ip, [r0, #524] ; 0x20c

1696c: e0233005 eor r3, r3, r5

16970: e0211008 eor r1, r1, r8

16974: e0055002 and r5, r5, r2

16978: e008800e and r8, r8, lr

1697c: e0255008 eor r5, r5, r8

16980: e00cc006 and ip, ip, r6

16984: e00ee005 and lr, lr, r5

16988: e024400a eor r4, r4, sl

1698c: e02ee003 eor lr, lr, r3

16990: e187300c orr r3, r7, ip

16994: e58041f0 str r4, [r0, #496] ; 0x1f0

16998: e0233009 eor r3, r3, r9

1699c: e5904204 ldr r4, [r0, #516] ; 0x204

169a0: e580a1f8 str sl, [r0, #504] ; 0x1f8

169a4: e5805218 str r5, [r0, #536] ; 0x218

169a8: e5802210 str r2, [r0, #528] ; 0x210

169ac: e5905228 ldr r5, [r0, #552] ; 0x228

169b0: e5902220 ldr r2, [r0, #544] ; 0x220

169b4: e0278006 eor r8, r7, r6

169b8: e5803208 str r3, [r0, #520] ; 0x208

169bc: e590322c ldr r3, [r0, #556] ; 0x22c

169c0: e028800c eor r8, r8, ip

169c4: e024400c eor r4, r4, ip

169c8: e022c005 eor ip, r2, r5

169cc: e02cc003 eor ip, ip, r3

169d0: e5806200 str r6, [r0, #512] ; 0x200

169d4: e580121c str r1, [r0, #540] ; 0x21c

169d8: e5906224 ldr r6, [r0, #548] ; 0x224

169dc: e002100c and r1, r2, ip

169e0: e0099008 and r9, r9, r8

169e4: e0299004 eor r9, r9, r4

169e8: e0234001 eor r4, r3, r1

169ec: e0067004 and r7, r6, r4

169f0: e02cc007 eor ip, ip, r7

169f4: e580e214 str lr, [r0, #532] ; 0x214

169f8: e186e003 orr lr, r6, r3

169fc: e1823003 orr r3, r2, r3

16a00: e0233006 eor r3, r3, r6

16a04: e021100e eor r1, r1, lr

16a08: e182200c orr r2, r2, ip

16a0c: e0022004 and r2, r2, r4

16a10: e0011005 and r1, r1, r5

16a14: e02c4003 eor r4, ip, r3

16a18: e02ee002 eor lr, lr, r2

16a1c: e0233001 eor r3, r3, r1

16a20: e0222004 eor r2, r2, r4

16a24: e580920c str r9, [r0, #524] ; 0x20c

16a28: e5808204 str r8, [r0, #516] ; 0x204

16a2c: e580e224 str lr, [r0, #548] ; 0x224

16a30: e580c228 str ip, [r0, #552] ; 0x228

16a34: e580222c str r2, [r0, #556] ; 0x22c

16a38: e5803220 str r3, [r0, #544] ; 0x220

16a3c: e3a00000 mov r0, #0

16a40: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

16a44: e3560c01 cmp r6, #256 ; 0x100

16a48: e2402004 sub r2, r0, #4

16a4c: 0afffccd beq 15d88 <mcrypt\_mutex\_register@@Base+0x115f4>

16a50: e1a04003 mov r4, r3

16a54: eafffcbc b 15d4c <mcrypt\_mutex\_register@@Base+0x115b8>

16a58: e3e00000 mvn r0, #0

16a5c: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

16a60: 9e3779b9 ; <UNDEFINED> instruction: 0x9e3779b9

16a64: e92d41f0 push {r4, r5, r6, r7, r8, lr}

16a68: e5902020 ldr r2, [r0, #32]

16a6c: e590c02c ldr ip, [r0, #44] ; 0x2c

16a70: e5915000 ldr r5, [r1]

16a74: e591e00c ldr lr, [r1, #12]

16a78: e5907024 ldr r7, [r0, #36] ; 0x24

16a7c: e5906028 ldr r6, [r0, #40] ; 0x28

16a80: e5914004 ldr r4, [r1, #4]

16a84: e5913008 ldr r3, [r1, #8]

16a88: e0255002 eor r5, r5, r2

16a8c: e02ee00c eor lr, lr, ip

16a90: e025c00e eor ip, r5, lr

16a94: e0244007 eor r4, r4, r7

16a98: e0233006 eor r3, r3, r6

16a9c: e004200c and r2, r4, ip

16aa0: e02cc003 eor ip, ip, r3

16aa4: e0222005 eor r2, r2, r5

16aa8: e024400c eor r4, r4, ip

16aac: e00ee005 and lr, lr, r5

16ab0: e02ee004 eor lr, lr, r4

16ab4: e02c6002 eor r6, ip, r2

16ab8: e006600e and r6, r6, lr

16abc: e1833002 orr r3, r3, r2

16ac0: e0233004 eor r3, r3, r4

16ac4: e0222006 eor r2, r2, r6

16ac8: e1a05ee3 ror r5, r3, #29

16acc: e1e029e2 mvn r2, r2, ror #19

16ad0: e02cc006 eor ip, ip, r6

16ad4: e02ee005 eor lr, lr, r5

16ad8: e02cc005 eor ip, ip, r5

16adc: e02ee182 eor lr, lr, r2, lsl #3

16ae0: e02cc002 eor ip, ip, r2

16ae4: e1a0ecee ror lr, lr, #25

16ae8: e1e0cfec mvn ip, ip, ror #31

16aec: e02e400c eor r4, lr, ip

16af0: e0222004 eor r2, r2, r4

16af4: e5907034 ldr r7, [r0, #52] ; 0x34

16af8: e5904030 ldr r4, [r0, #48] ; 0x30

16afc: e027700c eor r7, r7, ip

16b00: e0244de2 eor r4, r4, r2, ror #27

16b04: e025500e eor r5, r5, lr

16b08: e590203c ldr r2, [r0, #60] ; 0x3c

16b0c: e025538c eor r5, r5, ip, lsl #7

16b10: e0243007 eor r3, r4, r7

16b14: e590c038 ldr ip, [r0, #56] ; 0x38

16b18: e1e03003 mvn r3, r3

16b1c: e02cc565 eor ip, ip, r5, ror #10

16b20: e02ee002 eor lr, lr, r2

16b24: e1845003 orr r5, r4, r3

16b28: e025500c eor r5, r5, ip

16b2c: e18e2003 orr r2, lr, r3

16b30: e0222007 eor r2, r2, r7

16b34: e02ee005 eor lr, lr, r5

16b38: e023600e eor r6, r3, lr

16b3c: e0254002 eor r4, r5, r2

16b40: e006c004 and ip, r6, r4

16b44: e1a0eeee ror lr, lr, #29

16b48: e0053002 and r3, r5, r2

16b4c: e02cc005 eor ip, ip, r5

16b50: e0233006 eor r3, r3, r6

16b54: e024200e eor r2, r4, lr

16b58: e1a0c9ec ror ip, ip, #19

16b5c: e0222003 eor r2, r2, r3

16b60: e022200c eor r2, r2, ip

16b64: e023300e eor r3, r3, lr

16b68: e023318c eor r3, r3, ip, lsl #3

16b6c: e1a02fe2 ror r2, r2, #31

16b70: e1a03ce3 ror r3, r3, #25

16b74: e02cc002 eor ip, ip, r2

16b78: e0234382 eor r4, r3, r2, lsl #7

16b7c: e02cc003 eor ip, ip, r3

16b80: e5906040 ldr r6, [r0, #64] ; 0x40

16b84: e5907048 ldr r7, [r0, #72] ; 0x48

16b88: e5905044 ldr r5, [r0, #68] ; 0x44

16b8c: e02ee004 eor lr, lr, r4

16b90: e590404c ldr r4, [r0, #76] ; 0x4c

16b94: e0266dec eor r6, r6, ip, ror #27

16b98: e027e56e eor lr, r7, lr, ror #10

16b9c: e0233004 eor r3, r3, r4

16ba0: e0255002 eor r5, r5, r2

16ba4: e1e02006 mvn r2, r6

16ba8: e0257003 eor r7, r5, r3

16bac: e00e4002 and r4, lr, r2

16bb0: e0244007 eor r4, r4, r7

16bb4: e02ec004 eor ip, lr, r4

16bb8: e005500c and r5, r5, ip

16bbc: e02ee002 eor lr, lr, r2

16bc0: e184c00e orr ip, r4, lr

16bc4: e1832002 orr r2, r3, r2

16bc8: e1833005 orr r3, r3, r5

16bcc: e1a049e4 ror r4, r4, #19

16bd0: e0222007 eor r2, r2, r7

16bd4: e00cc003 and ip, ip, r3

16bd8: e02cc006 eor ip, ip, r6

16bdc: e02e3005 eor r3, lr, r5

16be0: e0222004 eor r2, r2, r4

16be4: e0222003 eor r2, r2, r3

16be8: e022200c eor r2, r2, ip

16bec: e0233184 eor r3, r3, r4, lsl #3

16bf0: e1a0ceec ror ip, ip, #29

16bf4: e022200c eor r2, r2, ip

16bf8: e023300c eor r3, r3, ip

16bfc: e1a02fe2 ror r2, r2, #31

16c00: e1a03ce3 ror r3, r3, #25

16c04: e023e002 eor lr, r3, r2

16c08: e02cc003 eor ip, ip, r3

16c0c: e5905050 ldr r5, [r0, #80] ; 0x50

16c10: e02cc382 eor ip, ip, r2, lsl #7

16c14: e5908058 ldr r8, [r0, #88] ; 0x58

16c18: e024400e eor r4, r4, lr

16c1c: e590e05c ldr lr, [r0, #92] ; 0x5c

16c20: e0254de4 eor r4, r5, r4, ror #27

16c24: e028856c eor r8, r8, ip, ror #10

16c28: e5905054 ldr r5, [r0, #84] ; 0x54

16c2c: e023300e eor r3, r3, lr

16c30: e0247008 eor r7, r4, r8

16c34: e0222005 eor r2, r2, r5

16c38: e0277003 eor r7, r7, r3

16c3c: e004e007 and lr, r4, r7

16c40: e1826003 orr r6, r2, r3

16c44: e02ec006 eor ip, lr, r6

16c48: e02ee003 eor lr, lr, r3

16c4c: e1843003 orr r3, r4, r3

16c50: e0233002 eor r3, r3, r2

16c54: e002500e and r5, r2, lr

16c58: e00cc008 and ip, ip, r8

16c5c: e0255007 eor r5, r5, r7

16c60: e02cc003 eor ip, ip, r3

16c64: e1844005 orr r4, r4, r5

16c68: e1a0c9ec ror ip, ip, #19

16c6c: e0233005 eor r3, r3, r5

16c70: e1a05ee5 ror r5, r5, #29

16c74: e004400e and r4, r4, lr

16c78: e0233005 eor r3, r3, r5

16c7c: e02ce005 eor lr, ip, r5

16c80: e026600e eor r6, r6, lr

16c84: e0233004 eor r3, r3, r4

16c88: e023318c eor r3, r3, ip, lsl #3

16c8c: e0244006 eor r4, r4, r6

16c90: e1a03ce3 ror r3, r3, #25

16c94: e1a04fe4 ror r4, r4, #31

16c98: e0232004 eor r2, r3, r4

16c9c: e590706c ldr r7, [r0, #108] ; 0x6c

16ca0: e02cc002 eor ip, ip, r2

16ca4: e590e064 ldr lr, [r0, #100] ; 0x64

16ca8: e5902060 ldr r2, [r0, #96] ; 0x60

16cac: e0255003 eor r5, r5, r3

16cb0: e5906068 ldr r6, [r0, #104] ; 0x68

16cb4: e0255384 eor r5, r5, r4, lsl #7

16cb8: e0222dec eor r2, r2, ip, ror #27

16cbc: e02ee004 eor lr, lr, r4

16cc0: e0233007 eor r3, r3, r7

16cc4: e0265565 eor r5, r6, r5, ror #10

16cc8: e1c37002 bic r7, r3, r2

16ccc: e1e0600e mvn r6, lr

16cd0: e0233002 eor r3, r3, r2

16cd4: e0277005 eor r7, r7, r5

16cd8: e1835006 orr r5, r3, r6

16cdc: e18ee007 orr lr, lr, r7

16ce0: e0266003 eor r6, r6, r3

16ce4: e0255007 eor r5, r5, r7

16ce8: e00e4006 and r4, lr, r6

16cec: e002c005 and ip, r2, r5

16cf0: e02cc004 eor ip, ip, r4

16cf4: e1a059e5 ror r5, r5, #19

16cf8: e023300e eor r3, r3, lr

16cfc: e0222007 eor r2, r2, r7

16d00: e006600c and r6, r6, ip

16d04: e0233185 eor r3, r3, r5, lsl #3

16d08: e1a0ceec ror ip, ip, #29

16d0c: e0222005 eor r2, r2, r5

16d10: e023300c eor r3, r3, ip

16d14: e0222006 eor r2, r2, r6

16d18: e022200c eor r2, r2, ip

16d1c: e1a03ce3 ror r3, r3, #25

16d20: e025e003 eor lr, r5, r3

16d24: e1a02fe2 ror r2, r2, #31

16d28: e022500e eor r5, r2, lr

16d2c: e5907074 ldr r7, [r0, #116] ; 0x74

16d30: e02cc003 eor ip, ip, r3

16d34: e590e070 ldr lr, [r0, #112] ; 0x70

16d38: e590607c ldr r6, [r0, #124] ; 0x7c

16d3c: e02c4382 eor r4, ip, r2, lsl #7

16d40: e590c078 ldr ip, [r0, #120] ; 0x78

16d44: e02eede5 eor lr, lr, r5, ror #27

16d48: e0222007 eor r2, r2, r7

16d4c: e0266003 eor r6, r6, r3

16d50: e02e5006 eor r5, lr, r6

16d54: e02e3002 eor r3, lr, r2

16d58: e02cc564 eor ip, ip, r4, ror #10

16d5c: e1e0400e mvn r4, lr

16d60: e02cc004 eor ip, ip, r4

16d64: e183e005 orr lr, r3, r5

16d68: e02cc00e eor ip, ip, lr

16d6c: e006600c and r6, r6, ip

16d70: e184e00c orr lr, r4, ip

16d74: e02ee005 eor lr, lr, r5

16d78: e1834006 orr r4, r3, r6

16d7c: e023300c eor r3, r3, ip

16d80: e1a0c9ec ror ip, ip, #19

16d84: e0233006 eor r3, r3, r6

16d88: e0222006 eor r2, r2, r6

16d8c: e024400e eor r4, r4, lr

16d90: e022218c eor r2, r2, ip, lsl #3

16d94: e00ee003 and lr, lr, r3

16d98: e1a04ee4 ror r4, r4, #29

16d9c: e022200e eor r2, r2, lr

16da0: e023300c eor r3, r3, ip

16da4: e0222004 eor r2, r2, r4

16da8: e0233004 eor r3, r3, r4

16dac: e1a02ce2 ror r2, r2, #25

16db0: e1a03fe3 ror r3, r3, #31

16db4: e0225003 eor r5, r2, r3

16db8: e025500c eor r5, r5, ip

16dbc: e590e08c ldr lr, [r0, #140] ; 0x8c

16dc0: e590c080 ldr ip, [r0, #128] ; 0x80

16dc4: e0244002 eor r4, r4, r2

16dc8: e5906088 ldr r6, [r0, #136] ; 0x88

16dcc: e022200e eor r2, r2, lr

16dd0: e0244383 eor r4, r4, r3, lsl #7

16dd4: e02ccde5 eor ip, ip, r5, ror #27

16dd8: e590e084 ldr lr, [r0, #132] ; 0x84

16ddc: e02c5002 eor r5, ip, r2

16de0: e1e0c00c mvn ip, ip

16de4: e0264564 eor r4, r6, r4, ror #10

16de8: e18cc005 orr ip, ip, r5

16dec: e02cc004 eor ip, ip, r4

16df0: e023300e eor r3, r3, lr

16df4: e023600c eor r6, r3, ip

16df8: e185e006 orr lr, r5, r6

16dfc: e02ee002 eor lr, lr, r2

16e00: e0233005 eor r3, r3, r5

16e04: e00c200e and r2, ip, lr

16e08: e0222003 eor r2, r2, r3

16e0c: e02ee00c eor lr, lr, ip

16e10: e022400e eor r4, r2, lr

16e14: e1a02ee2 ror r2, r2, #29

16e18: e1a059e4 ror r5, r4, #19

16e1c: e003300e and r3, r3, lr

16e20: e026e002 eor lr, r6, r2

16e24: e02ee005 eor lr, lr, r5

16e28: e023300c eor r3, r3, ip

16e2c: e0233002 eor r3, r3, r2

16e30: e1a0efee ror lr, lr, #31

16e34: e0233185 eor r3, r3, r5, lsl #3

16e38: e022238e eor r2, r2, lr, lsl #7

16e3c: e1e0cce3 mvn ip, r3, ror #25

16e40: e022200c eor r2, r2, ip

16e44: e5903098 ldr r3, [r0, #152] ; 0x98

16e48: e5906094 ldr r6, [r0, #148] ; 0x94

16e4c: e025400e eor r4, r5, lr

16e50: e0232562 eor r2, r3, r2, ror #10

16e54: e590309c ldr r3, [r0, #156] ; 0x9c

16e58: e5908090 ldr r8, [r0, #144] ; 0x90

16e5c: e1e07002 mvn r7, r2

16e60: e02ee006 eor lr, lr, r6

16e64: e024400c eor r4, r4, ip

16e68: e18e6007 orr r6, lr, r7

16e6c: e02cc003 eor ip, ip, r3

16e70: e02c3006 eor r3, ip, r6

16e74: e0284de4 eor r4, r8, r4, ror #27

16e78: e0045003 and r5, r4, r3

16e7c: e022200e eor r2, r2, lr

16e80: e02ee005 eor lr, lr, r5

16e84: e18ee002 orr lr, lr, r2

16e88: e024400c eor r4, r4, ip

16e8c: e024400e eor r4, r4, lr

16e90: e0222005 eor r2, r2, r5

16e94: e0255004 eor r5, r5, r4

16e98: e002e005 and lr, r2, r5

16e9c: e1873003 orr r3, r7, r3

16ea0: e00cc006 and ip, ip, r6

16ea4: e02cc00e eor ip, ip, lr

16ea8: e0233005 eor r3, r3, r5

16eac: e023300c eor r3, r3, ip

16eb0: e1a0ceec ror ip, ip, #29

16eb4: e1a039e3 ror r3, r3, #19

16eb8: e024400c eor r4, r4, ip

16ebc: e0244003 eor r4, r4, r3

16ec0: e022200c eor r2, r2, ip

16ec4: e0222183 eor r2, r2, r3, lsl #3

16ec8: e1a04fe4 ror r4, r4, #31

16ecc: e1a02ce2 ror r2, r2, #25

16ed0: e0233004 eor r3, r3, r4

16ed4: e0225384 eor r5, r2, r4, lsl #7

16ed8: e59070ac ldr r7, [r0, #172] ; 0xac

16edc: e0233002 eor r3, r3, r2

16ee0: e590e0a0 ldr lr, [r0, #160] ; 0xa0

16ee4: e59060a4 ldr r6, [r0, #164] ; 0xa4

16ee8: e02cc005 eor ip, ip, r5

16eec: e59050a8 ldr r5, [r0, #168] ; 0xa8

16ef0: e02eede3 eor lr, lr, r3, ror #27

16ef4: e0222007 eor r2, r2, r7

16ef8: e025556c eor r5, r5, ip, ror #10

16efc: e0244006 eor r4, r4, r6

16f00: e02ec002 eor ip, lr, r2

16f04: e004300c and r3, r4, ip

16f08: e02cc005 eor ip, ip, r5

16f0c: e023300e eor r3, r3, lr

16f10: e024400c eor r4, r4, ip

16f14: e002200e and r2, r2, lr

16f18: e0222004 eor r2, r2, r4

16f1c: e02ce003 eor lr, ip, r3

16f20: e00ee002 and lr, lr, r2

16f24: e1855003 orr r5, r5, r3

16f28: e0244005 eor r4, r4, r5

16f2c: e023300e eor r3, r3, lr

16f30: e1a05ee4 ror r5, r4, #29

16f34: e1e039e3 mvn r3, r3, ror #19

16f38: e02cc00e eor ip, ip, lr

16f3c: e02cc005 eor ip, ip, r5

16f40: e022e005 eor lr, r2, r5

16f44: e02ee183 eor lr, lr, r3, lsl #3

16f48: e02cc003 eor ip, ip, r3

16f4c: e1a0ecee ror lr, lr, #25

16f50: e1e0cfec mvn ip, ip, ror #31

16f54: e02e400c eor r4, lr, ip

16f58: e59020b4 ldr r2, [r0, #180] ; 0xb4

16f5c: e0233004 eor r3, r3, r4

16f60: e59040b0 ldr r4, [r0, #176] ; 0xb0

16f64: e02c7002 eor r7, ip, r2

16f68: e0244de3 eor r4, r4, r3, ror #27

16f6c: e025500e eor r5, r5, lr

16f70: e59020bc ldr r2, [r0, #188] ; 0xbc

16f74: e025538c eor r5, r5, ip, lsl #7

16f78: e0243007 eor r3, r4, r7

16f7c: e590c0b8 ldr ip, [r0, #184] ; 0xb8

16f80: e1e03003 mvn r3, r3

16f84: e02cc565 eor ip, ip, r5, ror #10

16f88: e02ee002 eor lr, lr, r2

16f8c: e1845003 orr r5, r4, r3

16f90: e025500c eor r5, r5, ip

16f94: e18e2003 orr r2, lr, r3

16f98: e0222007 eor r2, r2, r7

16f9c: e02ee005 eor lr, lr, r5

16fa0: e023600e eor r6, r3, lr

16fa4: e0254002 eor r4, r5, r2

16fa8: e006c004 and ip, r6, r4

16fac: e1a0eeee ror lr, lr, #29

16fb0: e0053002 and r3, r5, r2

16fb4: e02cc005 eor ip, ip, r5

16fb8: e0233006 eor r3, r3, r6

16fbc: e024400e eor r4, r4, lr

16fc0: e1a0c9ec ror ip, ip, #19

16fc4: e0244003 eor r4, r4, r3

16fc8: e024400c eor r4, r4, ip

16fcc: e023300e eor r3, r3, lr

16fd0: e023318c eor r3, r3, ip, lsl #3

16fd4: e1a04fe4 ror r4, r4, #31

16fd8: e1a05ce3 ror r5, r3, #25

16fdc: e02cc004 eor ip, ip, r4

16fe0: e0256384 eor r6, r5, r4, lsl #7

16fe4: e02cc005 eor ip, ip, r5

16fe8: e59070c0 ldr r7, [r0, #192] ; 0xc0

16fec: e59030c4 ldr r3, [r0, #196] ; 0xc4

16ff0: e59020cc ldr r2, [r0, #204] ; 0xcc

16ff4: e02ee006 eor lr, lr, r6

16ff8: e59060c8 ldr r6, [r0, #200] ; 0xc8

16ffc: e0277dec eor r7, r7, ip, ror #27

17000: e0255002 eor r5, r5, r2

17004: e026656e eor r6, r6, lr, ror #10

17008: e0244003 eor r4, r4, r3

1700c: e1e03007 mvn r3, r7

17010: e0248005 eor r8, r4, r5

17014: e006e003 and lr, r6, r3

17018: e02ee008 eor lr, lr, r8

1701c: e026200e eor r2, r6, lr

17020: e0044002 and r4, r4, r2

17024: e0266003 eor r6, r6, r3

17028: e1852003 orr r2, r5, r3

1702c: e18ec006 orr ip, lr, r6

17030: e1855004 orr r5, r5, r4

17034: e1a0e9ee ror lr, lr, #19

17038: e00cc005 and ip, ip, r5

1703c: e0222008 eor r2, r2, r8

17040: e0263004 eor r3, r6, r4

17044: e02cc007 eor ip, ip, r7

17048: e022200e eor r2, r2, lr

1704c: e0222003 eor r2, r2, r3

17050: e022200c eor r2, r2, ip

17054: e023318e eor r3, r3, lr, lsl #3

17058: e1a0ceec ror ip, ip, #29

1705c: e022200c eor r2, r2, ip

17060: e023300c eor r3, r3, ip

17064: e1a02fe2 ror r2, r2, #31

17068: e1a03ce3 ror r3, r3, #25

1706c: e0234002 eor r4, r3, r2

17070: e02cc003 eor ip, ip, r3

17074: e02cc382 eor ip, ip, r2, lsl #7

17078: e59080d8 ldr r8, [r0, #216] ; 0xd8

1707c: e02ee004 eor lr, lr, r4

17080: e59040d0 ldr r4, [r0, #208] ; 0xd0

17084: e59060dc ldr r6, [r0, #220] ; 0xdc

17088: e028856c eor r8, r8, ip, ror #10

1708c: e0244dee eor r4, r4, lr, ror #27

17090: e59050d4 ldr r5, [r0, #212] ; 0xd4

17094: e0233006 eor r3, r3, r6

17098: e0247008 eor r7, r4, r8

1709c: e0222005 eor r2, r2, r5

170a0: e0277003 eor r7, r7, r3

170a4: e004e007 and lr, r4, r7

170a8: e1826003 orr r6, r2, r3

170ac: e02ec006 eor ip, lr, r6

170b0: e02ee003 eor lr, lr, r3

170b4: e1843003 orr r3, r4, r3

170b8: e0233002 eor r3, r3, r2

170bc: e002500e and r5, r2, lr

170c0: e00cc008 and ip, ip, r8

170c4: e0255007 eor r5, r5, r7

170c8: e02cc003 eor ip, ip, r3

170cc: e1844005 orr r4, r4, r5

170d0: e1a0c9ec ror ip, ip, #19

170d4: e0233005 eor r3, r3, r5

170d8: e1a05ee5 ror r5, r5, #29

170dc: e004400e and r4, r4, lr

170e0: e0233005 eor r3, r3, r5

170e4: e02ce005 eor lr, ip, r5

170e8: e026600e eor r6, r6, lr

170ec: e0233004 eor r3, r3, r4

170f0: e023318c eor r3, r3, ip, lsl #3

170f4: e0244006 eor r4, r4, r6

170f8: e1a03ce3 ror r3, r3, #25

170fc: e1a04fe4 ror r4, r4, #31

17100: e0232004 eor r2, r3, r4

17104: e59070ec ldr r7, [r0, #236] ; 0xec

17108: e02cc002 eor ip, ip, r2

1710c: e590e0e4 ldr lr, [r0, #228] ; 0xe4

17110: e59020e0 ldr r2, [r0, #224] ; 0xe0

17114: e0255003 eor r5, r5, r3

17118: e59060e8 ldr r6, [r0, #232] ; 0xe8

1711c: e0255384 eor r5, r5, r4, lsl #7

17120: e0222dec eor r2, r2, ip, ror #27

17124: e02ee004 eor lr, lr, r4

17128: e0233007 eor r3, r3, r7

1712c: e0265565 eor r5, r6, r5, ror #10

17130: e1c37002 bic r7, r3, r2

17134: e1e0600e mvn r6, lr

17138: e0233002 eor r3, r3, r2

1713c: e0277005 eor r7, r7, r5

17140: e1835006 orr r5, r3, r6

17144: e18ee007 orr lr, lr, r7

17148: e0266003 eor r6, r6, r3

1714c: e0255007 eor r5, r5, r7

17150: e00e4006 and r4, lr, r6

17154: e002c005 and ip, r2, r5

17158: e02cc004 eor ip, ip, r4

1715c: e1a059e5 ror r5, r5, #19

17160: e023300e eor r3, r3, lr

17164: e0222007 eor r2, r2, r7

17168: e006600c and r6, r6, ip

1716c: e0233185 eor r3, r3, r5, lsl #3

17170: e1a0ceec ror ip, ip, #29

17174: e0222005 eor r2, r2, r5

17178: e023300c eor r3, r3, ip

1717c: e0222006 eor r2, r2, r6

17180: e022200c eor r2, r2, ip

17184: e1a03ce3 ror r3, r3, #25

17188: e025e003 eor lr, r5, r3

1718c: e1a02fe2 ror r2, r2, #31

17190: e02e5002 eor r5, lr, r2

17194: e59070f4 ldr r7, [r0, #244] ; 0xf4

17198: e02cc003 eor ip, ip, r3

1719c: e590e0f0 ldr lr, [r0, #240] ; 0xf0

171a0: e59060fc ldr r6, [r0, #252] ; 0xfc

171a4: e02c4382 eor r4, ip, r2, lsl #7

171a8: e590c0f8 ldr ip, [r0, #248] ; 0xf8

171ac: e02eede5 eor lr, lr, r5, ror #27

171b0: e0222007 eor r2, r2, r7

171b4: e0266003 eor r6, r6, r3

171b8: e02e5006 eor r5, lr, r6

171bc: e02e3002 eor r3, lr, r2

171c0: e02cc564 eor ip, ip, r4, ror #10

171c4: e1e0400e mvn r4, lr

171c8: e02cc004 eor ip, ip, r4

171cc: e183e005 orr lr, r3, r5

171d0: e02cc00e eor ip, ip, lr

171d4: e006600c and r6, r6, ip

171d8: e184e00c orr lr, r4, ip

171dc: e02ee005 eor lr, lr, r5

171e0: e1834006 orr r4, r3, r6

171e4: e023300c eor r3, r3, ip

171e8: e1a0c9ec ror ip, ip, #19

171ec: e0233006 eor r3, r3, r6

171f0: e0222006 eor r2, r2, r6

171f4: e024400e eor r4, r4, lr

171f8: e022218c eor r2, r2, ip, lsl #3

171fc: e00ee003 and lr, lr, r3

17200: e1a04ee4 ror r4, r4, #29

17204: e022200e eor r2, r2, lr

17208: e023300c eor r3, r3, ip

1720c: e0222004 eor r2, r2, r4

17210: e0233004 eor r3, r3, r4

17214: e1a02ce2 ror r2, r2, #25

17218: e1a03fe3 ror r3, r3, #31

1721c: e0225003 eor r5, r2, r3

17220: e025500c eor r5, r5, ip

17224: e590e10c ldr lr, [r0, #268] ; 0x10c

17228: e590c100 ldr ip, [r0, #256] ; 0x100

1722c: e0244002 eor r4, r4, r2

17230: e5906108 ldr r6, [r0, #264] ; 0x108

17234: e022200e eor r2, r2, lr

17238: e0244383 eor r4, r4, r3, lsl #7

1723c: e02ccde5 eor ip, ip, r5, ror #27

17240: e590e104 ldr lr, [r0, #260] ; 0x104

17244: e02c5002 eor r5, ip, r2

17248: e1e0c00c mvn ip, ip

1724c: e0264564 eor r4, r6, r4, ror #10

17250: e18cc005 orr ip, ip, r5

17254: e02cc004 eor ip, ip, r4

17258: e023300e eor r3, r3, lr

1725c: e023600c eor r6, r3, ip

17260: e185e006 orr lr, r5, r6

17264: e02ee002 eor lr, lr, r2

17268: e0233005 eor r3, r3, r5

1726c: e00c200e and r2, ip, lr

17270: e0222003 eor r2, r2, r3

17274: e02ee00c eor lr, lr, ip

17278: e022400e eor r4, r2, lr

1727c: e1a02ee2 ror r2, r2, #29

17280: e1a059e4 ror r5, r4, #19

17284: e003300e and r3, r3, lr

17288: e026e002 eor lr, r6, r2

1728c: e02ee005 eor lr, lr, r5

17290: e023300c eor r3, r3, ip

17294: e0233002 eor r3, r3, r2

17298: e1a0efee ror lr, lr, #31

1729c: e0233185 eor r3, r3, r5, lsl #3

172a0: e022238e eor r2, r2, lr, lsl #7

172a4: e1e0cce3 mvn ip, r3, ror #25

172a8: e022200c eor r2, r2, ip

172ac: e5903118 ldr r3, [r0, #280] ; 0x118

172b0: e5906114 ldr r6, [r0, #276] ; 0x114

172b4: e025400e eor r4, r5, lr

172b8: e0232562 eor r2, r3, r2, ror #10

172bc: e590311c ldr r3, [r0, #284] ; 0x11c

172c0: e5908110 ldr r8, [r0, #272] ; 0x110

172c4: e1e07002 mvn r7, r2

172c8: e02ee006 eor lr, lr, r6

172cc: e024400c eor r4, r4, ip

172d0: e18e6007 orr r6, lr, r7

172d4: e02cc003 eor ip, ip, r3

172d8: e02c3006 eor r3, ip, r6

172dc: e0284de4 eor r4, r8, r4, ror #27

172e0: e0045003 and r5, r4, r3

172e4: e022200e eor r2, r2, lr

172e8: e02ee005 eor lr, lr, r5

172ec: e18ee002 orr lr, lr, r2

172f0: e024400c eor r4, r4, ip

172f4: e024400e eor r4, r4, lr

172f8: e0222005 eor r2, r2, r5

172fc: e0255004 eor r5, r5, r4

17300: e002e005 and lr, r2, r5

17304: e1873003 orr r3, r7, r3

17308: e00cc006 and ip, ip, r6

1730c: e02cc00e eor ip, ip, lr

17310: e0233005 eor r3, r3, r5

17314: e023300c eor r3, r3, ip

17318: e1a0ceec ror ip, ip, #29

1731c: e1a039e3 ror r3, r3, #19

17320: e024400c eor r4, r4, ip

17324: e0244003 eor r4, r4, r3

17328: e022200c eor r2, r2, ip

1732c: e0222183 eor r2, r2, r3, lsl #3

17330: e1a04fe4 ror r4, r4, #31

17334: e1a02ce2 ror r2, r2, #25

17338: e0233004 eor r3, r3, r4

1733c: e0225384 eor r5, r2, r4, lsl #7

17340: e590712c ldr r7, [r0, #300] ; 0x12c

17344: e0233002 eor r3, r3, r2

17348: e590e120 ldr lr, [r0, #288] ; 0x120

1734c: e5906124 ldr r6, [r0, #292] ; 0x124

17350: e02cc005 eor ip, ip, r5

17354: e5905128 ldr r5, [r0, #296] ; 0x128

17358: e02eede3 eor lr, lr, r3, ror #27

1735c: e0222007 eor r2, r2, r7

17360: e025556c eor r5, r5, ip, ror #10

17364: e0244006 eor r4, r4, r6

17368: e02ec002 eor ip, lr, r2

1736c: e004300c and r3, r4, ip

17370: e02cc005 eor ip, ip, r5

17374: e023300e eor r3, r3, lr

17378: e024400c eor r4, r4, ip

1737c: e002200e and r2, r2, lr

17380: e0222004 eor r2, r2, r4

17384: e02ce003 eor lr, ip, r3

17388: e00ee002 and lr, lr, r2

1738c: e1855003 orr r5, r5, r3

17390: e0244005 eor r4, r4, r5

17394: e023300e eor r3, r3, lr

17398: e1a05ee4 ror r5, r4, #29

1739c: e1e039e3 mvn r3, r3, ror #19

173a0: e02cc00e eor ip, ip, lr

173a4: e02cc005 eor ip, ip, r5

173a8: e022e005 eor lr, r2, r5

173ac: e02ee183 eor lr, lr, r3, lsl #3

173b0: e02cc003 eor ip, ip, r3

173b4: e1a0ecee ror lr, lr, #25

173b8: e1e0cfec mvn ip, ip, ror #31

173bc: e02e400c eor r4, lr, ip

173c0: e5902134 ldr r2, [r0, #308] ; 0x134

173c4: e0233004 eor r3, r3, r4

173c8: e5904130 ldr r4, [r0, #304] ; 0x130

173cc: e02c7002 eor r7, ip, r2

173d0: e0244de3 eor r4, r4, r3, ror #27

173d4: e025500e eor r5, r5, lr

173d8: e590213c ldr r2, [r0, #316] ; 0x13c

173dc: e025538c eor r5, r5, ip, lsl #7

173e0: e0243007 eor r3, r4, r7

173e4: e590c138 ldr ip, [r0, #312] ; 0x138

173e8: e1e03003 mvn r3, r3

173ec: e02cc565 eor ip, ip, r5, ror #10

173f0: e02ee002 eor lr, lr, r2

173f4: e1845003 orr r5, r4, r3

173f8: e025500c eor r5, r5, ip

173fc: e18e2003 orr r2, lr, r3

17400: e0222007 eor r2, r2, r7

17404: e02ee005 eor lr, lr, r5

17408: e023600e eor r6, r3, lr

1740c: e0254002 eor r4, r5, r2

17410: e006c004 and ip, r6, r4

17414: e1a0eeee ror lr, lr, #29

17418: e0053002 and r3, r5, r2

1741c: e02cc005 eor ip, ip, r5

17420: e0233006 eor r3, r3, r6

17424: e024400e eor r4, r4, lr

17428: e1a0c9ec ror ip, ip, #19

1742c: e0244003 eor r4, r4, r3

17430: e024400c eor r4, r4, ip

17434: e023300e eor r3, r3, lr

17438: e023318c eor r3, r3, ip, lsl #3

1743c: e1a04fe4 ror r4, r4, #31

17440: e1a05ce3 ror r5, r3, #25

17444: e02cc004 eor ip, ip, r4

17448: e0256384 eor r6, r5, r4, lsl #7

1744c: e02cc005 eor ip, ip, r5

17450: e5907140 ldr r7, [r0, #320] ; 0x140

17454: e5903144 ldr r3, [r0, #324] ; 0x144

17458: e590214c ldr r2, [r0, #332] ; 0x14c

1745c: e02ee006 eor lr, lr, r6

17460: e5906148 ldr r6, [r0, #328] ; 0x148

17464: e0277dec eor r7, r7, ip, ror #27

17468: e0255002 eor r5, r5, r2

1746c: e026656e eor r6, r6, lr, ror #10

17470: e0244003 eor r4, r4, r3

17474: e1e03007 mvn r3, r7

17478: e0248005 eor r8, r4, r5

1747c: e006e003 and lr, r6, r3

17480: e02ee008 eor lr, lr, r8

17484: e026200e eor r2, r6, lr

17488: e0044002 and r4, r4, r2

1748c: e0266003 eor r6, r6, r3

17490: e1852003 orr r2, r5, r3

17494: e18ec006 orr ip, lr, r6

17498: e1855004 orr r5, r5, r4

1749c: e1a0e9ee ror lr, lr, #19

174a0: e00cc005 and ip, ip, r5

174a4: e0222008 eor r2, r2, r8

174a8: e0263004 eor r3, r6, r4

174ac: e02cc007 eor ip, ip, r7

174b0: e022200e eor r2, r2, lr

174b4: e0222003 eor r2, r2, r3

174b8: e022200c eor r2, r2, ip

174bc: e023318e eor r3, r3, lr, lsl #3

174c0: e1a0ceec ror ip, ip, #29

174c4: e022200c eor r2, r2, ip

174c8: e023300c eor r3, r3, ip

174cc: e1a02fe2 ror r2, r2, #31

174d0: e1a03ce3 ror r3, r3, #25

174d4: e0234002 eor r4, r3, r2

174d8: e02cc003 eor ip, ip, r3

174dc: e02cc382 eor ip, ip, r2, lsl #7

174e0: e5908158 ldr r8, [r0, #344] ; 0x158

174e4: e02ee004 eor lr, lr, r4

174e8: e5904150 ldr r4, [r0, #336] ; 0x150

174ec: e590615c ldr r6, [r0, #348] ; 0x15c

174f0: e028856c eor r8, r8, ip, ror #10

174f4: e0244dee eor r4, r4, lr, ror #27

174f8: e5905154 ldr r5, [r0, #340] ; 0x154

174fc: e0233006 eor r3, r3, r6

17500: e0247008 eor r7, r4, r8

17504: e0222005 eor r2, r2, r5

17508: e0277003 eor r7, r7, r3

1750c: e004e007 and lr, r4, r7

17510: e1826003 orr r6, r2, r3

17514: e02ec006 eor ip, lr, r6

17518: e02ee003 eor lr, lr, r3

1751c: e1843003 orr r3, r4, r3

17520: e0233002 eor r3, r3, r2

17524: e002500e and r5, r2, lr

17528: e00cc008 and ip, ip, r8

1752c: e0255007 eor r5, r5, r7

17530: e02cc003 eor ip, ip, r3

17534: e1844005 orr r4, r4, r5

17538: e1a0c9ec ror ip, ip, #19

1753c: e0233005 eor r3, r3, r5

17540: e1a05ee5 ror r5, r5, #29

17544: e004400e and r4, r4, lr

17548: e0233005 eor r3, r3, r5

1754c: e02ce005 eor lr, ip, r5

17550: e026600e eor r6, r6, lr

17554: e0233004 eor r3, r3, r4

17558: e023318c eor r3, r3, ip, lsl #3

1755c: e0244006 eor r4, r4, r6

17560: e1a03ce3 ror r3, r3, #25

17564: e1a04fe4 ror r4, r4, #31

17568: e0232004 eor r2, r3, r4

1756c: e590716c ldr r7, [r0, #364] ; 0x16c

17570: e02cc002 eor ip, ip, r2

17574: e590e164 ldr lr, [r0, #356] ; 0x164

17578: e5902160 ldr r2, [r0, #352] ; 0x160

1757c: e0255003 eor r5, r5, r3

17580: e5906168 ldr r6, [r0, #360] ; 0x168

17584: e0255384 eor r5, r5, r4, lsl #7

17588: e0222dec eor r2, r2, ip, ror #27

1758c: e02ee004 eor lr, lr, r4

17590: e0233007 eor r3, r3, r7

17594: e0265565 eor r5, r6, r5, ror #10

17598: e1c37002 bic r7, r3, r2

1759c: e1e0600e mvn r6, lr

175a0: e0233002 eor r3, r3, r2

175a4: e0277005 eor r7, r7, r5

175a8: e1835006 orr r5, r3, r6

175ac: e18ee007 orr lr, lr, r7

175b0: e0266003 eor r6, r6, r3

175b4: e0255007 eor r5, r5, r7

175b8: e00e4006 and r4, lr, r6

175bc: e002c005 and ip, r2, r5

175c0: e02cc004 eor ip, ip, r4

175c4: e1a059e5 ror r5, r5, #19

175c8: e023300e eor r3, r3, lr

175cc: e0222007 eor r2, r2, r7

175d0: e006600c and r6, r6, ip

175d4: e0233185 eor r3, r3, r5, lsl #3

175d8: e1a0ceec ror ip, ip, #29

175dc: e0222005 eor r2, r2, r5

175e0: e023300c eor r3, r3, ip

175e4: e0222006 eor r2, r2, r6

175e8: e022200c eor r2, r2, ip

175ec: e1a03ce3 ror r3, r3, #25

175f0: e025e003 eor lr, r5, r3

175f4: e1a02fe2 ror r2, r2, #31

175f8: e02e5002 eor r5, lr, r2

175fc: e5907174 ldr r7, [r0, #372] ; 0x174

17600: e02cc003 eor ip, ip, r3

17604: e590e170 ldr lr, [r0, #368] ; 0x170

17608: e590617c ldr r6, [r0, #380] ; 0x17c

1760c: e02c4382 eor r4, ip, r2, lsl #7

17610: e590c178 ldr ip, [r0, #376] ; 0x178

17614: e02eede5 eor lr, lr, r5, ror #27

17618: e0222007 eor r2, r2, r7

1761c: e0266003 eor r6, r6, r3

17620: e02e5006 eor r5, lr, r6

17624: e02e3002 eor r3, lr, r2

17628: e02cc564 eor ip, ip, r4, ror #10

1762c: e1e0400e mvn r4, lr

17630: e02cc004 eor ip, ip, r4

17634: e183e005 orr lr, r3, r5

17638: e02cc00e eor ip, ip, lr

1763c: e006600c and r6, r6, ip

17640: e184e00c orr lr, r4, ip

17644: e02ee005 eor lr, lr, r5

17648: e1834006 orr r4, r3, r6

1764c: e023300c eor r3, r3, ip

17650: e1a0c9ec ror ip, ip, #19

17654: e0233006 eor r3, r3, r6

17658: e0222006 eor r2, r2, r6

1765c: e024400e eor r4, r4, lr

17660: e022218c eor r2, r2, ip, lsl #3

17664: e00ee003 and lr, lr, r3

17668: e1a04ee4 ror r4, r4, #29

1766c: e022200e eor r2, r2, lr

17670: e023300c eor r3, r3, ip

17674: e0222004 eor r2, r2, r4

17678: e0233004 eor r3, r3, r4

1767c: e1a02ce2 ror r2, r2, #25

17680: e1a03fe3 ror r3, r3, #31

17684: e0225003 eor r5, r2, r3

17688: e025500c eor r5, r5, ip

1768c: e590e18c ldr lr, [r0, #396] ; 0x18c

17690: e590c180 ldr ip, [r0, #384] ; 0x180

17694: e0244002 eor r4, r4, r2

17698: e5906188 ldr r6, [r0, #392] ; 0x188

1769c: e022200e eor r2, r2, lr

176a0: e0244383 eor r4, r4, r3, lsl #7

176a4: e02ccde5 eor ip, ip, r5, ror #27

176a8: e590e184 ldr lr, [r0, #388] ; 0x184

176ac: e02c5002 eor r5, ip, r2

176b0: e1e0c00c mvn ip, ip

176b4: e0264564 eor r4, r6, r4, ror #10

176b8: e18cc005 orr ip, ip, r5

176bc: e02cc004 eor ip, ip, r4

176c0: e023300e eor r3, r3, lr

176c4: e023600c eor r6, r3, ip

176c8: e185e006 orr lr, r5, r6

176cc: e02ee002 eor lr, lr, r2

176d0: e0233005 eor r3, r3, r5

176d4: e00c200e and r2, ip, lr

176d8: e0222003 eor r2, r2, r3

176dc: e02ee00c eor lr, lr, ip

176e0: e022400e eor r4, r2, lr

176e4: e1a02ee2 ror r2, r2, #29

176e8: e1a059e4 ror r5, r4, #19

176ec: e003300e and r3, r3, lr

176f0: e026e002 eor lr, r6, r2

176f4: e02ee005 eor lr, lr, r5

176f8: e023300c eor r3, r3, ip

176fc: e0233002 eor r3, r3, r2

17700: e1a0efee ror lr, lr, #31

17704: e0233185 eor r3, r3, r5, lsl #3

17708: e022238e eor r2, r2, lr, lsl #7

1770c: e1e0cce3 mvn ip, r3, ror #25

17710: e022200c eor r2, r2, ip

17714: e5903198 ldr r3, [r0, #408] ; 0x198

17718: e5906194 ldr r6, [r0, #404] ; 0x194

1771c: e025400e eor r4, r5, lr

17720: e0232562 eor r2, r3, r2, ror #10

17724: e590319c ldr r3, [r0, #412] ; 0x19c

17728: e5908190 ldr r8, [r0, #400] ; 0x190

1772c: e1e07002 mvn r7, r2

17730: e02ee006 eor lr, lr, r6

17734: e024400c eor r4, r4, ip

17738: e18e6007 orr r6, lr, r7

1773c: e02cc003 eor ip, ip, r3

17740: e02c3006 eor r3, ip, r6

17744: e0284de4 eor r4, r8, r4, ror #27

17748: e0045003 and r5, r4, r3

1774c: e022200e eor r2, r2, lr

17750: e02ee005 eor lr, lr, r5

17754: e18ee002 orr lr, lr, r2

17758: e024400c eor r4, r4, ip

1775c: e024400e eor r4, r4, lr

17760: e0222005 eor r2, r2, r5

17764: e0255004 eor r5, r5, r4

17768: e002e005 and lr, r2, r5

1776c: e1873003 orr r3, r7, r3

17770: e00cc006 and ip, ip, r6

17774: e02cc00e eor ip, ip, lr

17778: e0233005 eor r3, r3, r5

1777c: e023300c eor r3, r3, ip

17780: e1a0ceec ror ip, ip, #29

17784: e1a039e3 ror r3, r3, #19

17788: e024400c eor r4, r4, ip

1778c: e0244003 eor r4, r4, r3

17790: e022200c eor r2, r2, ip

17794: e0222183 eor r2, r2, r3, lsl #3

17798: e1a04fe4 ror r4, r4, #31

1779c: e1a02ce2 ror r2, r2, #25

177a0: e0233004 eor r3, r3, r4

177a4: e0225384 eor r5, r2, r4, lsl #7

177a8: e59071ac ldr r7, [r0, #428] ; 0x1ac

177ac: e0233002 eor r3, r3, r2

177b0: e590e1a0 ldr lr, [r0, #416] ; 0x1a0

177b4: e59061a4 ldr r6, [r0, #420] ; 0x1a4

177b8: e02cc005 eor ip, ip, r5

177bc: e59051a8 ldr r5, [r0, #424] ; 0x1a8

177c0: e02eede3 eor lr, lr, r3, ror #27

177c4: e0222007 eor r2, r2, r7

177c8: e025556c eor r5, r5, ip, ror #10

177cc: e0244006 eor r4, r4, r6

177d0: e02ec002 eor ip, lr, r2

177d4: e004300c and r3, r4, ip

177d8: e02cc005 eor ip, ip, r5

177dc: e023300e eor r3, r3, lr

177e0: e024400c eor r4, r4, ip

177e4: e002200e and r2, r2, lr

177e8: e0222004 eor r2, r2, r4

177ec: e02ce003 eor lr, ip, r3

177f0: e00ee002 and lr, lr, r2

177f4: e1855003 orr r5, r5, r3

177f8: e0244005 eor r4, r4, r5

177fc: e023300e eor r3, r3, lr

17800: e1a05ee4 ror r5, r4, #29

17804: e1e039e3 mvn r3, r3, ror #19

17808: e02cc00e eor ip, ip, lr

1780c: e02cc005 eor ip, ip, r5

17810: e022e005 eor lr, r2, r5

17814: e02ee183 eor lr, lr, r3, lsl #3

17818: e02cc003 eor ip, ip, r3

1781c: e1a0ecee ror lr, lr, #25

17820: e1e0cfec mvn ip, ip, ror #31

17824: e02e400c eor r4, lr, ip

17828: e59021b4 ldr r2, [r0, #436] ; 0x1b4

1782c: e0233004 eor r3, r3, r4

17830: e59041b0 ldr r4, [r0, #432] ; 0x1b0

17834: e02c7002 eor r7, ip, r2

17838: e0244de3 eor r4, r4, r3, ror #27

1783c: e025500e eor r5, r5, lr

17840: e59021bc ldr r2, [r0, #444] ; 0x1bc

17844: e025538c eor r5, r5, ip, lsl #7

17848: e0243007 eor r3, r4, r7

1784c: e590c1b8 ldr ip, [r0, #440] ; 0x1b8

17850: e1e03003 mvn r3, r3

17854: e02cc565 eor ip, ip, r5, ror #10

17858: e02ee002 eor lr, lr, r2

1785c: e1845003 orr r5, r4, r3

17860: e025500c eor r5, r5, ip

17864: e18e2003 orr r2, lr, r3

17868: e0222007 eor r2, r2, r7

1786c: e02ee005 eor lr, lr, r5

17870: e023600e eor r6, r3, lr

17874: e0254002 eor r4, r5, r2

17878: e006c004 and ip, r6, r4

1787c: e1a0eeee ror lr, lr, #29

17880: e0053002 and r3, r5, r2

17884: e02cc005 eor ip, ip, r5

17888: e0233006 eor r3, r3, r6

1788c: e024400e eor r4, r4, lr

17890: e1a0c9ec ror ip, ip, #19

17894: e0244003 eor r4, r4, r3

17898: e024400c eor r4, r4, ip

1789c: e023300e eor r3, r3, lr

178a0: e023318c eor r3, r3, ip, lsl #3

178a4: e1a04fe4 ror r4, r4, #31

178a8: e1a05ce3 ror r5, r3, #25

178ac: e02cc004 eor ip, ip, r4

178b0: e0256384 eor r6, r5, r4, lsl #7

178b4: e02cc005 eor ip, ip, r5

178b8: e59071c0 ldr r7, [r0, #448] ; 0x1c0

178bc: e59031c4 ldr r3, [r0, #452] ; 0x1c4

178c0: e59021cc ldr r2, [r0, #460] ; 0x1cc

178c4: e02ee006 eor lr, lr, r6

178c8: e59061c8 ldr r6, [r0, #456] ; 0x1c8

178cc: e0277dec eor r7, r7, ip, ror #27

178d0: e0255002 eor r5, r5, r2

178d4: e026656e eor r6, r6, lr, ror #10

178d8: e0244003 eor r4, r4, r3

178dc: e1e03007 mvn r3, r7

178e0: e0248005 eor r8, r4, r5

178e4: e006e003 and lr, r6, r3

178e8: e02ee008 eor lr, lr, r8

178ec: e026200e eor r2, r6, lr

178f0: e0044002 and r4, r4, r2

178f4: e0266003 eor r6, r6, r3

178f8: e1852003 orr r2, r5, r3

178fc: e18ec006 orr ip, lr, r6

17900: e1855004 orr r5, r5, r4

17904: e1a0e9ee ror lr, lr, #19

17908: e00cc005 and ip, ip, r5

1790c: e0222008 eor r2, r2, r8

17910: e0263004 eor r3, r6, r4

17914: e02cc007 eor ip, ip, r7

17918: e022200e eor r2, r2, lr

1791c: e0222003 eor r2, r2, r3

17920: e022200c eor r2, r2, ip

17924: e023318e eor r3, r3, lr, lsl #3

17928: e1a0ceec ror ip, ip, #29

1792c: e022200c eor r2, r2, ip

17930: e023300c eor r3, r3, ip

17934: e1a02fe2 ror r2, r2, #31

17938: e1a03ce3 ror r3, r3, #25

1793c: e0234002 eor r4, r3, r2

17940: e02cc003 eor ip, ip, r3

17944: e02cc382 eor ip, ip, r2, lsl #7

17948: e59081d8 ldr r8, [r0, #472] ; 0x1d8

1794c: e02ee004 eor lr, lr, r4

17950: e59041d0 ldr r4, [r0, #464] ; 0x1d0

17954: e59061dc ldr r6, [r0, #476] ; 0x1dc

17958: e028856c eor r8, r8, ip, ror #10

1795c: e0244dee eor r4, r4, lr, ror #27

17960: e59051d4 ldr r5, [r0, #468] ; 0x1d4

17964: e0233006 eor r3, r3, r6

17968: e0247008 eor r7, r4, r8

1796c: e0222005 eor r2, r2, r5

17970: e0277003 eor r7, r7, r3

17974: e004e007 and lr, r4, r7

17978: e1826003 orr r6, r2, r3

1797c: e02ec006 eor ip, lr, r6

17980: e02ee003 eor lr, lr, r3

17984: e1843003 orr r3, r4, r3

17988: e0233002 eor r3, r3, r2

1798c: e002500e and r5, r2, lr

17990: e00cc008 and ip, ip, r8

17994: e0255007 eor r5, r5, r7

17998: e02cc003 eor ip, ip, r3

1799c: e1844005 orr r4, r4, r5

179a0: e1a0c9ec ror ip, ip, #19

179a4: e0233005 eor r3, r3, r5

179a8: e1a05ee5 ror r5, r5, #29

179ac: e004400e and r4, r4, lr

179b0: e0233005 eor r3, r3, r5

179b4: e02ce005 eor lr, ip, r5

179b8: e026600e eor r6, r6, lr

179bc: e0233004 eor r3, r3, r4

179c0: e023318c eor r3, r3, ip, lsl #3

179c4: e0244006 eor r4, r4, r6

179c8: e1a03ce3 ror r3, r3, #25

179cc: e1a04fe4 ror r4, r4, #31

179d0: e0232004 eor r2, r3, r4

179d4: e59061ec ldr r6, [r0, #492] ; 0x1ec

179d8: e02cc002 eor ip, ip, r2

179dc: e590e1e4 ldr lr, [r0, #484] ; 0x1e4

179e0: e59021e0 ldr r2, [r0, #480] ; 0x1e0

179e4: e0255003 eor r5, r5, r3

179e8: e59071e8 ldr r7, [r0, #488] ; 0x1e8

179ec: e0255384 eor r5, r5, r4, lsl #7

179f0: e0222dec eor r2, r2, ip, ror #27

179f4: e0233006 eor r3, r3, r6

179f8: e02ee004 eor lr, lr, r4

179fc: e0275565 eor r5, r7, r5, ror #10

17a00: e1e0600e mvn r6, lr

17a04: e1c37002 bic r7, r3, r2

17a08: e0233002 eor r3, r3, r2

17a0c: e0277005 eor r7, r7, r5

17a10: e1835006 orr r5, r3, r6

17a14: e18ee007 orr lr, lr, r7

17a18: e0266003 eor r6, r6, r3

17a1c: e0255007 eor r5, r5, r7

17a20: e00e4006 and r4, lr, r6

17a24: e002c005 and ip, r2, r5

17a28: e02cc004 eor ip, ip, r4

17a2c: e1a059e5 ror r5, r5, #19

17a30: e023300e eor r3, r3, lr

17a34: e0222007 eor r2, r2, r7

17a38: e006600c and r6, r6, ip

17a3c: e0233185 eor r3, r3, r5, lsl #3

17a40: e1a0ceec ror ip, ip, #29

17a44: e0222005 eor r2, r2, r5

17a48: e023300c eor r3, r3, ip

17a4c: e0222006 eor r2, r2, r6

17a50: e022200c eor r2, r2, ip

17a54: e1a03ce3 ror r3, r3, #25

17a58: e025e003 eor lr, r5, r3

17a5c: e1a02fe2 ror r2, r2, #31

17a60: e02e5002 eor r5, lr, r2

17a64: e59071f4 ldr r7, [r0, #500] ; 0x1f4

17a68: e02cc003 eor ip, ip, r3

17a6c: e590e1f0 ldr lr, [r0, #496] ; 0x1f0

17a70: e59061fc ldr r6, [r0, #508] ; 0x1fc

17a74: e02c4382 eor r4, ip, r2, lsl #7

17a78: e590c1f8 ldr ip, [r0, #504] ; 0x1f8

17a7c: e02eede5 eor lr, lr, r5, ror #27

17a80: e0222007 eor r2, r2, r7

17a84: e0266003 eor r6, r6, r3

17a88: e02e5006 eor r5, lr, r6

17a8c: e02e3002 eor r3, lr, r2

17a90: e02cc564 eor ip, ip, r4, ror #10

17a94: e1e0400e mvn r4, lr

17a98: e02cc004 eor ip, ip, r4

17a9c: e183e005 orr lr, r3, r5

17aa0: e02cc00e eor ip, ip, lr

17aa4: e006600c and r6, r6, ip

17aa8: e184e00c orr lr, r4, ip

17aac: e02ee005 eor lr, lr, r5

17ab0: e1835006 orr r5, r3, r6

17ab4: e023300c eor r3, r3, ip

17ab8: e1a0c9ec ror ip, ip, #19

17abc: e0233006 eor r3, r3, r6

17ac0: e0222006 eor r2, r2, r6

17ac4: e025500e eor r5, r5, lr

17ac8: e022218c eor r2, r2, ip, lsl #3

17acc: e00ee003 and lr, lr, r3

17ad0: e1a05ee5 ror r5, r5, #29

17ad4: e022200e eor r2, r2, lr

17ad8: e023300c eor r3, r3, ip

17adc: e0222005 eor r2, r2, r5

17ae0: e0233005 eor r3, r3, r5

17ae4: e1a02ce2 ror r2, r2, #25

17ae8: e1a03fe3 ror r3, r3, #31

17aec: e0226003 eor r6, r2, r3

17af0: e026600c eor r6, r6, ip

17af4: e590420c ldr r4, [r0, #524] ; 0x20c

17af8: e590c200 ldr ip, [r0, #512] ; 0x200

17afc: e025e002 eor lr, r5, r2

17b00: e02ee383 eor lr, lr, r3, lsl #7

17b04: e5905208 ldr r5, [r0, #520] ; 0x208

17b08: e0222004 eor r2, r2, r4

17b0c: e02ccde6 eor ip, ip, r6, ror #27

17b10: e5904204 ldr r4, [r0, #516] ; 0x204

17b14: e02c6002 eor r6, ip, r2

17b18: e1e0c00c mvn ip, ip

17b1c: e025e56e eor lr, r5, lr, ror #10

17b20: e18cc006 orr ip, ip, r6

17b24: e02cc00e eor ip, ip, lr

17b28: e0233004 eor r3, r3, r4

17b2c: e023500c eor r5, r3, ip

17b30: e1864005 orr r4, r6, r5

17b34: e0244002 eor r4, r4, r2

17b38: e0233006 eor r3, r3, r6

17b3c: e00c2004 and r2, ip, r4

17b40: e0222003 eor r2, r2, r3

17b44: e024400c eor r4, r4, ip

17b48: e022e004 eor lr, r2, r4

17b4c: e1a02ee2 ror r2, r2, #29

17b50: e1a0e9ee ror lr, lr, #19

17b54: e0033004 and r3, r3, r4

17b58: e0254002 eor r4, r5, r2

17b5c: e024400e eor r4, r4, lr

17b60: e023300c eor r3, r3, ip

17b64: e0233002 eor r3, r3, r2

17b68: e1a04fe4 ror r4, r4, #31

17b6c: e023318e eor r3, r3, lr, lsl #3

17b70: e0222384 eor r2, r2, r4, lsl #7

17b74: e1e03ce3 mvn r3, r3, ror #25

17b78: e5908218 ldr r8, [r0, #536] ; 0x218

17b7c: e0222003 eor r2, r2, r3

17b80: e590c214 ldr ip, [r0, #532] ; 0x214

17b84: e590521c ldr r5, [r0, #540] ; 0x21c

17b88: e02ee004 eor lr, lr, r4

17b8c: e0288562 eor r8, r8, r2, ror #10

17b90: e5906210 ldr r6, [r0, #528] ; 0x210

17b94: e02cc004 eor ip, ip, r4

17b98: e02ee003 eor lr, lr, r3

17b9c: e1e02008 mvn r2, r8

17ba0: e0233005 eor r3, r3, r5

17ba4: e18c5002 orr r5, ip, r2

17ba8: e0237005 eor r7, r3, r5

17bac: e026edee eor lr, r6, lr, ror #27

17bb0: e00e6007 and r6, lr, r7

17bb4: e02c4008 eor r4, ip, r8

17bb8: e02cc006 eor ip, ip, r6

17bbc: e18cc004 orr ip, ip, r4

17bc0: e02ee003 eor lr, lr, r3

17bc4: e02ee00c eor lr, lr, ip

17bc8: e590c220 ldr ip, [r0, #544] ; 0x220

17bcc: e0244006 eor r4, r4, r6

17bd0: e1822007 orr r2, r2, r7

17bd4: e026600e eor r6, r6, lr

17bd8: e0033005 and r3, r3, r5

17bdc: e022200c eor r2, r2, ip

17be0: e5905228 ldr r5, [r0, #552] ; 0x228

17be4: e590c224 ldr ip, [r0, #548] ; 0x224

17be8: e0047006 and r7, r4, r6

17bec: e590022c ldr r0, [r0, #556] ; 0x22c

17bf0: e0233007 eor r3, r3, r7

17bf4: e0222006 eor r2, r2, r6

17bf8: e0222003 eor r2, r2, r3

17bfc: e02ee00c eor lr, lr, ip

17c00: e0233005 eor r3, r3, r5

17c04: e0244000 eor r4, r4, r0

17c08: e5812000 str r2, [r1]

17c0c: e5813008 str r3, [r1, #8]

17c10: e581e004 str lr, [r1, #4]

17c14: e581400c str r4, [r1, #12]

17c18: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

17c1c: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

17c20: e89100a0 ldm r1, {r5, r7}

17c24: e5903220 ldr r3, [r0, #544] ; 0x220

17c28: e5902224 ldr r2, [r0, #548] ; 0x224

17c2c: e590c22c ldr ip, [r0, #556] ; 0x22c

17c30: e5914008 ldr r4, [r1, #8]

17c34: e590e228 ldr lr, [r0, #552] ; 0x228

17c38: e591600c ldr r6, [r1, #12]

17c3c: e0255003 eor r5, r5, r3

17c40: e0222007 eor r2, r2, r7

17c44: e0053002 and r3, r5, r2

17c48: e02ee004 eor lr, lr, r4

17c4c: e026600c eor r6, r6, ip

17c50: e1854002 orr r4, r5, r2

17c54: e183c00e orr ip, r3, lr

17c58: e0043006 and r3, r4, r6

17c5c: e02c7003 eor r7, ip, r3

17c60: e0264007 eor r4, r6, r7

17c64: e0222003 eor r2, r2, r3

17c68: e1e04004 mvn r4, r4

17c6c: e5903218 ldr r3, [r0, #536] ; 0x218

17c70: e1844002 orr r4, r4, r2

17c74: e0244005 eor r4, r4, r5

17c78: e5908214 ldr r8, [r0, #532] ; 0x214

17c7c: e1866004 orr r6, r6, r4

17c80: e0055007 and r5, r5, r7

17c84: e022200e eor r2, r2, lr

17c88: e023300c eor r3, r3, ip

17c8c: e590e210 ldr lr, [r0, #528] ; 0x210

17c90: e590c21c ldr ip, [r0, #540] ; 0x21c

17c94: e0222006 eor r2, r2, r6

17c98: e0233005 eor r3, r3, r5

17c9c: e02cc007 eor ip, ip, r7

17ca0: e02ee002 eor lr, lr, r2

17ca4: e0233004 eor r3, r3, r4

17ca8: e0244008 eor r4, r4, r8

17cac: e024500c eor r5, r4, ip

17cb0: e0233002 eor r3, r3, r2

17cb4: e02552ee eor r5, r5, lr, ror #5

17cb8: e02c2384 eor r2, ip, r4, lsl #7

17cbc: e0223b63 eor r3, r2, r3, ror #22

17cc0: e0232005 eor r2, r3, r5

17cc4: e1a0e6e5 ror lr, r5, #13

17cc8: e02240e4 eor r4, r2, r4, ror #1

17ccc: e1e0700e mvn r7, lr

17cd0: e023c3ec eor ip, r3, ip, ror #7

17cd4: e1a031e3 ror r3, r3, #3

17cd8: e02cc185 eor ip, ip, r5, lsl #3

17cdc: e02ee004 eor lr, lr, r4

17ce0: e1836007 orr r6, r3, r7

17ce4: e026600c eor r6, r6, ip

17ce8: e023300e eor r3, r3, lr

17cec: e0032006 and r2, r3, r6

17cf0: e02ee002 eor lr, lr, r2

17cf4: e5908204 ldr r8, [r0, #516] ; 0x204

17cf8: e184200e orr r2, r4, lr

17cfc: e5905208 ldr r5, [r0, #520] ; 0x208

17d00: e00cc007 and ip, ip, r7

17d04: e02cc003 eor ip, ip, r3

17d08: e5907200 ldr r7, [r0, #512] ; 0x200

17d0c: e0233006 eor r3, r3, r6

17d10: e0266002 eor r6, r6, r2

17d14: e590220c ldr r2, [r0, #524] ; 0x20c

17d18: e0233008 eor r3, r3, r8

17d1c: e1844006 orr r4, r4, r6

17d20: e025500c eor r5, r5, ip

17d24: e0266002 eor r6, r6, r2

17d28: e0255004 eor r5, r5, r4

17d2c: e02ee007 eor lr, lr, r7

17d30: e026c383 eor ip, r6, r3, lsl #7

17d34: e02ee004 eor lr, lr, r4

17d38: e02c2b65 eor r2, ip, r5, ror #22

17d3c: e023c006 eor ip, r3, r6

17d40: e02ce2ee eor lr, ip, lr, ror #5

17d44: e022800e eor r8, r2, lr

17d48: e1a0a1e2 ror sl, r2, #3

17d4c: e02880e3 eor r8, r8, r3, ror #1

17d50: e02223e6 eor r2, r2, r6, ror #7

17d54: e1e0600a mvn r6, sl

17d58: e022218e eor r2, r2, lr, lsl #3

17d5c: e0084006 and r4, r8, r6

17d60: e1a0e6ee ror lr, lr, #13

17d64: e0244002 eor r4, r4, r2

17d68: e59031f0 ldr r3, [r0, #496] ; 0x1f0

17d6c: e00e9004 and r9, lr, r4

17d70: e182200e orr r2, r2, lr

17d74: e590c1f8 ldr ip, [r0, #504] ; 0x1f8

17d78: e0287006 eor r7, r8, r6

17d7c: e02aa00e eor sl, sl, lr

17d80: e0277009 eor r7, r7, r9

17d84: e0085002 and r5, r8, r2

17d88: e18aa009 orr sl, sl, r9

17d8c: e0233006 eor r3, r3, r6

17d90: e59091f4 ldr r9, [r0, #500] ; 0x1f4

17d94: e1888007 orr r8, r8, r7

17d98: e025500c eor r5, r5, ip

17d9c: e0233002 eor r3, r3, r2

17da0: e59021fc ldr r2, [r0, #508] ; 0x1fc

17da4: e00ec008 and ip, lr, r8

17da8: e0244009 eor r4, r4, r9

17dac: e025e00a eor lr, r5, sl

17db0: e0233008 eor r3, r3, r8

17db4: e024400c eor r4, r4, ip

17db8: e0222007 eor r2, r2, r7

17dbc: e02432e3 eor r3, r4, r3, ror #5

17dc0: e022eb6e eor lr, r2, lr, ror #22

17dc4: e02ee384 eor lr, lr, r4, lsl #7

17dc8: e0233002 eor r3, r3, r2

17dcc: e02ec003 eor ip, lr, r3

17dd0: e02e23e2 eor r2, lr, r2, ror #7

17dd4: e1a051ee ror r5, lr, #3

17dd8: e0222183 eor r2, r2, r3, lsl #3

17ddc: e02cc0e4 eor ip, ip, r4, ror #1

17de0: e1a036e3 ror r3, r3, #13

17de4: e182600c orr r6, r2, ip

17de8: e59081e4 ldr r8, [r0, #484] ; 0x1e4

17dec: e1824005 orr r4, r2, r5

17df0: e0227003 eor r7, r2, r3

17df4: e024400c eor r4, r4, ip

17df8: e590e1e8 ldr lr, [r0, #488] ; 0x1e8

17dfc: e0077006 and r7, r7, r6

17e00: e003a004 and sl, r3, r4

17e04: e0277004 eor r7, r7, r4

17e08: e025c008 eor ip, r5, r8

17e0c: e59091e0 ldr r9, [r0, #480] ; 0x1e0

17e10: e185b00a orr fp, r5, sl

17e14: e59081ec ldr r8, [r0, #492] ; 0x1ec

17e18: e1e03003 mvn r3, r3

17e1c: e022200c eor r2, r2, ip

17e20: e026600e eor r6, r6, lr

17e24: e025e007 eor lr, r5, r7

17e28: e026600b eor r6, r6, fp

17e2c: e022500a eor r5, r2, sl

17e30: e187b003 orr fp, r7, r3

17e34: e18ee003 orr lr, lr, r3

17e38: e0244009 eor r4, r4, r9

17e3c: e0277008 eor r7, r7, r8

17e40: e026300b eor r3, r6, fp

17e44: e024400e eor r4, r4, lr

17e48: e0272385 eor r2, r7, r5, lsl #7

17e4c: e025e007 eor lr, r5, r7

17e50: e02ee2e4 eor lr, lr, r4, ror #5

17e54: e0223b63 eor r3, r2, r3, ror #22

17e58: e023c00e eor ip, r3, lr

17e5c: e1a041e3 ror r4, r3, #3

17e60: e02cc0e5 eor ip, ip, r5, ror #1

17e64: e1a056ee ror r5, lr, #13

17e68: e0246005 eor r6, r4, r5

17e6c: e02333e7 eor r3, r3, r7, ror #7

17e70: e18c7004 orr r7, ip, r4

17e74: e0266007 eor r6, r6, r7

17e78: e023318e eor r3, r3, lr, lsl #3

17e7c: e1832006 orr r2, r3, r6

17e80: e02cc004 eor ip, ip, r4

17e84: e0233005 eor r3, r3, r5

17e88: e590a1d8 ldr sl, [r0, #472] ; 0x1d8

17e8c: e59091d4 ldr r9, [r0, #468] ; 0x1d4

17e90: e0233007 eor r3, r3, r7

17e94: e18c4002 orr r4, ip, r2

17e98: e0244003 eor r4, r4, r3

17e9c: e02cc002 eor ip, ip, r2

17ea0: e0058006 and r8, r5, r6

17ea4: e590e1d0 ldr lr, [r0, #464] ; 0x1d0

17ea8: e0052004 and r2, r5, r4

17eac: e0288009 eor r8, r8, r9

17eb0: e59051dc ldr r5, [r0, #476] ; 0x1dc

17eb4: e183300c orr r3, r3, ip

17eb8: e024400a eor r4, r4, sl

17ebc: e0233008 eor r3, r3, r8

17ec0: e0222007 eor r2, r2, r7

17ec4: e02ee00c eor lr, lr, ip

17ec8: e002200c and r2, r2, ip

17ecc: e1a04b64 ror r4, r4, #22

17ed0: e026c005 eor ip, r6, r5

17ed4: e0244383 eor r4, r4, r3, lsl #7

17ed8: e022200c eor r2, r2, ip

17edc: e023e2ee eor lr, r3, lr, ror #5

17ee0: e022500e eor r5, r2, lr

17ee4: e022c004 eor ip, r2, r4

17ee8: e02ee0e3 eor lr, lr, r3, ror #1

17eec: e02c23e2 eor r2, ip, r2, ror #7

17ef0: e0222185 eor r2, r2, r5, lsl #3

17ef4: e02ee004 eor lr, lr, r4

17ef8: e022600e eor r6, r2, lr

17efc: e1a056e5 ror r5, r5, #13

17f00: e1e04006 mvn r4, r6

17f04: e1a0c1ec ror ip, ip, #3

17f08: e1844005 orr r4, r4, r5

17f0c: e02c3005 eor r3, ip, r5

17f10: e0244002 eor r4, r4, r2

17f14: e59051c8 ldr r5, [r0, #456] ; 0x1c8

17f18: e02cc006 eor ip, ip, r6

17f1c: e59071c0 ldr r7, [r0, #448] ; 0x1c0

17f20: e1844003 orr r4, r4, r3

17f24: e00ee00c and lr, lr, ip

17f28: e1e0c00c mvn ip, ip

17f2c: e0244006 eor r4, r4, r6

17f30: e59081c4 ldr r8, [r0, #452] ; 0x1c4

17f34: e02ee003 eor lr, lr, r3

17f38: e59061cc ldr r6, [r0, #460] ; 0x1cc

17f3c: e002200c and r2, r2, ip

17f40: e0233005 eor r3, r3, r5

17f44: e0233002 eor r3, r3, r2

17f48: e18e5004 orr r5, lr, r4

17f4c: e02ee007 eor lr, lr, r7

17f50: e0242006 eor r2, r4, r6

17f54: e02cc008 eor ip, ip, r8

17f58: e0233005 eor r3, r3, r5

17f5c: e02cc005 eor ip, ip, r5

17f60: e022e2ee eor lr, r2, lr, ror #5

17f64: e02ee00c eor lr, lr, ip

17f68: e1a03b63 ror r3, r3, #22

17f6c: e023338c eor r3, r3, ip, lsl #7

17f70: e1a073e2 ror r7, r2, #7

17f74: e0233002 eor r3, r3, r2

17f78: e027718e eor r7, r7, lr, lsl #3

17f7c: e0277003 eor r7, r7, r3

17f80: e02ec0ec eor ip, lr, ip, ror #1

17f84: e1a0e6ee ror lr, lr, #13

17f88: e02cc003 eor ip, ip, r3

17f8c: e027800e eor r8, r7, lr

17f90: e1a031e3 ror r3, r3, #3

17f94: e1834008 orr r4, r3, r8

17f98: e02c6003 eor r6, ip, r3

17f9c: e59021b0 ldr r2, [r0, #432] ; 0x1b0

17fa0: e026600e eor r6, r6, lr

17fa4: e024400c eor r4, r4, ip

17fa8: e00c500e and r5, ip, lr

17fac: e0044006 and r4, r4, r6

17fb0: e187c00c orr ip, r7, ip

17fb4: e0288004 eor r8, r8, r4

17fb8: e026600c eor r6, r6, ip

17fbc: e00ee007 and lr, lr, r7

17fc0: e590c1b4 ldr ip, [r0, #436] ; 0x1b4

17fc4: e0244005 eor r4, r4, r5

17fc8: e0233002 eor r3, r3, r2

17fcc: e59021bc ldr r2, [r0, #444] ; 0x1bc

17fd0: e1e05004 mvn r5, r4

17fd4: e023300e eor r3, r3, lr

17fd8: e0064008 and r4, r6, r8

17fdc: e590e1b8 ldr lr, [r0, #440] ; 0x1b8

17fe0: e02cc008 eor ip, ip, r8

17fe4: e0266002 eor r6, r6, r2

17fe8: e0233005 eor r3, r3, r5

17fec: e0244005 eor r4, r4, r5

17ff0: e02c2006 eor r2, ip, r6

17ff4: e024400e eor r4, r4, lr

17ff8: e02222e3 eor r2, r2, r3, ror #5

17ffc: e026338c eor r3, r6, ip, lsl #7

18000: e0234b64 eor r4, r3, r4, ror #22

18004: e022c0ec eor ip, r2, ip, ror #1

18008: e1a036e2 ror r3, r2, #13

1800c: e02cc004 eor ip, ip, r4

18010: e02cc003 eor ip, ip, r3

18014: e1e05003 mvn r5, r3

18018: e0242182 eor r2, r4, r2, lsl #3

1801c: e02223e6 eor r2, r2, r6, ror #7

18020: e185e00c orr lr, r5, ip

18024: e02ee002 eor lr, lr, r2

18028: e02e41e4 eor r4, lr, r4, ror #3

1802c: e002200c and r2, r2, ip

18030: e0222005 eor r2, r2, r5

18034: e02cc004 eor ip, ip, r4

18038: e59071a8 ldr r7, [r0, #424] ; 0x1a8

1803c: e59081a0 ldr r8, [r0, #416] ; 0x1a0

18040: e002600c and r6, r2, ip

18044: e59051a4 ldr r5, [r0, #420] ; 0x1a4

18048: e026600e eor r6, r6, lr

1804c: e003300e and r3, r3, lr

18050: e02cc007 eor ip, ip, r7

18054: e590e1ac ldr lr, [r0, #428] ; 0x1ac

18058: e1847006 orr r7, r4, r6

1805c: e0222008 eor r2, r2, r8

18060: e0266005 eor r6, r6, r5

18064: e0233007 eor r3, r3, r7

18068: e0222004 eor r2, r2, r4

1806c: e0222003 eor r2, r2, r3

18070: e1a0cb6c ror ip, ip, #22

18074: e023300e eor r3, r3, lr

18078: e0267003 eor r7, r6, r3

1807c: e02cc386 eor ip, ip, r6, lsl #7

18080: e02772e2 eor r7, r7, r2, ror #5

18084: e02cc003 eor ip, ip, r3

18088: e02c60e6 eor r6, ip, r6, ror #1

1808c: e1a056e7 ror r5, r7, #13

18090: e0266007 eor r6, r6, r7

18094: e02c33e3 eor r3, ip, r3, ror #7

18098: e0233187 eor r3, r3, r7, lsl #3

1809c: e0062005 and r2, r6, r5

180a0: e1864005 orr r4, r6, r5

180a4: e1a0c1ec ror ip, ip, #3

180a8: e0044003 and r4, r4, r3

180ac: e182200c orr r2, r2, ip

180b0: e0228004 eor r8, r2, r4

180b4: e023e008 eor lr, r3, r8

180b8: e0266004 eor r6, r6, r4

180bc: e1e0e00e mvn lr, lr

180c0: e5904198 ldr r4, [r0, #408] ; 0x198

180c4: e18ee006 orr lr, lr, r6

180c8: e02ee005 eor lr, lr, r5

180cc: e02cc006 eor ip, ip, r6

180d0: e183700e orr r7, r3, lr

180d4: e5906190 ldr r6, [r0, #400] ; 0x190

180d8: e0055008 and r5, r5, r8

180dc: e590319c ldr r3, [r0, #412] ; 0x19c

180e0: e0222004 eor r2, r2, r4

180e4: e5904194 ldr r4, [r0, #404] ; 0x194

180e8: e027700c eor r7, r7, ip

180ec: e0222005 eor r2, r2, r5

180f0: e0233008 eor r3, r3, r8

180f4: e022200e eor r2, r2, lr

180f8: e02ee004 eor lr, lr, r4

180fc: e0274006 eor r4, r7, r6

18100: e0222007 eor r2, r2, r7

18104: e02e6003 eor r6, lr, r3

18108: e023c38e eor ip, r3, lr, lsl #7

1810c: e02662e4 eor r6, r6, r4, ror #5

18110: e02c2b62 eor r2, ip, r2, ror #22

18114: e0224006 eor r4, r2, r6

18118: e1a056e6 ror r5, r6, #13

1811c: e02440ee eor r4, r4, lr, ror #1

18120: e1e0c005 mvn ip, r5

18124: e02233e3 eor r3, r2, r3, ror #7

18128: e1a021e2 ror r2, r2, #3

1812c: e0255004 eor r5, r5, r4

18130: e0233186 eor r3, r3, r6, lsl #3

18134: e182700c orr r7, r2, ip

18138: e0277003 eor r7, r7, r3

1813c: e0222005 eor r2, r2, r5

18140: e002e007 and lr, r2, r7

18144: e02ee005 eor lr, lr, r5

18148: e5908188 ldr r8, [r0, #392] ; 0x188

1814c: e184500e orr r5, r4, lr

18150: e5906184 ldr r6, [r0, #388] ; 0x184

18154: e003300c and r3, r3, ip

18158: e0233002 eor r3, r3, r2

1815c: e022c007 eor ip, r2, r7

18160: e590218c ldr r2, [r0, #396] ; 0x18c

18164: e0277005 eor r7, r7, r5

18168: e5905180 ldr r5, [r0, #384] ; 0x180

1816c: e1844007 orr r4, r4, r7

18170: e026600c eor r6, r6, ip

18174: e0233008 eor r3, r3, r8

18178: e0277002 eor r7, r7, r2

1817c: e0233004 eor r3, r3, r4

18180: e02ee005 eor lr, lr, r5

18184: e027c386 eor ip, r7, r6, lsl #7

18188: e02ee004 eor lr, lr, r4

1818c: e02c2b63 eor r2, ip, r3, ror #22

18190: e026c007 eor ip, r6, r7

18194: e02ce2ee eor lr, ip, lr, ror #5

18198: e022c00e eor ip, r2, lr

1819c: e1a091e2 ror r9, r2, #3

181a0: e02c60e6 eor r6, ip, r6, ror #1

181a4: e1e05009 mvn r5, r9

181a8: e02223e7 eor r2, r2, r7, ror #7

181ac: e022218e eor r2, r2, lr, lsl #3

181b0: e0064005 and r4, r6, r5

181b4: e1a0e6ee ror lr, lr, #13

181b8: e0244002 eor r4, r4, r2

181bc: e5903170 ldr r3, [r0, #368] ; 0x170

181c0: e00e8004 and r8, lr, r4

181c4: e182200e orr r2, r2, lr

181c8: e590a178 ldr sl, [r0, #376] ; 0x178

181cc: e0267005 eor r7, r6, r5

181d0: e029c00e eor ip, r9, lr

181d4: e0277008 eor r7, r7, r8

181d8: e18c9008 orr r9, ip, r8

181dc: e0233005 eor r3, r3, r5

181e0: e5908174 ldr r8, [r0, #372] ; 0x174

181e4: e0065002 and r5, r6, r2

181e8: e025c00a eor ip, r5, sl

181ec: e1866007 orr r6, r6, r7

181f0: e0233002 eor r3, r3, r2

181f4: e590217c ldr r2, [r0, #380] ; 0x17c

181f8: e00ee006 and lr, lr, r6

181fc: e02cc009 eor ip, ip, r9

18200: e0244008 eor r4, r4, r8

18204: e0233006 eor r3, r3, r6

18208: e024400e eor r4, r4, lr

1820c: e0222007 eor r2, r2, r7

18210: e02432e3 eor r3, r4, r3, ror #5

18214: e022cb6c eor ip, r2, ip, ror #22

18218: e02cc384 eor ip, ip, r4, lsl #7

1821c: e0233002 eor r3, r3, r2

18220: e02ce003 eor lr, ip, r3

18224: e02c23e2 eor r2, ip, r2, ror #7

18228: e1a051ec ror r5, ip, #3

1822c: e0222183 eor r2, r2, r3, lsl #3

18230: e02ee0e4 eor lr, lr, r4, ror #1

18234: e1a036e3 ror r3, r3, #13

18238: e182600e orr r6, r2, lr

1823c: e5908164 ldr r8, [r0, #356] ; 0x164

18240: e1824005 orr r4, r2, r5

18244: e0227003 eor r7, r2, r3

18248: e024400e eor r4, r4, lr

1824c: e590c168 ldr ip, [r0, #360] ; 0x168

18250: e0077006 and r7, r7, r6

18254: e003a004 and sl, r3, r4

18258: e0277004 eor r7, r7, r4

1825c: e025e008 eor lr, r5, r8

18260: e5909160 ldr r9, [r0, #352] ; 0x160

18264: e185b00a orr fp, r5, sl

18268: e590816c ldr r8, [r0, #364] ; 0x16c

1826c: e1e03003 mvn r3, r3

18270: e022200e eor r2, r2, lr

18274: e026600c eor r6, r6, ip

18278: e025c007 eor ip, r5, r7

1827c: e026600b eor r6, r6, fp

18280: e022500a eor r5, r2, sl

18284: e187b003 orr fp, r7, r3

18288: e18cc003 orr ip, ip, r3

1828c: e0244009 eor r4, r4, r9

18290: e0277008 eor r7, r7, r8

18294: e026300b eor r3, r6, fp

18298: e024400c eor r4, r4, ip

1829c: e0272385 eor r2, r7, r5, lsl #7

182a0: e025c007 eor ip, r5, r7

182a4: e02cc2e4 eor ip, ip, r4, ror #5

182a8: e0223b63 eor r3, r2, r3, ror #22

182ac: e023e00c eor lr, r3, ip

182b0: e1a041e3 ror r4, r3, #3

182b4: e02ee0e5 eor lr, lr, r5, ror #1

182b8: e1a056ec ror r5, ip, #13

182bc: e0246005 eor r6, r4, r5

182c0: e02333e7 eor r3, r3, r7, ror #7

182c4: e18e7004 orr r7, lr, r4

182c8: e0266007 eor r6, r6, r7

182cc: e023318c eor r3, r3, ip, lsl #3

182d0: e1832006 orr r2, r3, r6

182d4: e02ee004 eor lr, lr, r4

182d8: e0233005 eor r3, r3, r5

182dc: e590a158 ldr sl, [r0, #344] ; 0x158

182e0: e5909154 ldr r9, [r0, #340] ; 0x154

182e4: e0233007 eor r3, r3, r7

182e8: e18e4002 orr r4, lr, r2

182ec: e0244003 eor r4, r4, r3

182f0: e02ee002 eor lr, lr, r2

182f4: e0058006 and r8, r5, r6

182f8: e590c150 ldr ip, [r0, #336] ; 0x150

182fc: e0052004 and r2, r5, r4

18300: e0288009 eor r8, r8, r9

18304: e590515c ldr r5, [r0, #348] ; 0x15c

18308: e183300e orr r3, r3, lr

1830c: e024400a eor r4, r4, sl

18310: e0233008 eor r3, r3, r8

18314: e0222007 eor r2, r2, r7

18318: e02cc00e eor ip, ip, lr

1831c: e002200e and r2, r2, lr

18320: e1a04b64 ror r4, r4, #22

18324: e026e005 eor lr, r6, r5

18328: e0244383 eor r4, r4, r3, lsl #7

1832c: e022200e eor r2, r2, lr

18330: e023c2ec eor ip, r3, ip, ror #5

18334: e022500c eor r5, r2, ip

18338: e022e004 eor lr, r2, r4

1833c: e02e23e2 eor r2, lr, r2, ror #7

18340: e02cc0e3 eor ip, ip, r3, ror #1

18344: e0226185 eor r6, r2, r5, lsl #3

18348: e02cc004 eor ip, ip, r4

1834c: e026700c eor r7, r6, ip

18350: e1a056e5 ror r5, r5, #13

18354: e1e04007 mvn r4, r7

18358: e1a0e1ee ror lr, lr, #3

1835c: e1844005 orr r4, r4, r5

18360: e5903148 ldr r3, [r0, #328] ; 0x148

18364: e025500e eor r5, r5, lr

18368: e0244006 eor r4, r4, r6

1836c: e02ee007 eor lr, lr, r7

18370: e1e0200e mvn r2, lr

18374: e1844005 orr r4, r4, r5

18378: e00cc00e and ip, ip, lr

1837c: e590e140 ldr lr, [r0, #320] ; 0x140

18380: e5908144 ldr r8, [r0, #324] ; 0x144

18384: e0244007 eor r4, r4, r7

18388: e0066002 and r6, r6, r2

1838c: e590714c ldr r7, [r0, #332] ; 0x14c

18390: e02cc005 eor ip, ip, r5

18394: e0233005 eor r3, r3, r5

18398: e0233006 eor r3, r3, r6

1839c: e18c5004 orr r5, ip, r4

183a0: e02cc00e eor ip, ip, lr

183a4: e0244007 eor r4, r4, r7

183a8: e0222008 eor r2, r2, r8

183ac: e0233005 eor r3, r3, r5

183b0: e0222005 eor r2, r2, r5

183b4: e024c2ec eor ip, r4, ip, ror #5

183b8: e02cc002 eor ip, ip, r2

183bc: e1a03b63 ror r3, r3, #22

183c0: e0233382 eor r3, r3, r2, lsl #7

183c4: e1a0e3e4 ror lr, r4, #7

183c8: e0233004 eor r3, r3, r4

183cc: e02e418c eor r4, lr, ip, lsl #3

183d0: e1a076ec ror r7, ip, #13

183d4: e0244003 eor r4, r4, r3

183d8: e02c20e2 eor r2, ip, r2, ror #1

183dc: e0222003 eor r2, r2, r3

183e0: e024c007 eor ip, r4, r7

183e4: e1a031e3 ror r3, r3, #3

183e8: e183e00c orr lr, r3, ip

183ec: e0229003 eor r9, r2, r3

183f0: e0299007 eor r9, r9, r7

183f4: e5908130 ldr r8, [r0, #304] ; 0x130

183f8: e02ee002 eor lr, lr, r2

183fc: e0025007 and r5, r2, r7

18400: e00ee009 and lr, lr, r9

18404: e1842002 orr r2, r4, r2

18408: e02c600e eor r6, ip, lr

1840c: e0222009 eor r2, r2, r9

18410: e0044007 and r4, r4, r7

18414: e590c134 ldr ip, [r0, #308] ; 0x134

18418: e590713c ldr r7, [r0, #316] ; 0x13c

1841c: e02ee005 eor lr, lr, r5

18420: e0233008 eor r3, r3, r8

18424: e1e0500e mvn r5, lr

18428: e5908138 ldr r8, [r0, #312] ; 0x138

1842c: e002e006 and lr, r2, r6

18430: e0233004 eor r3, r3, r4

18434: e02cc006 eor ip, ip, r6

18438: e0224007 eor r4, r2, r7

1843c: e0233005 eor r3, r3, r5

18440: e02ee005 eor lr, lr, r5

18444: e02c2004 eor r2, ip, r4

18448: e02ee008 eor lr, lr, r8

1844c: e02222e3 eor r2, r2, r3, ror #5

18450: e024338c eor r3, r4, ip, lsl #7

18454: e023eb6e eor lr, r3, lr, ror #22

18458: e022c0ec eor ip, r2, ip, ror #1

1845c: e1a036e2 ror r3, r2, #13

18460: e02cc00e eor ip, ip, lr

18464: e02cc003 eor ip, ip, r3

18468: e1e05003 mvn r5, r3

1846c: e02e2182 eor r2, lr, r2, lsl #3

18470: e02223e4 eor r2, r2, r4, ror #7

18474: e185400c orr r4, r5, ip

18478: e0244002 eor r4, r4, r2

1847c: e024e1ee eor lr, r4, lr, ror #3

18480: e002200c and r2, r2, ip

18484: e0222005 eor r2, r2, r5

18488: e02cc00e eor ip, ip, lr

1848c: e5907128 ldr r7, [r0, #296] ; 0x128

18490: e5908120 ldr r8, [r0, #288] ; 0x120

18494: e002600c and r6, r2, ip

18498: e5905124 ldr r5, [r0, #292] ; 0x124

1849c: e0266004 eor r6, r6, r4

184a0: e0033004 and r3, r3, r4

184a4: e02cc007 eor ip, ip, r7

184a8: e590412c ldr r4, [r0, #300] ; 0x12c

184ac: e18e7006 orr r7, lr, r6

184b0: e0222008 eor r2, r2, r8

184b4: e0266005 eor r6, r6, r5

184b8: e0233007 eor r3, r3, r7

184bc: e022200e eor r2, r2, lr

184c0: e0222003 eor r2, r2, r3

184c4: e1a0cb6c ror ip, ip, #22

184c8: e0233004 eor r3, r3, r4

184cc: e0267003 eor r7, r6, r3

184d0: e02cc386 eor ip, ip, r6, lsl #7

184d4: e02772e2 eor r7, r7, r2, ror #5

184d8: e02cc003 eor ip, ip, r3

184dc: e02c60e6 eor r6, ip, r6, ror #1

184e0: e1a056e7 ror r5, r7, #13

184e4: e0266007 eor r6, r6, r7

184e8: e02c33e3 eor r3, ip, r3, ror #7

184ec: e0233187 eor r3, r3, r7, lsl #3

184f0: e0062005 and r2, r6, r5

184f4: e1864005 orr r4, r6, r5

184f8: e1a0c1ec ror ip, ip, #3

184fc: e0044003 and r4, r4, r3

18500: e182200c orr r2, r2, ip

18504: e0228004 eor r8, r2, r4

18508: e023e008 eor lr, r3, r8

1850c: e0266004 eor r6, r6, r4

18510: e1e0e00e mvn lr, lr

18514: e5904118 ldr r4, [r0, #280] ; 0x118

18518: e18ee006 orr lr, lr, r6

1851c: e02ee005 eor lr, lr, r5

18520: e02cc006 eor ip, ip, r6

18524: e183700e orr r7, r3, lr

18528: e5906110 ldr r6, [r0, #272] ; 0x110

1852c: e0055008 and r5, r5, r8

18530: e590311c ldr r3, [r0, #284] ; 0x11c

18534: e0222004 eor r2, r2, r4

18538: e5904114 ldr r4, [r0, #276] ; 0x114

1853c: e027700c eor r7, r7, ip

18540: e0222005 eor r2, r2, r5

18544: e0233008 eor r3, r3, r8

18548: e022200e eor r2, r2, lr

1854c: e02ee004 eor lr, lr, r4

18550: e0274006 eor r4, r7, r6

18554: e0222007 eor r2, r2, r7

18558: e02e6003 eor r6, lr, r3

1855c: e023c38e eor ip, r3, lr, lsl #7

18560: e02662e4 eor r6, r6, r4, ror #5

18564: e02c2b62 eor r2, ip, r2, ror #22

18568: e0224006 eor r4, r2, r6

1856c: e1a056e6 ror r5, r6, #13

18570: e02440ee eor r4, r4, lr, ror #1

18574: e1e0c005 mvn ip, r5

18578: e02233e3 eor r3, r2, r3, ror #7

1857c: e1a021e2 ror r2, r2, #3

18580: e0255004 eor r5, r5, r4

18584: e0233186 eor r3, r3, r6, lsl #3

18588: e182700c orr r7, r2, ip

1858c: e0277003 eor r7, r7, r3

18590: e0222005 eor r2, r2, r5

18594: e002e007 and lr, r2, r7

18598: e02ee005 eor lr, lr, r5

1859c: e5908108 ldr r8, [r0, #264] ; 0x108

185a0: e184500e orr r5, r4, lr

185a4: e5906104 ldr r6, [r0, #260] ; 0x104

185a8: e003300c and r3, r3, ip

185ac: e0233002 eor r3, r3, r2

185b0: e022c007 eor ip, r2, r7

185b4: e590210c ldr r2, [r0, #268] ; 0x10c

185b8: e0277005 eor r7, r7, r5

185bc: e5905100 ldr r5, [r0, #256] ; 0x100

185c0: e1844007 orr r4, r4, r7

185c4: e026600c eor r6, r6, ip

185c8: e0233008 eor r3, r3, r8

185cc: e0277002 eor r7, r7, r2

185d0: e0233004 eor r3, r3, r4

185d4: e02ee005 eor lr, lr, r5

185d8: e027c386 eor ip, r7, r6, lsl #7

185dc: e02ee004 eor lr, lr, r4

185e0: e02c2b63 eor r2, ip, r3, ror #22

185e4: e026c007 eor ip, r6, r7

185e8: e02ce2ee eor lr, ip, lr, ror #5

185ec: e022c00e eor ip, r2, lr

185f0: e1a091e2 ror r9, r2, #3

185f4: e02c60e6 eor r6, ip, r6, ror #1

185f8: e1e05009 mvn r5, r9

185fc: e02223e7 eor r2, r2, r7, ror #7

18600: e022218e eor r2, r2, lr, lsl #3

18604: e0064005 and r4, r6, r5

18608: e1a0e6ee ror lr, lr, #13

1860c: e0244002 eor r4, r4, r2

18610: e59030f0 ldr r3, [r0, #240] ; 0xf0

18614: e00e8004 and r8, lr, r4

18618: e182200e orr r2, r2, lr

1861c: e590a0f8 ldr sl, [r0, #248] ; 0xf8

18620: e0267005 eor r7, r6, r5

18624: e029c00e eor ip, r9, lr

18628: e0277008 eor r7, r7, r8

1862c: e18c9008 orr r9, ip, r8

18630: e0233005 eor r3, r3, r5

18634: e59080f4 ldr r8, [r0, #244] ; 0xf4

18638: e0065002 and r5, r6, r2

1863c: e025c00a eor ip, r5, sl

18640: e1866007 orr r6, r6, r7

18644: e0233002 eor r3, r3, r2

18648: e59020fc ldr r2, [r0, #252] ; 0xfc

1864c: e00ee006 and lr, lr, r6

18650: e02cc009 eor ip, ip, r9

18654: e0244008 eor r4, r4, r8

18658: e0233006 eor r3, r3, r6

1865c: e024400e eor r4, r4, lr

18660: e0222007 eor r2, r2, r7

18664: e02432e3 eor r3, r4, r3, ror #5

18668: e022cb6c eor ip, r2, ip, ror #22

1866c: e02cc384 eor ip, ip, r4, lsl #7

18670: e0233002 eor r3, r3, r2

18674: e02ce003 eor lr, ip, r3

18678: e02c23e2 eor r2, ip, r2, ror #7

1867c: e1a051ec ror r5, ip, #3

18680: e0222183 eor r2, r2, r3, lsl #3

18684: e02ee0e4 eor lr, lr, r4, ror #1

18688: e1a036e3 ror r3, r3, #13

1868c: e182600e orr r6, r2, lr

18690: e59080e4 ldr r8, [r0, #228] ; 0xe4

18694: e1824005 orr r4, r2, r5

18698: e0227003 eor r7, r2, r3

1869c: e024400e eor r4, r4, lr

186a0: e590c0e8 ldr ip, [r0, #232] ; 0xe8

186a4: e0077006 and r7, r7, r6

186a8: e003a004 and sl, r3, r4

186ac: e0277004 eor r7, r7, r4

186b0: e025e008 eor lr, r5, r8

186b4: e59090e0 ldr r9, [r0, #224] ; 0xe0

186b8: e185b00a orr fp, r5, sl

186bc: e59080ec ldr r8, [r0, #236] ; 0xec

186c0: e1e03003 mvn r3, r3

186c4: e022200e eor r2, r2, lr

186c8: e026600c eor r6, r6, ip

186cc: e025c007 eor ip, r5, r7

186d0: e026600b eor r6, r6, fp

186d4: e022500a eor r5, r2, sl

186d8: e18cc003 orr ip, ip, r3

186dc: e187b003 orr fp, r7, r3

186e0: e0244009 eor r4, r4, r9

186e4: e0277008 eor r7, r7, r8

186e8: e024400c eor r4, r4, ip

186ec: e026300b eor r3, r6, fp

186f0: e0272385 eor r2, r7, r5, lsl #7

186f4: e025c007 eor ip, r5, r7

186f8: e02cc2e4 eor ip, ip, r4, ror #5

186fc: e0223b63 eor r3, r2, r3, ror #22

18700: e023e00c eor lr, r3, ip

18704: e1a041e3 ror r4, r3, #3

18708: e02ee0e5 eor lr, lr, r5, ror #1

1870c: e1a056ec ror r5, ip, #13

18710: e0246005 eor r6, r4, r5

18714: e02333e7 eor r3, r3, r7, ror #7

18718: e18e7004 orr r7, lr, r4

1871c: e0266007 eor r6, r6, r7

18720: e023318c eor r3, r3, ip, lsl #3

18724: e1832006 orr r2, r3, r6

18728: e02ee004 eor lr, lr, r4

1872c: e0233005 eor r3, r3, r5

18730: e590a0d8 ldr sl, [r0, #216] ; 0xd8

18734: e59090d4 ldr r9, [r0, #212] ; 0xd4

18738: e0233007 eor r3, r3, r7

1873c: e18e4002 orr r4, lr, r2

18740: e0244003 eor r4, r4, r3

18744: e02ee002 eor lr, lr, r2

18748: e0058006 and r8, r5, r6

1874c: e590c0d0 ldr ip, [r0, #208] ; 0xd0

18750: e0052004 and r2, r5, r4

18754: e0288009 eor r8, r8, r9

18758: e59050dc ldr r5, [r0, #220] ; 0xdc

1875c: e183300e orr r3, r3, lr

18760: e024400a eor r4, r4, sl

18764: e0233008 eor r3, r3, r8

18768: e0222007 eor r2, r2, r7

1876c: e02cc00e eor ip, ip, lr

18770: e002200e and r2, r2, lr

18774: e1a04b64 ror r4, r4, #22

18778: e026e005 eor lr, r6, r5

1877c: e0244383 eor r4, r4, r3, lsl #7

18780: e022200e eor r2, r2, lr

18784: e023c2ec eor ip, r3, ip, ror #5

18788: e022500c eor r5, r2, ip

1878c: e022e004 eor lr, r2, r4

18790: e02e23e2 eor r2, lr, r2, ror #7

18794: e02cc0e3 eor ip, ip, r3, ror #1

18798: e0226185 eor r6, r2, r5, lsl #3

1879c: e02cc004 eor ip, ip, r4

187a0: e026700c eor r7, r6, ip

187a4: e1a056e5 ror r5, r5, #13

187a8: e1e04007 mvn r4, r7

187ac: e1a0e1ee ror lr, lr, #3

187b0: e1844005 orr r4, r4, r5

187b4: e59030c8 ldr r3, [r0, #200] ; 0xc8

187b8: e025500e eor r5, r5, lr

187bc: e0244006 eor r4, r4, r6

187c0: e02ee007 eor lr, lr, r7

187c4: e1e0200e mvn r2, lr

187c8: e1844005 orr r4, r4, r5

187cc: e00cc00e and ip, ip, lr

187d0: e590e0c0 ldr lr, [r0, #192] ; 0xc0

187d4: e59080c4 ldr r8, [r0, #196] ; 0xc4

187d8: e0244007 eor r4, r4, r7

187dc: e0066002 and r6, r6, r2

187e0: e59070cc ldr r7, [r0, #204] ; 0xcc

187e4: e02cc005 eor ip, ip, r5

187e8: e0233005 eor r3, r3, r5

187ec: e0233006 eor r3, r3, r6

187f0: e18c5004 orr r5, ip, r4

187f4: e02cc00e eor ip, ip, lr

187f8: e0244007 eor r4, r4, r7

187fc: e0222008 eor r2, r2, r8

18800: e0233005 eor r3, r3, r5

18804: e0222005 eor r2, r2, r5

18808: e024c2ec eor ip, r4, ip, ror #5

1880c: e02cc002 eor ip, ip, r2

18810: e1a03b63 ror r3, r3, #22

18814: e0233382 eor r3, r3, r2, lsl #7

18818: e1a0e3e4 ror lr, r4, #7

1881c: e0233004 eor r3, r3, r4

18820: e02e418c eor r4, lr, ip, lsl #3

18824: e1a076ec ror r7, ip, #13

18828: e0244003 eor r4, r4, r3

1882c: e02c20e2 eor r2, ip, r2, ror #1

18830: e0222003 eor r2, r2, r3

18834: e024c007 eor ip, r4, r7

18838: e1a031e3 ror r3, r3, #3

1883c: e183e00c orr lr, r3, ip

18840: e0229003 eor r9, r2, r3

18844: e0299007 eor r9, r9, r7

18848: e59080b0 ldr r8, [r0, #176] ; 0xb0

1884c: e02ee002 eor lr, lr, r2

18850: e0025007 and r5, r2, r7

18854: e00ee009 and lr, lr, r9

18858: e1842002 orr r2, r4, r2

1885c: e02c600e eor r6, ip, lr

18860: e0222009 eor r2, r2, r9

18864: e0044007 and r4, r4, r7

18868: e590c0b4 ldr ip, [r0, #180] ; 0xb4

1886c: e59070bc ldr r7, [r0, #188] ; 0xbc

18870: e02ee005 eor lr, lr, r5

18874: e0233008 eor r3, r3, r8

18878: e1e0500e mvn r5, lr

1887c: e59080b8 ldr r8, [r0, #184] ; 0xb8

18880: e002e006 and lr, r2, r6

18884: e0233004 eor r3, r3, r4

18888: e02cc006 eor ip, ip, r6

1888c: e0224007 eor r4, r2, r7

18890: e0233005 eor r3, r3, r5

18894: e02ee005 eor lr, lr, r5

18898: e02c2004 eor r2, ip, r4

1889c: e02ee008 eor lr, lr, r8

188a0: e02222e3 eor r2, r2, r3, ror #5

188a4: e024338c eor r3, r4, ip, lsl #7

188a8: e023eb6e eor lr, r3, lr, ror #22

188ac: e022c0ec eor ip, r2, ip, ror #1

188b0: e1a036e2 ror r3, r2, #13

188b4: e02cc00e eor ip, ip, lr

188b8: e02cc003 eor ip, ip, r3

188bc: e1e05003 mvn r5, r3

188c0: e02e2182 eor r2, lr, r2, lsl #3

188c4: e02223e4 eor r2, r2, r4, ror #7

188c8: e185400c orr r4, r5, ip

188cc: e0244002 eor r4, r4, r2

188d0: e024e1ee eor lr, r4, lr, ror #3

188d4: e002200c and r2, r2, ip

188d8: e0222005 eor r2, r2, r5

188dc: e02cc00e eor ip, ip, lr

188e0: e59070a8 ldr r7, [r0, #168] ; 0xa8

188e4: e59080a0 ldr r8, [r0, #160] ; 0xa0

188e8: e002600c and r6, r2, ip

188ec: e59050a4 ldr r5, [r0, #164] ; 0xa4

188f0: e0266004 eor r6, r6, r4

188f4: e0033004 and r3, r3, r4

188f8: e02cc007 eor ip, ip, r7

188fc: e59040ac ldr r4, [r0, #172] ; 0xac

18900: e18e7006 orr r7, lr, r6

18904: e0222008 eor r2, r2, r8

18908: e0266005 eor r6, r6, r5

1890c: e0233007 eor r3, r3, r7

18910: e022200e eor r2, r2, lr

18914: e0222003 eor r2, r2, r3

18918: e1a0cb6c ror ip, ip, #22

1891c: e0233004 eor r3, r3, r4

18920: e0267003 eor r7, r6, r3

18924: e02cc386 eor ip, ip, r6, lsl #7

18928: e02772e2 eor r7, r7, r2, ror #5

1892c: e02cc003 eor ip, ip, r3

18930: e02c60e6 eor r6, ip, r6, ror #1

18934: e1a056e7 ror r5, r7, #13

18938: e0266007 eor r6, r6, r7

1893c: e02c33e3 eor r3, ip, r3, ror #7

18940: e0233187 eor r3, r3, r7, lsl #3

18944: e0062005 and r2, r6, r5

18948: e1864005 orr r4, r6, r5

1894c: e1a0c1ec ror ip, ip, #3

18950: e0044003 and r4, r4, r3

18954: e182200c orr r2, r2, ip

18958: e0228004 eor r8, r2, r4

1895c: e023e008 eor lr, r3, r8

18960: e0266004 eor r6, r6, r4

18964: e1e0e00e mvn lr, lr

18968: e5904098 ldr r4, [r0, #152] ; 0x98

1896c: e18ee006 orr lr, lr, r6

18970: e02ee005 eor lr, lr, r5

18974: e02cc006 eor ip, ip, r6

18978: e183700e orr r7, r3, lr

1897c: e5906090 ldr r6, [r0, #144] ; 0x90

18980: e0055008 and r5, r5, r8

18984: e590309c ldr r3, [r0, #156] ; 0x9c

18988: e0222004 eor r2, r2, r4

1898c: e5904094 ldr r4, [r0, #148] ; 0x94

18990: e027700c eor r7, r7, ip

18994: e0222005 eor r2, r2, r5

18998: e0233008 eor r3, r3, r8

1899c: e022200e eor r2, r2, lr

189a0: e02ee004 eor lr, lr, r4

189a4: e0274006 eor r4, r7, r6

189a8: e0222007 eor r2, r2, r7

189ac: e02e6003 eor r6, lr, r3

189b0: e023c38e eor ip, r3, lr, lsl #7

189b4: e02662e4 eor r6, r6, r4, ror #5

189b8: e02c2b62 eor r2, ip, r2, ror #22

189bc: e0224006 eor r4, r2, r6

189c0: e1a056e6 ror r5, r6, #13

189c4: e02440ee eor r4, r4, lr, ror #1

189c8: e1e0c005 mvn ip, r5

189cc: e02233e3 eor r3, r2, r3, ror #7

189d0: e1a021e2 ror r2, r2, #3

189d4: e0255004 eor r5, r5, r4

189d8: e0233186 eor r3, r3, r6, lsl #3

189dc: e182700c orr r7, r2, ip

189e0: e0277003 eor r7, r7, r3

189e4: e0222005 eor r2, r2, r5

189e8: e002e007 and lr, r2, r7

189ec: e02ee005 eor lr, lr, r5

189f0: e5908088 ldr r8, [r0, #136] ; 0x88

189f4: e184500e orr r5, r4, lr

189f8: e5906084 ldr r6, [r0, #132] ; 0x84

189fc: e003300c and r3, r3, ip

18a00: e0233002 eor r3, r3, r2

18a04: e022c007 eor ip, r2, r7

18a08: e590208c ldr r2, [r0, #140] ; 0x8c

18a0c: e0277005 eor r7, r7, r5

18a10: e5905080 ldr r5, [r0, #128] ; 0x80

18a14: e1844007 orr r4, r4, r7

18a18: e026600c eor r6, r6, ip

18a1c: e0233008 eor r3, r3, r8

18a20: e0277002 eor r7, r7, r2

18a24: e0233004 eor r3, r3, r4

18a28: e02ee005 eor lr, lr, r5

18a2c: e027c386 eor ip, r7, r6, lsl #7

18a30: e02ee004 eor lr, lr, r4

18a34: e02c2b63 eor r2, ip, r3, ror #22

18a38: e026c007 eor ip, r6, r7

18a3c: e02ce2ee eor lr, ip, lr, ror #5

18a40: e022c00e eor ip, r2, lr

18a44: e1a091e2 ror r9, r2, #3

18a48: e02c60e6 eor r6, ip, r6, ror #1

18a4c: e02223e7 eor r2, r2, r7, ror #7

18a50: e1e07009 mvn r7, r9

18a54: e022218e eor r2, r2, lr, lsl #3

18a58: e0065007 and r5, r6, r7

18a5c: e1a0e6ee ror lr, lr, #13

18a60: e0255002 eor r5, r5, r2

18a64: e5903070 ldr r3, [r0, #112] ; 0x70

18a68: e00e4005 and r4, lr, r5

18a6c: e182200e orr r2, r2, lr

18a70: e590a078 ldr sl, [r0, #120] ; 0x78

18a74: e0268007 eor r8, r6, r7

18a78: e029c00e eor ip, r9, lr

18a7c: e0288004 eor r8, r8, r4

18a80: e18c9004 orr r9, ip, r4

18a84: e0233007 eor r3, r3, r7

18a88: e5904074 ldr r4, [r0, #116] ; 0x74

18a8c: e0067002 and r7, r6, r2

18a90: e027c00a eor ip, r7, sl

18a94: e1866008 orr r6, r6, r8

18a98: e0233002 eor r3, r3, r2

18a9c: e590207c ldr r2, [r0, #124] ; 0x7c

18aa0: e0255004 eor r5, r5, r4

18aa4: e00ee006 and lr, lr, r6

18aa8: e02cc009 eor ip, ip, r9

18aac: e0233006 eor r3, r3, r6

18ab0: e02ee005 eor lr, lr, r5

18ab4: e0222008 eor r2, r2, r8

18ab8: e02e32e3 eor r3, lr, r3, ror #5

18abc: e022cb6c eor ip, r2, ip, ror #22

18ac0: e0233002 eor r3, r3, r2

18ac4: e02cc38e eor ip, ip, lr, lsl #7

18ac8: e02c5003 eor r5, ip, r3

18acc: e02c23e2 eor r2, ip, r2, ror #7

18ad0: e0222183 eor r2, r2, r3, lsl #3

18ad4: e02550ee eor r5, r5, lr, ror #1

18ad8: e1a0c1ec ror ip, ip, #3

18adc: e1a036e3 ror r3, r3, #13

18ae0: e0228003 eor r8, r2, r3

18ae4: e1827005 orr r7, r2, r5

18ae8: e182400c orr r4, r2, ip

18aec: e5906064 ldr r6, [r0, #100] ; 0x64

18af0: e0244005 eor r4, r4, r5

18af4: e0085007 and r5, r8, r7

18af8: e5908068 ldr r8, [r0, #104] ; 0x68

18afc: e003e004 and lr, r3, r4

18b00: e0255004 eor r5, r5, r4

18b04: e5909060 ldr r9, [r0, #96] ; 0x60

18b08: e026600c eor r6, r6, ip

18b0c: e18ca00e orr sl, ip, lr

18b10: e1e03003 mvn r3, r3

18b14: e02cc005 eor ip, ip, r5

18b18: e0277008 eor r7, r7, r8

18b1c: e0222006 eor r2, r2, r6

18b20: e590806c ldr r8, [r0, #108] ; 0x6c

18b24: e022600e eor r6, r2, lr

18b28: e027700a eor r7, r7, sl

18b2c: e18ce003 orr lr, ip, r3

18b30: e185a003 orr sl, r5, r3

18b34: e0244009 eor r4, r4, r9

18b38: e0255008 eor r5, r5, r8

18b3c: e027300a eor r3, r7, sl

18b40: e024400e eor r4, r4, lr

18b44: e0252386 eor r2, r5, r6, lsl #7

18b48: e026e005 eor lr, r6, r5

18b4c: e02ee2e4 eor lr, lr, r4, ror #5

18b50: e0223b63 eor r3, r2, r3, ror #22

18b54: e023200e eor r2, r3, lr

18b58: e1a041e3 ror r4, r3, #3

18b5c: e02220e6 eor r2, r2, r6, ror #1

18b60: e1a066ee ror r6, lr, #13

18b64: e1827004 orr r7, r2, r4

18b68: e024c006 eor ip, r4, r6

18b6c: e02333e5 eor r3, r3, r5, ror #7

18b70: e023318e eor r3, r3, lr, lsl #3

18b74: e02ce007 eor lr, ip, r7

18b78: e183c00e orr ip, r3, lr

18b7c: e0222004 eor r2, r2, r4

18b80: e0233006 eor r3, r3, r6

18b84: e0233007 eor r3, r3, r7

18b88: e182500c orr r5, r2, ip

18b8c: e590a058 ldr sl, [r0, #88] ; 0x58

18b90: e5909054 ldr r9, [r0, #84] ; 0x54

18b94: e022200c eor r2, r2, ip

18b98: e0255003 eor r5, r5, r3

18b9c: e006800e and r8, r6, lr

18ba0: e5904050 ldr r4, [r0, #80] ; 0x50

18ba4: e006c005 and ip, r6, r5

18ba8: e1833002 orr r3, r3, r2

18bac: e590605c ldr r6, [r0, #92] ; 0x5c

18bb0: e0288009 eor r8, r8, r9

18bb4: e025500a eor r5, r5, sl

18bb8: e0233008 eor r3, r3, r8

18bbc: e02cc007 eor ip, ip, r7

18bc0: e0244002 eor r4, r4, r2

18bc4: e00cc002 and ip, ip, r2

18bc8: e1a05b65 ror r5, r5, #22

18bcc: e02e2006 eor r2, lr, r6

18bd0: e0255383 eor r5, r5, r3, lsl #7

18bd4: e02cc002 eor ip, ip, r2

18bd8: e02342e4 eor r4, r3, r4, ror #5

18bdc: e02c2004 eor r2, ip, r4

18be0: e02ce005 eor lr, ip, r5

18be4: e02ec3ec eor ip, lr, ip, ror #7

18be8: e02440e3 eor r4, r4, r3, ror #1

18bec: e02c6182 eor r6, ip, r2, lsl #3

18bf0: e0244005 eor r4, r4, r5

18bf4: e0267004 eor r7, r6, r4

18bf8: e1a026e2 ror r2, r2, #13

18bfc: e1e05007 mvn r5, r7

18c00: e1a0e1ee ror lr, lr, #3

18c04: e1855002 orr r5, r5, r2

18c08: e5903048 ldr r3, [r0, #72] ; 0x48

18c0c: e022200e eor r2, r2, lr

18c10: e0255006 eor r5, r5, r6

18c14: e02ee007 eor lr, lr, r7

18c18: e5908040 ldr r8, [r0, #64] ; 0x40

18c1c: e004400e and r4, r4, lr

18c20: e1855002 orr r5, r5, r2

18c24: e1e0e00e mvn lr, lr

18c28: e0255007 eor r5, r5, r7

18c2c: e024c002 eor ip, r4, r2

18c30: e006600e and r6, r6, lr

18c34: e0233002 eor r3, r3, r2

18c38: e5902044 ldr r2, [r0, #68] ; 0x44

18c3c: e590704c ldr r7, [r0, #76] ; 0x4c

18c40: e18c4005 orr r4, ip, r5

18c44: e0233006 eor r3, r3, r6

18c48: e02cc008 eor ip, ip, r8

18c4c: e0255007 eor r5, r5, r7

18c50: e022200e eor r2, r2, lr

18c54: e0233004 eor r3, r3, r4

18c58: e0222004 eor r2, r2, r4

18c5c: e025c2ec eor ip, r5, ip, ror #5

18c60: e02cc002 eor ip, ip, r2

18c64: e1a03b63 ror r3, r3, #22

18c68: e0233382 eor r3, r3, r2, lsl #7

18c6c: e1a063e5 ror r6, r5, #7

18c70: e0233005 eor r3, r3, r5

18c74: e026618c eor r6, r6, ip, lsl #3

18c78: e0266003 eor r6, r6, r3

18c7c: e02c20e2 eor r2, ip, r2, ror #1

18c80: e1a0c6ec ror ip, ip, #13

18c84: e0222003 eor r2, r2, r3

18c88: e026700c eor r7, r6, ip

18c8c: e1a031e3 ror r3, r3, #3

18c90: e183e007 orr lr, r3, r7

18c94: e0225003 eor r5, r2, r3

18c98: e5908030 ldr r8, [r0, #48] ; 0x30

18c9c: e025500c eor r5, r5, ip

18ca0: e02ee002 eor lr, lr, r2

18ca4: e002400c and r4, r2, ip

18ca8: e00ee005 and lr, lr, r5

18cac: e1862002 orr r2, r6, r2

18cb0: e027700e eor r7, r7, lr

18cb4: e0255002 eor r5, r5, r2

18cb8: e00cc006 and ip, ip, r6

18cbc: e5902034 ldr r2, [r0, #52] ; 0x34

18cc0: e590603c ldr r6, [r0, #60] ; 0x3c

18cc4: e02ee004 eor lr, lr, r4

18cc8: e0233008 eor r3, r3, r8

18ccc: e1e0400e mvn r4, lr

18cd0: e5908038 ldr r8, [r0, #56] ; 0x38

18cd4: e005e007 and lr, r5, r7

18cd8: e023300c eor r3, r3, ip

18cdc: e0255006 eor r5, r5, r6

18ce0: e023c004 eor ip, r3, r4

18ce4: e0222007 eor r2, r2, r7

18ce8: e02ee004 eor lr, lr, r4

18cec: e0223005 eor r3, r2, r5

18cf0: e02ee008 eor lr, lr, r8

18cf4: e02332ec eor r3, r3, ip, ror #5

18cf8: e025c382 eor ip, r5, r2, lsl #7

18cfc: e02ceb6e eor lr, ip, lr, ror #22

18d00: e02320e2 eor r2, r3, r2, ror #1

18d04: e1a046e3 ror r4, r3, #13

18d08: e022200e eor r2, r2, lr

18d0c: e0222004 eor r2, r2, r4

18d10: e1e07004 mvn r7, r4

18d14: e02e3183 eor r3, lr, r3, lsl #3

18d18: e187c002 orr ip, r7, r2

18d1c: e02333e5 eor r3, r3, r5, ror #7

18d20: e02c6003 eor r6, ip, r3

18d24: e026e1ee eor lr, r6, lr, ror #3

18d28: e0033002 and r3, r3, r2

18d2c: e0233007 eor r3, r3, r7

18d30: e022200e eor r2, r2, lr

18d34: e5907020 ldr r7, [r0, #32]

18d38: e003c002 and ip, r3, r2

18d3c: e02c5006 eor r5, ip, r6

18d40: e0233007 eor r3, r3, r7

18d44: e004c006 and ip, r4, r6

18d48: e5907024 ldr r7, [r0, #36] ; 0x24

18d4c: e5904028 ldr r4, [r0, #40] ; 0x28

18d50: e590602c ldr r6, [r0, #44] ; 0x2c

18d54: e18e8005 orr r8, lr, r5

18d58: e02c0008 eor r0, ip, r8

18d5c: e023300e eor r3, r3, lr

18d60: e0233000 eor r3, r3, r0

18d64: e025c007 eor ip, r5, r7

18d68: e0200006 eor r0, r0, r6

18d6c: e0222004 eor r2, r2, r4

18d70: e5813000 str r3, [r1]

18d74: e581000c str r0, [r1, #12]

18d78: e581c004 str ip, [r1, #4]

18d7c: e5812008 str r2, [r1, #8]

18d80: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

18d84: e3a00e23 mov r0, #560 ; 0x230

18d88: e12fff1e bx lr

18d8c: e3a00010 mov r0, #16

18d90: e12fff1e bx lr

18d94: e3a00001 mov r0, #1

18d98: e12fff1e bx lr

18d9c: e3a00020 mov r0, #32

18da0: e12fff1e bx lr

18da4: e3a03003 mov r3, #3

18da8: e5803000 str r3, [r0]

18dac: e59f0004 ldr r0, [pc, #4] ; 18db8 <mcrypt\_mutex\_register@@Base+0x14624>

18db0: e08f0000 add r0, pc, r0

18db4: e12fff1e bx lr

18db8: 00009034 andeq r9, r0, r4, lsr r0

18dbc: e59f0004 ldr r0, [pc, #4] ; 18dc8 <mcrypt\_mutex\_register@@Base+0x14634>

18dc0: e08f0000 add r0, pc, r0

18dc4: e12fff1e bx lr

18dc8: 00009030 andeq r9, r0, r0, lsr r0

18dcc: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

18dd0: e24dd0e8 sub sp, sp, #232 ; 0xe8

18dd4: ebffffec bl 18d8c <mcrypt\_mutex\_register@@Base+0x145f8>

18dd8: e1a06000 mov r6, r0

18ddc: ebffffee bl 18d9c <mcrypt\_mutex\_register@@Base+0x14608>

18de0: e1a01000 mov r1, r0

18de4: e3a00001 mov r0, #1

18de8: ebffa511 bl 2234 <calloc@plt>

18dec: e2507000 subs r7, r0, #0

18df0: 0a000064 beq 18f88 <mcrypt\_mutex\_register@@Base+0x147f4>

18df4: e2475001 sub r5, r7, #1

18df8: e3a0400a mov r4, #10

18dfc: e2678001 rsb r8, r7, #1

18e00: ea000004 b 18e18 <mcrypt\_mutex\_register@@Base+0x14684>

18e04: e2742000 rsbs r2, r4, #0

18e08: e2844002 add r4, r4, #2

18e0c: e6ef2072 uxtb r2, r2

18e10: 52623000 rsbpl r3, r2, #0

18e14: e5e53001 strb r3, [r5, #1]!

18e18: ebffffdf bl 18d9c <mcrypt\_mutex\_register@@Base+0x14608>

18e1c: e0882005 add r2, r8, r5

18e20: e6ef3074 uxtb r3, r4

18e24: e1500002 cmp r0, r2

18e28: cafffff5 bgt 18e04 <mcrypt\_mutex\_register@@Base+0x14670>

18e2c: e3560000 cmp r6, #0

18e30: da000039 ble 18f1c <mcrypt\_mutex\_register@@Base+0x14788>

18e34: e1a0400d mov r4, sp

18e38: e1a02004 mov r2, r4

18e3c: e3a03000 mov r3, #0

18e40: e4c23001 strb r3, [r2], #1

18e44: e2833001 add r3, r3, #1

18e48: e1560003 cmp r6, r3

18e4c: 1afffffb bne 18e40 <mcrypt\_mutex\_register@@Base+0x146ac>

18e50: ebffffcb bl 18d84 <mcrypt\_mutex\_register@@Base+0x145f0>

18e54: ebffa526 bl 22f4 <malloc@plt>

18e58: e2509000 subs r9, r0, #0

18e5c: 0a000049 beq 18f88 <mcrypt\_mutex\_register@@Base+0x147f4>

18e60: e28d8010 add r8, sp, #16

18e64: e1a01004 mov r1, r4

18e68: e1a02006 mov r2, r6

18e6c: e1a00008 mov r0, r8

18e70: ebffa504 bl 2288 <memcpy@plt>

18e74: ebffffc8 bl 18d9c <mcrypt\_mutex\_register@@Base+0x14608>

18e78: e1a01007 mov r1, r7

18e7c: e28da020 add sl, sp, #32

18e80: e0886006 add r6, r8, r6

18e84: e1a05008 mov r5, r8

18e88: e1a0400a mov r4, sl

18e8c: e1a02000 mov r2, r0

18e90: e1a00009 mov r0, r9

18e94: ebfff399 bl 15d00 <mcrypt\_mutex\_register@@Base+0x1156c>

18e98: e1a00007 mov r0, r7

18e9c: ebffa4f6 bl 227c <free@plt>

18ea0: e1a01008 mov r1, r8

18ea4: e1a00009 mov r0, r9

18ea8: ebfff6ed bl 16a64 <mcrypt\_mutex\_register@@Base+0x122d0>

18eac: e59f70f0 ldr r7, [pc, #240] ; 18fa4 <mcrypt\_mutex\_register@@Base+0x14810>

18eb0: e08f7007 add r7, pc, r7

18eb4: e4d52001 ldrb r2, [r5], #1

18eb8: e1a00004 mov r0, r4

18ebc: e1a01007 mov r1, r7

18ec0: ebffa529 bl 236c <sprintf@plt>

18ec4: e1550006 cmp r5, r6

18ec8: e2844002 add r4, r4, #2

18ecc: 1afffff8 bne 18eb4 <mcrypt\_mutex\_register@@Base+0x14720>

18ed0: e59f40d0 ldr r4, [pc, #208] ; 18fa8 <mcrypt\_mutex\_register@@Base+0x14814>

18ed4: e1a0000a mov r0, sl

18ed8: e08f4004 add r4, pc, r4

18edc: e1a01004 mov r1, r4

18ee0: ebffa4d9 bl 224c <strcmp@plt>

18ee4: e3500000 cmp r0, #0

18ee8: 1a00001c bne 18f60 <mcrypt\_mutex\_register@@Base+0x147cc>

18eec: e1a01008 mov r1, r8

18ef0: e1a00009 mov r0, r9

18ef4: ebfffb48 bl 17c1c <mcrypt\_mutex\_register@@Base+0x13488>

18ef8: e1a00009 mov r0, r9

18efc: ebffa4de bl 227c <free@plt>

18f00: e1a00008 mov r0, r8

18f04: e1a0100d mov r1, sp

18f08: ebffa4cf bl 224c <strcmp@plt>

18f0c: e3500000 cmp r0, #0

18f10: 1a00001e bne 18f90 <mcrypt\_mutex\_register@@Base+0x147fc>

18f14: e28dd0e8 add sp, sp, #232 ; 0xe8

18f18: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

18f1c: ebffff98 bl 18d84 <mcrypt\_mutex\_register@@Base+0x145f0>

18f20: ebffa4f3 bl 22f4 <malloc@plt>

18f24: e2509000 subs r9, r0, #0

18f28: 0a000016 beq 18f88 <mcrypt\_mutex\_register@@Base+0x147f4>

18f2c: ebffff9a bl 18d9c <mcrypt\_mutex\_register@@Base+0x14608>

18f30: e1a01007 mov r1, r7

18f34: e28d8010 add r8, sp, #16

18f38: e28da020 add sl, sp, #32

18f3c: e1a02000 mov r2, r0

18f40: e1a00009 mov r0, r9

18f44: ebfff36d bl 15d00 <mcrypt\_mutex\_register@@Base+0x1156c>

18f48: e1a00007 mov r0, r7

18f4c: ebffa4ca bl 227c <free@plt>

18f50: e1a01008 mov r1, r8

18f54: e1a00009 mov r0, r9

18f58: ebfff6c1 bl 16a64 <mcrypt\_mutex\_register@@Base+0x122d0>

18f5c: eaffffdb b 18ed0 <mcrypt\_mutex\_register@@Base+0x1473c>

18f60: e59f0044 ldr r0, [pc, #68] ; 18fac <mcrypt\_mutex\_register@@Base+0x14818>

18f64: e08f0000 add r0, pc, r0

18f68: ebffa4de bl 22e8 <puts@plt>

18f6c: e59f003c ldr r0, [pc, #60] ; 18fb0 <mcrypt\_mutex\_register@@Base+0x1481c>

18f70: e1a0200a mov r2, sl

18f74: e1a01004 mov r1, r4

18f78: e08f0000 add r0, pc, r0

18f7c: ebffa4b8 bl 2264 <printf@plt>

18f80: e1a00009 mov r0, r9

18f84: ebffa4bc bl 227c <free@plt>

18f88: e3e00000 mvn r0, #0

18f8c: eaffffe0 b 18f14 <mcrypt\_mutex\_register@@Base+0x14780>

18f90: e59f001c ldr r0, [pc, #28] ; 18fb4 <mcrypt\_mutex\_register@@Base+0x14820>

18f94: e08f0000 add r0, pc, r0

18f98: ebffa4d2 bl 22e8 <puts@plt>

18f9c: e3e00000 mvn r0, #0

18fa0: eaffffdb b 18f14 <mcrypt\_mutex\_register@@Base+0x14780>

18fa4: 000077b0 ; <UNDEFINED> instruction: 0x000077b0

18fa8: 00008f20 andeq r8, r0, r0, lsr #30

18fac: 00007718 andeq r7, r0, r8, lsl r7

18fb0: 0000771c andeq r7, r0, ip, lsl r7

18fb4: 00007718 andeq r7, r0, r8, lsl r7

18fb8: e59f0000 ldr r0, [pc] ; 18fc0 <mcrypt\_mutex\_register@@Base+0x1482c>

18fbc: e12fff1e bx lr

18fc0: 01315731 teqeq r1, r1, lsr r7

18fc4: e92d4010 push {r4, lr}

18fc8: e3a04000 mov r4, #0

18fcc: e5804000 str r4, [r0]

18fd0: e5804008 str r4, [r0, #8]

18fd4: e5804004 str r4, [r0, #4]

18fd8: e580400c str r4, [r0, #12]

18fdc: ebffa4a3 bl 2270 <memmove@plt>

18fe0: e1a00004 mov r0, r4

18fe4: e8bd8010 pop {r4, pc}

18fe8: e92d40f0 push {r4, r5, r6, r7, lr}

18fec: e3a02000 mov r2, #0

18ff0: e591e000 ldr lr, [r1]

18ff4: e591c004 ldr ip, [r1, #4]

18ff8: e59f5074 ldr r5, [pc, #116] ; 19074 <mcrypt\_mutex\_register@@Base+0x148e0>

18ffc: e59f4074 ldr r4, [pc, #116] ; 19078 <mcrypt\_mutex\_register@@Base+0x148e4>

19000: e6bfef3e rev lr, lr

19004: e6bfcf3c rev ip, ip

19008: e2026003 and r6, r2, #3

1900c: e1a032ac lsr r3, ip, #5

19010: e7906106 ldr r6, [r0, r6, lsl #2]

19014: e023320c eor r3, r3, ip, lsl #4

19018: e6bf6f36 rev r6, r6

1901c: e0866002 add r6, r6, r2

19020: e0822005 add r2, r2, r5

19024: e083300c add r3, r3, ip

19028: e1a075a2 lsr r7, r2, #11

1902c: e0233006 eor r3, r3, r6

19030: e2076003 and r6, r7, #3

19034: e08ee003 add lr, lr, r3

19038: e7906106 ldr r6, [r0, r6, lsl #2]

1903c: e1a032ae lsr r3, lr, #5

19040: e023320e eor r3, r3, lr, lsl #4

19044: e6bf6f36 rev r6, r6

19048: e083300e add r3, r3, lr

1904c: e0826006 add r6, r2, r6

19050: e0233006 eor r3, r3, r6

19054: e1520004 cmp r2, r4

19058: e08cc003 add ip, ip, r3

1905c: 1affffe9 bne 19008 <mcrypt\_mutex\_register@@Base+0x14874>

19060: e6bfef3e rev lr, lr

19064: e6bfcf3c rev ip, ip

19068: e581e000 str lr, [r1]

1906c: e581c004 str ip, [r1, #4]

19070: e8bd80f0 pop {r4, r5, r6, r7, pc}

19074: 9e3779b9 ; <UNDEFINED> instruction: 0x9e3779b9

19078: c6ef3720 strbtgt r3, [pc], r0, lsr #14

1907c: e92d4030 push {r4, r5, lr}

19080: e591c004 ldr ip, [r1, #4]

19084: e591e000 ldr lr, [r1]

19088: e59f2074 ldr r2, [pc, #116] ; 19104 <mcrypt\_mutex\_register@@Base+0x14970>

1908c: e59f4074 ldr r4, [pc, #116] ; 19108 <mcrypt\_mutex\_register@@Base+0x14974>

19090: e6bfef3e rev lr, lr

19094: e6bfcf3c rev ip, ip

19098: e1a055a2 lsr r5, r2, #11

1909c: e2055003 and r5, r5, #3

190a0: e1a032ae lsr r3, lr, #5

190a4: e7905105 ldr r5, [r0, r5, lsl #2]

190a8: e023320e eor r3, r3, lr, lsl #4

190ac: e6bf5f35 rev r5, r5

190b0: e0855002 add r5, r5, r2

190b4: e083300e add r3, r3, lr

190b8: e0822004 add r2, r2, r4

190bc: e0233005 eor r3, r3, r5

190c0: e2025003 and r5, r2, #3

190c4: e04cc003 sub ip, ip, r3

190c8: e7905105 ldr r5, [r0, r5, lsl #2]

190cc: e1a032ac lsr r3, ip, #5

190d0: e023320c eor r3, r3, ip, lsl #4

190d4: e6bf5f35 rev r5, r5

190d8: e083300c add r3, r3, ip

190dc: e0825005 add r5, r2, r5

190e0: e0233005 eor r3, r3, r5

190e4: e3520000 cmp r2, #0

190e8: e04ee003 sub lr, lr, r3

190ec: 1affffe9 bne 19098 <mcrypt\_mutex\_register@@Base+0x14904>

190f0: e6bfef3e rev lr, lr

190f4: e6bfcf3c rev ip, ip

190f8: e581e000 str lr, [r1]

190fc: e581c004 str ip, [r1, #4]

19100: e8bd8030 pop {r4, r5, pc}

19104: c6ef3720 strbtgt r3, [pc], r0, lsr #14

19108: 61c88647 bicvs r8, r8, r7, asr #12

1910c: e3a00010 mov r0, #16

19110: e12fff1e bx lr

19114: e3a00008 mov r0, #8

19118: e12fff1e bx lr

1911c: e3a00001 mov r0, #1

19120: e12fff1e bx lr

19124: e3a00010 mov r0, #16

19128: e12fff1e bx lr

1912c: e3a03001 mov r3, #1

19130: e5803000 str r3, [r0]

19134: e59f0004 ldr r0, [pc, #4] ; 19140 <mcrypt\_mutex\_register@@Base+0x149ac>

19138: e08f0000 add r0, pc, r0

1913c: e12fff1e bx lr

19140: 00008ce4 andeq r8, r0, r4, ror #25

19144: e59f0004 ldr r0, [pc, #4] ; 19150 <mcrypt\_mutex\_register@@Base+0x149bc>

19148: e08f0000 add r0, pc, r0

1914c: e12fff1e bx lr

19150: 00008cd8 ldrdeq r8, [r0], -r8 ; <UNPREDICTABLE>

19154: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

19158: e24dd0e8 sub sp, sp, #232 ; 0xe8

1915c: ebffffec bl 19114 <mcrypt\_mutex\_register@@Base+0x14980>

19160: e1a06000 mov r6, r0

19164: ebffffee bl 19124 <mcrypt\_mutex\_register@@Base+0x14990>

19168: e1a01000 mov r1, r0

1916c: e3a00001 mov r0, #1

19170: ebffa42f bl 2234 <calloc@plt>

19174: e2507000 subs r7, r0, #0

19178: 12475001 subne r5, r7, #1

1917c: 13a0400a movne r4, #10

19180: 12678001 rsbne r8, r7, #1

19184: 1a000005 bne 191a0 <mcrypt\_mutex\_register@@Base+0x14a0c>

19188: ea000060 b 19310 <mcrypt\_mutex\_register@@Base+0x14b7c>

1918c: e2742000 rsbs r2, r4, #0

19190: e2844002 add r4, r4, #2

19194: e6ef2072 uxtb r2, r2

19198: 52623000 rsbpl r3, r2, #0

1919c: e5e53001 strb r3, [r5, #1]!

191a0: ebffffdf bl 19124 <mcrypt\_mutex\_register@@Base+0x14990>

191a4: e0882005 add r2, r8, r5

191a8: e6ef3074 uxtb r3, r4

191ac: e1500002 cmp r0, r2

191b0: cafffff5 bgt 1918c <mcrypt\_mutex\_register@@Base+0x149f8>

191b4: e3560000 cmp r6, #0

191b8: da000039 ble 192a4 <mcrypt\_mutex\_register@@Base+0x14b10>

191bc: e1a0400d mov r4, sp

191c0: e1a02004 mov r2, r4

191c4: e3a03000 mov r3, #0

191c8: e4c23001 strb r3, [r2], #1

191cc: e2833001 add r3, r3, #1

191d0: e1560003 cmp r6, r3

191d4: 1afffffb bne 191c8 <mcrypt\_mutex\_register@@Base+0x14a34>

191d8: ebffffcb bl 1910c <mcrypt\_mutex\_register@@Base+0x14978>

191dc: ebffa444 bl 22f4 <malloc@plt>

191e0: e2509000 subs r9, r0, #0

191e4: 0a000049 beq 19310 <mcrypt\_mutex\_register@@Base+0x14b7c>

191e8: e28d8010 add r8, sp, #16

191ec: e1a01004 mov r1, r4

191f0: e1a02006 mov r2, r6

191f4: e1a00008 mov r0, r8

191f8: ebffa422 bl 2288 <memcpy@plt>

191fc: ebffffc8 bl 19124 <mcrypt\_mutex\_register@@Base+0x14990>

19200: e1a01007 mov r1, r7

19204: e28da020 add sl, sp, #32

19208: e0886006 add r6, r8, r6

1920c: e1a05008 mov r5, r8

19210: e1a0400a mov r4, sl

19214: e1a02000 mov r2, r0

19218: e1a00009 mov r0, r9

1921c: ebffff68 bl 18fc4 <mcrypt\_mutex\_register@@Base+0x14830>

19220: e1a00007 mov r0, r7

19224: ebffa414 bl 227c <free@plt>

19228: e1a01008 mov r1, r8

1922c: e1a00009 mov r0, r9

19230: ebffff6c bl 18fe8 <mcrypt\_mutex\_register@@Base+0x14854>

19234: e59f70f0 ldr r7, [pc, #240] ; 1932c <mcrypt\_mutex\_register@@Base+0x14b98>

19238: e08f7007 add r7, pc, r7

1923c: e4d52001 ldrb r2, [r5], #1

19240: e1a00004 mov r0, r4

19244: e1a01007 mov r1, r7

19248: ebffa447 bl 236c <sprintf@plt>

1924c: e1560005 cmp r6, r5

19250: e2844002 add r4, r4, #2

19254: 1afffff8 bne 1923c <mcrypt\_mutex\_register@@Base+0x14aa8>

19258: e59f40d0 ldr r4, [pc, #208] ; 19330 <mcrypt\_mutex\_register@@Base+0x14b9c>

1925c: e1a0000a mov r0, sl

19260: e08f4004 add r4, pc, r4

19264: e1a01004 mov r1, r4

19268: ebffa3f7 bl 224c <strcmp@plt>

1926c: e3500000 cmp r0, #0

19270: 1a00001c bne 192e8 <mcrypt\_mutex\_register@@Base+0x14b54>

19274: e1a01008 mov r1, r8

19278: e1a00009 mov r0, r9

1927c: ebffff7e bl 1907c <mcrypt\_mutex\_register@@Base+0x148e8>

19280: e1a00009 mov r0, r9

19284: ebffa3fc bl 227c <free@plt>

19288: e1a00008 mov r0, r8

1928c: e1a0100d mov r1, sp

19290: ebffa3ed bl 224c <strcmp@plt>

19294: e3500000 cmp r0, #0

19298: 1a00001e bne 19318 <mcrypt\_mutex\_register@@Base+0x14b84>

1929c: e28dd0e8 add sp, sp, #232 ; 0xe8

192a0: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

192a4: ebffff98 bl 1910c <mcrypt\_mutex\_register@@Base+0x14978>

192a8: ebffa411 bl 22f4 <malloc@plt>

192ac: e2509000 subs r9, r0, #0

192b0: 0a000016 beq 19310 <mcrypt\_mutex\_register@@Base+0x14b7c>

192b4: ebffff9a bl 19124 <mcrypt\_mutex\_register@@Base+0x14990>

192b8: e1a01007 mov r1, r7

192bc: e28d8010 add r8, sp, #16

192c0: e28da020 add sl, sp, #32

192c4: e1a02000 mov r2, r0

192c8: e1a00009 mov r0, r9

192cc: ebffff3c bl 18fc4 <mcrypt\_mutex\_register@@Base+0x14830>

192d0: e1a00007 mov r0, r7

192d4: ebffa3e8 bl 227c <free@plt>

192d8: e1a01008 mov r1, r8

192dc: e1a00009 mov r0, r9

192e0: ebffff40 bl 18fe8 <mcrypt\_mutex\_register@@Base+0x14854>

192e4: eaffffdb b 19258 <mcrypt\_mutex\_register@@Base+0x14ac4>

192e8: e59f0044 ldr r0, [pc, #68] ; 19334 <mcrypt\_mutex\_register@@Base+0x14ba0>

192ec: e08f0000 add r0, pc, r0

192f0: ebffa3fc bl 22e8 <puts@plt>

192f4: e59f003c ldr r0, [pc, #60] ; 19338 <mcrypt\_mutex\_register@@Base+0x14ba4>

192f8: e1a0200a mov r2, sl

192fc: e1a01004 mov r1, r4

19300: e08f0000 add r0, pc, r0

19304: ebffa3d6 bl 2264 <printf@plt>

19308: e1a00009 mov r0, r9

1930c: ebffa3da bl 227c <free@plt>

19310: e3e00000 mvn r0, #0

19314: eaffffe0 b 1929c <mcrypt\_mutex\_register@@Base+0x14b08>

19318: e59f001c ldr r0, [pc, #28] ; 1933c <mcrypt\_mutex\_register@@Base+0x14ba8>

1931c: e08f0000 add r0, pc, r0

19320: ebffa3f0 bl 22e8 <puts@plt>

19324: e3e00000 mvn r0, #0

19328: eaffffdb b 1929c <mcrypt\_mutex\_register@@Base+0x14b08>

1932c: 00007428 andeq r7, r0, r8, lsr #8

19330: 00008bc8 andeq r8, r0, r8, asr #23

19334: 00007390 muleq r0, r0, r3

19338: 00007394 muleq r0, r4, r3

1933c: 00007390 muleq r0, r0, r3

19340: e59f0000 ldr r0, [pc] ; 19348 <mcrypt\_mutex\_register@@Base+0x14bb4>

19344: e12fff1e bx lr

19348: 01315731 teqeq r1, r1, lsr r7

1934c: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

19350: e280eeff add lr, r0, #4080 ; 0xff0

19354: e2806a01 add r6, r0, #4096 ; 0x1000

19358: e8910018 ldm r1, {r3, r4}

1935c: e28ee00c add lr, lr, #12

19360: e286503c add r5, r6, #60 ; 0x3c

19364: e5be2004 ldr r2, [lr, #4]!

19368: e0222003 eor r2, r2, r3

1936c: e155000e cmp r5, lr

19370: e1a03822 lsr r3, r2, #16

19374: e1a0c422 lsr ip, r2, #8

19378: e6ef3073 uxtb r3, r3

1937c: e1a08c22 lsr r8, r2, #24

19380: e2833c01 add r3, r3, #256 ; 0x100

19384: e6efc07c uxtb ip, ip

19388: e28ccc02 add ip, ip, #512 ; 0x200

1938c: e6ef7072 uxtb r7, r2

19390: e7909108 ldr r9, [r0, r8, lsl #2]

19394: e7903103 ldr r3, [r0, r3, lsl #2]

19398: e2877c03 add r7, r7, #768 ; 0x300

1939c: e790810c ldr r8, [r0, ip, lsl #2]

193a0: e0833009 add r3, r3, r9

193a4: e790c107 ldr ip, [r0, r7, lsl #2]

193a8: e0233008 eor r3, r3, r8

193ac: e083300c add r3, r3, ip

193b0: e0233004 eor r3, r3, r4

193b4: e1a04002 mov r4, r2

193b8: 1affffe9 bne 19364 <mcrypt\_mutex\_register@@Base+0x14bd0>

193bc: e5960040 ldr r0, [r6, #64] ; 0x40

193c0: e5962044 ldr r2, [r6, #68] ; 0x44

193c4: e0233000 eor r3, r3, r0

193c8: e0244002 eor r4, r4, r2

193cc: e5813004 str r3, [r1, #4]

193d0: e5814000 str r4, [r1]

193d4: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

193d8: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

193dc: e280eeff add lr, r0, #4080 ; 0xff0

193e0: e8910018 ldm r1, {r3, r4}

193e4: e2805a01 add r5, r0, #4096 ; 0x1000

193e8: e28ee00c add lr, lr, #12

193ec: e6bf3f33 rev r3, r3

193f0: e6bf4f34 rev r4, r4

193f4: e285603c add r6, r5, #60 ; 0x3c

193f8: e5be2004 ldr r2, [lr, #4]!

193fc: e0222003 eor r2, r2, r3

19400: e156000e cmp r6, lr

19404: e1a03822 lsr r3, r2, #16

19408: e1a0c422 lsr ip, r2, #8

1940c: e6ef3073 uxtb r3, r3

19410: e1a08c22 lsr r8, r2, #24

19414: e2833c01 add r3, r3, #256 ; 0x100

19418: e6efc07c uxtb ip, ip

1941c: e28ccc02 add ip, ip, #512 ; 0x200

19420: e6ef7072 uxtb r7, r2

19424: e7909108 ldr r9, [r0, r8, lsl #2]

19428: e7903103 ldr r3, [r0, r3, lsl #2]

1942c: e2877c03 add r7, r7, #768 ; 0x300

19430: e790810c ldr r8, [r0, ip, lsl #2]

19434: e0833009 add r3, r3, r9

19438: e790c107 ldr ip, [r0, r7, lsl #2]

1943c: e0233008 eor r3, r3, r8

19440: e083300c add r3, r3, ip

19444: e0233004 eor r3, r3, r4

19448: e1a04002 mov r4, r2

1944c: 1affffe9 bne 193f8 <mcrypt\_mutex\_register@@Base+0x14c64>

19450: e5950040 ldr r0, [r5, #64] ; 0x40

19454: e5952044 ldr r2, [r5, #68] ; 0x44

19458: e0233000 eor r3, r3, r0

1945c: e0244002 eor r4, r4, r2

19460: e6bf3f33 rev r3, r3

19464: e6bf4f34 rev r4, r4

19468: e5813004 str r3, [r1, #4]

1946c: e5814000 str r4, [r1]

19470: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

19474: e92d43f0 push {r4, r5, r6, r7, r8, r9, lr}

19478: e280ed41 add lr, r0, #4160 ; 0x1040

1947c: e8910018 ldm r1, {r3, r4}

19480: e2805a01 add r5, r0, #4096 ; 0x1000

19484: e28ee008 add lr, lr, #8

19488: e6bf3f33 rev r3, r3

1948c: e6bf4f34 rev r4, r4

19490: e2856008 add r6, r5, #8

19494: e53e2004 ldr r2, [lr, #-4]!

19498: e0222003 eor r2, r2, r3

1949c: e156000e cmp r6, lr

194a0: e1a03822 lsr r3, r2, #16

194a4: e1a0c422 lsr ip, r2, #8

194a8: e6ef3073 uxtb r3, r3

194ac: e1a08c22 lsr r8, r2, #24

194b0: e2833c01 add r3, r3, #256 ; 0x100

194b4: e6efc07c uxtb ip, ip

194b8: e28ccc02 add ip, ip, #512 ; 0x200

194bc: e6ef7072 uxtb r7, r2

194c0: e7909108 ldr r9, [r0, r8, lsl #2]

194c4: e7903103 ldr r3, [r0, r3, lsl #2]

194c8: e2877c03 add r7, r7, #768 ; 0x300

194cc: e790810c ldr r8, [r0, ip, lsl #2]

194d0: e0833009 add r3, r3, r9

194d4: e790c107 ldr ip, [r0, r7, lsl #2]

194d8: e0233008 eor r3, r3, r8

194dc: e083300c add r3, r3, ip

194e0: e0233004 eor r3, r3, r4

194e4: e1a04002 mov r4, r2

194e8: 1affffe9 bne 19494 <mcrypt\_mutex\_register@@Base+0x14d00>

194ec: e5950004 ldr r0, [r5, #4]

194f0: e5952000 ldr r2, [r5]

194f4: e0233000 eor r3, r3, r0

194f8: e0244002 eor r4, r4, r2

194fc: e6bf3f33 rev r3, r3

19500: e6bf4f34 rev r4, r4

19504: e5813004 str r3, [r1, #4]

19508: e5814000 str r4, [r1]

1950c: e8bd83f0 pop {r4, r5, r6, r7, r8, r9, pc}

19510: e59f31c0 ldr r3, [pc, #448] ; 196d8 <mcrypt\_mutex\_register@@Base+0x14f44>

19514: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

19518: e08f3003 add r3, pc, r3

1951c: e1a07000 mov r7, r0

19520: e2830b01 add r0, r3, #1024 ; 0x400

19524: e1a06001 mov r6, r1

19528: e6bf5072 sxth r5, r2

1952c: e2831004 add r1, r3, #4

19530: e2839b02 add r9, r3, #2048 ; 0x800

19534: e24dd014 sub sp, sp, #20

19538: e59f819c ldr r8, [pc, #412] ; 196dc <mcrypt\_mutex\_register@@Base+0x14f48>

1953c: e59f419c ldr r4, [pc, #412] ; 196e0 <mcrypt\_mutex\_register@@Base+0x14f4c>

19540: e59fe19c ldr lr, [pc, #412] ; 196e4 <mcrypt\_mutex\_register@@Base+0x14f50>

19544: e59fc19c ldr ip, [pc, #412] ; 196e8 <mcrypt\_mutex\_register@@Base+0x14f54>

19548: e1a0a000 mov sl, r0

1954c: e2472004 sub r2, r7, #4

19550: e2833b03 add r3, r3, #3072 ; 0xc00

19554: ea000003 b 19568 <mcrypt\_mutex\_register@@Base+0x14dd4>

19558: e491c004 ldr ip, [r1], #4

1955c: e5b0e004 ldr lr, [r0, #4]!

19560: e5b94004 ldr r4, [r9, #4]!

19564: e5b38004 ldr r8, [r3, #4]!

19568: e151000a cmp r1, sl

1956c: e5a2c004 str ip, [r2, #4]!

19570: e582e400 str lr, [r2, #1024] ; 0x400

19574: e5824800 str r4, [r2, #2048] ; 0x800

19578: e5828c00 str r8, [r2, #3072] ; 0xc00

1957c: 1afffff5 bne 19558 <mcrypt\_mutex\_register@@Base+0x14dc4>

19580: e59f3164 ldr r3, [pc, #356] ; 196ec <mcrypt\_mutex\_register@@Base+0x14f58>

19584: e2878eff add r8, r7, #4080 ; 0xff0

19588: e08f3003 add r3, pc, r3

1958c: e2433eff sub r3, r3, #4080 ; 0xff0

19590: e288800c add r8, r8, #12

19594: e287bd41 add fp, r7, #4160 ; 0x1040

19598: e2433008 sub r3, r3, #8

1959c: e1a01008 mov r1, r8

195a0: e28b2004 add r2, fp, #4

195a4: e287aa01 add sl, r7, #4096 ; 0x1000

195a8: e1a04007 mov r4, r7

195ac: e59f013c ldr r0, [pc, #316] ; 196f0 <mcrypt\_mutex\_register@@Base+0x14f5c>

195b0: ea000000 b 195b8 <mcrypt\_mutex\_register@@Base+0x14e24>

195b4: e5b30004 ldr r0, [r3, #4]!

195b8: e5a10004 str r0, [r1, #4]!

195bc: e1520001 cmp r2, r1

195c0: 1afffffb bne 195b4 <mcrypt\_mutex\_register@@Base+0x14e20>

195c4: e3a09000 mov r9, #0

195c8: e1a00009 mov r0, r9

195cc: e1a01005 mov r1, r5

195d0: e58d2004 str r2, [sp, #4]

195d4: eb0009d1 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

195d8: e2890001 add r0, r9, #1

195dc: e7d63001 ldrb r3, [r6, r1]

195e0: e1a01005 mov r1, r5

195e4: e1a03403 lsl r3, r3, #8

195e8: e58d3000 str r3, [sp]

195ec: eb0009cb bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

195f0: e59d3000 ldr r3, [sp]

195f4: e2890002 add r0, r9, #2

195f8: e7d6c001 ldrb ip, [r6, r1]

195fc: e1a01005 mov r1, r5

19600: e183300c orr r3, r3, ip

19604: e1a03403 lsl r3, r3, #8

19608: e58d3000 str r3, [sp]

1960c: eb0009c3 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

19610: e59d3000 ldr r3, [sp]

19614: e2890003 add r0, r9, #3

19618: e7d6c001 ldrb ip, [r6, r1]

1961c: e1a01005 mov r1, r5

19620: e18c3003 orr r3, ip, r3

19624: e1a03403 lsl r3, r3, #8

19628: e58d3000 str r3, [sp]

1962c: eb0009bb bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

19630: e59d3000 ldr r3, [sp]

19634: e5b8c004 ldr ip, [r8, #4]!

19638: e2890004 add r0, r9, #4

1963c: e7d6e001 ldrb lr, [r6, r1]

19640: e1a01005 mov r1, r5

19644: e18e3003 orr r3, lr, r3

19648: e023300c eor r3, r3, ip

1964c: e5883000 str r3, [r8]

19650: eb0009b2 bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

19654: e59d2004 ldr r2, [sp, #4]

19658: e1520008 cmp r2, r8

1965c: e6bf9071 sxth r9, r1

19660: 1affffd8 bne 195c8 <mcrypt\_mutex\_register@@Base+0x14e34>

19664: e28bb008 add fp, fp, #8

19668: e1a0500a mov r5, sl

1966c: e28d6008 add r6, sp, #8

19670: e3a03000 mov r3, #0

19674: e58d3008 str r3, [sp, #8]

19678: e58d300c str r3, [sp, #12]

1967c: e1a01006 mov r1, r6

19680: e1a00007 mov r0, r7

19684: ebffff30 bl 1934c <mcrypt\_mutex\_register@@Base+0x14bb8>

19688: e1cd20d8 ldrd r2, [sp, #8]

1968c: e2855008 add r5, r5, #8

19690: e14520f8 strd r2, [r5, #-8]

19694: e15b0005 cmp fp, r5

19698: 1afffff7 bne 1967c <mcrypt\_mutex\_register@@Base+0x14ee8>

1969c: e2845b01 add r5, r4, #1024 ; 0x400

196a0: e1a01006 mov r1, r6

196a4: e1a00007 mov r0, r7

196a8: ebffff27 bl 1934c <mcrypt\_mutex\_register@@Base+0x14bb8>

196ac: e1cd20d8 ldrd r2, [sp, #8]

196b0: e2844008 add r4, r4, #8

196b4: e14420f8 strd r2, [r4, #-8]

196b8: e1550004 cmp r5, r4

196bc: 1afffff7 bne 196a0 <mcrypt\_mutex\_register@@Base+0x14f0c>

196c0: e155000a cmp r5, sl

196c4: e1a04005 mov r4, r5

196c8: 1afffff3 bne 1969c <mcrypt\_mutex\_register@@Base+0x14f08>

196cc: e3a00000 mov r0, #0

196d0: e28dd014 add sp, sp, #20

196d4: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

196d8: 00008924 andeq r8, r0, r4, lsr #18

196dc: 3a39ce37 bcc e8cfc0 <mcrypt\_mutex\_register@@Base+0xe8882c>

196e0: e93d5a68 ldmdb sp!, {r3, r5, r6, r9, fp, ip, lr}

196e4: 4b7a70e9 blmi 1eb5a90 <mcrypt\_mutex\_register@@Base+0x1eb12fc>

196e8: d1310ba6 teqle r1, r6, lsr #23

196ec: 0000a8ac andeq sl, r0, ip, lsr #17

196f0: 243f6a88 ldrtcs r6, [pc], #-2696 ; 196f8 <mcrypt\_mutex\_register@@Base+0x14f64>

196f4: e59f0000 ldr r0, [pc] ; 196fc <mcrypt\_mutex\_register@@Base+0x14f68>

196f8: e12fff1e bx lr

196fc: 00001048 andeq r1, r0, r8, asr #32

19700: e3a00008 mov r0, #8

19704: e12fff1e bx lr

19708: e3a00001 mov r0, #1

1970c: e12fff1e bx lr

19710: e3a00038 mov r0, #56 ; 0x38

19714: e12fff1e bx lr

19718: e3a03000 mov r3, #0

1971c: e5803000 str r3, [r0]

19720: e1a00003 mov r0, r3

19724: e12fff1e bx lr

19728: e59f0004 ldr r0, [pc, #4] ; 19734 <mcrypt\_mutex\_register@@Base+0x14fa0>

1972c: e08f0000 add r0, pc, r0

19730: e12fff1e bx lr

19734: 000082d4 ldrdeq r8, [r0], -r4

19738: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

1973c: e24dd0e8 sub sp, sp, #232 ; 0xe8

19740: ebffffee bl 19700 <mcrypt\_mutex\_register@@Base+0x14f6c>

19744: e1a06000 mov r6, r0

19748: ebfffff0 bl 19710 <mcrypt\_mutex\_register@@Base+0x14f7c>

1974c: e1a01000 mov r1, r0

19750: e3a00001 mov r0, #1

19754: ebffa2b6 bl 2234 <calloc@plt>

19758: e2507000 subs r7, r0, #0

1975c: 12475001 subne r5, r7, #1

19760: 13a0400a movne r4, #10

19764: 12678001 rsbne r8, r7, #1

19768: 1a000005 bne 19784 <mcrypt\_mutex\_register@@Base+0x14ff0>

1976c: ea000060 b 198f4 <mcrypt\_mutex\_register@@Base+0x15160>

19770: e2742000 rsbs r2, r4, #0

19774: e2844002 add r4, r4, #2

19778: e6ef2072 uxtb r2, r2

1977c: 52623000 rsbpl r3, r2, #0

19780: e5e53001 strb r3, [r5, #1]!

19784: ebffffe1 bl 19710 <mcrypt\_mutex\_register@@Base+0x14f7c>

19788: e0882005 add r2, r8, r5

1978c: e6ef3074 uxtb r3, r4

19790: e1500002 cmp r0, r2

19794: cafffff5 bgt 19770 <mcrypt\_mutex\_register@@Base+0x14fdc>

19798: e3560000 cmp r6, #0

1979c: da000039 ble 19888 <mcrypt\_mutex\_register@@Base+0x150f4>

197a0: e1a0400d mov r4, sp

197a4: e1a02004 mov r2, r4

197a8: e3a03000 mov r3, #0

197ac: e4c23001 strb r3, [r2], #1

197b0: e2833001 add r3, r3, #1

197b4: e1560003 cmp r6, r3

197b8: 1afffffb bne 197ac <mcrypt\_mutex\_register@@Base+0x15018>

197bc: ebffffcc bl 196f4 <mcrypt\_mutex\_register@@Base+0x14f60>

197c0: ebffa2cb bl 22f4 <malloc@plt>

197c4: e2509000 subs r9, r0, #0

197c8: 0a000049 beq 198f4 <mcrypt\_mutex\_register@@Base+0x15160>

197cc: e28d8010 add r8, sp, #16

197d0: e1a01004 mov r1, r4

197d4: e1a02006 mov r2, r6

197d8: e1a00008 mov r0, r8

197dc: ebffa2a9 bl 2288 <memcpy@plt>

197e0: ebffffca bl 19710 <mcrypt\_mutex\_register@@Base+0x14f7c>

197e4: e1a01007 mov r1, r7

197e8: e28da020 add sl, sp, #32

197ec: e0886006 add r6, r8, r6

197f0: e1a05008 mov r5, r8

197f4: e1a0400a mov r4, sl

197f8: e1a02000 mov r2, r0

197fc: e1a00009 mov r0, r9

19800: ebffff42 bl 19510 <mcrypt\_mutex\_register@@Base+0x14d7c>

19804: e1a00007 mov r0, r7

19808: ebffa29b bl 227c <free@plt>

1980c: e1a01008 mov r1, r8

19810: e1a00009 mov r0, r9

19814: ebfffeef bl 193d8 <mcrypt\_mutex\_register@@Base+0x14c44>

19818: e59f70f0 ldr r7, [pc, #240] ; 19910 <mcrypt\_mutex\_register@@Base+0x1517c>

1981c: e08f7007 add r7, pc, r7

19820: e4d52001 ldrb r2, [r5], #1

19824: e1a00004 mov r0, r4

19828: e1a01007 mov r1, r7

1982c: ebffa2ce bl 236c <sprintf@plt>

19830: e1560005 cmp r6, r5

19834: e2844002 add r4, r4, #2

19838: 1afffff8 bne 19820 <mcrypt\_mutex\_register@@Base+0x1508c>

1983c: e59f40d0 ldr r4, [pc, #208] ; 19914 <mcrypt\_mutex\_register@@Base+0x15180>

19840: e1a0000a mov r0, sl

19844: e08f4004 add r4, pc, r4

19848: e1a01004 mov r1, r4

1984c: ebffa27e bl 224c <strcmp@plt>

19850: e3500000 cmp r0, #0

19854: 1a00001c bne 198cc <mcrypt\_mutex\_register@@Base+0x15138>

19858: e1a01008 mov r1, r8

1985c: e1a00009 mov r0, r9

19860: ebffff03 bl 19474 <mcrypt\_mutex\_register@@Base+0x14ce0>

19864: e1a00009 mov r0, r9

19868: ebffa283 bl 227c <free@plt>

1986c: e1a00008 mov r0, r8

19870: e1a0100d mov r1, sp

19874: ebffa274 bl 224c <strcmp@plt>

19878: e3500000 cmp r0, #0

1987c: 1a00001e bne 198fc <mcrypt\_mutex\_register@@Base+0x15168>

19880: e28dd0e8 add sp, sp, #232 ; 0xe8

19884: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

19888: ebffff99 bl 196f4 <mcrypt\_mutex\_register@@Base+0x14f60>

1988c: ebffa298 bl 22f4 <malloc@plt>

19890: e2509000 subs r9, r0, #0

19894: 0a000016 beq 198f4 <mcrypt\_mutex\_register@@Base+0x15160>

19898: ebffff9c bl 19710 <mcrypt\_mutex\_register@@Base+0x14f7c>

1989c: e1a01007 mov r1, r7

198a0: e28d8010 add r8, sp, #16

198a4: e28da020 add sl, sp, #32

198a8: e1a02000 mov r2, r0

198ac: e1a00009 mov r0, r9

198b0: ebffff16 bl 19510 <mcrypt\_mutex\_register@@Base+0x14d7c>

198b4: e1a00007 mov r0, r7

198b8: ebffa26f bl 227c <free@plt>

198bc: e1a01008 mov r1, r8

198c0: e1a00009 mov r0, r9

198c4: ebfffec3 bl 193d8 <mcrypt\_mutex\_register@@Base+0x14c44>

198c8: eaffffdb b 1983c <mcrypt\_mutex\_register@@Base+0x150a8>

198cc: e59f0044 ldr r0, [pc, #68] ; 19918 <mcrypt\_mutex\_register@@Base+0x15184>

198d0: e08f0000 add r0, pc, r0

198d4: ebffa283 bl 22e8 <puts@plt>

198d8: e59f003c ldr r0, [pc, #60] ; 1991c <mcrypt\_mutex\_register@@Base+0x15188>

198dc: e1a0200a mov r2, sl

198e0: e1a01004 mov r1, r4

198e4: e08f0000 add r0, pc, r0

198e8: ebffa25d bl 2264 <printf@plt>

198ec: e1a00009 mov r0, r9

198f0: ebffa261 bl 227c <free@plt>

198f4: e3e00000 mvn r0, #0

198f8: eaffffe0 b 19880 <mcrypt\_mutex\_register@@Base+0x150ec>

198fc: e59f001c ldr r0, [pc, #28] ; 19920 <mcrypt\_mutex\_register@@Base+0x1518c>

19900: e08f0000 add r0, pc, r0

19904: ebffa277 bl 22e8 <puts@plt>

19908: e3e00000 mvn r0, #0

1990c: eaffffdb b 19880 <mcrypt\_mutex\_register@@Base+0x150ec>

19910: 00006e44 andeq r6, r0, r4, asr #28

19914: 00009640 andeq r9, r0, r0, asr #12

19918: 00006dac andeq r6, r0, ip, lsr #27

1991c: 00006db0 ; <UNDEFINED> instruction: 0x00006db0

19920: 00006dac andeq r6, r0, ip, lsr #27

19924: e59f0000 ldr r0, [pc] ; 1992c <mcrypt\_mutex\_register@@Base+0x15198>

19928: e12fff1e bx lr

1992c: 01315731 teqeq r1, r1, lsr r7

19930: e92d4ff8 push {r3, r4, r5, r6, r7, r8, r9, sl, fp, lr}

19934: e1a05002 mov r5, r2

19938: e1a06000 mov r6, r0

1993c: e3a02e42 mov r2, #1056 ; 0x420

19940: e1a07001 mov r7, r1

19944: e3a01000 mov r1, #0

19948: ebffa27e bl 2348 <memset@plt>

1994c: e2864b01 add r4, r6, #1024 ; 0x400

19950: e355000d cmp r5, #13

19954: b1a02005 movlt r2, r5

19958: a3a0200d movge r2, #13

1995c: e1a01007 mov r1, r7

19960: e1a00004 mov r0, r4

19964: ebffa241 bl 2270 <memmove@plt>

19968: e59fc154 ldr ip, [pc, #340] ; 19ac4 <mcrypt\_mutex\_register@@Base+0x15330>

1996c: e2863fff add r3, r6, #1020 ; 0x3fc

19970: e284000c add r0, r4, #12

19974: e2833003 add r3, r3, #3

19978: e04cc006 sub ip, ip, r6

1997c: e3a0507b mov r5, #123 ; 0x7b

19980: e08c2003 add r2, ip, r3

19984: e1f310d1 ldrsb r1, [r3, #1]!

19988: e1530000 cmp r3, r0

1998c: e0252195 mla r5, r5, r1, r2

19990: 1afffffa bne 19980 <mcrypt\_mutex\_register@@Base+0x151ec>

19994: e2468001 sub r8, r6, #1

19998: e1a02008 mov r2, r8

1999c: e3a03000 mov r3, #0

199a0: e6af1073 sxtb r1, r3

199a4: e2833001 add r3, r3, #1

199a8: e3530c01 cmp r3, #256 ; 0x100

199ac: e5e21001 strb r1, [r2, #1]!

199b0: e5c21300 strb r1, [r2, #768] ; 0x300

199b4: 1afffff9 bne 199a0 <mcrypt\_mutex\_register@@Base+0x1520c>

199b8: e59fa108 ldr sl, [pc, #264] ; 19ac8 <mcrypt\_mutex\_register@@Base+0x15334>

199bc: e59f9108 ldr r9, [pc, #264] ; 19acc <mcrypt\_mutex\_register@@Base+0x15338>

199c0: e2867c01 add r7, r6, #256 ; 0x100

199c4: e3a040ff mov r4, #255 ; 0xff

199c8: ea000001 b 199d4 <mcrypt\_mutex\_register@@Base+0x15240>

199cc: e2544001 subs r4, r4, #1

199d0: 3a00002f bcc 19a94 <mcrypt\_mutex\_register@@Base+0x15300>

199d4: e26430ff rsb r3, r4, #255 ; 0xff

199d8: e0855105 add r5, r5, r5, lsl #2

199dc: e082139a umull r1, r2, sl, r3

199e0: e2841001 add r1, r4, #1

199e4: e1a02122 lsr r2, r2, #2

199e8: e0820082 add r0, r2, r2, lsl #1

199ec: e0822100 add r2, r2, r0, lsl #2

199f0: e0433002 sub r3, r3, r2

199f4: e0863003 add r3, r6, r3

199f8: e2833b01 add r3, r3, #1024 ; 0x400

199fc: e1d330d0 ldrsb r3, [r3]

19a00: e0835005 add r5, r3, r5

19a04: e0cb3599 smull r3, fp, r9, r5

19a08: e08b0005 add r0, fp, r5

19a0c: e1a0bfc5 asr fp, r5, #31

19a10: e06bb7c0 rsb fp, fp, r0, asr #15

19a14: e06b060b rsb r0, fp, fp, lsl #12

19a18: e08bb200 add fp, fp, r0, lsl #4

19a1c: e045b00b sub fp, r5, fp

19a20: e6ef007b uxtb r0, fp

19a24: eb00082d bl 1bae0 <mcrypt\_mutex\_register@@Base+0x1734c>

19a28: e17720d1 ldrsb r2, [r7, #-1]!

19a2c: e1a03007 mov r3, r7

19a30: e19600d1 ldrsb r0, [r6, r1]

19a34: e4c30200 strb r0, [r3], #512 ; 0x200

19a38: e7c62001 strb r2, [r6, r1]

19a3c: e1d330d0 ldrsb r3, [r3]

19a40: e3530000 cmp r3, #0

19a44: 1affffe0 bne 199cc <mcrypt\_mutex\_register@@Base+0x15238>

19a48: e1a0042b lsr r0, fp, #8

19a4c: e1a01004 mov r1, r4

19a50: e6ef0070 uxtb r0, r0

19a54: eb000821 bl 1bae0 <mcrypt\_mutex\_register@@Base+0x1734c>

19a58: e0863001 add r3, r6, r1

19a5c: e5d32200 ldrb r2, [r3, #512] ; 0x200

19a60: e3520000 cmp r2, #0

19a64: 0a000006 beq 19a84 <mcrypt\_mutex\_register@@Base+0x152f0>

19a68: e2810001 add r0, r1, #1

19a6c: e1a01004 mov r1, r4

19a70: eb0008aa bl 1bd20 <mcrypt\_mutex\_register@@Base+0x1758c>

19a74: e0863001 add r3, r6, r1

19a78: e5d32200 ldrb r2, [r3, #512] ; 0x200

19a7c: e3520000 cmp r2, #0

19a80: 1afffff8 bne 19a68 <mcrypt\_mutex\_register@@Base+0x152d4>

19a84: e5c71200 strb r1, [r7, #512] ; 0x200

19a88: e5c34200 strb r4, [r3, #512] ; 0x200

19a8c: e2544001 subs r4, r4, #1

19a90: 2affffcf bcs 199d4 <mcrypt\_mutex\_register@@Base+0x15240>

19a94: e28600ff add r0, r6, #255 ; 0xff

19a98: e1a02008 mov r2, r8

19a9c: e2822001 add r2, r2, #1

19aa0: e1a03002 mov r3, r2

19aa4: e1500002 cmp r0, r2

19aa8: e4531001 ldrb r1, [r3], #-1

19aac: e0861001 add r1, r6, r1

19ab0: e0433008 sub r3, r3, r8

19ab4: e5c13100 strb r3, [r1, #256] ; 0x100

19ab8: 1afffff7 bne 19a9c <mcrypt\_mutex\_register@@Base+0x15308>

19abc: e3a00000 mov r0, #0

19ac0: e8bd8ff8 pop {r3, r4, r5, r6, r7, r8, r9, sl, fp, pc}

19ac4: fffffc01 ; <UNDEFINED> instruction: 0xfffffc01

19ac8: 4ec4ec4f cdpmi 12, 12, cr14, cr4, cr15, {2}

19acc: 80078071 andhi r8, r7, r1, ror r0

19ad0: e59f30a4 ldr r3, [pc, #164] ; 19b7c <mcrypt\_mutex\_register@@Base+0x153e8>

19ad4: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

19ad8: e08f3003 add r3, pc, r3

19adc: e59f709c ldr r7, [pc, #156] ; 19b80 <mcrypt\_mutex\_register@@Base+0x153ec>

19ae0: e5934000 ldr r4, [r3]

19ae4: e59f6098 ldr r6, [pc, #152] ; 19b84 <mcrypt\_mutex\_register@@Base+0x153f0>

19ae8: e1a09000 mov r9, r0

19aec: e2808b01 add r8, r0, #1024 ; 0x400

19af0: e3a05c01 mov r5, #256 ; 0x100

19af4: e2653c01 rsb r3, r5, #256 ; 0x100

19af8: e0844104 add r4, r4, r4, lsl #2

19afc: e0821397 umull r1, r2, r7, r3

19b00: e1a01005 mov r1, r5

19b04: e1a02122 lsr r2, r2, #2

19b08: e0820082 add r0, r2, r2, lsl #1

19b0c: e0822100 add r2, r2, r0, lsl #2

19b10: e0433002 sub r3, r3, r2

19b14: e0893003 add r3, r9, r3

19b18: e2833b01 add r3, r3, #1024 ; 0x400

19b1c: e1d330d0 ldrsb r3, [r3]

19b20: e0834004 add r4, r3, r4

19b24: e0c03496 smull r3, r0, r6, r4

19b28: e0803004 add r3, r0, r4

19b2c: e1a00fc4 asr r0, r4, #31

19b30: e06007c3 rsb r0, r0, r3, asr #15

19b34: e0603600 rsb r3, r0, r0, lsl #12

19b38: e0800203 add r0, r0, r3, lsl #4

19b3c: e0440000 sub r0, r4, r0

19b40: e6ef0070 uxtb r0, r0

19b44: eb0007e5 bl 1bae0 <mcrypt\_mutex\_register@@Base+0x1734c>

19b48: e17830d1 ldrsb r3, [r8, #-1]!

19b4c: e2555001 subs r5, r5, #1

19b50: e0891001 add r1, r9, r1

19b54: e2812c03 add r2, r1, #768 ; 0x300

19b58: e1d220d0 ldrsb r2, [r2]

19b5c: e5c82000 strb r2, [r8]

19b60: e5c13300 strb r3, [r1, #768] ; 0x300

19b64: 1affffe2 bne 19af4 <mcrypt\_mutex\_register@@Base+0x15360>

19b68: e59f3018 ldr r3, [pc, #24] ; 19b88 <mcrypt\_mutex\_register@@Base+0x153f4>

19b6c: e1a00005 mov r0, r5

19b70: e08f3003 add r3, pc, r3

19b74: e5834000 str r4, [r3]

19b78: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

19b7c: 0001b9b8 ; <UNDEFINED> instruction: 0x0001b9b8

19b80: 4ec4ec4f cdpmi 12, 12, cr14, cr4, cr15, {2}

19b84: 80078071 andhi r8, r7, r1, ror r0

19b88: 0001b920 andeq fp, r1, r0, lsr #18

19b8c: e3520000 cmp r2, #0

19b90: d12fff1e bxle lr

19b94: e2422001 sub r2, r2, #1

19b98: e92d4030 push {r4, r5, lr}

19b9c: e081e002 add lr, r1, r2

19ba0: e3a04000 mov r4, #0

19ba4: e2411001 sub r1, r1, #1

19ba8: e5f12001 ldrb r2, [r1, #1]!

19bac: e590c410 ldr ip, [r0, #1040] ; 0x410

19bb0: e590541c ldr r5, [r0, #1052] ; 0x41c

19bb4: e08c2002 add r2, ip, r2

19bb8: e580c418 str ip, [r0, #1048] ; 0x418

19bbc: e6ef2072 uxtb r2, r2

19bc0: e19020d2 ldrsb r2, [r0, r2]

19bc4: e0822005 add r2, r2, r5

19bc8: e6e02072 uxtab r2, r0, r2

19bcc: e2822c02 add r2, r2, #512 ; 0x200

19bd0: e1d230d0 ldrsb r3, [r2]

19bd4: e0433005 sub r3, r3, r5

19bd8: e6e03073 uxtab r3, r0, r3

19bdc: e2833c01 add r3, r3, #256 ; 0x100

19be0: e1d330d0 ldrsb r3, [r3]

19be4: e043c00c sub ip, r3, ip

19be8: e5c1c000 strb ip, [r1]

19bec: e5903410 ldr r3, [r0, #1040] ; 0x410

19bf0: e2833001 add r3, r3, #1

19bf4: e3530c01 cmp r3, #256 ; 0x100

19bf8: 15803410 strne r3, [r0, #1040] ; 0x410

19bfc: 1a000007 bne 19c20 <mcrypt\_mutex\_register@@Base+0x1548c>

19c00: e5903414 ldr r3, [r0, #1044] ; 0x414

19c04: e5804410 str r4, [r0, #1040] ; 0x410

19c08: e2833001 add r3, r3, #1

19c0c: e3530c01 cmp r3, #256 ; 0x100

19c10: 03a03000 moveq r3, #0

19c14: 15803414 strne r3, [r0, #1044] ; 0x414

19c18: 05804414 streq r4, [r0, #1044] ; 0x414

19c1c: e580341c str r3, [r0, #1052] ; 0x41c

19c20: e15e0001 cmp lr, r1

19c24: 1affffdf bne 19ba8 <mcrypt\_mutex\_register@@Base+0x15414>

19c28: e8bd8030 pop {r4, r5, pc}

19c2c: eaffffd6 b 19b8c <mcrypt\_mutex\_register@@Base+0x153f8>

19c30: e3a00e42 mov r0, #1056 ; 0x420

19c34: e12fff1e bx lr

19c38: e3a00001 mov r0, #1

19c3c: e12fff1e bx lr

19c40: e3a00000 mov r0, #0

19c44: e12fff1e bx lr

19c48: e3a00000 mov r0, #0

19c4c: e12fff1e bx lr

19c50: e3a0000d mov r0, #13

19c54: e12fff1e bx lr

19c58: e3a03000 mov r3, #0

19c5c: e5803000 str r3, [r0]

19c60: e1a00003 mov r0, r3

19c64: e12fff1e bx lr

19c68: e59f0004 ldr r0, [pc, #4] ; 19c74 <mcrypt\_mutex\_register@@Base+0x154e0>

19c6c: e08f0000 add r0, pc, r0

19c70: e12fff1e bx lr

19c74: 00004524 andeq r4, r0, r4, lsr #10

19c78: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

19c7c: e24ddf41 sub sp, sp, #260 ; 0x104

19c80: ebfffff2 bl 19c50 <mcrypt\_mutex\_register@@Base+0x154bc>

19c84: e1a01000 mov r1, r0

19c88: e3a00001 mov r0, #1

19c8c: ebffa168 bl 2234 <calloc@plt>

19c90: e250a000 subs sl, r0, #0

19c94: 0a00005b beq 19e08 <mcrypt\_mutex\_register@@Base+0x15674>

19c98: e59f3194 ldr r3, [pc, #404] ; 19e34 <mcrypt\_mutex\_register@@Base+0x156a0>

19c9c: e28de010 add lr, sp, #16

19ca0: e08f3003 add r3, pc, r3

19ca4: e1a0c00a mov ip, sl

19ca8: e8930007 ldm r3, {r0, r1, r2}

19cac: e3a04000 mov r4, #0

19cb0: e1a0300e mov r3, lr

19cb4: e58de008 str lr, [sp, #8]

19cb8: e8ac0003 stmia ip!, {r0, r1}

19cbc: e5cc2000 strb r2, [ip]

19cc0: e4c34001 strb r4, [r3], #1

19cc4: e2844001 add r4, r4, #1

19cc8: e3540014 cmp r4, #20

19ccc: 1afffffb bne 19cc0 <mcrypt\_mutex\_register@@Base+0x1552c>

19cd0: ebffffd6 bl 19c30 <mcrypt\_mutex\_register@@Base+0x1549c>

19cd4: ebffa186 bl 22f4 <malloc@plt>

19cd8: e2509000 subs r9, r0, #0

19cdc: 0a00004b beq 19e10 <mcrypt\_mutex\_register@@Base+0x1567c>

19ce0: e59de008 ldr lr, [sp, #8]

19ce4: e28dc024 add ip, sp, #36 ; 0x24

19ce8: e1a0b00c mov fp, ip

19cec: e8be000f ldm lr!, {r0, r1, r2, r3}

19cf0: e28d5038 add r5, sp, #56 ; 0x38

19cf4: e59f813c ldr r8, [pc, #316] ; 19e38 <mcrypt\_mutex\_register@@Base+0x156a4>

19cf8: e1a0600b mov r6, fp

19cfc: e59ee000 ldr lr, [lr]

19d00: e8ac000f stmia ip!, {r0, r1, r2, r3}

19d04: e08f8008 add r8, pc, r8

19d08: e58ce000 str lr, [ip]

19d0c: ebffffcf bl 19c50 <mcrypt\_mutex\_register@@Base+0x154bc>

19d10: e3a03000 mov r3, #0

19d14: e1a0100a mov r1, sl

19d18: e58d3000 str r3, [sp]

19d1c: e58d500c str r5, [sp, #12]

19d20: e1a07005 mov r7, r5

19d24: e1a02000 mov r2, r0

19d28: e1a00009 mov r0, r9

19d2c: ebfffeff bl 19930 <mcrypt\_mutex\_register@@Base+0x1519c>

19d30: e1a02004 mov r2, r4

19d34: e1a0100b mov r1, fp

19d38: e1a00009 mov r0, r9

19d3c: ebffff92 bl 19b8c <mcrypt\_mutex\_register@@Base+0x153f8>

19d40: e4d62001 ldrb r2, [r6], #1

19d44: e1a00005 mov r0, r5

19d48: e1a01008 mov r1, r8

19d4c: ebffa186 bl 236c <sprintf@plt>

19d50: e1570006 cmp r7, r6

19d54: e2855002 add r5, r5, #2

19d58: 1afffff8 bne 19d40 <mcrypt\_mutex\_register@@Base+0x155ac>

19d5c: e59f50d8 ldr r5, [pc, #216] ; 19e3c <mcrypt\_mutex\_register@@Base+0x156a8>

19d60: e59d000c ldr r0, [sp, #12]

19d64: e08f5005 add r5, pc, r5

19d68: e1a01005 mov r1, r5

19d6c: ebffa136 bl 224c <strcmp@plt>

19d70: e2504000 subs r4, r0, #0

19d74: 1a000015 bne 19dd0 <mcrypt\_mutex\_register@@Base+0x1563c>

19d78: ebffffb4 bl 19c50 <mcrypt\_mutex\_register@@Base+0x154bc>

19d7c: e1a03004 mov r3, r4

19d80: e1a0100a mov r1, sl

19d84: e58d4000 str r4, [sp]

19d88: e1a02000 mov r2, r0

19d8c: e1a00009 mov r0, r9

19d90: ebfffee6 bl 19930 <mcrypt\_mutex\_register@@Base+0x1519c>

19d94: e1a0000a mov r0, sl

19d98: ebffa137 bl 227c <free@plt>

19d9c: e3a02014 mov r2, #20

19da0: e1a0100b mov r1, fp

19da4: e1a00009 mov r0, r9

19da8: ebffff9f bl 19c2c <mcrypt\_mutex\_register@@Base+0x15498>

19dac: e1a00009 mov r0, r9

19db0: ebffa131 bl 227c <free@plt>

19db4: e59d1008 ldr r1, [sp, #8]

19db8: e1a0000b mov r0, fp

19dbc: ebffa122 bl 224c <strcmp@plt>

19dc0: e3500000 cmp r0, #0

19dc4: 1a000015 bne 19e20 <mcrypt\_mutex\_register@@Base+0x1568c>

19dc8: e28ddf41 add sp, sp, #260 ; 0x104

19dcc: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

19dd0: e59f0068 ldr r0, [pc, #104] ; 19e40 <mcrypt\_mutex\_register@@Base+0x156ac>

19dd4: e08f0000 add r0, pc, r0

19dd8: ebffa142 bl 22e8 <puts@plt>

19ddc: e59f0060 ldr r0, [pc, #96] ; 19e44 <mcrypt\_mutex\_register@@Base+0x156b0>

19de0: e59d200c ldr r2, [sp, #12]

19de4: e1a01005 mov r1, r5

19de8: e08f0000 add r0, pc, r0

19dec: ebffa11c bl 2264 <printf@plt>

19df0: e1a0000a mov r0, sl

19df4: ebffa120 bl 227c <free@plt>

19df8: e1a00009 mov r0, r9

19dfc: ebffa11e bl 227c <free@plt>

19e00: e3e00000 mvn r0, #0

19e04: eaffffef b 19dc8 <mcrypt\_mutex\_register@@Base+0x15634>

19e08: e3e00000 mvn r0, #0

19e0c: eaffffed b 19dc8 <mcrypt\_mutex\_register@@Base+0x15634>

19e10: e1a0000a mov r0, sl

19e14: ebffa118 bl 227c <free@plt>

19e18: e3e00000 mvn r0, #0

19e1c: eaffffe9 b 19dc8 <mcrypt\_mutex\_register@@Base+0x15634>

19e20: e59f0020 ldr r0, [pc, #32] ; 19e48 <mcrypt\_mutex\_register@@Base+0x156b4>

19e24: e08f0000 add r0, pc, r0

19e28: ebffa12e bl 22e8 <puts@plt>

19e2c: e3e00000 mvn r0, #0

19e30: eaffffe4 b 19dc8 <mcrypt\_mutex\_register@@Base+0x15634>

19e34: 000091f8 strdeq r9, [r0], -r8

19e38: 0000695c andeq r6, r0, ip, asr r9

19e3c: 00009140 andeq r9, r0, r0, asr #2

19e40: 000068a8 andeq r6, r0, r8, lsr #17

19e44: 000068ac andeq r6, r0, ip, lsr #17

19e48: 00006888 andeq r6, r0, r8, lsl #17

19e4c: e59f0000 ldr r0, [pc] ; 19e54 <mcrypt\_mutex\_register@@Base+0x156c0>

19e50: e12fff1e bx lr

19e54: 01315731 teqeq r1, r1, lsr r7

19e58: e92d4070 push {r4, r5, r6, lr}

19e5c: e1a05002 mov r5, r2

19e60: e1a04000 mov r4, r0

19e64: ebffa101 bl 2270 <memmove@plt>

19e68: e355007f cmp r5, #127 ; 0x7f

19e6c: 8a00000e bhi 19eac <mcrypt\_mutex\_register@@Base+0x15718>

19e70: e59fe074 ldr lr, [pc, #116] ; 19eec <mcrypt\_mutex\_register@@Base+0x15758>

19e74: e2452001 sub r2, r5, #1

19e78: e265c07f rsb ip, r5, #127 ; 0x7f

19e7c: e08fe00e add lr, pc, lr

19e80: e0842002 add r2, r4, r2

19e84: e084c00c add ip, r4, ip

19e88: e2441001 sub r1, r4, #1

19e8c: e5f13001 ldrb r3, [r1, #1]!

19e90: e5d20000 ldrb r0, [r2]

19e94: e15c0001 cmp ip, r1

19e98: e0833000 add r3, r3, r0

19e9c: e6ef3073 uxtb r3, r3

19ea0: e7de3003 ldrb r3, [lr, r3]

19ea4: e5e23001 strb r3, [r2, #1]!

19ea8: 1afffff7 bne 19e8c <mcrypt\_mutex\_register@@Base+0x156f8>

19eac: e5d41000 ldrb r1, [r4]

19eb0: e59f2038 ldr r2, [pc, #56] ; 19ef0 <mcrypt\_mutex\_register@@Base+0x1575c>

19eb4: e1a03004 mov r3, r4

19eb8: e08f2002 add r2, pc, r2

19ebc: e2440080 sub r0, r4, #128 ; 0x80

19ec0: e7d22001 ldrb r2, [r2, r1]

19ec4: e5c42000 strb r2, [r4]

19ec8: e5d3107f ldrb r1, [r3, #127] ; 0x7f

19ecc: e5d3207e ldrb r2, [r3, #126] ; 0x7e

19ed0: e2433002 sub r3, r3, #2

19ed4: e0822401 add r2, r2, r1, lsl #8

19ed8: e1c328b0 strh r2, [r3, #128] ; 0x80

19edc: e1500003 cmp r0, r3

19ee0: 1afffff8 bne 19ec8 <mcrypt\_mutex\_register@@Base+0x15734>

19ee4: e3a00000 mov r0, #0

19ee8: e8bd8070 pop {r4, r5, r6, pc}

19eec: 00009054 andeq r9, r0, r4, asr r0

19ef0: 00009018 andeq r9, r0, r8, lsl r0

19ef4: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

19ef8: e1a04000 mov r4, r0

19efc: e1d1c0b6 ldrh ip, [r1, #6]

19f00: e1d130b4 ldrh r3, [r1, #4]

19f04: e1d120b2 ldrh r2, [r1, #2]

19f08: e1d170b0 ldrh r7, [r1]

19f0c: e3a06000 mov r6, #0

19f10: e1d480b0 ldrh r8, [r4]

19f14: e00ce003 and lr, ip, r3

19f18: e6bf507c sxth r5, ip

19f1c: e08ee008 add lr, lr, r8

19f20: e1c28005 bic r8, r2, r5

19f24: e08ee008 add lr, lr, r8

19f28: e087700e add r7, r7, lr

19f2c: e1d4e0b2 ldrh lr, [r4, #2]

19f30: e6ff7077 uxth r7, r7

19f34: e1d480b4 ldrh r8, [r4, #4]

19f38: e1a097a7 lsr r9, r7, #15

19f3c: e1897087 orr r7, r9, r7, lsl #1

19f40: e082200e add r2, r2, lr

19f44: e6bfe077 sxth lr, r7

19f48: e1c3900e bic r9, r3, lr

19f4c: e6ff707e uxth r7, lr

19f50: e007a00c and sl, r7, ip

19f54: e082200a add r2, r2, sl

19f58: e0833008 add r3, r3, r8

19f5c: e6f92072 uxtah r2, r9, r2

19f60: e1d480b6 ldrh r8, [r4, #6]

19f64: e6ff2072 uxth r2, r2

19f68: e6ff9076 uxth r9, r6

19f6c: e1a0a722 lsr sl, r2, #14

19f70: e18a2102 orr r2, sl, r2, lsl #2

19f74: e08cc008 add ip, ip, r8

19f78: e6bf2072 sxth r2, r2

19f7c: e1c55002 bic r5, r5, r2

19f80: e6ff2072 uxth r2, r2

19f84: e0078002 and r8, r7, r2

19f88: e0833008 add r3, r3, r8

19f8c: e359000a cmp r9, #10

19f90: 13590004 cmpne r9, #4

19f94: e6f55073 uxtah r5, r5, r3

19f98: e2866001 add r6, r6, #1

19f9c: e6ff5075 uxth r5, r5

19fa0: e2844008 add r4, r4, #8

19fa4: e1a036a5 lsr r3, r5, #13

19fa8: e1833185 orr r3, r3, r5, lsl #3

19fac: e6bf3073 sxth r3, r3

19fb0: e1ce5003 bic r5, lr, r3

19fb4: e6ff3073 uxth r3, r3

19fb8: e002e003 and lr, r2, r3

19fbc: e08ce00e add lr, ip, lr

19fc0: e6f5e07e uxtah lr, r5, lr

19fc4: e6ffe07e uxth lr, lr

19fc8: e1a0c5ae lsr ip, lr, #11

19fcc: e18cc28e orr ip, ip, lr, lsl #5

19fd0: e6ffc07c uxth ip, ip

19fd4: e20ce03f and lr, ip, #63 ; 0x3f

19fd8: 1a000012 bne 1a028 <mcrypt\_mutex\_register@@Base+0x15894>

19fdc: e1a0e08e lsl lr, lr, #1

19fe0: e190e0be ldrh lr, [r0, lr]

19fe4: e087700e add r7, r7, lr

19fe8: e6ff7077 uxth r7, r7

19fec: e207e03f and lr, r7, #63 ; 0x3f

19ff0: e1a0e08e lsl lr, lr, #1

19ff4: e190e0be ldrh lr, [r0, lr]

19ff8: e082200e add r2, r2, lr

19ffc: e6ff2072 uxth r2, r2

1a000: e202e03f and lr, r2, #63 ; 0x3f

1a004: e1a0e08e lsl lr, lr, #1

1a008: e190e0be ldrh lr, [r0, lr]

1a00c: e083300e add r3, r3, lr

1a010: e6ff3073 uxth r3, r3

1a014: e203e03f and lr, r3, #63 ; 0x3f

1a018: e1a0e08e lsl lr, lr, #1

1a01c: e190e0be ldrh lr, [r0, lr]

1a020: e08cc00e add ip, ip, lr

1a024: e6ffc07c uxth ip, ip

1a028: e3560010 cmp r6, #16

1a02c: 1affffb7 bne 19f10 <mcrypt\_mutex\_register@@Base+0x1577c>

1a030: e1c170b0 strh r7, [r1]

1a034: e1c120b2 strh r2, [r1, #2]

1a038: e1c130b4 strh r3, [r1, #4]

1a03c: e1c1c0b6 strh ip, [r1, #6]

1a040: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1a044: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1a048: e1a05000 mov r5, r0

1a04c: e1d140b6 ldrh r4, [r1, #6]

1a050: e1d1e0b4 ldrh lr, [r1, #4]

1a054: e1d1c0b2 ldrh ip, [r1, #2]

1a058: e1d130b0 ldrh r3, [r1]

1a05c: e2408080 sub r8, r0, #128 ; 0x80

1a060: e3a0600f mov r6, #15

1a064: e1d597be ldrh r9, [r5, #126] ; 0x7e

1a068: e00e700c and r7, lr, ip

1a06c: e1a02584 lsl r2, r4, #11

1a070: e0877009 add r7, r7, r9

1a074: e6bfa073 sxth sl, r3

1a078: e18242a4 orr r4, r2, r4, lsr #5

1a07c: e0444007 sub r4, r4, r7

1a080: e1ca200e bic r2, sl, lr

1a084: e0444002 sub r4, r4, r2

1a088: e1d577bc ldrh r7, [r5, #124] ; 0x7c

1a08c: e6ff4074 uxth r4, r4

1a090: e00cb003 and fp, ip, r3

1a094: e1a0268e lsl r2, lr, #13

1a098: e08bb007 add fp, fp, r7

1a09c: e6bf9074 sxth r9, r4

1a0a0: e182e1ae orr lr, r2, lr, lsr #3

1a0a4: e1a0270c lsl r2, ip, #14

1a0a8: e1c9700c bic r7, r9, ip

1a0ac: e04ee00b sub lr, lr, fp

1a0b0: e182c12c orr ip, r2, ip, lsr #2

1a0b4: e1d527ba ldrh r2, [r5, #122] ; 0x7a

1a0b8: e04ee007 sub lr, lr, r7

1a0bc: e6bfc07c sxth ip, ip

1a0c0: e6ffe07e uxth lr, lr

1a0c4: e003b004 and fp, r3, r4

1a0c8: e04cc002 sub ip, ip, r2

1a0cc: e1a02783 lsl r2, r3, #15

1a0d0: e04cc00b sub ip, ip, fp

1a0d4: e18230a3 orr r3, r2, r3, lsr #1

1a0d8: e1cea00a bic sl, lr, sl

1a0dc: e1d527b8 ldrh r2, [r5, #120] ; 0x78

1a0e0: e04ca00a sub sl, ip, sl

1a0e4: e6bf3073 sxth r3, r3

1a0e8: e6ffc07a uxth ip, sl

1a0ec: e0433002 sub r3, r3, r2

1a0f0: e004b00e and fp, r4, lr

1a0f4: e1cc9009 bic r9, ip, r9

1a0f8: e043300b sub r3, r3, fp

1a0fc: e0433009 sub r3, r3, r9

1a100: e20e703f and r7, lr, #63 ; 0x3f

1a104: e20ca03f and sl, ip, #63 ; 0x3f

1a108: e6ff3073 uxth r3, r3

1a10c: e356000b cmp r6, #11

1a110: 13560005 cmpne r6, #5

1a114: e1a07087 lsl r7, r7, #1

1a118: e1a0a08a lsl sl, sl, #1

1a11c: e2469001 sub r9, r6, #1

1a120: e2455008 sub r5, r5, #8

1a124: e203203f and r2, r3, #63 ; 0x3f

1a128: 1a00000e bne 1a168 <mcrypt\_mutex\_register@@Base+0x159d4>

1a12c: e19070b7 ldrh r7, [r0, r7]

1a130: e1a02082 lsl r2, r2, #1

1a134: e19060ba ldrh r6, [r0, sl]

1a138: e0444007 sub r4, r4, r7

1a13c: e19020b2 ldrh r2, [r0, r2]

1a140: e6ff4074 uxth r4, r4

1a144: e204703f and r7, r4, #63 ; 0x3f

1a148: e04cc002 sub ip, ip, r2

1a14c: e1a02087 lsl r2, r7, #1

1a150: e04ee006 sub lr, lr, r6

1a154: e19020b2 ldrh r2, [r0, r2]

1a158: e6ffc07c uxth ip, ip

1a15c: e6ffe07e uxth lr, lr

1a160: e0433002 sub r3, r3, r2

1a164: e6ff3073 uxth r3, r3

1a168: e1580005 cmp r8, r5

1a16c: e6ff6079 uxth r6, r9

1a170: 1affffbb bne 1a064 <mcrypt\_mutex\_register@@Base+0x158d0>

1a174: e1c130b0 strh r3, [r1]

1a178: e1c1c0b2 strh ip, [r1, #2]

1a17c: e1c1e0b4 strh lr, [r1, #4]

1a180: e1c140b6 strh r4, [r1, #6]

1a184: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1a188: e3a00080 mov r0, #128 ; 0x80

1a18c: e12fff1e bx lr

1a190: e3a00008 mov r0, #8

1a194: e12fff1e bx lr

1a198: e3a00001 mov r0, #1

1a19c: e12fff1e bx lr

1a1a0: e3a00080 mov r0, #128 ; 0x80

1a1a4: e12fff1e bx lr

1a1a8: e3a03000 mov r3, #0

1a1ac: e5803000 str r3, [r0]

1a1b0: e1a00003 mov r0, r3

1a1b4: e12fff1e bx lr

1a1b8: e59f0004 ldr r0, [pc, #4] ; 1a1c4 <mcrypt\_mutex\_register@@Base+0x15a30>

1a1bc: e08f0000 add r0, pc, r0

1a1c0: e12fff1e bx lr

1a1c4: 00008e14 andeq r8, r0, r4, lsl lr

1a1c8: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

1a1cc: e24dd0e8 sub sp, sp, #232 ; 0xe8

1a1d0: ebffffee bl 1a190 <mcrypt\_mutex\_register@@Base+0x159fc>

1a1d4: e1a06000 mov r6, r0

1a1d8: ebfffff0 bl 1a1a0 <mcrypt\_mutex\_register@@Base+0x15a0c>

1a1dc: e1a01000 mov r1, r0

1a1e0: e3a00001 mov r0, #1

1a1e4: ebffa012 bl 2234 <calloc@plt>

1a1e8: e2507000 subs r7, r0, #0

1a1ec: 0a00006f beq 1a3b0 <mcrypt\_mutex\_register@@Base+0x15c1c>

1a1f0: e2475001 sub r5, r7, #1

1a1f4: e3a0400a mov r4, #10

1a1f8: e2678001 rsb r8, r7, #1

1a1fc: ea000004 b 1a214 <mcrypt\_mutex\_register@@Base+0x15a80>

1a200: e2742000 rsbs r2, r4, #0

1a204: e2844002 add r4, r4, #2

1a208: e6ef2072 uxtb r2, r2

1a20c: 52623000 rsbpl r3, r2, #0

1a210: e5e53001 strb r3, [r5, #1]!

1a214: ebffffe1 bl 1a1a0 <mcrypt\_mutex\_register@@Base+0x15a0c>

1a218: e0882005 add r2, r8, r5

1a21c: e6ef3074 uxtb r3, r4

1a220: e1500002 cmp r0, r2

1a224: cafffff5 bgt 1a200 <mcrypt\_mutex\_register@@Base+0x15a6c>

1a228: e3560000 cmp r6, #0

1a22c: da000039 ble 1a318 <mcrypt\_mutex\_register@@Base+0x15b84>

1a230: e1a0400d mov r4, sp

1a234: e1a02004 mov r2, r4

1a238: e3a03000 mov r3, #0

1a23c: e4c23001 strb r3, [r2], #1

1a240: e2833001 add r3, r3, #1

1a244: e1560003 cmp r6, r3

1a248: 1afffffb bne 1a23c <mcrypt\_mutex\_register@@Base+0x15aa8>

1a24c: ebffffcd bl 1a188 <mcrypt\_mutex\_register@@Base+0x159f4>

1a250: ebffa027 bl 22f4 <malloc@plt>

1a254: e2509000 subs r9, r0, #0

1a258: 0a000050 beq 1a3a0 <mcrypt\_mutex\_register@@Base+0x15c0c>

1a25c: e28d8010 add r8, sp, #16

1a260: e1a01004 mov r1, r4

1a264: e1a02006 mov r2, r6

1a268: e1a00008 mov r0, r8

1a26c: ebffa005 bl 2288 <memcpy@plt>

1a270: ebffffca bl 1a1a0 <mcrypt\_mutex\_register@@Base+0x15a0c>

1a274: e1a01007 mov r1, r7

1a278: e28da020 add sl, sp, #32

1a27c: e0886006 add r6, r8, r6

1a280: e1a05008 mov r5, r8

1a284: e1a0400a mov r4, sl

1a288: e1a02000 mov r2, r0

1a28c: e1a00009 mov r0, r9

1a290: ebfffef0 bl 19e58 <mcrypt\_mutex\_register@@Base+0x156c4>

1a294: e1a00007 mov r0, r7

1a298: ebff9ff7 bl 227c <free@plt>

1a29c: e1a01008 mov r1, r8

1a2a0: e1a00009 mov r0, r9

1a2a4: ebffff12 bl 19ef4 <mcrypt\_mutex\_register@@Base+0x15760>

1a2a8: e59f7108 ldr r7, [pc, #264] ; 1a3b8 <mcrypt\_mutex\_register@@Base+0x15c24>

1a2ac: e08f7007 add r7, pc, r7

1a2b0: e4d52001 ldrb r2, [r5], #1

1a2b4: e1a00004 mov r0, r4

1a2b8: e1a01007 mov r1, r7

1a2bc: ebffa02a bl 236c <sprintf@plt>

1a2c0: e1560005 cmp r6, r5

1a2c4: e2844002 add r4, r4, #2

1a2c8: 1afffff8 bne 1a2b0 <mcrypt\_mutex\_register@@Base+0x15b1c>

1a2cc: e59f40e8 ldr r4, [pc, #232] ; 1a3bc <mcrypt\_mutex\_register@@Base+0x15c28>

1a2d0: e1a0000a mov r0, sl

1a2d4: e08f4004 add r4, pc, r4

1a2d8: e1a01004 mov r1, r4

1a2dc: ebff9fda bl 224c <strcmp@plt>

1a2e0: e3500000 cmp r0, #0

1a2e4: 1a00001c bne 1a35c <mcrypt\_mutex\_register@@Base+0x15bc8>

1a2e8: e1a01008 mov r1, r8

1a2ec: e1a00009 mov r0, r9

1a2f0: ebffff53 bl 1a044 <mcrypt\_mutex\_register@@Base+0x158b0>

1a2f4: e1a00009 mov r0, r9

1a2f8: ebff9fdf bl 227c <free@plt>

1a2fc: e1a00008 mov r0, r8

1a300: e1a0100d mov r1, sp

1a304: ebff9fd0 bl 224c <strcmp@plt>

1a308: e3500000 cmp r0, #0

1a30c: 1a00001e bne 1a38c <mcrypt\_mutex\_register@@Base+0x15bf8>

1a310: e28dd0e8 add sp, sp, #232 ; 0xe8

1a314: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1a318: ebffff9a bl 1a188 <mcrypt\_mutex\_register@@Base+0x159f4>

1a31c: ebff9ff4 bl 22f4 <malloc@plt>

1a320: e2509000 subs r9, r0, #0

1a324: 0a00001d beq 1a3a0 <mcrypt\_mutex\_register@@Base+0x15c0c>

1a328: ebffff9c bl 1a1a0 <mcrypt\_mutex\_register@@Base+0x15a0c>

1a32c: e1a01007 mov r1, r7

1a330: e28d8010 add r8, sp, #16

1a334: e28da020 add sl, sp, #32

1a338: e1a02000 mov r2, r0

1a33c: e1a00009 mov r0, r9

1a340: ebfffec4 bl 19e58 <mcrypt\_mutex\_register@@Base+0x156c4>

1a344: e1a00007 mov r0, r7

1a348: ebff9fcb bl 227c <free@plt>

1a34c: e1a01008 mov r1, r8

1a350: e1a00009 mov r0, r9

1a354: ebfffee6 bl 19ef4 <mcrypt\_mutex\_register@@Base+0x15760>

1a358: eaffffdb b 1a2cc <mcrypt\_mutex\_register@@Base+0x15b38>

1a35c: e59f005c ldr r0, [pc, #92] ; 1a3c0 <mcrypt\_mutex\_register@@Base+0x15c2c>

1a360: e08f0000 add r0, pc, r0

1a364: ebff9fdf bl 22e8 <puts@plt>

1a368: e59f0054 ldr r0, [pc, #84] ; 1a3c4 <mcrypt\_mutex\_register@@Base+0x15c30>

1a36c: e1a0200a mov r2, sl

1a370: e1a01004 mov r1, r4

1a374: e08f0000 add r0, pc, r0

1a378: ebff9fb9 bl 2264 <printf@plt>

1a37c: e1a00009 mov r0, r9

1a380: ebff9fbd bl 227c <free@plt>

1a384: e3e00000 mvn r0, #0

1a388: eaffffe0 b 1a310 <mcrypt\_mutex\_register@@Base+0x15b7c>

1a38c: e59f0034 ldr r0, [pc, #52] ; 1a3c8 <mcrypt\_mutex\_register@@Base+0x15c34>

1a390: e08f0000 add r0, pc, r0

1a394: ebff9fd3 bl 22e8 <puts@plt>

1a398: e3e00000 mvn r0, #0

1a39c: eaffffdb b 1a310 <mcrypt\_mutex\_register@@Base+0x15b7c>

1a3a0: e1a00007 mov r0, r7

1a3a4: ebff9fb4 bl 227c <free@plt>

1a3a8: e3e00000 mvn r0, #0

1a3ac: eaffffd7 b 1a310 <mcrypt\_mutex\_register@@Base+0x15b7c>

1a3b0: e3e00000 mvn r0, #0

1a3b4: eaffffd5 b 1a310 <mcrypt\_mutex\_register@@Base+0x15b7c>

1a3b8: 000063b4 ; <UNDEFINED> instruction: 0x000063b4

1a3bc: 00008d00 andeq r8, r0, r0, lsl #26

1a3c0: 0000631c andeq r6, r0, ip, lsl r3

1a3c4: 00006320 andeq r6, r0, r0, lsr #6

1a3c8: 0000631c andeq r6, r0, ip, lsl r3

1a3cc: e59f0000 ldr r0, [pc] ; 1a3d4 <mcrypt\_mutex\_register@@Base+0x15c40>

1a3d0: e12fff1e bx lr

1a3d4: 01315731 teqeq r1, r1, lsr r7

1a3d8: e92d41f0 push {r4, r5, r6, r7, r8, lr}

1a3dc: e0801581 add r1, r0, r1, lsl #11

1a3e0: e5d3c005 ldrb ip, [r3, #5]

1a3e4: e5d34004 ldrb r4, [r3, #4]

1a3e8: e5d3e007 ldrb lr, [r3, #7]

1a3ec: e02cc3a2 eor ip, ip, r2, lsr #7

1a3f0: e02445a2 eor r4, r4, r2, lsr #11

1a3f4: e20cc03f and ip, ip, #63 ; 0x3f

1a3f8: e2811d22 add r1, r1, #2176 ; 0x880

1a3fc: e02eefe2 eor lr, lr, r2, ror #31

1a400: e204403f and r4, r4, #63 ; 0x3f

1a404: e081c10c add ip, r1, ip, lsl #2

1a408: e20ee03f and lr, lr, #63 ; 0x3f

1a40c: e5d36003 ldrb r6, [r3, #3]

1a410: e0814104 add r4, r1, r4, lsl #2

1a414: e51c8200 ldr r8, [ip, #-512] ; 0xfffffe00

1a418: e791010e ldr r0, [r1, lr, lsl #2]

1a41c: e5d35002 ldrb r5, [r3, #2]

1a420: e514c300 ldr ip, [r4, #-768] ; 0xfffffd00

1a424: e5d37001 ldrb r7, [r3, #1]

1a428: e1800008 orr r0, r0, r8

1a42c: e5d3e006 ldrb lr, [r3, #6]

1a430: e02667a2 eor r6, r6, r2, lsr #15

1a434: e5d38000 ldrb r8, [r3]

1a438: e02559a2 eor r5, r5, r2, lsr #19

1a43c: e180300c orr r3, r0, ip

1a440: e206603f and r6, r6, #63 ; 0x3f

1a444: e1a0c282 lsl ip, r2, #5

1a448: e0277ba2 eor r7, r7, r2, lsr #23

1a44c: e205503f and r5, r5, #63 ; 0x3f

1a450: e20cc020 and ip, ip, #32

1a454: e0816106 add r6, r1, r6, lsl #2

1a458: e207703f and r7, r7, #63 ; 0x3f

1a45c: e02ee1a2 eor lr, lr, r2, lsr #3

1a460: e18ccda2 orr ip, ip, r2, lsr #27

1a464: e0815105 add r5, r1, r5, lsl #2

1a468: e20ee03f and lr, lr, #63 ; 0x3f

1a46c: e02cc008 eor ip, ip, r8

1a470: e0814107 add r4, r1, r7, lsl #2

1a474: e5166400 ldr r6, [r6, #-1024] ; 0xfffffc00

1a478: e20cc03f and ip, ip, #63 ; 0x3f

1a47c: e081210e add r2, r1, lr, lsl #2

1a480: e5155500 ldr r5, [r5, #-1280] ; 0xfffffb00

1a484: e5140600 ldr r0, [r4, #-1536] ; 0xfffffa00

1a488: e1833006 orr r3, r3, r6

1a48c: e081110c add r1, r1, ip, lsl #2

1a490: e1833005 orr r3, r3, r5

1a494: e5122100 ldr r2, [r2, #-256] ; 0xffffff00

1a498: e1833000 orr r3, r3, r0

1a49c: e5110700 ldr r0, [r1, #-1792] ; 0xfffff900

1a4a0: e1833002 orr r3, r3, r2

1a4a4: e1830000 orr r0, r3, r0

1a4a8: e8bd81f0 pop {r4, r5, r6, r7, r8, pc}

1a4ac: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1a4b0: e24dd024 sub sp, sp, #36 ; 0x24

1a4b4: e1a0c00d mov ip, sp

1a4b8: e1a0500c mov r5, ip

1a4bc: e3a04000 mov r4, #0

1a4c0: e59fe0e8 ldr lr, [pc, #232] ; 1a5b0 <mcrypt\_mutex\_register@@Base+0x15e1c>

1a4c4: e3a03010 mov r3, #16

1a4c8: e08fe00e add lr, pc, lr

1a4cc: e3a02000 mov r2, #0

1a4d0: ea000003 b 1a4e4 <mcrypt\_mutex\_register@@Base+0x15d50>

1a4d4: e2822001 add r2, r2, #1

1a4d8: e3520020 cmp r2, #32

1a4dc: 0a000004 beq 1a4f4 <mcrypt\_mutex\_register@@Base+0x15d60>

1a4e0: e5fe3001 ldrb r3, [lr, #1]!

1a4e4: e2433001 sub r3, r3, #1

1a4e8: e1530004 cmp r3, r4

1a4ec: 1afffff8 bne 1a4d4 <mcrypt\_mutex\_register@@Base+0x15d40>

1a4f0: e5c52000 strb r2, [r5]

1a4f4: e2844001 add r4, r4, #1

1a4f8: e3540020 cmp r4, #32

1a4fc: e2855001 add r5, r5, #1

1a500: 1affffee bne 1a4c0 <mcrypt\_mutex\_register@@Base+0x15d2c>

1a504: e59f30a8 ldr r3, [pc, #168] ; 1a5b4 <mcrypt\_mutex\_register@@Base+0x15e20>

1a508: e59f90a8 ldr r9, [pc, #168] ; 1a5b8 <mcrypt\_mutex\_register@@Base+0x15e24>

1a50c: e0800581 add r0, r0, r1, lsl #11

1a510: e08f3003 add r3, pc, r3

1a514: e2807f5f add r7, r0, #380 ; 0x17c

1a518: e2838020 add r8, r3, #32

1a51c: e3a0a00e mov sl, #14

1a520: e3a06000 mov r6, #0

1a524: e3a00008 mov r0, #8

1a528: e3a04102 mov r4, #-2147483648 ; 0x80000000

1a52c: e08f9009 add r9, pc, r9

1a530: e1a0e007 mov lr, r7

1a534: e3a01000 mov r1, #0

1a538: e0895306 add r5, r9, r6, lsl #6

1a53c: e3a02000 mov r2, #0

1a540: e1a03002 mov r3, r2

1a544: e01ab350 ands fp, sl, r0, asr r3

1a548: 17dcb003 ldrbne fp, [ip, r3]

1a54c: e2833001 add r3, r3, #1

1a550: 11822b34 orrne r2, r2, r4, lsr fp

1a554: e3530004 cmp r3, #4

1a558: 1afffff9 bne 1a544 <mcrypt\_mutex\_register@@Base+0x15db0>

1a55c: e2811001 add r1, r1, #1

1a560: e3510040 cmp r1, #64 ; 0x40

1a564: e5ae2004 str r2, [lr, #4]!

1a568: 0a000008 beq 1a590 <mcrypt\_mutex\_register@@Base+0x15dfc>

1a56c: e1a0ad81 lsl sl, r1, #27

1a570: e2012020 and r2, r1, #32

1a574: e1a03201 lsl r3, r1, #4

1a578: e1822e2a orr r2, r2, sl, lsr #28

1a57c: e2033010 and r3, r3, #16

1a580: e1823003 orr r3, r2, r3

1a584: e0853003 add r3, r5, r3

1a588: e5d3a020 ldrb sl, [r3, #32]

1a58c: eaffffea b 1a53c <mcrypt\_mutex\_register@@Base+0x15da8>

1a590: e2866001 add r6, r6, #1

1a594: e3560008 cmp r6, #8

1a598: e28cc004 add ip, ip, #4

1a59c: e2877c01 add r7, r7, #256 ; 0x100

1a5a0: 17d8a306 ldrbne sl, [r8, r6, lsl #6]

1a5a4: 1affffe1 bne 1a530 <mcrypt\_mutex\_register@@Base+0x15d9c>

1a5a8: e28dd024 add sp, sp, #36 ; 0x24

1a5ac: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1a5b0: 00008b20 andeq r8, r0, r0, lsr #22

1a5b4: 00008ad8 ldrdeq r8, [r0], -r8 ; <UNPREDICTABLE>

1a5b8: 00008abc ; <UNDEFINED> instruction: 0x00008abc

1a5bc: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

1a5c0: e3a02b02 mov r2, #2048 ; 0x800

1a5c4: e1a04001 mov r4, r1

1a5c8: e3a01000 mov r1, #0

1a5cc: e1a08000 mov r8, r0

1a5d0: ebff9f5c bl 2348 <memset@plt>

1a5d4: e59fc080 ldr ip, [pc, #128] ; 1a65c <mcrypt\_mutex\_register@@Base+0x15ec8>

1a5d8: e2645001 rsb r5, r4, #1

1a5dc: e2449001 sub r9, r4, #1

1a5e0: e284e03f add lr, r4, #63 ; 0x3f

1a5e4: e08fc00c add ip, pc, ip

1a5e8: e3a04000 mov r4, #0

1a5ec: e3a07000 mov r7, #0

1a5f0: e1a02009 mov r2, r9

1a5f4: e0886187 add r6, r8, r7, lsl #3

1a5f8: e0850002 add r0, r5, r2

1a5fc: e5f23001 ldrb r3, [r2, #1]!

1a600: e2433001 sub r3, r3, #1

1a604: e2031003 and r1, r3, #3

1a608: e1540143 cmp r4, r3, asr #2

1a60c: e08c1101 add r1, ip, r1, lsl #2

1a610: 1a000007 bne 1a634 <mcrypt\_mutex\_register@@Base+0x15ea0>

1a614: e5911220 ldr r1, [r1, #544] ; 0x220

1a618: e2003007 and r3, r0, #7

1a61c: e1170001 tst r7, r1

1a620: e08c3103 add r3, ip, r3, lsl #2

1a624: 17d611c0 ldrbne r1, [r6, r0, asr #3]

1a628: 15933230 ldrne r3, [r3, #560] ; 0x230

1a62c: 11833001 orrne r3, r3, r1

1a630: 17c631c0 strbne r3, [r6, r0, asr #3]

1a634: e15e0002 cmp lr, r2

1a638: 1affffee bne 1a5f8 <mcrypt\_mutex\_register@@Base+0x15e64>

1a63c: e2877001 add r7, r7, #1

1a640: e3570010 cmp r7, #16

1a644: 1affffe9 bne 1a5f0 <mcrypt\_mutex\_register@@Base+0x15e5c>

1a648: e2844001 add r4, r4, #1

1a64c: e3540010 cmp r4, #16

1a650: e2888080 add r8, r8, #128 ; 0x80

1a654: 1affffe4 bne 1a5ec <mcrypt\_mutex\_register@@Base+0x15e58>

1a658: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1a65c: 00008a04 andeq r8, r0, r4, lsl #20

1a660: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1a664: e251e000 subs lr, r1, #0

1a668: e24dd014 sub sp, sp, #20

1a66c: 0a000048 beq 1a794 <mcrypt\_mutex\_register@@Base+0x16000>

1a670: e3a03000 mov r3, #0

1a674: e5823000 str r3, [r2]

1a678: e5823004 str r3, [r2, #4]

1a67c: e5d2a000 ldrb sl, [r2]

1a680: e5d23007 ldrb r3, [r2, #7]

1a684: e5d29001 ldrb r9, [r2, #1]

1a688: e5d28002 ldrb r8, [r2, #2]

1a68c: e5d27003 ldrb r7, [r2, #3]

1a690: e5d26004 ldrb r6, [r2, #4]

1a694: e5d25005 ldrb r5, [r2, #5]

1a698: e5d24006 ldrb r4, [r2, #6]

1a69c: e240b001 sub fp, r0, #1

1a6a0: e58d3004 str r3, [sp, #4]

1a6a4: e2803007 add r3, r0, #7

1a6a8: e58d300c str r3, [sp, #12]

1a6ac: e58d4008 str r4, [sp, #8]

1a6b0: e5fb3001 ldrb r3, [fp, #1]!

1a6b4: e28e1080 add r1, lr, #128 ; 0x80

1a6b8: e203c00f and ip, r3, #15

1a6bc: e1a03223 lsr r3, r3, #4

1a6c0: e7d1018c ldrb r0, [r1, ip, lsl #3]

1a6c4: e081118c add r1, r1, ip, lsl #3

1a6c8: e1a04000 mov r4, r0

1a6cc: e7de0183 ldrb r0, [lr, r3, lsl #3]

1a6d0: e08e3183 add r3, lr, r3, lsl #3

1a6d4: e28eec01 add lr, lr, #256 ; 0x100

1a6d8: e1800004 orr r0, r0, r4

1a6dc: e180a00a orr sl, r0, sl

1a6e0: e5c2a000 strb sl, [r2]

1a6e4: e5d1c001 ldrb ip, [r1, #1]

1a6e8: e5d30001 ldrb r0, [r3, #1]

1a6ec: e59d400c ldr r4, [sp, #12]

1a6f0: e180000c orr r0, r0, ip

1a6f4: e1809009 orr r9, r0, r9

1a6f8: e5c29001 strb r9, [r2, #1]

1a6fc: e5d1c002 ldrb ip, [r1, #2]

1a700: e5d30002 ldrb r0, [r3, #2]

1a704: e154000b cmp r4, fp

1a708: e180000c orr r0, r0, ip

1a70c: e1808008 orr r8, r0, r8

1a710: e5c28002 strb r8, [r2, #2]

1a714: e5d1c003 ldrb ip, [r1, #3]

1a718: e5d30003 ldrb r0, [r3, #3]

1a71c: e180000c orr r0, r0, ip

1a720: e1807007 orr r7, r0, r7

1a724: e5c27003 strb r7, [r2, #3]

1a728: e5d1c004 ldrb ip, [r1, #4]

1a72c: e5d30004 ldrb r0, [r3, #4]

1a730: e180000c orr r0, r0, ip

1a734: e1806006 orr r6, r0, r6

1a738: e5c26004 strb r6, [r2, #4]

1a73c: e5d1c005 ldrb ip, [r1, #5]

1a740: e5d30005 ldrb r0, [r3, #5]

1a744: e180000c orr r0, r0, ip

1a748: e1805005 orr r5, r0, r5

1a74c: e5c25005 strb r5, [r2, #5]

1a750: e5d1c006 ldrb ip, [r1, #6]

1a754: e5d30006 ldrb r0, [r3, #6]

1a758: e180000c orr r0, r0, ip

1a75c: e59dc008 ldr ip, [sp, #8]

1a760: e180000c orr r0, r0, ip

1a764: e5c20006 strb r0, [r2, #6]

1a768: e5d11007 ldrb r1, [r1, #7]

1a76c: e5d33007 ldrb r3, [r3, #7]

1a770: e58d0008 str r0, [sp, #8]

1a774: e1833001 orr r3, r3, r1

1a778: e59d1004 ldr r1, [sp, #4]

1a77c: e1833001 orr r3, r3, r1

1a780: e58d3004 str r3, [sp, #4]

1a784: e5c23007 strb r3, [r2, #7]

1a788: 1affffc8 bne 1a6b0 <mcrypt\_mutex\_register@@Base+0x15f1c>

1a78c: e28dd014 add sp, sp, #20

1a790: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1a794: e1a01000 mov r1, r0

1a798: e1a00002 mov r0, r2

1a79c: e3a02008 mov r2, #8

1a7a0: e28dd014 add sp, sp, #20

1a7a4: e8bd4ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1a7a8: eaff9eb0 b 2270 <memmove@plt>

1a7ac: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1a7b0: e24dd09c sub sp, sp, #156 ; 0x9c

1a7b4: e1a05000 mov r5, r0

1a7b8: e1a06001 mov r6, r1

1a7bc: e59f44b4 ldr r4, [pc, #1204] ; 1ac78 <mcrypt\_mutex\_register@@Base+0x164e4>

1a7c0: e3a01000 mov r1, #0

1a7c4: e58d0004 str r0, [sp, #4]

1a7c8: ebffff37 bl 1a4ac <mcrypt\_mutex\_register@@Base+0x15d18>

1a7cc: e1a00005 mov r0, r5

1a7d0: e3a01001 mov r1, #1

1a7d4: ebffff34 bl 1a4ac <mcrypt\_mutex\_register@@Base+0x15d18>

1a7d8: e08f4004 add r4, pc, r4

1a7dc: e1a00005 mov r0, r5

1a7e0: e3a01002 mov r1, #2

1a7e4: ebffff30 bl 1a4ac <mcrypt\_mutex\_register@@Base+0x15d18>

1a7e8: e2850d66 add r0, r5, #6528 ; 0x1980

1a7ec: e1a01004 mov r1, r4

1a7f0: ebffff71 bl 1a5bc <mcrypt\_mutex\_register@@Base+0x15e28>

1a7f4: e2841040 add r1, r4, #64 ; 0x40

1a7f8: e2850d86 add r0, r5, #8576 ; 0x2180

1a7fc: ebffff6e bl 1a5bc <mcrypt\_mutex\_register@@Base+0x15e28>

1a800: e3a02080 mov r2, #128 ; 0x80

1a804: e3a01000 mov r1, #0

1a808: e1a00005 mov r0, r5

1a80c: ebff9ecd bl 2348 <memset@plt>

1a810: e3a02080 mov r2, #128 ; 0x80

1a814: e3a01000 mov r1, #0

1a818: e2850080 add r0, r5, #128 ; 0x80

1a81c: ebff9ec9 bl 2348 <memset@plt>

1a820: e3a02080 mov r2, #128 ; 0x80

1a824: e3a01000 mov r1, #0

1a828: e2850c01 add r0, r5, #256 ; 0x100

1a82c: ebff9ec5 bl 2348 <memset@plt>

1a830: e59f3444 ldr r3, [pc, #1092] ; 1ac7c <mcrypt\_mutex\_register@@Base+0x164e8>

1a834: e59fc444 ldr ip, [pc, #1092] ; 1ac80 <mcrypt\_mutex\_register@@Base+0x164ec>

1a838: e08f3003 add r3, pc, r3

1a83c: e283be25 add fp, r3, #592 ; 0x250

1a840: e28d9027 add r9, sp, #39 ; 0x27

1a844: e08fc00c add ip, pc, ip

1a848: e1a0000b mov r0, fp

1a84c: e1a01009 mov r1, r9

1a850: e28da05f add sl, sp, #95 ; 0x5f

1a854: e3a02080 mov r2, #128 ; 0x80

1a858: e3a03038 mov r3, #56 ; 0x38

1a85c: ea000004 b 1a874 <mcrypt\_mutex\_register@@Base+0x160e0>

1a860: e5f03001 ldrb r3, [r0, #1]!

1a864: e2433001 sub r3, r3, #1

1a868: e2032007 and r2, r3, #7

1a86c: e08c2102 add r2, ip, r2, lsl #2

1a870: e5922230 ldr r2, [r2, #560] ; 0x230

1a874: e7d631c3 ldrb r3, [r6, r3, asr #3]

1a878: e1130002 tst r3, r2

1a87c: 13a03001 movne r3, #1

1a880: 03a03000 moveq r3, #0

1a884: e5e13001 strb r3, [r1, #1]!

1a888: e151000a cmp r1, sl

1a88c: 1afffff3 bne 1a860 <mcrypt\_mutex\_register@@Base+0x160cc>

1a890: e59f23ec ldr r2, [pc, #1004] ; 1ac84 <mcrypt\_mutex\_register@@Base+0x164f0>

1a894: e3a03001 mov r3, #1

1a898: e08f2002 add r2, pc, r2

1a89c: e1a01002 mov r1, r2

1a8a0: e2818fa6 add r8, r1, #664 ; 0x298

1a8a4: e0881003 add r1, r8, r3

1a8a8: e59f53d8 ldr r5, [pc, #984] ; 1ac88 <mcrypt\_mutex\_register@@Base+0x164f4>

1a8ac: e58d200c str r2, [sp, #12]

1a8b0: e58d1000 str r1, [sp]

1a8b4: e2822fa2 add r2, r2, #648 ; 0x288

1a8b8: e28d1060 add r1, sp, #96 ; 0x60

1a8bc: e28d0028 add r0, sp, #40 ; 0x28

1a8c0: e59f43c4 ldr r4, [pc, #964] ; 1ac8c <mcrypt\_mutex\_register@@Base+0x164f8>

1a8c4: e58d901c str r9, [sp, #28]

1a8c8: e58da020 str sl, [sp, #32]

1a8cc: e58d6024 str r6, [sp, #36] ; 0x24

1a8d0: e3a07000 mov r7, #0

1a8d4: e08f5005 add r5, pc, r5

1a8d8: e1a06002 mov r6, r2

1a8dc: e1a09001 mov r9, r1

1a8e0: e1a0a000 mov sl, r0

1a8e4: e58d2010 str r2, [sp, #16]

1a8e8: e58d1014 str r1, [sp, #20]

1a8ec: e58db018 str fp, [sp, #24]

1a8f0: e0490003 sub r0, r9, r3

1a8f4: e08ac003 add ip, sl, r3

1a8f8: e3a02000 mov r2, #0

1a8fc: ea00000a b 1a92c <mcrypt\_mutex\_register@@Base+0x16198>

1a900: e3530037 cmp r3, #55 ; 0x37

1a904: da000000 ble 1a90c <mcrypt\_mutex\_register@@Base+0x16178>

1a908: e243101c sub r1, r3, #28

1a90c: e28de098 add lr, sp, #152 ; 0x98

1a910: e08e1001 add r1, lr, r1

1a914: e3520037 cmp r2, #55 ; 0x37

1a918: e5511070 ldrb r1, [r1, #-112] ; 0xffffff90

1a91c: e7c01003 strb r1, [r0, r3]

1a920: 0a000009 beq 1a94c <mcrypt\_mutex\_register@@Base+0x161b8>

1a924: e2822001 add r2, r2, #1

1a928: e2833001 add r3, r3, #1

1a92c: e352001b cmp r2, #27

1a930: e1a01003 mov r1, r3

1a934: 8afffff1 bhi 1a900 <mcrypt\_mutex\_register@@Base+0x1616c>

1a938: e353001b cmp r3, #27

1a93c: cafffff1 bgt 1a908 <mcrypt\_mutex\_register@@Base+0x16174>

1a940: e7dc1002 ldrb r1, [ip, r2]

1a944: e7c01003 strb r1, [r0, r3]

1a948: eafffff5 b 1a924 <mcrypt\_mutex\_register@@Base+0x16190>

1a94c: e59d300c ldr r3, [sp, #12]

1a950: e59d2004 ldr r2, [sp, #4]

1a954: e283efb2 add lr, r3, #712 ; 0x2c8

1a958: e59d1000 ldr r1, [sp]

1a95c: e3a0300e mov r3, #14

1a960: e1e0c008 mvn ip, r8

1a964: e0820187 add r0, r2, r7, lsl #3

1a968: e58d6008 str r6, [sp, #8]

1a96c: ea000000 b 1a974 <mcrypt\_mutex\_register@@Base+0x161e0>

1a970: e4d13001 ldrb r3, [r1], #1

1a974: e28d2098 add r2, sp, #152 ; 0x98

1a978: e0823003 add r3, r2, r3

1a97c: e081200c add r2, r1, ip

1a980: e553b039 ldrb fp, [r3, #-57] ; 0xffffffc7

1a984: e0c36294 smull r6, r3, r4, r2

1a988: e0433fc2 sub r3, r3, r2, asr #31

1a98c: e35b0000 cmp fp, #0

1a990: e083b083 add fp, r3, r3, lsl #1

1a994: 0a000005 beq 1a9b0 <mcrypt\_mutex\_register@@Base+0x1621c>

1a998: e042208b sub r2, r2, fp, lsl #1

1a99c: e7d0b003 ldrb fp, [r0, r3]

1a9a0: e0852102 add r2, r5, r2, lsl #2

1a9a4: e5922230 ldr r2, [r2, #560] ; 0x230

1a9a8: e18bb142 orr fp, fp, r2, asr #2

1a9ac: e7c0b003 strb fp, [r0, r3]

1a9b0: e151000e cmp r1, lr

1a9b4: 1affffed bne 1a970 <mcrypt\_mutex\_register@@Base+0x161dc>

1a9b8: e2877001 add r7, r7, #1

1a9bc: e3570010 cmp r7, #16

1a9c0: e59d6008 ldr r6, [sp, #8]

1a9c4: 15f63001 ldrbne r3, [r6, #1]!

1a9c8: 1affffc8 bne 1a8f0 <mcrypt\_mutex\_register@@Base+0x1615c>

1a9cc: e59db018 ldr fp, [sp, #24]

1a9d0: e59d901c ldr r9, [sp, #28]

1a9d4: e59f42b4 ldr r4, [pc, #692] ; 1ac90 <mcrypt\_mutex\_register@@Base+0x164fc>

1a9d8: e59da020 ldr sl, [sp, #32]

1a9dc: e59d6024 ldr r6, [sp, #36] ; 0x24

1a9e0: e08f4004 add r4, pc, r4

1a9e4: e1a0e00b mov lr, fp

1a9e8: e1a00009 mov r0, r9

1a9ec: e3a02080 mov r2, #128 ; 0x80

1a9f0: e3a03038 mov r3, #56 ; 0x38

1a9f4: ea000004 b 1aa0c <mcrypt\_mutex\_register@@Base+0x16278>

1a9f8: e5fe3001 ldrb r3, [lr, #1]!

1a9fc: e2433001 sub r3, r3, #1

1aa00: e2032007 and r2, r3, #7

1aa04: e0842102 add r2, r4, r2, lsl #2

1aa08: e5922230 ldr r2, [r2, #560] ; 0x230

1aa0c: e08631c3 add r3, r6, r3, asr #3

1aa10: e5d33008 ldrb r3, [r3, #8]

1aa14: e1130002 tst r3, r2

1aa18: 13a03001 movne r3, #1

1aa1c: 03a03000 moveq r3, #0

1aa20: e5e03001 strb r3, [r0, #1]!

1aa24: e150000a cmp r0, sl

1aa28: 1afffff2 bne 1a9f8 <mcrypt\_mutex\_register@@Base+0x16264>

1aa2c: e59f8260 ldr r8, [pc, #608] ; 1ac94 <mcrypt\_mutex\_register@@Base+0x16500>

1aa30: e59d2010 ldr r2, [sp, #16]

1aa34: e3a00000 mov r0, #0

1aa38: e28de028 add lr, sp, #40 ; 0x28

1aa3c: e58da018 str sl, [sp, #24]

1aa40: e59f7244 ldr r7, [pc, #580] ; 1ac8c <mcrypt\_mutex\_register@@Base+0x164f8>

1aa44: e59da014 ldr sl, [sp, #20]

1aa48: e58db008 str fp, [sp, #8]

1aa4c: e58d900c str r9, [sp, #12]

1aa50: e58d601c str r6, [sp, #28]

1aa54: e08f8008 add r8, pc, r8

1aa58: e3a03001 mov r3, #1

1aa5c: e1a09002 mov r9, r2

1aa60: e1a06000 mov r6, r0

1aa64: e1a0b00e mov fp, lr

1aa68: e04ae003 sub lr, sl, r3

1aa6c: e08b4003 add r4, fp, r3

1aa70: e3a02000 mov r2, #0

1aa74: ea00000a b 1aaa4 <mcrypt\_mutex\_register@@Base+0x16310>

1aa78: e3530037 cmp r3, #55 ; 0x37

1aa7c: da000000 ble 1aa84 <mcrypt\_mutex\_register@@Base+0x162f0>

1aa80: e243001c sub r0, r3, #28

1aa84: e28d5098 add r5, sp, #152 ; 0x98

1aa88: e0850000 add r0, r5, r0

1aa8c: e3520037 cmp r2, #55 ; 0x37

1aa90: e5500070 ldrb r0, [r0, #-112] ; 0xffffff90

1aa94: e7ce0003 strb r0, [lr, r3]

1aa98: 0a000009 beq 1aac4 <mcrypt\_mutex\_register@@Base+0x16330>

1aa9c: e2822001 add r2, r2, #1

1aaa0: e2833001 add r3, r3, #1

1aaa4: e352001b cmp r2, #27

1aaa8: e1a00003 mov r0, r3

1aaac: 8afffff1 bhi 1aa78 <mcrypt\_mutex\_register@@Base+0x162e4>

1aab0: e353001b cmp r3, #27

1aab4: cafffff1 bgt 1aa80 <mcrypt\_mutex\_register@@Base+0x162ec>

1aab8: e7d40002 ldrb r0, [r4, r2]

1aabc: e7ce0003 strb r0, [lr, r3]

1aac0: eafffff5 b 1aa9c <mcrypt\_mutex\_register@@Base+0x16308>

1aac4: e59d2004 ldr r2, [sp, #4]

1aac8: e59d0000 ldr r0, [sp]

1aacc: e3a0300e mov r3, #14

1aad0: e0825186 add r5, r2, r6, lsl #3

1aad4: ea000000 b 1aadc <mcrypt\_mutex\_register@@Base+0x16348>

1aad8: e4d03001 ldrb r3, [r0], #1

1aadc: e28d2098 add r2, sp, #152 ; 0x98

1aae0: e0823003 add r3, r2, r3

1aae4: e080200c add r2, r0, ip

1aae8: e5534039 ldrb r4, [r3, #-57] ; 0xffffffc7

1aaec: e0c3e297 smull lr, r3, r7, r2

1aaf0: e0433fc2 sub r3, r3, r2, asr #31

1aaf4: e085e003 add lr, r5, r3

1aaf8: e0833083 add r3, r3, r3, lsl #1

1aafc: e3540000 cmp r4, #0

1ab00: e0423083 sub r3, r2, r3, lsl #1

1ab04: 15de2080 ldrbne r2, [lr, #128] ; 0x80

1ab08: 10883103 addne r3, r8, r3, lsl #2

1ab0c: 15933230 ldrne r3, [r3, #560] ; 0x230

1ab10: 11823143 orrne r3, r2, r3, asr #2

1ab14: 15ce3080 strbne r3, [lr, #128] ; 0x80

1ab18: e1500001 cmp r0, r1

1ab1c: 1affffed bne 1aad8 <mcrypt\_mutex\_register@@Base+0x16344>

1ab20: e2866001 add r6, r6, #1

1ab24: e3560010 cmp r6, #16

1ab28: 15f93001 ldrbne r3, [r9, #1]!

1ab2c: 1affffcd bne 1aa68 <mcrypt\_mutex\_register@@Base+0x162d4>

1ab30: e59f0160 ldr r0, [pc, #352] ; 1ac98 <mcrypt\_mutex\_register@@Base+0x16504>

1ab34: e59db008 ldr fp, [sp, #8]

1ab38: e59d900c ldr r9, [sp, #12]

1ab3c: e59da018 ldr sl, [sp, #24]

1ab40: e59d601c ldr r6, [sp, #28]

1ab44: e08f0000 add r0, pc, r0

1ab48: e3a02080 mov r2, #128 ; 0x80

1ab4c: e3a03038 mov r3, #56 ; 0x38

1ab50: ea000004 b 1ab68 <mcrypt\_mutex\_register@@Base+0x163d4>

1ab54: e5fb3001 ldrb r3, [fp, #1]!

1ab58: e2433001 sub r3, r3, #1

1ab5c: e2032007 and r2, r3, #7

1ab60: e0802102 add r2, r0, r2, lsl #2

1ab64: e5922230 ldr r2, [r2, #560] ; 0x230

1ab68: e08631c3 add r3, r6, r3, asr #3

1ab6c: e5d33010 ldrb r3, [r3, #16]

1ab70: e1130002 tst r3, r2

1ab74: 13a03001 movne r3, #1

1ab78: 03a03000 moveq r3, #0

1ab7c: e5e93001 strb r3, [r9, #1]!

1ab80: e159000a cmp r9, sl

1ab84: 1afffff2 bne 1ab54 <mcrypt\_mutex\_register@@Base+0x163c0>

1ab88: e59f710c ldr r7, [pc, #268] ; 1ac9c <mcrypt\_mutex\_register@@Base+0x16508>

1ab8c: e59f60f8 ldr r6, [pc, #248] ; 1ac8c <mcrypt\_mutex\_register@@Base+0x164f8>

1ab90: e1cda1d0 ldrd sl, [sp, #16]

1ab94: e08f7007 add r7, pc, r7

1ab98: e3a03001 mov r3, #1

1ab9c: e3a08000 mov r8, #0

1aba0: e28d9028 add r9, sp, #40 ; 0x28

1aba4: e04be003 sub lr, fp, r3

1aba8: e0894003 add r4, r9, r3

1abac: e3a02000 mov r2, #0

1abb0: ea00000a b 1abe0 <mcrypt\_mutex\_register@@Base+0x1644c>

1abb4: e3530037 cmp r3, #55 ; 0x37

1abb8: da000000 ble 1abc0 <mcrypt\_mutex\_register@@Base+0x1642c>

1abbc: e243001c sub r0, r3, #28

1abc0: e28d5098 add r5, sp, #152 ; 0x98

1abc4: e0850000 add r0, r5, r0

1abc8: e3520037 cmp r2, #55 ; 0x37

1abcc: e5500070 ldrb r0, [r0, #-112] ; 0xffffff90

1abd0: e7ce0003 strb r0, [lr, r3]

1abd4: 0a000009 beq 1ac00 <mcrypt\_mutex\_register@@Base+0x1646c>

1abd8: e2822001 add r2, r2, #1

1abdc: e2833001 add r3, r3, #1

1abe0: e352001b cmp r2, #27

1abe4: e1a00003 mov r0, r3

1abe8: 8afffff1 bhi 1abb4 <mcrypt\_mutex\_register@@Base+0x16420>

1abec: e353001b cmp r3, #27

1abf0: cafffff1 bgt 1abbc <mcrypt\_mutex\_register@@Base+0x16428>

1abf4: e7d40002 ldrb r0, [r4, r2]

1abf8: e7ce0003 strb r0, [lr, r3]

1abfc: eafffff5 b 1abd8 <mcrypt\_mutex\_register@@Base+0x16444>

1ac00: e59d2004 ldr r2, [sp, #4]

1ac04: e59d0000 ldr r0, [sp]

1ac08: e3a0300e mov r3, #14

1ac0c: e0825188 add r5, r2, r8, lsl #3

1ac10: ea000000 b 1ac18 <mcrypt\_mutex\_register@@Base+0x16484>

1ac14: e4d03001 ldrb r3, [r0], #1

1ac18: e28d2098 add r2, sp, #152 ; 0x98

1ac1c: e0823003 add r3, r2, r3

1ac20: e08c2000 add r2, ip, r0

1ac24: e5534039 ldrb r4, [r3, #-57] ; 0xffffffc7

1ac28: e0c3e296 smull lr, r3, r6, r2

1ac2c: e0433fc2 sub r3, r3, r2, asr #31

1ac30: e085e003 add lr, r5, r3

1ac34: e0833083 add r3, r3, r3, lsl #1

1ac38: e3540000 cmp r4, #0

1ac3c: e0423083 sub r3, r2, r3, lsl #1

1ac40: 15de2100 ldrbne r2, [lr, #256] ; 0x100

1ac44: 10873103 addne r3, r7, r3, lsl #2

1ac48: 15933230 ldrne r3, [r3, #560] ; 0x230

1ac4c: 11823143 orrne r3, r2, r3, asr #2

1ac50: 15ce3100 strbne r3, [lr, #256] ; 0x100

1ac54: e1510000 cmp r1, r0

1ac58: 1affffed bne 1ac14 <mcrypt\_mutex\_register@@Base+0x16480>

1ac5c: e2888001 add r8, r8, #1

1ac60: e3580010 cmp r8, #16

1ac64: 15fa3001 ldrbne r3, [sl, #1]!

1ac68: 1affffcd bne 1aba4 <mcrypt\_mutex\_register@@Base+0x16410>

1ac6c: e3a00000 mov r0, #0

1ac70: e28dd09c add sp, sp, #156 ; 0x9c

1ac74: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1ac78: 0001acbc ; <UNDEFINED> instruction: 0x0001acbc

1ac7c: 000087b0 ; <UNDEFINED> instruction: 0x000087b0

1ac80: 000087a4 andeq r8, r0, r4, lsr #15

1ac84: 00008750 andeq r8, r0, r0, asr r7

1ac88: 00008714 andeq r8, r0, r4, lsl r7

1ac8c: 2aaaaaab bcs feac5740 <mcrypt\_mutex\_register@@Base+0xfeac0fac>

1ac90: 00008608 andeq r8, r0, r8, lsl #12

1ac94: 00008594 muleq r0, r4, r5

1ac98: 000084a4 andeq r8, r0, r4, lsr #9

1ac9c: 00008454 andeq r8, r0, r4, asr r4

1aca0: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

1aca4: e24dd008 sub sp, sp, #8

1aca8: e1a07001 mov r7, r1

1acac: e1a0600d mov r6, sp

1acb0: e1a04000 mov r4, r0

1acb4: e2801d66 add r1, r0, #6528 ; 0x1980

1acb8: e1a02006 mov r2, r6

1acbc: e1a00007 mov r0, r7

1acc0: ebfffe66 bl 1a660 <mcrypt\_mutex\_register@@Base+0x15ecc>

1acc4: e59d5004 ldr r5, [sp, #4]

1acc8: e1a03004 mov r3, r4

1accc: e6bf5f35 rev r5, r5

1acd0: e1a02005 mov r2, r5

1acd4: e1a00004 mov r0, r4

1acd8: e3a01000 mov r1, #0

1acdc: ebfffdbd bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ace0: e59d9000 ldr r9, [sp]

1ace4: e2843008 add r3, r4, #8

1ace8: e6bf9f39 rev r9, r9

1acec: e3a01000 mov r1, #0

1acf0: e0299000 eor r9, r9, r0

1acf4: e1a02009 mov r2, r9

1acf8: e1a00004 mov r0, r4

1acfc: ebfffdb5 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad00: e2843010 add r3, r4, #16

1ad04: e3a01000 mov r1, #0

1ad08: e0255000 eor r5, r5, r0

1ad0c: e1a02005 mov r2, r5

1ad10: e1a00004 mov r0, r4

1ad14: ebfffdaf bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad18: e2843018 add r3, r4, #24

1ad1c: e3a01000 mov r1, #0

1ad20: e0299000 eor r9, r9, r0

1ad24: e1a02009 mov r2, r9

1ad28: e1a00004 mov r0, r4

1ad2c: ebfffda9 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad30: e2843020 add r3, r4, #32

1ad34: e3a01000 mov r1, #0

1ad38: e0255000 eor r5, r5, r0

1ad3c: e1a02005 mov r2, r5

1ad40: e1a00004 mov r0, r4

1ad44: ebfffda3 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad48: e2843028 add r3, r4, #40 ; 0x28

1ad4c: e3a01000 mov r1, #0

1ad50: e0299000 eor r9, r9, r0

1ad54: e1a02009 mov r2, r9

1ad58: e1a00004 mov r0, r4

1ad5c: ebfffd9d bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad60: e2843030 add r3, r4, #48 ; 0x30

1ad64: e3a01000 mov r1, #0

1ad68: e0255000 eor r5, r5, r0

1ad6c: e1a02005 mov r2, r5

1ad70: e1a00004 mov r0, r4

1ad74: ebfffd97 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad78: e2843038 add r3, r4, #56 ; 0x38

1ad7c: e3a01000 mov r1, #0

1ad80: e0299000 eor r9, r9, r0

1ad84: e1a02009 mov r2, r9

1ad88: e1a00004 mov r0, r4

1ad8c: ebfffd91 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ad90: e2843040 add r3, r4, #64 ; 0x40

1ad94: e3a01000 mov r1, #0

1ad98: e0255000 eor r5, r5, r0

1ad9c: e1a02005 mov r2, r5

1ada0: e1a00004 mov r0, r4

1ada4: ebfffd8b bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ada8: e2843048 add r3, r4, #72 ; 0x48

1adac: e3a01000 mov r1, #0

1adb0: e0299000 eor r9, r9, r0

1adb4: e1a02009 mov r2, r9

1adb8: e1a00004 mov r0, r4

1adbc: ebfffd85 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1adc0: e2843050 add r3, r4, #80 ; 0x50

1adc4: e3a01000 mov r1, #0

1adc8: e0255000 eor r5, r5, r0

1adcc: e1a02005 mov r2, r5

1add0: e1a00004 mov r0, r4

1add4: ebfffd7f bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1add8: e2843058 add r3, r4, #88 ; 0x58

1addc: e3a01000 mov r1, #0

1ade0: e0299000 eor r9, r9, r0

1ade4: e1a02009 mov r2, r9

1ade8: e1a00004 mov r0, r4

1adec: ebfffd79 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1adf0: e2843060 add r3, r4, #96 ; 0x60

1adf4: e3a01000 mov r1, #0

1adf8: e0255000 eor r5, r5, r0

1adfc: e1a02005 mov r2, r5

1ae00: e1a00004 mov r0, r4

1ae04: ebfffd73 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae08: e2843068 add r3, r4, #104 ; 0x68

1ae0c: e3a01000 mov r1, #0

1ae10: e0299000 eor r9, r9, r0

1ae14: e1a02009 mov r2, r9

1ae18: e1a00004 mov r0, r4

1ae1c: ebfffd6d bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae20: e2843070 add r3, r4, #112 ; 0x70

1ae24: e3a01000 mov r1, #0

1ae28: e025a000 eor sl, r5, r0

1ae2c: e1a0200a mov r2, sl

1ae30: e1a00004 mov r0, r4

1ae34: ebfffd67 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae38: e2843078 add r3, r4, #120 ; 0x78

1ae3c: e3a01000 mov r1, #0

1ae40: e0299000 eor r9, r9, r0

1ae44: e1a02009 mov r2, r9

1ae48: e1a00004 mov r0, r4

1ae4c: ebfffd61 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae50: e1a02009 mov r2, r9

1ae54: e28430f8 add r3, r4, #248 ; 0xf8

1ae58: e3a01001 mov r1, #1

1ae5c: e1a05000 mov r5, r0

1ae60: e1a00004 mov r0, r4

1ae64: ebfffd5b bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae68: e28430f0 add r3, r4, #240 ; 0xf0

1ae6c: e3a01001 mov r1, #1

1ae70: e0255000 eor r5, r5, r0

1ae74: e025500a eor r5, r5, sl

1ae78: e1a02005 mov r2, r5

1ae7c: e1a00004 mov r0, r4

1ae80: ebfffd54 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae84: e28430e8 add r3, r4, #232 ; 0xe8

1ae88: e3a01001 mov r1, #1

1ae8c: e0298000 eor r8, r9, r0

1ae90: e1a02008 mov r2, r8

1ae94: e1a00004 mov r0, r4

1ae98: ebfffd4e bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1ae9c: e28430e0 add r3, r4, #224 ; 0xe0

1aea0: e3a01001 mov r1, #1

1aea4: e0255000 eor r5, r5, r0

1aea8: e1a02005 mov r2, r5

1aeac: e1a00004 mov r0, r4

1aeb0: ebfffd48 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1aeb4: e28430d8 add r3, r4, #216 ; 0xd8

1aeb8: e3a01001 mov r1, #1

1aebc: e0288000 eor r8, r8, r0

1aec0: e1a02008 mov r2, r8

1aec4: e1a00004 mov r0, r4

1aec8: ebfffd42 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1aecc: e28430d0 add r3, r4, #208 ; 0xd0

1aed0: e3a01001 mov r1, #1

1aed4: e0255000 eor r5, r5, r0

1aed8: e1a02005 mov r2, r5

1aedc: e1a00004 mov r0, r4

1aee0: ebfffd3c bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1aee4: e28430c8 add r3, r4, #200 ; 0xc8

1aee8: e3a01001 mov r1, #1

1aeec: e0288000 eor r8, r8, r0

1aef0: e1a02008 mov r2, r8

1aef4: e1a00004 mov r0, r4

1aef8: ebfffd36 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1aefc: e28430c0 add r3, r4, #192 ; 0xc0

1af00: e3a01001 mov r1, #1

1af04: e0255000 eor r5, r5, r0

1af08: e1a02005 mov r2, r5

1af0c: e1a00004 mov r0, r4

1af10: ebfffd30 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1af14: e28430b8 add r3, r4, #184 ; 0xb8

1af18: e3a01001 mov r1, #1

1af1c: e0288000 eor r8, r8, r0

1af20: e1a02008 mov r2, r8

1af24: e1a00004 mov r0, r4

1af28: ebfffd2a bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1af2c: e28430b0 add r3, r4, #176 ; 0xb0

1af30: e3a01001 mov r1, #1

1af34: e0255000 eor r5, r5, r0

1af38: e1a02005 mov r2, r5

1af3c: e1a00004 mov r0, r4

1af40: ebfffd24 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1af44: e28430a8 add r3, r4, #168 ; 0xa8

1af48: e3a01001 mov r1, #1

1af4c: e0288000 eor r8, r8, r0

1af50: e1a02008 mov r2, r8

1af54: e1a00004 mov r0, r4

1af58: ebfffd1e bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1af5c: e28430a0 add r3, r4, #160 ; 0xa0

1af60: e3a01001 mov r1, #1

1af64: e0255000 eor r5, r5, r0

1af68: e1a02005 mov r2, r5

1af6c: e1a00004 mov r0, r4

1af70: ebfffd18 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1af74: e2843098 add r3, r4, #152 ; 0x98

1af78: e3a01001 mov r1, #1

1af7c: e0288000 eor r8, r8, r0

1af80: e1a02008 mov r2, r8

1af84: e1a00004 mov r0, r4

1af88: ebfffd12 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1af8c: e2843090 add r3, r4, #144 ; 0x90

1af90: e3a01001 mov r1, #1

1af94: e0255000 eor r5, r5, r0

1af98: e1a02005 mov r2, r5

1af9c: e1a00004 mov r0, r4

1afa0: ebfffd0c bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1afa4: e2843088 add r3, r4, #136 ; 0x88

1afa8: e3a01001 mov r1, #1

1afac: e0288000 eor r8, r8, r0

1afb0: e1a02008 mov r2, r8

1afb4: e1a00004 mov r0, r4

1afb8: ebfffd06 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1afbc: e2843080 add r3, r4, #128 ; 0x80

1afc0: e3a01001 mov r1, #1

1afc4: e0255000 eor r5, r5, r0

1afc8: e1a02005 mov r2, r5

1afcc: e1a00004 mov r0, r4

1afd0: ebfffd00 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1afd4: e1a02005 mov r2, r5

1afd8: e2843c01 add r3, r4, #256 ; 0x100

1afdc: e3a01002 mov r1, #2

1afe0: e1a09000 mov r9, r0

1afe4: e1a00004 mov r0, r4

1afe8: ebfffcfa bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1afec: e2843f42 add r3, r4, #264 ; 0x108

1aff0: e3a01002 mov r1, #2

1aff4: e0200009 eor r0, r0, r9

1aff8: e0288000 eor r8, r8, r0

1affc: e1a02008 mov r2, r8

1b000: e1a00004 mov r0, r4

1b004: ebfffcf3 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b008: e2843e11 add r3, r4, #272 ; 0x110

1b00c: e3a01002 mov r1, #2

1b010: e0255000 eor r5, r5, r0

1b014: e1a02005 mov r2, r5

1b018: e1a00004 mov r0, r4

1b01c: ebfffced bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b020: e2843f46 add r3, r4, #280 ; 0x118

1b024: e3a01002 mov r1, #2

1b028: e0288000 eor r8, r8, r0

1b02c: e1a02008 mov r2, r8

1b030: e1a00004 mov r0, r4

1b034: ebfffce7 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b038: e2843e12 add r3, r4, #288 ; 0x120

1b03c: e3a01002 mov r1, #2

1b040: e0255000 eor r5, r5, r0

1b044: e1a02005 mov r2, r5

1b048: e1a00004 mov r0, r4

1b04c: ebfffce1 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b050: e2843f4a add r3, r4, #296 ; 0x128

1b054: e3a01002 mov r1, #2

1b058: e0288000 eor r8, r8, r0

1b05c: e1a02008 mov r2, r8

1b060: e1a00004 mov r0, r4

1b064: ebfffcdb bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b068: e2843e13 add r3, r4, #304 ; 0x130

1b06c: e3a01002 mov r1, #2

1b070: e0255000 eor r5, r5, r0

1b074: e1a02005 mov r2, r5

1b078: e1a00004 mov r0, r4

1b07c: ebfffcd5 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b080: e2843f4e add r3, r4, #312 ; 0x138

1b084: e3a01002 mov r1, #2

1b088: e0288000 eor r8, r8, r0

1b08c: e1a02008 mov r2, r8

1b090: e1a00004 mov r0, r4

1b094: ebfffccf bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b098: e2843d05 add r3, r4, #320 ; 0x140

1b09c: e3a01002 mov r1, #2

1b0a0: e0255000 eor r5, r5, r0

1b0a4: e1a02005 mov r2, r5

1b0a8: e1a00004 mov r0, r4

1b0ac: ebfffcc9 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b0b0: e2843f52 add r3, r4, #328 ; 0x148

1b0b4: e3a01002 mov r1, #2

1b0b8: e0288000 eor r8, r8, r0

1b0bc: e1a02008 mov r2, r8

1b0c0: e1a00004 mov r0, r4

1b0c4: ebfffcc3 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b0c8: e2843e15 add r3, r4, #336 ; 0x150

1b0cc: e3a01002 mov r1, #2

1b0d0: e0255000 eor r5, r5, r0

1b0d4: e1a02005 mov r2, r5

1b0d8: e1a00004 mov r0, r4

1b0dc: ebfffcbd bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b0e0: e2843f56 add r3, r4, #344 ; 0x158

1b0e4: e3a01002 mov r1, #2

1b0e8: e0288000 eor r8, r8, r0

1b0ec: e1a02008 mov r2, r8

1b0f0: e1a00004 mov r0, r4

1b0f4: ebfffcb7 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b0f8: e2843e16 add r3, r4, #352 ; 0x160

1b0fc: e3a01002 mov r1, #2

1b100: e0255000 eor r5, r5, r0

1b104: e1a02005 mov r2, r5

1b108: e1a00004 mov r0, r4

1b10c: ebfffcb1 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b110: e2843f5a add r3, r4, #360 ; 0x168

1b114: e3a01002 mov r1, #2

1b118: e0288000 eor r8, r8, r0

1b11c: e1a02008 mov r2, r8

1b120: e1a00004 mov r0, r4

1b124: ebfffcab bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b128: e2843e17 add r3, r4, #368 ; 0x170

1b12c: e3a01002 mov r1, #2

1b130: e0255000 eor r5, r5, r0

1b134: e1a02005 mov r2, r5

1b138: e1a00004 mov r0, r4

1b13c: ebfffca5 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b140: e2843f5e add r3, r4, #376 ; 0x178

1b144: e3a01002 mov r1, #2

1b148: e0288000 eor r8, r8, r0

1b14c: e1a02008 mov r2, r8

1b150: e1a00004 mov r0, r4

1b154: ebfffc9f bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b158: e6bf8f38 rev r8, r8

1b15c: e1a02007 mov r2, r7

1b160: e2841d86 add r1, r4, #8576 ; 0x2180

1b164: e58d8004 str r8, [sp, #4]

1b168: e0255000 eor r5, r5, r0

1b16c: e6bf5f35 rev r5, r5

1b170: e1a00006 mov r0, r6

1b174: e58d5000 str r5, [sp]

1b178: ebfffd38 bl 1a660 <mcrypt\_mutex\_register@@Base+0x15ecc>

1b17c: e28dd008 add sp, sp, #8

1b180: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1b184: e92d47f0 push {r4, r5, r6, r7, r8, r9, sl, lr}

1b188: e24dd008 sub sp, sp, #8

1b18c: e1a07001 mov r7, r1

1b190: e1a0600d mov r6, sp

1b194: e1a04000 mov r4, r0

1b198: e2801d66 add r1, r0, #6528 ; 0x1980

1b19c: e1a02006 mov r2, r6

1b1a0: e1a00007 mov r0, r7

1b1a4: ebfffd2d bl 1a660 <mcrypt\_mutex\_register@@Base+0x15ecc>

1b1a8: e59d5004 ldr r5, [sp, #4]

1b1ac: e2843f5e add r3, r4, #376 ; 0x178

1b1b0: e6bf5f35 rev r5, r5

1b1b4: e1a02005 mov r2, r5

1b1b8: e1a00004 mov r0, r4

1b1bc: e3a01002 mov r1, #2

1b1c0: ebfffc84 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b1c4: e59d9000 ldr r9, [sp]

1b1c8: e2843e17 add r3, r4, #368 ; 0x170

1b1cc: e6bf9f39 rev r9, r9

1b1d0: e3a01002 mov r1, #2

1b1d4: e0299000 eor r9, r9, r0

1b1d8: e1a02009 mov r2, r9

1b1dc: e1a00004 mov r0, r4

1b1e0: ebfffc7c bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b1e4: e2843f5a add r3, r4, #360 ; 0x168

1b1e8: e3a01002 mov r1, #2

1b1ec: e0255000 eor r5, r5, r0

1b1f0: e1a02005 mov r2, r5

1b1f4: e1a00004 mov r0, r4

1b1f8: ebfffc76 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b1fc: e2843e16 add r3, r4, #352 ; 0x160

1b200: e3a01002 mov r1, #2

1b204: e0299000 eor r9, r9, r0

1b208: e1a02009 mov r2, r9

1b20c: e1a00004 mov r0, r4

1b210: ebfffc70 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b214: e2843f56 add r3, r4, #344 ; 0x158

1b218: e3a01002 mov r1, #2

1b21c: e0255000 eor r5, r5, r0

1b220: e1a02005 mov r2, r5

1b224: e1a00004 mov r0, r4

1b228: ebfffc6a bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b22c: e2843e15 add r3, r4, #336 ; 0x150

1b230: e3a01002 mov r1, #2

1b234: e0299000 eor r9, r9, r0

1b238: e1a02009 mov r2, r9

1b23c: e1a00004 mov r0, r4

1b240: ebfffc64 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b244: e2843f52 add r3, r4, #328 ; 0x148

1b248: e3a01002 mov r1, #2

1b24c: e0255000 eor r5, r5, r0

1b250: e1a02005 mov r2, r5

1b254: e1a00004 mov r0, r4

1b258: ebfffc5e bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b25c: e2843d05 add r3, r4, #320 ; 0x140

1b260: e3a01002 mov r1, #2

1b264: e0299000 eor r9, r9, r0

1b268: e1a02009 mov r2, r9

1b26c: e1a00004 mov r0, r4

1b270: ebfffc58 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b274: e2843f4e add r3, r4, #312 ; 0x138

1b278: e3a01002 mov r1, #2

1b27c: e0255000 eor r5, r5, r0

1b280: e1a02005 mov r2, r5

1b284: e1a00004 mov r0, r4

1b288: ebfffc52 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b28c: e2843e13 add r3, r4, #304 ; 0x130

1b290: e3a01002 mov r1, #2

1b294: e0299000 eor r9, r9, r0

1b298: e1a02009 mov r2, r9

1b29c: e1a00004 mov r0, r4

1b2a0: ebfffc4c bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b2a4: e2843f4a add r3, r4, #296 ; 0x128

1b2a8: e3a01002 mov r1, #2

1b2ac: e0255000 eor r5, r5, r0

1b2b0: e1a02005 mov r2, r5

1b2b4: e1a00004 mov r0, r4

1b2b8: ebfffc46 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b2bc: e2843e12 add r3, r4, #288 ; 0x120

1b2c0: e3a01002 mov r1, #2

1b2c4: e0299000 eor r9, r9, r0

1b2c8: e1a02009 mov r2, r9

1b2cc: e1a00004 mov r0, r4

1b2d0: ebfffc40 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b2d4: e2843f46 add r3, r4, #280 ; 0x118

1b2d8: e3a01002 mov r1, #2

1b2dc: e0255000 eor r5, r5, r0

1b2e0: e1a02005 mov r2, r5

1b2e4: e1a00004 mov r0, r4

1b2e8: ebfffc3a bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b2ec: e2843e11 add r3, r4, #272 ; 0x110

1b2f0: e3a01002 mov r1, #2

1b2f4: e0299000 eor r9, r9, r0

1b2f8: e1a02009 mov r2, r9

1b2fc: e1a00004 mov r0, r4

1b300: ebfffc34 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b304: e2843f42 add r3, r4, #264 ; 0x108

1b308: e3a01002 mov r1, #2

1b30c: e025a000 eor sl, r5, r0

1b310: e1a0200a mov r2, sl

1b314: e1a00004 mov r0, r4

1b318: ebfffc2e bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b31c: e2843c01 add r3, r4, #256 ; 0x100

1b320: e3a01002 mov r1, #2

1b324: e0299000 eor r9, r9, r0

1b328: e1a02009 mov r2, r9

1b32c: e1a00004 mov r0, r4

1b330: ebfffc28 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b334: e1a02009 mov r2, r9

1b338: e2843080 add r3, r4, #128 ; 0x80

1b33c: e3a01001 mov r1, #1

1b340: e1a05000 mov r5, r0

1b344: e1a00004 mov r0, r4

1b348: ebfffc22 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b34c: e2843088 add r3, r4, #136 ; 0x88

1b350: e3a01001 mov r1, #1

1b354: e0255000 eor r5, r5, r0

1b358: e025500a eor r5, r5, sl

1b35c: e1a02005 mov r2, r5

1b360: e1a00004 mov r0, r4

1b364: ebfffc1b bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b368: e2843090 add r3, r4, #144 ; 0x90

1b36c: e3a01001 mov r1, #1

1b370: e0298000 eor r8, r9, r0

1b374: e1a02008 mov r2, r8

1b378: e1a00004 mov r0, r4

1b37c: ebfffc15 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b380: e2843098 add r3, r4, #152 ; 0x98

1b384: e3a01001 mov r1, #1

1b388: e0255000 eor r5, r5, r0

1b38c: e1a02005 mov r2, r5

1b390: e1a00004 mov r0, r4

1b394: ebfffc0f bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b398: e28430a0 add r3, r4, #160 ; 0xa0

1b39c: e3a01001 mov r1, #1

1b3a0: e0288000 eor r8, r8, r0

1b3a4: e1a02008 mov r2, r8

1b3a8: e1a00004 mov r0, r4

1b3ac: ebfffc09 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b3b0: e28430a8 add r3, r4, #168 ; 0xa8

1b3b4: e3a01001 mov r1, #1

1b3b8: e0255000 eor r5, r5, r0

1b3bc: e1a02005 mov r2, r5

1b3c0: e1a00004 mov r0, r4

1b3c4: ebfffc03 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b3c8: e28430b0 add r3, r4, #176 ; 0xb0

1b3cc: e3a01001 mov r1, #1

1b3d0: e0288000 eor r8, r8, r0

1b3d4: e1a02008 mov r2, r8

1b3d8: e1a00004 mov r0, r4

1b3dc: ebfffbfd bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b3e0: e28430b8 add r3, r4, #184 ; 0xb8

1b3e4: e3a01001 mov r1, #1

1b3e8: e0255000 eor r5, r5, r0

1b3ec: e1a02005 mov r2, r5

1b3f0: e1a00004 mov r0, r4

1b3f4: ebfffbf7 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b3f8: e28430c0 add r3, r4, #192 ; 0xc0

1b3fc: e3a01001 mov r1, #1

1b400: e0288000 eor r8, r8, r0

1b404: e1a02008 mov r2, r8

1b408: e1a00004 mov r0, r4

1b40c: ebfffbf1 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b410: e28430c8 add r3, r4, #200 ; 0xc8

1b414: e3a01001 mov r1, #1

1b418: e0255000 eor r5, r5, r0

1b41c: e1a02005 mov r2, r5

1b420: e1a00004 mov r0, r4

1b424: ebfffbeb bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b428: e28430d0 add r3, r4, #208 ; 0xd0

1b42c: e3a01001 mov r1, #1

1b430: e0288000 eor r8, r8, r0

1b434: e1a02008 mov r2, r8

1b438: e1a00004 mov r0, r4

1b43c: ebfffbe5 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b440: e28430d8 add r3, r4, #216 ; 0xd8

1b444: e3a01001 mov r1, #1

1b448: e0255000 eor r5, r5, r0

1b44c: e1a02005 mov r2, r5

1b450: e1a00004 mov r0, r4

1b454: ebfffbdf bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b458: e28430e0 add r3, r4, #224 ; 0xe0

1b45c: e3a01001 mov r1, #1

1b460: e0288000 eor r8, r8, r0

1b464: e1a02008 mov r2, r8

1b468: e1a00004 mov r0, r4

1b46c: ebfffbd9 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b470: e28430e8 add r3, r4, #232 ; 0xe8

1b474: e3a01001 mov r1, #1

1b478: e0255000 eor r5, r5, r0

1b47c: e1a02005 mov r2, r5

1b480: e1a00004 mov r0, r4

1b484: ebfffbd3 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b488: e28430f0 add r3, r4, #240 ; 0xf0

1b48c: e3a01001 mov r1, #1

1b490: e0288000 eor r8, r8, r0

1b494: e1a02008 mov r2, r8

1b498: e1a00004 mov r0, r4

1b49c: ebfffbcd bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b4a0: e28430f8 add r3, r4, #248 ; 0xf8

1b4a4: e3a01001 mov r1, #1

1b4a8: e0255000 eor r5, r5, r0

1b4ac: e1a02005 mov r2, r5

1b4b0: e1a00004 mov r0, r4

1b4b4: ebfffbc7 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b4b8: e1a02005 mov r2, r5

1b4bc: e2843078 add r3, r4, #120 ; 0x78

1b4c0: e3a01000 mov r1, #0

1b4c4: e1a09000 mov r9, r0

1b4c8: e1a00004 mov r0, r4

1b4cc: ebfffbc1 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b4d0: e2843070 add r3, r4, #112 ; 0x70

1b4d4: e3a01000 mov r1, #0

1b4d8: e0200009 eor r0, r0, r9

1b4dc: e0288000 eor r8, r8, r0

1b4e0: e1a02008 mov r2, r8

1b4e4: e1a00004 mov r0, r4

1b4e8: ebfffbba bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b4ec: e2843068 add r3, r4, #104 ; 0x68

1b4f0: e3a01000 mov r1, #0

1b4f4: e0255000 eor r5, r5, r0

1b4f8: e1a02005 mov r2, r5

1b4fc: e1a00004 mov r0, r4

1b500: ebfffbb4 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b504: e2843060 add r3, r4, #96 ; 0x60

1b508: e3a01000 mov r1, #0

1b50c: e0288000 eor r8, r8, r0

1b510: e1a02008 mov r2, r8

1b514: e1a00004 mov r0, r4

1b518: ebfffbae bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b51c: e2843058 add r3, r4, #88 ; 0x58

1b520: e3a01000 mov r1, #0

1b524: e0255000 eor r5, r5, r0

1b528: e1a02005 mov r2, r5

1b52c: e1a00004 mov r0, r4

1b530: ebfffba8 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b534: e2843050 add r3, r4, #80 ; 0x50

1b538: e3a01000 mov r1, #0

1b53c: e0288000 eor r8, r8, r0

1b540: e1a02008 mov r2, r8

1b544: e1a00004 mov r0, r4

1b548: ebfffba2 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b54c: e2843048 add r3, r4, #72 ; 0x48

1b550: e3a01000 mov r1, #0

1b554: e0255000 eor r5, r5, r0

1b558: e1a02005 mov r2, r5

1b55c: e1a00004 mov r0, r4

1b560: ebfffb9c bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b564: e2843040 add r3, r4, #64 ; 0x40

1b568: e3a01000 mov r1, #0

1b56c: e0288000 eor r8, r8, r0

1b570: e1a02008 mov r2, r8

1b574: e1a00004 mov r0, r4

1b578: ebfffb96 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b57c: e2843038 add r3, r4, #56 ; 0x38

1b580: e3a01000 mov r1, #0

1b584: e0255000 eor r5, r5, r0

1b588: e1a02005 mov r2, r5

1b58c: e1a00004 mov r0, r4

1b590: ebfffb90 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b594: e2843030 add r3, r4, #48 ; 0x30

1b598: e3a01000 mov r1, #0

1b59c: e0288000 eor r8, r8, r0

1b5a0: e1a02008 mov r2, r8

1b5a4: e1a00004 mov r0, r4

1b5a8: ebfffb8a bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b5ac: e2843028 add r3, r4, #40 ; 0x28

1b5b0: e3a01000 mov r1, #0

1b5b4: e0255000 eor r5, r5, r0

1b5b8: e1a02005 mov r2, r5

1b5bc: e1a00004 mov r0, r4

1b5c0: ebfffb84 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b5c4: e2843020 add r3, r4, #32

1b5c8: e3a01000 mov r1, #0

1b5cc: e0288000 eor r8, r8, r0

1b5d0: e1a02008 mov r2, r8

1b5d4: e1a00004 mov r0, r4

1b5d8: ebfffb7e bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b5dc: e2843018 add r3, r4, #24

1b5e0: e3a01000 mov r1, #0

1b5e4: e0255000 eor r5, r5, r0

1b5e8: e1a02005 mov r2, r5

1b5ec: e1a00004 mov r0, r4

1b5f0: ebfffb78 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b5f4: e2843010 add r3, r4, #16

1b5f8: e3a01000 mov r1, #0

1b5fc: e0288000 eor r8, r8, r0

1b600: e1a02008 mov r2, r8

1b604: e1a00004 mov r0, r4

1b608: ebfffb72 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b60c: e2843008 add r3, r4, #8

1b610: e3a01000 mov r1, #0

1b614: e0255000 eor r5, r5, r0

1b618: e1a02005 mov r2, r5

1b61c: e1a00004 mov r0, r4

1b620: ebfffb6c bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b624: e1a03004 mov r3, r4

1b628: e3a01000 mov r1, #0

1b62c: e0288000 eor r8, r8, r0

1b630: e1a02008 mov r2, r8

1b634: e1a00004 mov r0, r4

1b638: ebfffb66 bl 1a3d8 <mcrypt\_mutex\_register@@Base+0x15c44>

1b63c: e6bf8f38 rev r8, r8

1b640: e1a02007 mov r2, r7

1b644: e2841d86 add r1, r4, #8576 ; 0x2180

1b648: e58d8004 str r8, [sp, #4]

1b64c: e0255000 eor r5, r5, r0

1b650: e6bf5f35 rev r5, r5

1b654: e1a00006 mov r0, r6

1b658: e58d5000 str r5, [sp]

1b65c: ebfffbff bl 1a660 <mcrypt\_mutex\_register@@Base+0x15ecc>

1b660: e28dd008 add sp, sp, #8

1b664: e8bd87f0 pop {r4, r5, r6, r7, r8, r9, sl, pc}

1b668: e3a00da6 mov r0, #10624 ; 0x2980

1b66c: e12fff1e bx lr

1b670: e3a00008 mov r0, #8

1b674: e12fff1e bx lr

1b678: e3a00001 mov r0, #1

1b67c: e12fff1e bx lr

1b680: e3a00018 mov r0, #24

1b684: e12fff1e bx lr

1b688: e59f3010 ldr r3, [pc, #16] ; 1b6a0 <mcrypt\_mutex\_register@@Base+0x16f0c>

1b68c: e3a02001 mov r2, #1

1b690: e08f3003 add r3, pc, r3

1b694: e5802000 str r2, [r0]

1b698: e2830fb2 add r0, r3, #712 ; 0x2c8

1b69c: e12fff1e bx lr

1b6a0: 00007958 andeq r7, r0, r8, asr r9

1b6a4: e59f0004 ldr r0, [pc, #4] ; 1b6b0 <mcrypt\_mutex\_register@@Base+0x16f1c>

1b6a8: e08f0000 add r0, pc, r0

1b6ac: e12fff1e bx lr

1b6b0: 00007c0c andeq r7, r0, ip, lsl #24

1b6b4: e92d4ff0 push {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1b6b8: e24dd0f4 sub sp, sp, #244 ; 0xf4

1b6bc: ebffffeb bl 1b670 <mcrypt\_mutex\_register@@Base+0x16edc>

1b6c0: e1a04000 mov r4, r0

1b6c4: ebffffed bl 1b680 <mcrypt\_mutex\_register@@Base+0x16eec>

1b6c8: e1a01000 mov r1, r0

1b6cc: e3a00001 mov r0, #1

1b6d0: ebff9ad7 bl 2234 <calloc@plt>

1b6d4: e2506000 subs r6, r0, #0

1b6d8: 0a000073 beq 1b8ac <mcrypt\_mutex\_register@@Base+0x17118>

1b6dc: e2467001 sub r7, r6, #1

1b6e0: e1a09007 mov r9, r7

1b6e4: e3a0500a mov r5, #10

1b6e8: e2668001 rsb r8, r6, #1

1b6ec: ea000004 b 1b704 <mcrypt\_mutex\_register@@Base+0x16f70>

1b6f0: e2752000 rsbs r2, r5, #0

1b6f4: e2855002 add r5, r5, #2

1b6f8: e6ef2072 uxtb r2, r2

1b6fc: 52623000 rsbpl r3, r2, #0

1b700: e5e93001 strb r3, [r9, #1]!

1b704: ebffffdd bl 1b680 <mcrypt\_mutex\_register@@Base+0x16eec>

1b708: e0882009 add r2, r8, r9

1b70c: e6ef3075 uxtb r3, r5

1b710: e1500002 cmp r0, r2

1b714: cafffff5 bgt 1b6f0 <mcrypt\_mutex\_register@@Base+0x16f5c>

1b718: e3540000 cmp r4, #0

1b71c: da000005 ble 1b738 <mcrypt\_mutex\_register@@Base+0x16fa4>

1b720: e28d2008 add r2, sp, #8

1b724: e3a03000 mov r3, #0

1b728: e4c23001 strb r3, [r2], #1

1b72c: e2833001 add r3, r3, #1

1b730: e1540003 cmp r4, r3

1b734: 1afffffb bne 1b728 <mcrypt\_mutex\_register@@Base+0x16f94>

1b738: e3a05000 mov r5, #0

1b73c: ea000001 b 1b748 <mcrypt\_mutex\_register@@Base+0x16fb4>

1b740: e5e75001 strb r5, [r7, #1]!

1b744: e2855001 add r5, r5, #1

1b748: ebffffcc bl 1b680 <mcrypt\_mutex\_register@@Base+0x16eec>

1b74c: e1500005 cmp r0, r5

1b750: cafffffa bgt 1b740 <mcrypt\_mutex\_register@@Base+0x16fac>

1b754: ebffffc3 bl 1b668 <mcrypt\_mutex\_register@@Base+0x16ed4>

1b758: ebff9ae5 bl 22f4 <malloc@plt>

1b75c: e250a000 subs sl, r0, #0

1b760: 0a000051 beq 1b8ac <mcrypt\_mutex\_register@@Base+0x17118>

1b764: e28d9018 add r9, sp, #24

1b768: e28d1008 add r1, sp, #8

1b76c: e1a02004 mov r2, r4

1b770: e1a00009 mov r0, r9

1b774: e58d1000 str r1, [sp]

1b778: ebff9ac2 bl 2288 <memcpy@plt>

1b77c: ebffffbf bl 1b680 <mcrypt\_mutex\_register@@Base+0x16eec>

1b780: e1a01006 mov r1, r6

1b784: e1a02000 mov r2, r0

1b788: e1a0000a mov r0, sl

1b78c: ebfffc06 bl 1a7ac <mcrypt\_mutex\_register@@Base+0x16018>

1b790: e1a00006 mov r0, r6

1b794: ebff9ab8 bl 227c <free@plt>

1b798: e1a01009 mov r1, r9

1b79c: e1a0000a mov r0, sl

1b7a0: ebfffd3e bl 1aca0 <mcrypt\_mutex\_register@@Base+0x1650c>

1b7a4: e28d3028 add r3, sp, #40 ; 0x28

1b7a8: e3540000 cmp r4, #0

1b7ac: e58d3004 str r3, [sp, #4]

1b7b0: da000026 ble 1b850 <mcrypt\_mutex\_register@@Base+0x170bc>

1b7b4: e59f8110 ldr r8, [pc, #272] ; 1b8cc <mcrypt\_mutex\_register@@Base+0x17138>

1b7b8: e0894004 add r4, r9, r4

1b7bc: e08f8008 add r8, pc, r8

1b7c0: e1a07003 mov r7, r3

1b7c4: e1a0b003 mov fp, r3

1b7c8: e1a06009 mov r6, r9

1b7cc: e1a05009 mov r5, r9

1b7d0: e4d52001 ldrb r2, [r5], #1

1b7d4: e1a0000b mov r0, fp

1b7d8: e1a01008 mov r1, r8

1b7dc: ebff9ae2 bl 236c <sprintf@plt>

1b7e0: e1540005 cmp r4, r5

1b7e4: e28bb002 add fp, fp, #2

1b7e8: 1afffff8 bne 1b7d0 <mcrypt\_mutex\_register@@Base+0x1703c>

1b7ec: e59f10dc ldr r1, [pc, #220] ; 1b8d0 <mcrypt\_mutex\_register@@Base+0x1713c>

1b7f0: e59d0004 ldr r0, [sp, #4]

1b7f4: e08f1001 add r1, pc, r1

1b7f8: ebff9a93 bl 224c <strcmp@plt>

1b7fc: e3500000 cmp r0, #0

1b800: 1a00001e bne 1b880 <mcrypt\_mutex\_register@@Base+0x170ec>

1b804: e1a0000a mov r0, sl

1b808: e1a01009 mov r1, r9

1b80c: ebfffe5c bl 1b184 <mcrypt\_mutex\_register@@Base+0x169f0>

1b810: e1a0000a mov r0, sl

1b814: ebff9a98 bl 227c <free@plt>

1b818: e4d62001 ldrb r2, [r6], #1

1b81c: e1a00007 mov r0, r7

1b820: e1a01008 mov r1, r8

1b824: ebff9ad0 bl 236c <sprintf@plt>

1b828: e1560004 cmp r6, r4

1b82c: e2877002 add r7, r7, #2

1b830: 1afffff8 bne 1b818 <mcrypt\_mutex\_register@@Base+0x17084>

1b834: e59d1000 ldr r1, [sp]

1b838: e1a00009 mov r0, r9

1b83c: ebff9a82 bl 224c <strcmp@plt>

1b840: e3500000 cmp r0, #0

1b844: 1a00001a bne 1b8b4 <mcrypt\_mutex\_register@@Base+0x17120>

1b848: e28dd0f4 add sp, sp, #244 ; 0xf4

1b84c: e8bd8ff0 pop {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1b850: e59f107c ldr r1, [pc, #124] ; 1b8d4 <mcrypt\_mutex\_register@@Base+0x17140>

1b854: e1a00003 mov r0, r3

1b858: e08f1001 add r1, pc, r1

1b85c: ebff9a7a bl 224c <strcmp@plt>

1b860: e3500000 cmp r0, #0

1b864: 1a000005 bne 1b880 <mcrypt\_mutex\_register@@Base+0x170ec>

1b868: e1a0000a mov r0, sl

1b86c: e1a01009 mov r1, r9

1b870: ebfffe43 bl 1b184 <mcrypt\_mutex\_register@@Base+0x169f0>

1b874: e1a0000a mov r0, sl

1b878: ebff9a7f bl 227c <free@plt>

1b87c: eaffffec b 1b834 <mcrypt\_mutex\_register@@Base+0x170a0>

1b880: e59f0050 ldr r0, [pc, #80] ; 1b8d8 <mcrypt\_mutex\_register@@Base+0x17144>

1b884: e08f0000 add r0, pc, r0

1b888: ebff9a96 bl 22e8 <puts@plt>

1b88c: e59f1048 ldr r1, [pc, #72] ; 1b8dc <mcrypt\_mutex\_register@@Base+0x17148>

1b890: e59f0048 ldr r0, [pc, #72] ; 1b8e0 <mcrypt\_mutex\_register@@Base+0x1714c>

1b894: e59d2004 ldr r2, [sp, #4]

1b898: e08f1001 add r1, pc, r1

1b89c: e08f0000 add r0, pc, r0

1b8a0: ebff9a6f bl 2264 <printf@plt>

1b8a4: e1a0000a mov r0, sl

1b8a8: ebff9a73 bl 227c <free@plt>

1b8ac: e3e00000 mvn r0, #0

1b8b0: eaffffe4 b 1b848 <mcrypt\_mutex\_register@@Base+0x170b4>

1b8b4: e59f0028 ldr r0, [pc, #40] ; 1b8e4 <mcrypt\_mutex\_register@@Base+0x17150>

1b8b8: e59d1004 ldr r1, [sp, #4]

1b8bc: e08f0000 add r0, pc, r0

1b8c0: ebff9a67 bl 2264 <printf@plt>

1b8c4: e3e00000 mvn r0, #0

1b8c8: eaffffde b 1b848 <mcrypt\_mutex\_register@@Base+0x170b4>

1b8cc: 00004ea4 andeq r4, r0, r4, lsr #29

1b8d0: 00007ac8 andeq r7, r0, r8, asr #21

1b8d4: 00007a64 andeq r7, r0, r4, ror #20

1b8d8: 00004df8 strdeq r4, [r0], -r8

1b8dc: 00007a24 andeq r7, r0, r4, lsr #20

1b8e0: 00004df8 strdeq r4, [r0], -r8

1b8e4: 00007a14 andeq r7, r0, r4, lsl sl

1b8e8: e59f0000 ldr r0, [pc] ; 1b8f0 <mcrypt\_mutex\_register@@Base+0x1715c>

1b8ec: e12fff1e bx lr

1b8f0: 01310a59 teqeq r1, r9, asr sl

1b8f4: e2512001 subs r2, r1, #1

1b8f8: 012fff1e bxeq lr

1b8fc: 3a000074 bcc 1bad4 <mcrypt\_mutex\_register@@Base+0x17340>

1b900: e1500001 cmp r0, r1

1b904: 9a00006b bls 1bab8 <mcrypt\_mutex\_register@@Base+0x17324>

1b908: e1110002 tst r1, r2

1b90c: 0a00006c beq 1bac4 <mcrypt\_mutex\_register@@Base+0x17330>

1b910: e16f3f10 clz r3, r0

1b914: e16f2f11 clz r2, r1

1b918: e0423003 sub r3, r2, r3

1b91c: e273301f rsbs r3, r3, #31

1b920: 10833083 addne r3, r3, r3, lsl #1

1b924: e3a02000 mov r2, #0

1b928: 108ff103 addne pc, pc, r3, lsl #2

1b92c: e1a00000 nop ; (mov r0, r0)

1b930: e1500f81 cmp r0, r1, lsl #31

1b934: e0a22002 adc r2, r2, r2

1b938: 20400f81 subcs r0, r0, r1, lsl #31

1b93c: e1500f01 cmp r0, r1, lsl #30

1b940: e0a22002 adc r2, r2, r2

1b944: 20400f01 subcs r0, r0, r1, lsl #30

1b948: e1500e81 cmp r0, r1, lsl #29

1b94c: e0a22002 adc r2, r2, r2

1b950: 20400e81 subcs r0, r0, r1, lsl #29

1b954: e1500e01 cmp r0, r1, lsl #28

1b958: e0a22002 adc r2, r2, r2

1b95c: 20400e01 subcs r0, r0, r1, lsl #28

1b960: e1500d81 cmp r0, r1, lsl #27

1b964: e0a22002 adc r2, r2, r2

1b968: 20400d81 subcs r0, r0, r1, lsl #27

1b96c: e1500d01 cmp r0, r1, lsl #26

1b970: e0a22002 adc r2, r2, r2

1b974: 20400d01 subcs r0, r0, r1, lsl #26

1b978: e1500c81 cmp r0, r1, lsl #25

1b97c: e0a22002 adc r2, r2, r2

1b980: 20400c81 subcs r0, r0, r1, lsl #25

1b984: e1500c01 cmp r0, r1, lsl #24

1b988: e0a22002 adc r2, r2, r2

1b98c: 20400c01 subcs r0, r0, r1, lsl #24

1b990: e1500b81 cmp r0, r1, lsl #23

1b994: e0a22002 adc r2, r2, r2

1b998: 20400b81 subcs r0, r0, r1, lsl #23

1b99c: e1500b01 cmp r0, r1, lsl #22

1b9a0: e0a22002 adc r2, r2, r2

1b9a4: 20400b01 subcs r0, r0, r1, lsl #22

1b9a8: e1500a81 cmp r0, r1, lsl #21

1b9ac: e0a22002 adc r2, r2, r2

1b9b0: 20400a81 subcs r0, r0, r1, lsl #21

1b9b4: e1500a01 cmp r0, r1, lsl #20

1b9b8: e0a22002 adc r2, r2, r2

1b9bc: 20400a01 subcs r0, r0, r1, lsl #20

1b9c0: e1500981 cmp r0, r1, lsl #19

1b9c4: e0a22002 adc r2, r2, r2

1b9c8: 20400981 subcs r0, r0, r1, lsl #19

1b9cc: e1500901 cmp r0, r1, lsl #18

1b9d0: e0a22002 adc r2, r2, r2

1b9d4: 20400901 subcs r0, r0, r1, lsl #18

1b9d8: e1500881 cmp r0, r1, lsl #17

1b9dc: e0a22002 adc r2, r2, r2

1b9e0: 20400881 subcs r0, r0, r1, lsl #17

1b9e4: e1500801 cmp r0, r1, lsl #16

1b9e8: e0a22002 adc r2, r2, r2

1b9ec: 20400801 subcs r0, r0, r1, lsl #16

1b9f0: e1500781 cmp r0, r1, lsl #15

1b9f4: e0a22002 adc r2, r2, r2

1b9f8: 20400781 subcs r0, r0, r1, lsl #15

1b9fc: e1500701 cmp r0, r1, lsl #14

1ba00: e0a22002 adc r2, r2, r2

1ba04: 20400701 subcs r0, r0, r1, lsl #14

1ba08: e1500681 cmp r0, r1, lsl #13

1ba0c: e0a22002 adc r2, r2, r2

1ba10: 20400681 subcs r0, r0, r1, lsl #13

1ba14: e1500601 cmp r0, r1, lsl #12

1ba18: e0a22002 adc r2, r2, r2

1ba1c: 20400601 subcs r0, r0, r1, lsl #12

1ba20: e1500581 cmp r0, r1, lsl #11

1ba24: e0a22002 adc r2, r2, r2

1ba28: 20400581 subcs r0, r0, r1, lsl #11

1ba2c: e1500501 cmp r0, r1, lsl #10

1ba30: e0a22002 adc r2, r2, r2

1ba34: 20400501 subcs r0, r0, r1, lsl #10

1ba38: e1500481 cmp r0, r1, lsl #9

1ba3c: e0a22002 adc r2, r2, r2

1ba40: 20400481 subcs r0, r0, r1, lsl #9

1ba44: e1500401 cmp r0, r1, lsl #8

1ba48: e0a22002 adc r2, r2, r2

1ba4c: 20400401 subcs r0, r0, r1, lsl #8

1ba50: e1500381 cmp r0, r1, lsl #7

1ba54: e0a22002 adc r2, r2, r2

1ba58: 20400381 subcs r0, r0, r1, lsl #7

1ba5c: e1500301 cmp r0, r1, lsl #6

1ba60: e0a22002 adc r2, r2, r2

1ba64: 20400301 subcs r0, r0, r1, lsl #6

1ba68: e1500281 cmp r0, r1, lsl #5

1ba6c: e0a22002 adc r2, r2, r2

1ba70: 20400281 subcs r0, r0, r1, lsl #5

1ba74: e1500201 cmp r0, r1, lsl #4

1ba78: e0a22002 adc r2, r2, r2

1ba7c: 20400201 subcs r0, r0, r1, lsl #4

1ba80: e1500181 cmp r0, r1, lsl #3

1ba84: e0a22002 adc r2, r2, r2

1ba88: 20400181 subcs r0, r0, r1, lsl #3

1ba8c: e1500101 cmp r0, r1, lsl #2

1ba90: e0a22002 adc r2, r2, r2

1ba94: 20400101 subcs r0, r0, r1, lsl #2

1ba98: e1500081 cmp r0, r1, lsl #1

1ba9c: e0a22002 adc r2, r2, r2

1baa0: 20400081 subcs r0, r0, r1, lsl #1

1baa4: e1500001 cmp r0, r1

1baa8: e0a22002 adc r2, r2, r2

1baac: 20400001 subcs r0, r0, r1

1bab0: e1a00002 mov r0, r2

1bab4: e12fff1e bx lr

1bab8: 03a00001 moveq r0, #1

1babc: 13a00000 movne r0, #0

1bac0: e12fff1e bx lr

1bac4: e16f2f11 clz r2, r1

1bac8: e262201f rsb r2, r2, #31

1bacc: e1a00230 lsr r0, r0, r2

1bad0: e12fff1e bx lr

1bad4: e3500000 cmp r0, #0

1bad8: 13e00000 mvnne r0, #0

1badc: ea000097 b 1bd40 <mcrypt\_mutex\_register@@Base+0x175ac>

1bae0: e3510000 cmp r1, #0

1bae4: 0afffffa beq 1bad4 <mcrypt\_mutex\_register@@Base+0x17340>

1bae8: e92d4003 push {r0, r1, lr}

1baec: ebffff80 bl 1b8f4 <mcrypt\_mutex\_register@@Base+0x17160>

1baf0: e8bd4006 pop {r1, r2, lr}

1baf4: e0030092 mul r3, r2, r0

1baf8: e0411003 sub r1, r1, r3

1bafc: e12fff1e bx lr

1bb00: e3510000 cmp r1, #0

1bb04: 0a000081 beq 1bd10 <mcrypt\_mutex\_register@@Base+0x1757c>

1bb08: e020c001 eor ip, r0, r1

1bb0c: 42611000 rsbmi r1, r1, #0

1bb10: e2512001 subs r2, r1, #1

1bb14: 0a000070 beq 1bcdc <mcrypt\_mutex\_register@@Base+0x17548>

1bb18: e1b03000 movs r3, r0

1bb1c: 42603000 rsbmi r3, r0, #0

1bb20: e1530001 cmp r3, r1

1bb24: 9a00006f bls 1bce8 <mcrypt\_mutex\_register@@Base+0x17554>

1bb28: e1110002 tst r1, r2

1bb2c: 0a000071 beq 1bcf8 <mcrypt\_mutex\_register@@Base+0x17564>

1bb30: e16f2f13 clz r2, r3

1bb34: e16f0f11 clz r0, r1

1bb38: e0402002 sub r2, r0, r2

1bb3c: e272201f rsbs r2, r2, #31

1bb40: 10822082 addne r2, r2, r2, lsl #1

1bb44: e3a00000 mov r0, #0

1bb48: 108ff102 addne pc, pc, r2, lsl #2

1bb4c: e1a00000 nop ; (mov r0, r0)

1bb50: e1530f81 cmp r3, r1, lsl #31

1bb54: e0a00000 adc r0, r0, r0

1bb58: 20433f81 subcs r3, r3, r1, lsl #31

1bb5c: e1530f01 cmp r3, r1, lsl #30

1bb60: e0a00000 adc r0, r0, r0

1bb64: 20433f01 subcs r3, r3, r1, lsl #30

1bb68: e1530e81 cmp r3, r1, lsl #29

1bb6c: e0a00000 adc r0, r0, r0

1bb70: 20433e81 subcs r3, r3, r1, lsl #29

1bb74: e1530e01 cmp r3, r1, lsl #28

1bb78: e0a00000 adc r0, r0, r0

1bb7c: 20433e01 subcs r3, r3, r1, lsl #28

1bb80: e1530d81 cmp r3, r1, lsl #27

1bb84: e0a00000 adc r0, r0, r0

1bb88: 20433d81 subcs r3, r3, r1, lsl #27

1bb8c: e1530d01 cmp r3, r1, lsl #26

1bb90: e0a00000 adc r0, r0, r0

1bb94: 20433d01 subcs r3, r3, r1, lsl #26

1bb98: e1530c81 cmp r3, r1, lsl #25

1bb9c: e0a00000 adc r0, r0, r0

1bba0: 20433c81 subcs r3, r3, r1, lsl #25

1bba4: e1530c01 cmp r3, r1, lsl #24

1bba8: e0a00000 adc r0, r0, r0

1bbac: 20433c01 subcs r3, r3, r1, lsl #24

1bbb0: e1530b81 cmp r3, r1, lsl #23

1bbb4: e0a00000 adc r0, r0, r0

1bbb8: 20433b81 subcs r3, r3, r1, lsl #23

1bbbc: e1530b01 cmp r3, r1, lsl #22

1bbc0: e0a00000 adc r0, r0, r0

1bbc4: 20433b01 subcs r3, r3, r1, lsl #22

1bbc8: e1530a81 cmp r3, r1, lsl #21

1bbcc: e0a00000 adc r0, r0, r0

1bbd0: 20433a81 subcs r3, r3, r1, lsl #21

1bbd4: e1530a01 cmp r3, r1, lsl #20

1bbd8: e0a00000 adc r0, r0, r0

1bbdc: 20433a01 subcs r3, r3, r1, lsl #20

1bbe0: e1530981 cmp r3, r1, lsl #19

1bbe4: e0a00000 adc r0, r0, r0

1bbe8: 20433981 subcs r3, r3, r1, lsl #19

1bbec: e1530901 cmp r3, r1, lsl #18

1bbf0: e0a00000 adc r0, r0, r0

1bbf4: 20433901 subcs r3, r3, r1, lsl #18

1bbf8: e1530881 cmp r3, r1, lsl #17

1bbfc: e0a00000 adc r0, r0, r0

1bc00: 20433881 subcs r3, r3, r1, lsl #17

1bc04: e1530801 cmp r3, r1, lsl #16

1bc08: e0a00000 adc r0, r0, r0

1bc0c: 20433801 subcs r3, r3, r1, lsl #16

1bc10: e1530781 cmp r3, r1, lsl #15

1bc14: e0a00000 adc r0, r0, r0

1bc18: 20433781 subcs r3, r3, r1, lsl #15

1bc1c: e1530701 cmp r3, r1, lsl #14

1bc20: e0a00000 adc r0, r0, r0

1bc24: 20433701 subcs r3, r3, r1, lsl #14

1bc28: e1530681 cmp r3, r1, lsl #13

1bc2c: e0a00000 adc r0, r0, r0

1bc30: 20433681 subcs r3, r3, r1, lsl #13

1bc34: e1530601 cmp r3, r1, lsl #12

1bc38: e0a00000 adc r0, r0, r0

1bc3c: 20433601 subcs r3, r3, r1, lsl #12

1bc40: e1530581 cmp r3, r1, lsl #11

1bc44: e0a00000 adc r0, r0, r0

1bc48: 20433581 subcs r3, r3, r1, lsl #11

1bc4c: e1530501 cmp r3, r1, lsl #10

1bc50: e0a00000 adc r0, r0, r0

1bc54: 20433501 subcs r3, r3, r1, lsl #10

1bc58: e1530481 cmp r3, r1, lsl #9

1bc5c: e0a00000 adc r0, r0, r0

1bc60: 20433481 subcs r3, r3, r1, lsl #9

1bc64: e1530401 cmp r3, r1, lsl #8

1bc68: e0a00000 adc r0, r0, r0

1bc6c: 20433401 subcs r3, r3, r1, lsl #8

1bc70: e1530381 cmp r3, r1, lsl #7

1bc74: e0a00000 adc r0, r0, r0

1bc78: 20433381 subcs r3, r3, r1, lsl #7

1bc7c: e1530301 cmp r3, r1, lsl #6

1bc80: e0a00000 adc r0, r0, r0

1bc84: 20433301 subcs r3, r3, r1, lsl #6

1bc88: e1530281 cmp r3, r1, lsl #5

1bc8c: e0a00000 adc r0, r0, r0

1bc90: 20433281 subcs r3, r3, r1, lsl #5

1bc94: e1530201 cmp r3, r1, lsl #4

1bc98: e0a00000 adc r0, r0, r0

1bc9c: 20433201 subcs r3, r3, r1, lsl #4

1bca0: e1530181 cmp r3, r1, lsl #3

1bca4: e0a00000 adc r0, r0, r0

1bca8: 20433181 subcs r3, r3, r1, lsl #3

1bcac: e1530101 cmp r3, r1, lsl #2

1bcb0: e0a00000 adc r0, r0, r0

1bcb4: 20433101 subcs r3, r3, r1, lsl #2

1bcb8: e1530081 cmp r3, r1, lsl #1

1bcbc: e0a00000 adc r0, r0, r0

1bcc0: 20433081 subcs r3, r3, r1, lsl #1

1bcc4: e1530001 cmp r3, r1

1bcc8: e0a00000 adc r0, r0, r0

1bccc: 20433001 subcs r3, r3, r1

1bcd0: e35c0000 cmp ip, #0

1bcd4: 42600000 rsbmi r0, r0, #0

1bcd8: e12fff1e bx lr

1bcdc: e13c0000 teq ip, r0

1bce0: 42600000 rsbmi r0, r0, #0

1bce4: e12fff1e bx lr

1bce8: 33a00000 movcc r0, #0

1bcec: 01a00fcc asreq r0, ip, #31

1bcf0: 03800001 orreq r0, r0, #1

1bcf4: e12fff1e bx lr

1bcf8: e16f2f11 clz r2, r1

1bcfc: e262201f rsb r2, r2, #31

1bd00: e35c0000 cmp ip, #0

1bd04: e1a00233 lsr r0, r3, r2

1bd08: 42600000 rsbmi r0, r0, #0

1bd0c: e12fff1e bx lr

1bd10: e3500000 cmp r0, #0

1bd14: c3e00102 mvngt r0, #-2147483648 ; 0x80000000

1bd18: b3a00102 movlt r0, #-2147483648 ; 0x80000000

1bd1c: ea000007 b 1bd40 <mcrypt\_mutex\_register@@Base+0x175ac>

1bd20: e3510000 cmp r1, #0

1bd24: 0afffff9 beq 1bd10 <mcrypt\_mutex\_register@@Base+0x1757c>

1bd28: e92d4003 push {r0, r1, lr}

1bd2c: ebffff75 bl 1bb08 <mcrypt\_mutex\_register@@Base+0x17374>

1bd30: e8bd4006 pop {r1, r2, lr}

1bd34: e0030092 mul r3, r2, r0

1bd38: e0411003 sub r1, r1, r3

1bd3c: e12fff1e bx lr

1bd40: e92d4002 push {r1, lr}

1bd44: e3a00008 mov r0, #8

1bd48: ebff993c bl 2240 <raise@plt>

1bd4c: e8bd8002 pop {r1, pc}

Disassembly of section .fini:

0001bd50 <.fini>:

1bd50: e92d4008 push {r3, lr}

1bd54: e8bd8008 pop {r3, pc}