/lib/arm-linux-gnueabihf/ld-2.28.so: file format elf32-littlearm

Disassembly of section .plt:

000009a8 <\_dl\_catch\_exception@plt-0x14>:

9a8: e52de004 push {lr} ; (str lr, [sp, #-4]!)

9ac: e59fe004 ldr lr, [pc, #4] ; 9b8 <GLIBC\_2.4@@GLIBC\_2.4+0x9b8>

9b0: e08fe00e add lr, pc, lr

9b4: e5bef008 ldr pc, [lr, #8]!

9b8: 00028648 andeq r8, r2, r8, asr #12

000009bc <\_dl\_catch\_exception@plt>:

9bc: e28fc600 add ip, pc, #0, 12

9c0: e28cca28 add ip, ip, #40, 20 ; 0x28000

9c4: e5bcf648 ldr pc, [ip, #1608]! ; 0x648

000009c8 <malloc@plt>:

9c8: 46c04778 ; <UNDEFINED> instruction: 0x46c04778

9cc: e28fc600 add ip, pc, #0, 12

9d0: e28cca28 add ip, ip, #40, 20 ; 0x28000

9d4: e5bcf63c ldr pc, [ip, #1596]! ; 0x63c

000009d8 <\_dl\_signal\_exception@plt>:

9d8: e28fc600 add ip, pc, #0, 12

9dc: e28cca28 add ip, ip, #40, 20 ; 0x28000

9e0: e5bcf634 ldr pc, [ip, #1588]! ; 0x634

000009e4 <\_\_tls\_get\_addr@plt>:

9e4: e28fc600 add ip, pc, #0, 12

9e8: e28cca28 add ip, ip, #40, 20 ; 0x28000

9ec: e5bcf62c ldr pc, [ip, #1580]! ; 0x62c

000009f0 <calloc@plt>:

9f0: e28fc600 add ip, pc, #0, 12

9f4: e28cca28 add ip, ip, #40, 20 ; 0x28000

9f8: e5bcf624 ldr pc, [ip, #1572]! ; 0x624

000009fc <realloc@plt>:

9fc: e28fc600 add ip, pc, #0, 12

a00: e28cca28 add ip, ip, #40, 20 ; 0x28000

a04: e5bcf61c ldr pc, [ip, #1564]! ; 0x61c

00000a08 <\_dl\_signal\_error@plt>:

a08: e28fc600 add ip, pc, #0, 12

a0c: e28cca28 add ip, ip, #40, 20 ; 0x28000

a10: e5bcf614 ldr pc, [ip, #1556]! ; 0x614

00000a14 <\_dl\_catch\_error@plt>:

a14: e28fc600 add ip, pc, #0, 12

a18: e28cca28 add ip, ip, #40, 20 ; 0x28000

a1c: e5bcf60c ldr pc, [ip, #1548]! ; 0x60c

00000a20 <free@plt>:

a20: 46c04778 ; <UNDEFINED> instruction: 0x46c04778

a24: e28fc600 add ip, pc, #0, 12

a28: e28cca28 add ip, ip, #40, 20 ; 0x28000

a2c: e5bcf600 ldr pc, [ip, #1536]! ; 0x600

Disassembly of section .text:

00000a40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE-0x6254>:

a40: e59fa094 ldr sl, [pc, #148] ; adc <free@plt+0xbc>

a44: e59f4094 ldr r4, [pc, #148] ; ae0 <free@plt+0xc0>

a48: e1a0000d mov r0, sp

a4c: fa00017e blx 104c <free@plt+0x62c>

a50: e28f6084 add r6, pc, #132 ; 0x84

a54: e08aa006 add sl, sl, r6

a58: e79a4004 ldr r4, [sl, r4]

a5c: e1a06000 mov r6, r0

a60: e59d1000 ldr r1, [sp]

a64: e28d2004 add r2, sp, #4

a68: e3540000 cmp r4, #0

a6c: 1a000007 bne a90 <free@plt+0x70>

a70: e0823101 add r3, r2, r1, lsl #2

a74: e2833004 add r3, r3, #4

a78: e59f006c ldr r0, [pc, #108] ; aec <free@plt+0xcc>

a7c: e79a0000 ldr r0, [sl, r0]

a80: fa002ad7 blx b5e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4950>

a84: e59f0058 ldr r0, [pc, #88] ; ae4 <free@plt+0xc4>

a88: e08a0000 add r0, sl, r0

a8c: e12fff16 bx r6

a90: e0411004 sub r1, r1, r4

a94: e58d1000 str r1, [sp]

a98: e1a03002 mov r3, r2

a9c: e0824104 add r4, r2, r4, lsl #2

aa0: e4945004 ldr r5, [r4], #4

aa4: e4835004 str r5, [r3], #4

aa8: e3550000 cmp r5, #0

aac: 1afffffb bne aa0 <free@plt+0x80>

ab0: e4945004 ldr r5, [r4], #4

ab4: e4835004 str r5, [r3], #4

ab8: e3550000 cmp r5, #0

abc: 1afffffb bne ab0 <free@plt+0x90>

ac0: e8b40021 ldm r4!, {r0, r5}

ac4: e8a30021 stmia r3!, {r0, r5}

ac8: e3500000 cmp r0, #0

acc: 1afffffb bne ac0 <free@plt+0xa0>

ad0: e59f3010 ldr r3, [pc, #16] ; ae8 <free@plt+0xc8>

ad4: e78a2003 str r2, [sl, r3]

ad8: eaffffe4 b a70 <free@plt+0x50>

adc: 00028524 andeq r8, r2, r4, lsr #10

ae0: 00000954 andeq r0, r0, r4, asr r9

ae4: fffe26c5 ; <UNDEFINED> instruction: 0xfffe26c5

ae8: 0000089c muleq r0, ip, r8

aec: 00000060 andeq r0, r0, r0, rrx

af0: 33016843 movwcc r6, #6211 ; 0x1843

af4: 47706043 ldrbmi r6, [r0, -r3, asr #32]!

af8: 3b016843 blcc 5ac0c <\_r\_debug@@GLIBC\_2.4+0x312b0>

afc: 47706043 ldrbmi r6, [r0, -r3, asr #32]!

b00: b087b530 addlt fp, r7, r0, lsr r5

b04: 46056841 strmi r6, [r5], -r1, asr #16

b08: 20022400 andcs r2, r2, r0, lsl #8

b0c: aa0560ac bge 158dc4 <\_r\_debug@@GLIBC\_2.4+0x12f468>

b10: f5019403 ; <UNDEFINED> instruction: 0xf5019403

b14: e9cd73ea stmib sp, {r1, r3, r5, r6, r7, r8, r9, ip, sp, lr}^

b18: 90024400 andls r4, r2, r0, lsl #8

b1c: 94056828 strls r6, [r5], #-2088 ; 0xfffff7d8

b20: fdbaf006 ldc2 0, cr15, [sl, #24]!

b24: b1629a05 cmnlt r2, r5, lsl #20

b28: f64f89d3 ; <UNDEFINED> instruction: 0xf64f89d3

b2c: 685271f1 ldmdavs r2, {r0, r4, r5, r6, r7, r8, ip, sp, lr}^

b30: bf18428b svclt 0x0018428b

b34: bf1a42a0 svclt 0x001a42a0

b38: 68032301 stmdavs r3, {r0, r8, r9, sp}

b3c: 44132300 ldrmi r2, [r3], #-768 ; 0xfffffd00

b40: b00760ab andlt r6, r7, fp, lsr #1

b44: bf00bd30 svclt 0x0000bd30

b48: 21014b0e tstcs r1, lr, lsl #22

b4c: 447bb570 ldrbtmi fp, [fp], #-1392 ; 0xfffffa90

b50: 46054c0d strmi r4, [r5], -sp, lsl #24

b54: b0844a0d addlt r4, r4, sp, lsl #20

b58: f6c8480d ; <UNDEFINED> instruction: 0xf6c8480d

b5c: 591e4100 ldmdbpl lr, {r8, lr}

b60: 4478447a ldrbtmi r4, [r8], #-1146 ; 0xfffffb86

b64: 33fff04f mvnscc pc, #79 ; 0x4f

b68: 68106804 ldmdavs r0, {r2, fp, sp, lr}

b6c: e9cd6832 stmib sp, {r1, r4, r5, fp, sp, lr}^

b70: 92020400 andls r0, r2, #0, 8

b74: 68284a07 stmdavs r8!, {r0, r1, r2, r9, fp, lr}

b78: f00c447a ; <UNDEFINED> instruction: 0xf00c447a

b7c: 6068ff93 mlsvs r8, r3, pc, pc ; <UNPREDICTABLE>

b80: bd70b004 ldcllt 0, cr11, [r0, #-16]!

b84: 000284ae andeq r8, r2, lr, lsr #9

b88: 0000003c andeq r0, r0, ip, lsr r0

b8c: 00027dac andeq r7, r2, ip, lsr #27

b90: 00028d36 andeq r8, r2, r6, lsr sp

b94: 00000bd9 ldrdeq r0, [r0], -r9

b98: 7809460b stmdavc r9, {r0, r1, r3, r9, sl, lr}

b9c: 4b06b921 blmi 1af028 <\_r\_debug@@GLIBC\_2.4+0x1856cc>

ba0: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

ba4: b123681b ; <UNDEFINED> instruction: 0xb123681b

ba8: 20024904 andcs r4, r2, r4, lsl #18

bac: f00b4479 ; <UNDEFINED> instruction: 0xf00b4479

bb0: 4b03b90d blmi eefec <\_r\_debug@@GLIBC\_2.4+0xc5690>

bb4: e7f7447b ; <UNDEFINED> instruction: 0xe7f7447b

bb8: 00028cf8 strdeq r8, [r2], -r8 ; <UNPREDICTABLE>

bbc: 00013a54 andeq r3, r1, r4, asr sl

bc0: 00013a34 andeq r3, r1, r4, lsr sl

bc4: 460b480a strmi r4, [fp], -sl, lsl #16

bc8: 44784611 ldrbtmi r4, [r8], #-1553 ; 0xfffff9ef

bcc: b083b500 addlt fp, r3, r0, lsl #10

bd0: 68126802 ldmdavs r2, {r1, fp, sp, lr}

bd4: 9100b142 tstls r0, r2, asr #2

bd8: 49062002 stmdbmi r6, {r1, sp}

bdc: f00b4479 ; <UNDEFINED> instruction: 0xf00b4479

be0: b003f8f5 strdlt pc, [r3], -r5

be4: fb04f85d blx 13ed62 <\_r\_debug@@GLIBC\_2.4+0x115406>

be8: 447a4a03 ldrbtmi r4, [sl], #-2563 ; 0xfffff5fd

bec: bf00e7f3 svclt 0x0000e7f3

bf0: 00028cce andeq r8, r2, lr, asr #25

bf4: 00013a30 andeq r3, r1, r0, lsr sl

bf8: 000139fe strdeq r3, [r1], -lr

bfc: 43f0e92d mvnsmi lr, #737280 ; 0xb4000

c00: 4d15b08b ldcmi 0, cr11, [r5, #-556] ; 0xfffffdd4

c04: 4b154606 blmi 552424 <\_r\_debug@@GLIBC\_2.4+0x528ac8>

c08: 447d4691 ldrbtmi r4, [sp], #-1681 ; 0xfffff96f

c0c: 447b9107 ldrbtmi r9, [fp], #-263 ; 0xfffffef9

c10: a905aa06 stmdbge r5, {r1, r2, r9, fp, sp, pc}

c14: a8049200 stmdage r4, {r9, ip, pc}

c18: 020ff10d andeq pc, pc, #1073741827 ; 0x40000003

c1c: 24009606 strcs r9, [r0], #-1542 ; 0xfffff9fa

c20: 8004f8d5 ldrdhi pc, [r4], -r5

c24: 6780f04f strvs pc, [r0, pc, asr #32]

c28: 97089405 strls r9, [r8, -r5, lsl #8]

c2c: eef2f7ff mrc 7, 7, APSR\_nzcv, cr2, cr15, {7}

c30: b93b9b05 ldmdblt fp!, {r0, r2, r8, r9, fp, ip, pc}

c34: ebb06868 bl fec1addc <\_r\_debug@@GLIBC\_2.4+0xfebf1480>

c38: bf180008 svclt 0x00180008

c3c: b00b2001 andlt r2, fp, r1

c40: 83f0e8bd mvnshi lr, #12386304 ; 0xbd0000

c44: 20024906 andcs r4, r2, r6, lsl #18

c48: 46329300 ldrtmi r9, [r2], -r0, lsl #6

c4c: 4479464b ldrbtmi r4, [r9], #-1611 ; 0xfffff9b5

c50: f8bcf00b ; <UNDEFINED> instruction: 0xf8bcf00b

c54: e7f24620 ldrb r4, [r2, r0, lsr #12]!

c58: 00028452 andeq r8, r2, r2, asr r4

c5c: 00000053 andeq r0, r0, r3, asr r0

c60: 000139ca andeq r3, r1, sl, asr #19

c64: 4604b530 ; <UNDEFINED> instruction: 0x4604b530

c68: b0836880 addlt r6, r3, r0, lsl #17

c6c: 68212500 stmdavs r1!, {r8, sl, sp}

c70: 4260f110 rsbmi pc, r0, #16, 2

c74: e9cd462b stmib sp, {r0, r1, r3, r5, r9, sl, lr}^

c78: bf180500 svclt 0x00180500

c7c: 68602201 stmdavs r0!, {r0, r9, sp}^

c80: fbd4f005 blx ff53cc9e <\_r\_debug@@GLIBC\_2.4+0xff513342>

c84: b00360e0 andlt r6, r3, r0, ror #1

c88: bf00bd30 svclt 0x0000bd30

c8c: 0200e9d0 andeq lr, r0, #208, 18 ; 0x340000

c90: f8d02300 ; <UNDEFINED> instruction: 0xf8d02300

c94: f00711d0 ; <UNDEFINED> instruction: 0xf00711d0

c98: bf00be07 svclt 0x0000be07

c9c: 4ff0e92d svcmi 0x00f0e92d

ca0: 7803b085 stmdavc r3, {r0, r2, r7, ip, sp, pc}

ca4: 4d5faf02 ldclmi 15, cr10, [pc, #-8] ; ca4 <free@plt+0x284>

ca8: 0801f241 stmdaeq r1, {r0, r6, r9, ip, sp, lr, pc}

cac: 46044a5e ; <UNDEFINED> instruction: 0x46044a5e

cb0: 4800f2c0 stmdami r0, {r6, r7, r9, ip, sp, lr, pc}

cb4: 447a447d ldrbtmi r4, [sl], #-1149 ; 0xfffffb83

cb8: 2b00607a blcs 18ea8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8140>

cbc: 3b20d04e blcc 834dfc <\_r\_debug@@GLIBC\_2.4+0x80b4a0>

cc0: 2b1ab2db blcs 6ad834 <\_r\_debug@@GLIBC\_2.4+0x683ed8>

cc4: 7863d956 stmdavc r3!, {r1, r2, r4, r6, r8, fp, ip, lr, pc}^

cc8: 0901f104 stmdbeq r1, {r2, r8, ip, sp, lr, pc}

ccc: bf182b3a svclt 0x00182b3a

cd0: bf142b2c svclt 0x00142b2c

cd4: 22002201 andcs r2, r0, #268435456 ; 0x10000000

cd8: 0fdff013 svceq 0x00dff013

cdc: 2200bf08 andcs fp, r0, #8, 30

ce0: d0502a00 subsle r2, r0, r0, lsl #20

ce4: f04f1ca1 ; <UNDEFINED> instruction: 0xf04f1ca1

ce8: 46890a01 strmi r0, [r9], r1, lsl #20

cec: 2b01f811 blcs 7ed38 <\_r\_debug@@GLIBC\_2.4+0x553dc>

cf0: 0a01f10a beq 7d120 <\_r\_debug@@GLIBC\_2.4+0x537c4>

cf4: bf182a2c svclt 0x00182a2c

cf8: bf142a3a svclt 0x00142a3a

cfc: 23002301 movwcs r2, #769 ; 0x301

d00: 0fdff012 svceq 0x00dff012

d04: 2300bf08 movwcs fp, #3848 ; 0xf08

d08: d1ee2b00 mvnle r2, r0, lsl #22

d0c: 0b01f105 bleq 7d128 <\_r\_debug@@GLIBC\_2.4+0x537cc>

d10: 26002204 strcs r2, [r0], -r4, lsl #4

d14: 3601e006 strcc lr, [r1], -r6

d18: 0b36f10b bleq dbd14c <\_r\_debug@@GLIBC\_2.4+0xd937f0>

d1c: d0622e0b rsble r2, r2, fp, lsl #28

d20: 2c01f81b stccs 8, cr15, [r1], {27}

d24: d1f64552 mvnsle r4, r2, asr r5

d28: 46204659 ; <UNDEFINED> instruction: 0x46204659

d2c: fe86f011 mcr2 0, 4, pc, cr6, cr1, {0} ; <UNPREDICTABLE>

d30: d1f02800 mvnsle r2, r0, lsl #16

d34: 20364a3d eorscs r4, r6, sp, lsr sl

d38: 2401493d strcs r4, [r1], #-2365 ; 0xfffff6c3

d3c: 4479447a ldrbtmi r4, [r9], #-1146 ; 0xfffffb86

d40: 2306fb00 movwcs pc, #27392 ; 0x6b00 ; <UNPREDICTABLE>

d44: 483b680a ldmdami fp!, {r1, r3, fp, sp, lr}

d48: 8e9b4478 mrchi 4, 4, r4, cr11, cr8, {3}

d4c: 464c6044 strbmi r6, [ip], -r4, asr #32

d50: 600a431a andvs r4, sl, sl, lsl r3

d54: 3000f899 mulcc r0, r9, r8

d58: d1b02b00 lslsle r2, r0, #22

d5c: 44794936 ldrbtmi r4, [r9], #-2358 ; 0xfffff6ca

d60: 05d0680a ldrbeq r6, [r0, #2058] ; 0x80a

d64: 62cbbf48 sbcvs fp, fp, #72, 30 ; 0x120

d68: d40f0553 strle r0, [pc], #-1363 ; d70 <free@plt+0x350>

d6c: 46bd370c ldrtmi r3, [sp], ip, lsl #14

d70: 8ff0e8bd svchi 0x00f0e8bd

d74: f203fa28 vpmax.s8 d15, d3, d24

d78: 0901f104 stmdbeq r1, {r2, r8, ip, sp, lr, pc}

d7c: 786307d6 stmdavc r3!, {r1, r2, r4, r6, r7, r8, r9, sl}^

d80: 464cd5a4 strbmi sp, [ip], -r4, lsr #11

d84: f04fe7e8 ; <UNDEFINED> instruction: 0xf04fe7e8

d88: e7bf0a01 ldr r0, [pc, r1, lsl #20]!

d8c: 2001492b andcs r4, r1, fp, lsr #18

d90: f8df4d2b ; <UNDEFINED> instruction: 0xf8df4d2b

d94: 447980b0 ldrbtmi r8, [r9], #-176 ; 0xffffff50

d98: 447d4e2b ldrbtmi r4, [sp], #-3627 ; 0xfffff1d5

d9c: f816f00b ; <UNDEFINED> instruction: 0xf816f00b

da0: 44f81c6c ldrbtmi r1, [r8], #3180 ; 0xc6c

da4: 447e2204 ldrbtmi r2, [lr], #-516 ; 0xfffffdfc

da8: 2553f205 ldrbcs pc, [r3, #-517] ; 0xfffffdfb ; <UNPREDICTABLE>

dac: f814e001 ; <UNDEFINED> instruction: 0xf814e001

db0: 1ed32c01 cdpne 12, 13, cr2, cr3, cr1, {0}

db4: 010af104 tsteq sl, r4, lsl #2 ; <UNPREDICTABLE>

db8: 91014433 tstls r1, r3, lsr r4

dbc: 46419300 strbmi r9, [r1], -r0, lsl #6

dc0: 20014623 andcs r4, r1, r3, lsr #12

dc4: f00b3436 ; <UNDEFINED> instruction: 0xf00b3436

dc8: 42a5f801 adcmi pc, r5, #65536 ; 0x10000

dcc: 491fd1ef ldmdbmi pc, {r0, r1, r2, r3, r5, r6, r7, r8, ip, lr, pc} ; <UNPREDICTABLE>

dd0: 44792001 ldrbtmi r2, [r9], #-1

dd4: fffaf00a ; <UNDEFINED> instruction: 0xfffaf00a

dd8: f0112000 ; <UNDEFINED> instruction: 0xf0112000

ddc: 370cf835 smuadxcc ip, r5, r8

de0: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

de4: 46518ff0 usub8mi r8, r1, r0

de8: f0114620 ; <UNDEFINED> instruction: 0xf0114620

dec: 4621fd17 ; <UNDEFINED> instruction: 0x4621fd17

df0: f1002400 ; <UNDEFINED> instruction: 0xf1002400

df4: 46020308 strmi r0, [r2], -r8, lsl #6

df8: 0307f023 movweq pc, #28707 ; 0x7023 ; <UNPREDICTABLE>

dfc: 0d03ebad vstreq d14, [r3, #-692] ; 0xfffffd4c

e00: 030ff10d movweq pc, #61709 ; 0xf10d ; <UNPREDICTABLE>

e04: f80008db ; <UNDEFINED> instruction: 0xf80008db

e08: 00d84033 sbcseq r4, r8, r3, lsr r0

e0c: e978f012 ldmdb r8!, {r1, r4, ip, sp, lr, pc}^

e10: 464c6879 ; <UNDEFINED> instruction: 0x464c6879

e14: 20024602 andcs r4, r2, r2, lsl #12

e18: ffd8f00a ; <UNDEFINED> instruction: 0xffd8f00a

e1c: 3000f899 mulcc r0, r9, r8

e20: bf00e79a svclt 0x0000e79a

e24: 000130d8 ldrdeq r3, [r1], -r8

e28: 00013a92 muleq r1, r2, sl

e2c: 00013050 andeq r3, r1, r0, asr r0

e30: 00027c2e andeq r7, r2, lr, lsr #24

e34: 00027bc4 andeq r7, r2, r4, asr #23

e38: 00027c0e andeq r7, r2, lr, lsl #24

e3c: 000138ca andeq r3, r1, sl, asr #17

e40: 00012ff2 strdeq r2, [r1], -r2 ; <UNPREDICTABLE>

e44: 000138fa strdeq r3, [r1], -sl

e48: 00013902 andeq r3, r1, r2, lsl #18

e4c: 000138e2 andeq r3, r1, r2, ror #17

e50: b5104b0b ldrlt r4, [r0, #-2827] ; 0xfffff4f5

e54: 4604447b ; <UNDEFINED> instruction: 0x4604447b

e58: b923681b stmdblt r3!, {r0, r1, r3, r4, fp, sp, lr}

e5c: 30007820 andcc r7, r0, r0, lsr #16

e60: 2001bf18 andcs fp, r1, r8, lsl pc

e64: f011bd10 ; <UNDEFINED> instruction: 0xf011bd10

e68: 28fefc6b ldmcs lr!, {r0, r1, r3, r5, r6, sl, fp, ip, sp, lr, pc}^

e6c: 4602d806 strmi sp, [r2], -r6, lsl #16

e70: 4620212f strtmi r2, [r0], -pc, lsr #2

e74: fd94f011 ldc2 0, cr15, [r4, #68] ; 0x44

e78: d0ef2800 rscle r2, pc, r0, lsl #16

e7c: bd102000 ldclt 0, cr2, [r0, #-0]

e80: 000280e8 andeq r8, r2, r8, ror #1

e84: 4c3bb5f8 cfldr32mi mvfx11, [fp], #-992 ; 0xfffffc20

e88: f8d4447c ; <UNDEFINED> instruction: 0xf8d4447c

e8c: f8d4580c ; <UNDEFINED> instruction: 0xf8d4580c

e90: f8c47824 ; <UNDEFINED> instruction: 0xf8c47824

e94: 2f005814 svccs 0x00005814

e98: f105d148 ; <UNDEFINED> instruction: 0xf105d148

e9c: 21010040 tstcs r1, r0, asr #32

ea0: f7ff00c0 ; <UNDEFINED> instruction: 0xf7ff00c0

ea4: 6ce2eda6 stclvs 13, cr14, [r2], #664 ; 0x298

ea8: 033ff105 teqeq pc, #1073741825 ; 0x40000001 ; <UNPREDICTABLE>

eac: f8c44606 ; <UNDEFINED> instruction: 0xf8c44606

eb0: f8460810 ; <UNDEFINED> instruction: 0xf8460810

eb4: 60473b08 subvs r3, r7, r8, lsl #22

eb8: d1462a00 cmple r6, r0, lsl #20

ebc: 2b006823 blcs 1af50 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa1e8>

ec0: f8d3d041 ; <UNDEFINED> instruction: 0xf8d3d041

ec4: 46110238 ; <UNDEFINED> instruction: 0x46110238

ec8: eb06b120 bl 1ad350 <\_r\_debug@@GLIBC\_2.4+0x1839f4>

ecc: 320100c2 andcc r0, r1, #194 ; 0xc2

ed0: 60434611 subvs r4, r3, r1, lsl r6

ed4: 2b0068db blcs 1b248 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa4e0>

ed8: 428dd1f3 addmi sp, sp, #-1073741764 ; 0xc000003c

edc: f00cd13f ; <UNDEFINED> instruction: 0xf00cd13f

ee0: f00cf86f ; <UNDEFINED> instruction: 0xf00cf86f

ee4: 4603f92b strmi pc, [r3], -fp, lsr #18

ee8: 4a23b318 bmi 8edb50 <\_r\_debug@@GLIBC\_2.4+0x8c41f4>

eec: 0c05f04f stceq 0, cr15, [r5], {79} ; 0x4f

ef0: f2c06801 vmlal.s8 q11, d0, d1

ef4: 447a0c0f ldrbtmi r0, [sl], #-3087 ; 0xfffff3f1

ef8: 1824f8c2 stmdane r4!, {r1, r6, r7, fp, ip, sp, lr, pc}

efc: f860f010 ; <UNDEFINED> instruction: 0xf860f010

f00: 5f80f510 svcpl 0x0080f510

f04: 4d1dd90c vldrmi.16 s26, [sp, #-24] ; 0xffffffe8 ; <UNPREDICTABLE>

f08: 447d4c1d ldrbtmi r4, [sp], #-3101 ; 0xfffff3e3

f0c: 462a447c ; <UNDEFINED> instruction: 0x462a447c

f10: 20024621 andcs r4, r2, r1, lsr #12

f14: ff5af00a ; <UNDEFINED> instruction: 0xff5af00a

f18: f010207f ; <UNDEFINED> instruction: 0xf010207f

f1c: e7f6ff95 ; <UNDEFINED> instruction: 0xe7f6ff95

f20: 21014a18 tstcs r1, r8, lsl sl

f24: 447a4618 ldrbtmi r4, [sl], #-1560 ; 0xfffff9e8

f28: bdf87111 ldflte f7, [r8, #68]! ; 0x44

f2c: 46182300 ldrmi r2, [r8], -r0, lsl #6

f30: 4c15bdf8 ldcmi 13, cr11, [r5], {248} ; 0xf8

f34: 4621447c ; <UNDEFINED> instruction: 0x4621447c

f38: f00a2002 ; <UNDEFINED> instruction: 0xf00a2002

f3c: 207fff47 rsbscs pc, pc, r7, asr #30

f40: ff82f010 ; <UNDEFINED> instruction: 0xff82f010

f44: 4619e7f7 ; <UNDEFINED> instruction: 0x4619e7f7

f48: 4b10e7c7 blmi 43ae6c <\_r\_debug@@GLIBC\_2.4+0x411510>

f4c: 22c5f240 sbccs pc, r5, #64, 4

f50: 4810490f ldmdami r0, {r0, r1, r2, r3, r8, fp, lr}

f54: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

f58: f00f4478 ; <UNDEFINED> instruction: 0xf00f4478

f5c: 4b0efd17 blmi 3c03c0 <\_r\_debug@@GLIBC\_2.4+0x396a64>

f60: 22d1f240 sbcscs pc, r1, #64, 4

f64: 480e490d stmdami lr, {r0, r2, r3, r8, fp, lr}

f68: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

f6c: f00f4478 ; <UNDEFINED> instruction: 0xf00f4478

f70: bf00fd0d svclt 0x0000fd0d

f74: 000281d4 ldrdeq r8, [r2], -r4

f78: 00028166 andeq r8, r2, r6, ror #2

f7c: 00013902 andeq r3, r1, r2, lsl #18

f80: 00013910 andeq r3, r1, r0, lsl r9

f84: 00028972 andeq r8, r2, r2, ror r9

f88: 000138a0 andeq r3, r1, r0, lsr #17

f8c: 00014834 andeq r4, r1, r4, lsr r8

f90: 0001382a andeq r3, r1, sl, lsr #16

f94: 00013830 andeq r3, r1, r0, lsr r8

f98: 00014820 andeq r4, r1, r0, lsr #16

f9c: 00013816 andeq r3, r1, r6, lsl r8

fa0: 0001384c andeq r3, r1, ip, asr #16

fa4: 4d1cb470 cfldrsmi mvf11, [ip, #-448] ; 0xfffffe40

fa8: f8d5447d ; <UNDEFINED> instruction: 0xf8d5447d

fac: b33b34c0 teqlt fp, #192, 8 ; 0xc0000000

fb0: fb06264c blx 18a8ea <\_r\_debug@@GLIBC\_2.4+0x160f8e>

fb4: 23005603 movwcs r5, #1539 ; 0x603

fb8: b1f2682a mvnslt r6, sl, lsr #16

fbc: 4164f8d2 ldrdmi pc, [r4, #-130]! ; 0xffffff7e

fc0: f8d2b1dc ; <UNDEFINED> instruction: 0xf8d2b1dc

fc4: eb011160 bl 4554c <\_r\_debug@@GLIBC\_2.4+0x1bbf0>

fc8: e00b0484 and r0, fp, r4, lsl #9

fcc: 00c0f8d2 ldrdeq pc, [r0], #130 ; 0x82

fd0: 6840b108 stmdavs r0, {r3, r8, ip, sp, pc}^

fd4: f8d24403 ; <UNDEFINED> instruction: 0xf8d24403

fd8: b10a20c4 smlabtlt sl, r4, r0, r2

fdc: 44136852 ldrmi r6, [r3], #-2130 ; 0xfffff7ae

fe0: d00a428c andle r4, sl, ip, lsl #5

fe4: 2b04f851 blcs 13f130 <\_r\_debug@@GLIBC\_2.4+0x1157d4>

fe8: 28006810 stmdacs r0, {r4, fp, sp, lr}

fec: f8d2d1ee ; <UNDEFINED> instruction: 0xf8d2d1ee

ff0: 28000120 stmdacs r0, {r5, r8}

ff4: 428cd0ef addmi sp, ip, #239 ; 0xef

ff8: 354cd1f4 strbcc sp, [ip, #-500] ; 0xfffffe0c

ffc: d1db42b5 ldrhle r4, [fp, #37] ; 0x25

1000: 48074906 stmdami r7, {r1, r2, r8, fp, lr}

1004: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

1008: 250cf8d1 strcs pc, [ip, #-2257] ; 0xfffff72f

100c: f8d1bc70 ; <UNDEFINED> instruction: 0xf8d1bc70

1010: f00a1508 ; <UNDEFINED> instruction: 0xf00a1508

1014: bf00beaf svclt 0x0000beaf

1018: 000280b4 strheq r8, [r2], -r4

101c: 00028058 andeq r8, r2, r8, asr r0

1020: 0001383e andeq r3, r1, lr, lsr r8

1024: 21014b08 tstcs r1, r8, lsl #22

1028: 447bb510 ldrbtmi fp, [fp], #-1296 ; 0xfffffaf0

102c: 68424604 stmdavs r2, {r2, r9, sl, lr}^

1030: f00b6818 ; <UNDEFINED> instruction: 0xf00b6818

1034: b108fa4d tstlt r8, sp, asr #20 ; <UNPREDICTABLE>

1038: b9036823 stmdblt r3, {r0, r1, r5, fp, sp, lr}

103c: 2001bd10 andcs fp, r1, r0, lsl sp

1040: 4010e8bd ; <UNDEFINED> instruction: 0x4010e8bd

1044: bf00f010 svclt 0x0000f010

1048: 00028032 andeq r8, r2, r2, lsr r0

104c: 3560f8df strbcc pc, [r0, #-2271]! ; 0xfffff721 ; <UNPREDICTABLE>

1050: 4ff0e92d svcmi 0x00f0e92d

1054: f8dfb089 ; <UNDEFINED> instruction: 0xf8dfb089

1058: 447b155c ldrbtmi r1, [fp], #-1372 ; 0xfffffaa4

105c: f8dfaf02 ; <UNDEFINED> instruction: 0xf8dfaf02

1060: 60f82558 rscsvs r2, r8, r8, asr r5

1064: 585b447a ldmdapl fp, {r1, r3, r4, r5, r6, sl, lr}^

1068: f0236810 ; <UNDEFINED> instruction: 0xf0236810

106c: f8df0301 ; <UNDEFINED> instruction: 0xf8df0301

1070: f2af154c vmls.f32 d1, d15, d12[0]

1074: f0250527 ; <UNDEFINED> instruction: 0xf0250527

1078: 44790501 ldrbtmi r0, [r9], #-1281 ; 0xfffffaff

107c: 182a1aed stmdane sl!, {r0, r2, r3, r5, r6, r7, r9, fp, ip}

1080: f8c1582b ; <UNDEFINED> instruction: 0xf8c1582b

1084: f8c12520 ; <UNDEFINED> instruction: 0xf8c12520

1088: b3bb5518 ; <UNDEFINED> instruction: 0xb3bb5518

108c: 6530f8df ldrvs pc, [r0, #-2271]! ; 0xfffff721

1090: 5efff64f cdppl 6, 15, cr15, cr15, cr15, {2}

1094: 052cf8df streq pc, [ip, #-2271]! ; 0xfffff721

1098: 7efff6c6 cdpvc 6, 15, cr15, cr15, cr6, {6}

109c: c528f8df strgt pc, [r8, #-2271]! ; 0xfffff721

10a0: f8df447e ; <UNDEFINED> instruction: 0xf8df447e

10a4: 44784528 ldrbtmi r4, [r8], #-1320 ; 0xfffffad8

10a8: f10644fc ; <UNDEFINED> instruction: 0xf10644fc

10ac: 447c4640 ldrbtmi r4, [ip], #-1600 ; 0xfffff9c0

10b0: 4040f100 submi pc, r0, r0, lsl #2

10b4: 760ff506 strvc pc, [pc], -r6, lsl #10

10b8: 70faf5a0 rscsvc pc, sl, r0, lsr #11

10bc: 6c04f20c sfmvs f7, 1, [r4], {12}

10c0: 64a7f504 strtvs pc, [r7], #1284 ; 0x504

10c4: f8dfe009 ; <UNDEFINED> instruction: 0xf8dfe009

10c8: 44791508 ldrbtmi r1, [r9], #-1288 ; 0xfffffaf8

10cc: 61a7f501 ; <UNDEFINED> instruction: 0x61a7f501

10d0: 2023f841 eorcs pc, r3, r1, asr #16

10d4: 3f08f852 svccc 0x0008f852

10d8: 2b22b183 blcs 8ad6ec <\_r\_debug@@GLIBC\_2.4+0x883d90>

10dc: f06fd9f3 ; <UNDEFINED> instruction: 0xf06fd9f3

10e0: 1ac94110 bne ff251528 <\_r\_debug@@GLIBC\_2.4+0xff227bcc>

10e4: f200290f vmla.i8 d2, d0, d15

10e8: f1c380bf ; <UNDEFINED> instruction: 0xf1c380bf

10ec: 332243e0 ; <UNDEFINED> instruction: 0x332243e0

10f0: 2023f844 eorcs pc, r3, r4, asr #16

10f4: 3f08f852 svccc 0x0008f852

10f8: d1ee2b00 mvnle r2, r0, lsl #22

10fc: d0472d00 suble r2, r7, r0, lsl #26

1100: 34d0f8df ldrbcc pc, [r0], #2271 ; 0x8df ; <UNPREDICTABLE>

1104: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

1108: b1133548 tstlt r3, r8, asr #10

110c: 442a685a strtmi r6, [sl], #-2138 ; 0xfffff7a6

1110: f8df605a ; <UNDEFINED> instruction: 0xf8df605a

1114: 447b34c4 ldrbtmi r3, [fp], #-1220 ; 0xfffffb3c

1118: 3544f8d3 strbcc pc, [r4, #-2259] ; 0xfffff72d ; <UNPREDICTABLE>

111c: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

1120: 605a442a subsvs r4, sl, sl, lsr #8

1124: 34b4f8df ldrtcc pc, [r4], #2271 ; 0x8df ; <UNPREDICTABLE>

1128: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

112c: b113354c tstlt r3, ip, asr #10

1130: 442a685a strtmi r6, [sl], #-2138 ; 0xfffff7a6

1134: f8df605a ; <UNDEFINED> instruction: 0xf8df605a

1138: 447b34a8 ldrbtmi r3, [fp], #-1192 ; 0xfffffb58

113c: 3550f8d3 ldrbcc pc, [r0, #-2259] ; 0xfffff72d ; <UNPREDICTABLE>

1140: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

1144: 605a442a subsvs r4, sl, sl, lsr #8

1148: 3498f8df ldrcc pc, [r8], #2271 ; 0x8df

114c: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

1150: b113357c tstlt r3, ip, ror r5

1154: 442a685a strtmi r6, [sl], #-2138 ; 0xfffff7a6

1158: f8df605a ; <UNDEFINED> instruction: 0xf8df605a

115c: 447b348c ldrbtmi r3, [fp], #-1164 ; 0xfffffb74

1160: 3594f8d3 ldrcc pc, [r4, #2259] ; 0x8d3

1164: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

1168: 605a442a subsvs r4, sl, sl, lsr #8

116c: 347cf8df ldrbtcc pc, [ip], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

1170: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

1174: b1133600 tstlt r3, r0, lsl #12

1178: 442a685a strtmi r6, [sl], #-2138 ; 0xfffff7a6

117c: f8df605a ; <UNDEFINED> instruction: 0xf8df605a

1180: 447b3470 ldrbtmi r3, [fp], #-1136 ; 0xfffffb90

1184: 3668f8d3 ; <UNDEFINED> instruction: 0x3668f8d3

1188: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

118c: 605a442a subsvs r4, sl, sl, lsr #8

1190: 3460f8df strbtcc pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

1194: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

1198: b1191588 tstlt r9, r8, lsl #11

119c: 2b11684b blcs 45b2d0 <\_r\_debug@@GLIBC\_2.4+0x431974>

11a0: 81bbf040 ; <UNDEFINED> instruction: 0x81bbf040

11a4: 2450f8df ldrbcs pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

11a8: f8d2447a ; <UNDEFINED> instruction: 0xf8d2447a

11ac: b12b357c ; <UNDEFINED> instruction: 0xb12b357c

11b0: 2584f8d2 strcs pc, [r4, #2258] ; 0x8d2

11b4: 2a086852 bcs 21b304 <\_r\_debug@@GLIBC\_2.4+0x1f19a8>

11b8: 81a6f040 ; <UNDEFINED> instruction: 0x81a6f040

11bc: 243cf8df ldrtcs pc, [ip], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

11c0: f8d2447a ; <UNDEFINED> instruction: 0xf8d2447a

11c4: b12225d4 ldrdlt r2, [r2, -r4]!

11c8: f0326852 ; <UNDEFINED> instruction: 0xf0326852

11cc: f0400201 ; <UNDEFINED> instruction: 0xf0400201

11d0: f8df81c8 ; <UNDEFINED> instruction: 0xf8df81c8

11d4: 447a242c ldrbtmi r2, [sl], #-1068 ; 0xfffffbd4

11d8: 25b0f8d2 ldrcs pc, [r0, #2258]! ; 0x8d2

11dc: 6852b122 ldmdavs r2, {r1, r5, r8, ip, sp, pc}^

11e0: 0208f032 andeq pc, r8, #50 ; 0x32

11e4: 81b4f040 ; <UNDEFINED> instruction: 0x81b4f040

11e8: 2418f8df ldrcs pc, [r8], #-2271 ; 0xfffff721

11ec: f8d2447a ; <UNDEFINED> instruction: 0xf8d2447a

11f0: 280005ac stmdacs r0, {r2, r3, r5, r7, r8, sl}

11f4: 81a3f040 ; <UNDEFINED> instruction: 0x81a3f040

11f8: 0574f8d2 ldrbeq pc, [r4, #-2258]! ; 0xfffff72e ; <UNPREDICTABLE>

11fc: f0402800 ; <UNDEFINED> instruction: 0xf0402800

1200: 2d008195 stfcsd f0, [r0, #-596] ; 0xfffffdac

1204: f8d2d143 ; <UNDEFINED> instruction: 0xf8d2d143

1208: 2a002638 bcs aaf0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e5c>

120c: 4cfed03f ldclmi 0, cr13, [lr], #252 ; 0xfc

1210: 447c4efe ldrbtmi r4, [ip], #-3838 ; 0xfffff102

1214: f5044dfe ; <UNDEFINED> instruction: 0xf5044dfe

1218: 447e68a3 ldrbtmi r6, [lr], #-2211 ; 0xfffff75d

121c: 36b0f894 ssatcc pc, #17, r4, lsl #17 ; <UNPREDICTABLE>

1220: 4640447d ; <UNDEFINED> instruction: 0x4640447d

1224: 0304f043 movweq pc, #16451 ; 0x4043 ; <UNPREDICTABLE>

1228: 36b0f884 ldrtcc pc, [r0], r4, lsl #17 ; <UNPREDICTABLE>

122c: f882f007 ; <UNDEFINED> instruction: 0xf882f007

1230: 49f94bf8 ldmibmi r9!, {r3, r4, r5, r6, r7, r8, r9, fp, lr}^

1234: 4af9447b bmi ffe52428 <\_r\_debug@@GLIBC\_2.4+0xffe28acc>

1238: 447968f8 ldrbtmi r6, [r9], #-2296 ; 0xfffff708

123c: 601f447a andsvs r4, pc, sl, ror r4 ; <UNPREDICTABLE>

1240: 26d0f8c4 ldrbcs pc, [r0], r4, asr #17 ; <UNPREDICTABLE>

1244: 852cf8c4 strhi pc, [ip, #-2244]! ; 0xfffff73c

1248: 66c8f8c4 strbvs pc, [r8], r4, asr #17 ; <UNPREDICTABLE>

124c: 56ccf8c4 strbpl pc, [ip], r4, asr #17 ; <UNPREDICTABLE>

1250: ffa4f00e ; <UNDEFINED> instruction: 0xffa4f00e

1254: 447b4bf2 ldrbtmi r4, [fp], #-3058 ; 0xfffff40e

1258: 061a681b ; <UNDEFINED> instruction: 0x061a681b

125c: 8148f100 mrshi pc, (UNDEF: 88) ; <UNPREDICTABLE>

1260: 46bd371c ssatmi r3, #30, ip, lsl #14

1264: 8ff0e8bd svchi 0x00f0e8bd

1268: 011ef343 tsteq lr, r3, asr #6 ; <UNPREDICTABLE>

126c: 0f04f111 svceq 0x0004f111

1270: ebacd904 bl feb37688 <\_r\_debug@@GLIBC\_2.4+0xfeb0dd2c>

1274: f8410181 ; <UNDEFINED> instruction: 0xf8410181

1278: e72b2c04 str r2, [fp, -r4, lsl #24]!

127c: 0103ebae smlatbeq r3, lr, fp, lr

1280: f200290b vmla.i8 d2, d0, d11

1284: 009b8084 addseq r8, fp, r4, lsl #1

1288: 501a425b andspl r4, sl, fp, asr r2

128c: 2b00e722 blcs 3af1c <\_r\_debug@@GLIBC\_2.4+0x115c0>

1290: 8106f000 mrshi pc, (UNDEF: 6) ; <UNPREDICTABLE>

1294: 68584ae3 ldmdavs r8, {r0, r1, r5, r6, r7, r9, fp, lr}^

1298: 4613447a ; <UNDEFINED> instruction: 0x4613447a

129c: 2580f8d2 strcs pc, [r0, #2258] ; 0x8d2

12a0: 45d8f8d3 ldrbmi pc, [r8, #2259] ; 0x8d3 ; <UNPREDICTABLE>

12a4: 68524603 ldmdavs r2, {r0, r1, r9, sl, lr}^

12a8: 0a02eb00 beq bbeb0 <\_r\_debug@@GLIBC\_2.4+0x92554>

12ac: f0002c00 ; <UNDEFINED> instruction: 0xf0002c00

12b0: 6864811d stmdavs r4!, {r0, r2, r3, r4, r8, pc}^

12b4: 04c4eb00 strbeq lr, [r4], #2816 ; 0xb00

12b8: 49dbb169 ldmibmi fp, {r0, r3, r5, r6, r8, ip, sp, pc}^

12bc: f8d14479 ; <UNDEFINED> instruction: 0xf8d14479

12c0: f8d16540 ; <UNDEFINED> instruction: 0xf8d16540

12c4: 68761594 ldmdavs r6!, {r2, r4, r7, r8, sl, ip}^

12c8: 44316849 ldrtmi r6, [r1], #-2121 ; 0xfffff7b7

12cc: bf1c4551 svclt 0x001c4551

12d0: 0a00eb02 beq 3bee0 <\_r\_debug@@GLIBC\_2.4+0x12584>

12d4: 4ad544b2 bmi ff5525a4 <\_r\_debug@@GLIBC\_2.4+0xff528c48>

12d8: 447a42a3 ldrbtmi r4, [sl], #-675 ; 0xfffffd5d

12dc: 2550f8d2 ldrbcs pc, [r0, #-2258] ; 0xfffff72e ; <UNPREDICTABLE>

12e0: 46166852 ; <UNDEFINED> instruction: 0x46166852

12e4: 1e60d20f cdpne 2, 6, cr13, cr0, cr15, {0}

12e8: 0210f103 andseq pc, r0, #-1073741824 ; 0xc0000000

12ec: 33081ac0 movwcc r1, #35520 ; 0x8ac0

12f0: 0007f020 andeq pc, r7, r0, lsr #32

12f4: f8534410 ; <UNDEFINED> instruction: 0xf8534410

12f8: 33081c08 movwcc r1, #35848 ; 0x8c08

12fc: 586a4283 stmdapl sl!, {r0, r1, r7, r9, lr}^

1300: 506a442a rsbpl r4, sl, sl, lsr #8

1304: 4bcad1f7 blmi ff2b5ae8 <\_r\_debug@@GLIBC\_2.4+0xff28c18c>

1308: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

130c: 2b003600 blcs eb14 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10fc>

1310: 813af000 teqhi sl, r0 ; <UNPREDICTABLE>

1314: f67f45a2 ; <UNDEFINED> instruction: 0xf67f45a2

1318: 4bc6af7a blmi ff1ad108 <\_r\_debug@@GLIBC\_2.4+0xff1837ac>

131c: f8df46b1 ; <UNDEFINED> instruction: 0xf8df46b1

1320: 46a8b318 ssatmi fp, #9, r8, lsl #6

1324: 603b447b eorsvs r4, fp, fp, ror r4

1328: 4bc444fb blmi ff11271c <\_r\_debug@@GLIBC\_2.4+0xff0e8dc0>

132c: 613b447b teqvs fp, fp, ror r4

1330: 6100e9d4 ldrdvs lr, [r0, -r4]

1334: 0306eb08 movweq lr, #27400 ; 0x6b08

1338: b2c90a0a sbclt r0, r9, #40960 ; 0xa000

133c: 1202eb19 andne lr, r2, #25600 ; 0x6400

1340: 4615bf08 ldrmi fp, [r5], -r8, lsl #30

1344: f8b2d012 ; <UNDEFINED> instruction: 0xf8b2d012

1348: f64fe00e ; <UNDEFINED> instruction: 0xf64fe00e

134c: f89270f1 ; <UNDEFINED> instruction: 0xf89270f1

1350: 4586c00c strmi ip, [r6, #12]

1354: f00c6855 ; <UNDEFINED> instruction: 0xf00c6855

1358: f0000c0f ; <UNDEFINED> instruction: 0xf0000c0f

135c: f8db808e ; <UNDEFINED> instruction: 0xf8db808e

1360: f1bc0518 ; <UNDEFINED> instruction: 0xf1bc0518

1364: 44050f0a strmi r0, [r5], #-3850 ; 0xfffff0f6

1368: 8096f000 addshi pc, r6, r0

136c: 28151e48 ldmdacs r5, {r3, r6, r9, sl, fp, ip}

1370: 80c3f200 sbchi pc, r3, r0, lsl #4

1374: f000e8df ; <UNDEFINED> instruction: 0xf000e8df

1378: c1c13a3f bicgt r3, r1, pc, lsr sl

137c: c1c1c1c1 bicgt ip, r1, r1, asr #3

1380: c1c1c1c1 bicgt ip, r1, r1, asr #3

1384: c1c1c129 bicgt ip, r1, r9, lsr #2

1388: 21c1c1c1 biccs ip, r1, r1, asr #3

138c: f64f1616 ; <UNDEFINED> instruction: 0xf64f1616

1390: f6c661ff ; <UNDEFINED> instruction: 0xf6c661ff

1394: 1ac971ff bne ff25db98 <\_r\_debug@@GLIBC\_2.4+0xff23423c>

1398: bf9e290a svclt 0x009e290a

139c: 425b009b subsmi r0, fp, #155 ; 0x9b

13a0: e697519a ; <UNDEFINED> instruction: 0xe697519a

13a4: d0652a00 rsble r2, r5, r0, lsl #20

13a8: 2b006853 blcs 1b4fc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa794>

13ac: f848d162 ; <UNDEFINED> instruction: 0xf848d162

13b0: 34083006 strcc r3, [r8], #-6

13b4: d8bb45a2 ldmle fp!, {r1, r5, r7, r8, sl, lr}

13b8: 2a00e729 bcs 3b064 <\_r\_debug@@GLIBC\_2.4+0x11708>

13bc: 6892d0f9 ldmvs r2, {r0, r3, r4, r5, r6, r7, ip, lr, pc}

13c0: 46184629 ldrmi r4, [r8], -r9, lsr #12

13c4: ee9cf011 mrc 0, 4, APSR\_nzcv, cr12, cr1, {0}

13c8: 6861e7f3 stmdavs r1!, {r0, r1, r4, r5, r6, r7, r8, r9, sl, sp, lr, pc}^

13cc: 0a09489c beq 253644 <\_r\_debug@@GLIBC\_2.4+0x229ce8>

13d0: bf144478 svclt 0x00144478

13d4: f8586855 ; <UNDEFINED> instruction: 0xf8586855

13d8: f8d05006 ; <UNDEFINED> instruction: 0xf8d05006

13dc: 4a99175c bmi fe647154 <\_r\_debug@@GLIBC\_2.4+0xfe61d7f8>

13e0: f8484429 ; <UNDEFINED> instruction: 0xf8484429

13e4: 447a1006 ldrbtmi r1, [sl], #-6

13e8: e7e2605a ubfx r6, sl, #0, #3

13ec: 441d681b ldrmi r6, [sp], #-2075 ; 0xfffff7e5

13f0: 5006f848 andpl pc, r6, r8, asr #16

13f4: f858e7dd ; <UNDEFINED> instruction: 0xf858e7dd

13f8: f3422006 vhadd.u8 d18, d2, d6

13fc: ebc30117 bl ff0c1860 <\_r\_debug@@GLIBC\_2.4+0xff097f04>

1400: 617b0381 cmnvs fp, r1, lsl #7

1404: f00318eb ; <UNDEFINED> instruction: 0xf00318eb

1408: f1b1417e ; <UNDEFINED> instruction: 0xf1b1417e

140c: bf184f7e svclt 0x00184f7e

1410: d0272900 eorle r2, r7, r0, lsl #18

1414: 6899693b ldmvs r9, {r0, r1, r3, r4, r5, r8, fp, sp, lr}

1418: d0462900 suble r2, r6, r0, lsl #18

141c: 68da693b ldmvs sl, {r0, r1, r3, r4, r5, r8, fp, sp, lr}^

1420: 188b4889 stmne fp, {r0, r3, r7, fp, lr}

1424: 0c04f24f sfmeq f7, 1, [r4], {79} ; 0x4f

1428: 5c1ff2ce lfmpl f7, 1, [pc], {206} ; 0xce

142c: f8414478 ; <UNDEFINED> instruction: 0xf8414478

1430: 605dc002 subsvs ip, sp, r2

1434: f8df3208 ; <UNDEFINED> instruction: 0xf8df3208

1438: 6901c214 stmdbvs r1, {r2, r4, r9, lr, pc}

143c: 428a44fc addmi r4, sl, #252, 8 ; 0xfc000000

1440: 200cf8cc andcs pc, ip, ip, asr #17

1444: 2200bf24 andcs fp, r0, #36, 30 ; 0x90

1448: 2202e9cc andcs lr, r2, #204, 18 ; 0x330000

144c: 4413697a ldrmi r6, [r3], #-2426 ; 0xfffff686

1450: 427ef003 rsbsmi pc, lr, #3

1454: 4f7ef1b2 svcmi 0x007ef1b2

1458: 2a00bf18 bcs 310c0 <\_r\_debug@@GLIBC\_2.4+0x7764>

145c: 808af040 addhi pc, sl, r0, asr #32

1460: 2006f858 andcs pc, r6, r8, asr r8 ; <UNPREDICTABLE>

1464: 427ff002 rsbsmi pc, pc, #2

1468: 0397f3c3 orrseq pc, r7, #201326595 ; 0xc000003

146c: f848431a ; <UNDEFINED> instruction: 0xf848431a

1470: e79e2006 ldr r2, [lr, r6]

1474: 5006f848 andpl pc, r6, r8, asr #16

1478: f1bce79b ; <UNDEFINED> instruction: 0xf1bce79b

147c: f47f0f0a ; <UNDEFINED> instruction: 0xf47f0f0a

1480: e9c7af75 stmib r7, {r0, r2, r4, r5, r6, r8, r9, sl, fp, sp, pc}^

1484: 683b1301 ldmdavs fp!, {r0, r8, r9, ip}

1488: 6c18617a ldfvss f6, [r8], {122} ; 0x7a

148c: 697a47a8 ldmdbvs sl!, {r3, r5, r7, r8, r9, sl, lr}^

1490: 1301e9d7 movwne lr, #6615 ; 0x19d7

1494: e7694605 strb r4, [r9, -r5, lsl #12]!

1498: 0f00f1be svceq 0x0000f1be

149c: e765d1f1 ; <UNDEFINED> instruction: 0xe765d1f1

14a0: 469a461c ; <UNDEFINED> instruction: 0x469a461c

14a4: 4618461a ; <UNDEFINED> instruction: 0x4618461a

14a8: 4869e706 stmdami r9!, {r1, r2, r8, r9, sl, sp, lr, pc}^

14ac: 32fff04f rscscc pc, pc, #79 ; 0x4f

14b0: 23229101 ; <UNDEFINED> instruction: 0x23229101

14b4: 92004478 andls r4, r0, #120, 8 ; 0x78000000

14b8: 220760b9 andcs r6, r7, #185 ; 0xb9

14bc: c010f8d0 ; <UNDEFINED> instruction: 0xc010f8d0

14c0: 46614608 strbtmi r4, [r1], -r8, lsl #12

14c4: fc56f010 mrrc2 0, 1, pc, r6, cr0 ; <UNPREDICTABLE>

14c8: 46011c43 strmi r1, [r1], -r3, asr #24

14cc: 693bd065 ldmdbvs fp!, {r0, r2, r5, r6, ip, lr, pc}

14d0: 609868da ; <UNDEFINED> instruction: 0x609868da

14d4: d0a32a00 adcle r2, r3, r0, lsl #20

14d8: f2404b5e vpadd.i8 q10, q0, q7

14dc: 495e124f ldmdbmi lr, {r0, r1, r2, r3, r6, r9, ip}^

14e0: 447b485e ldrbtmi r4, [fp], #-2142 ; 0xfffff7a2

14e4: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

14e8: fa50f00f blx 143d52c <\_r\_debug@@GLIBC\_2.4+0x1413bd0>

14ec: e6e34604 strbt r4, [r3], r4, lsl #12

14f0: f7ff6178 ; <UNDEFINED> instruction: 0xf7ff6178

14f4: 6978fd57 ldmdbvs r8!, {r0, r1, r2, r4, r6, r8, sl, fp, ip, sp, lr, pc}^

14f8: 4b59e6b2 blmi 167afc8 <\_r\_debug@@GLIBC\_2.4+0x165166c>

14fc: 447b2200 ldrbtmi r2, [fp], #-512 ; 0xfffffe00

1500: 60a3f503 adcvs pc, r3, r3, lsl #10

1504: f9acf007 ; <UNDEFINED> instruction: 0xf9acf007

1508: 22874b56 addcs r4, r7, #88064 ; 0x15800

150c: 48574956 ldmdami r7, {r1, r2, r4, r6, r8, fp, lr}^

1510: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

1514: f00f4478 ; <UNDEFINED> instruction: 0xf00f4478

1518: 4b55fa39 blmi 157fe04 <\_r\_debug@@GLIBC\_2.4+0x15564a8>

151c: 49552279 ldmdbmi r5, {r0, r3, r4, r5, r6, r9, sp}^

1520: 447b4855 ldrbtmi r4, [fp], #-2133 ; 0xfffff7ab

1524: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

1528: fa30f00f blx c3d56c <\_r\_debug@@GLIBC\_2.4+0xc13c10>

152c: 22934b53 addscs r4, r3, #84992 ; 0x14c00

1530: 48544953 ldmdami r4, {r0, r1, r4, r6, r8, fp, lr}^

1534: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

1538: f00f4478 ; <UNDEFINED> instruction: 0xf00f4478

153c: 4b52fa27 blmi 14bfde0 <\_r\_debug@@GLIBC\_2.4+0x1496484>

1540: 49522292 ldmdbmi r2, {r1, r4, r7, r9, sp}^

1544: 447b4852 ldrbtmi r4, [fp], #-2130 ; 0xfffff7ae

1548: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

154c: fa1ef00f blx 7bd590 <\_r\_debug@@GLIBC\_2.4+0x793c34>

1550: 228f4b50 addcs r4, pc, #80, 22 ; 0x14000

1554: 48514950 ldmdami r1, {r4, r6, r8, fp, lr}^

1558: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

155c: f00f4478 ; <UNDEFINED> instruction: 0xf00f4478

1560: 4b4ffa15 blmi 13ffdbc <\_r\_debug@@GLIBC\_2.4+0x13d6460>

1564: 494f228c stmdbmi pc, {r2, r3, r7, r9, sp}^ ; <UNPREDICTABLE>

1568: 447b484f ldrbtmi r4, [fp], #-2127 ; 0xfffff7b1

156c: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

1570: fa0cf00f blx 33d5b4 <\_r\_debug@@GLIBC\_2.4+0x313c58>

1574: 2200494d andcs r4, r0, #1261568 ; 0x134000

1578: 46104b4d ldrmi r4, [r0], -sp, asr #22

157c: 447b4479 ldrbtmi r4, [fp], #-1145 ; 0xfffffb87

1580: 151cf8d1 ldrne pc, [ip, #-2257] ; 0xfffff72f

1584: ea40f7ff b 103f588 <\_r\_debug@@GLIBC\_2.4+0x1015c2c>

1588: 22744b4a rsbscs r4, r4, #75776 ; 0x12800

158c: 484b494a stmdami fp, {r1, r3, r6, r8, fp, lr}^

1590: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

1594: f00f4478 ; <UNDEFINED> instruction: 0xf00f4478

1598: 4949f9f9 stmdbmi r9, {r0, r3, r4, r5, r6, r7, r8, fp, ip, sp, lr, pc}^

159c: 447968ba ldrbtmi r6, [r9], #-2234 ; 0xfffff746

15a0: 46104b48 ldrmi r4, [r0], -r8, asr #22

15a4: f8d1447b ; <UNDEFINED> instruction: 0xf8d1447b

15a8: f7ff151c ; <UNDEFINED> instruction: 0xf7ff151c

15ac: bf00ea2e svclt 0x0000ea2e

15b0: 00027fa2 andeq r7, r2, r2, lsr #31

15b4: 00000030 andeq r0, r0, r0, lsr r0

15b8: 00027f98 muleq r2, r8, pc ; <UNPREDICTABLE>

15bc: 00027fe2 andeq r7, r2, r2, ror #31

15c0: 00027fbc ; <UNDEFINED> instruction: 0x00027fbc

15c4: 00027fb6 ; <UNDEFINED> instruction: 0x00027fb6

15c8: 00027fb4 ; <UNDEFINED> instruction: 0x00027fb4

15cc: 00027fae andeq r7, r2, lr, lsr #31

15d0: 00027f92 muleq r2, r2, pc ; <UNPREDICTABLE>

15d4: 00027f58 andeq r7, r2, r8, asr pc

15d8: 00027f46 andeq r7, r2, r6, asr #30

15dc: 00027f34 andeq r7, r2, r4, lsr pc

15e0: 00027f22 andeq r7, r2, r2, lsr #30

15e4: 00027f10 andeq r7, r2, r0, lsl pc

15e8: 00027efe strdeq r7, [r2], -lr

15ec: 00027eec andeq r7, r2, ip, ror #29

15f0: 00027eda ldrdeq r7, [r2], -sl

15f4: 00027ec8 andeq r7, r2, r8, asr #29

15f8: 00027eb4 ; <UNDEFINED> instruction: 0x00027eb4

15fc: 00027e9c muleq r2, ip, lr

1600: 00027e86 andeq r7, r2, r6, lsl #29

1604: 00027e70 andeq r7, r2, r0, ror lr

1608: 00027e4a andeq r7, r2, sl, asr #28

160c: ffffede2 ; <UNDEFINED> instruction: 0xffffede2

1610: 00028750 andeq r8, r2, r0, asr r7

1614: 00027d00 andeq r7, r2, r0, lsl #26

1618: 00000517 andeq r0, r0, r7, lsl r5

161c: 00012b50 andeq r2, r1, r0, asr fp

1620: 00027716 andeq r7, r2, r6, lsl r7

1624: 00027dc4 andeq r7, r2, r4, asr #27

1628: 00027da0 andeq r7, r2, r0, lsr #27

162c: 00027d82 andeq r7, r2, r2, lsl #27

1630: 00027d54 andeq r7, r2, r4, asr sp

1634: 00027648 andeq r7, r2, r8, asr #12

1638: 00027d34 andeq r7, r2, r4, lsr sp

163c: 0002856c andeq r8, r2, ip, ror #10

1640: 00027c8c andeq r7, r2, ip, lsl #25

1644: 0000ed6b andeq lr, r0, fp, ror #26

1648: 00027540 andeq r7, r2, r0, asr #10

164c: 0002845c andeq r8, r2, ip, asr r4

1650: 000274b8 ; <UNDEFINED> instruction: 0x000274b8

1654: 000142f2 strdeq r4, [r1], -r2

1658: 00013594 muleq r1, r4, r5

165c: 000135ae andeq r3, r1, lr, lsr #11

1660: 00027b5e andeq r7, r2, lr, asr fp

1664: 00014290 muleq r1, r0, r2

1668: 000133ba ; <UNDEFINED> instruction: 0x000133ba

166c: 000133f4 strdeq r3, [r1], -r4

1670: 0001427e andeq r4, r1, lr, ror r2

1674: 000133a8 andeq r3, r1, r8, lsr #7

1678: 000133ba ; <UNDEFINED> instruction: 0x000133ba

167c: 0001426c andeq r4, r1, ip, ror #4

1680: 00013396 muleq r1, r6, r3

1684: 000134d0 ldrdeq r3, [r1], -r0

1688: 0001425a andeq r4, r1, sl, asr r2

168c: 00013384 andeq r3, r1, r4, lsl #7

1690: 000134a2 andeq r3, r1, r2, lsr #9

1694: 00014248 andeq r4, r1, r8, asr #4

1698: 00013372 andeq r3, r1, r2, ror r3

169c: 00013444 andeq r3, r1, r4, asr #8

16a0: 00014236 andeq r4, r1, r6, lsr r2

16a4: 00013360 andeq r3, r1, r0, ror #6

16a8: 000133ce andeq r3, r1, lr, asr #7

16ac: 00027ae0 andeq r7, r2, r0, ror #21

16b0: 00013526 andeq r3, r1, r6, lsr #10

16b4: 00014254 andeq r4, r1, r4, asr r2

16b8: 0001348e andeq r3, r1, lr, lsl #9

16bc: 00013498 muleq r1, r8, r4

16c0: 00027abe ; <UNDEFINED> instruction: 0x00027abe

16c4: 000134b4 ; <UNDEFINED> instruction: 0x000134b4

16c8: 4ff0e92d svcmi 0x00f0e92d

16cc: 5d80f5ad cfstr32pl mvfx15, [r0, #692] ; 0x2b4

16d0: b0814f1e addlt r4, r1, lr, lsl pc

16d4: a078f8df ldrsbtge pc, [r8], #-143 ; 0xffffff71 ; <UNPREDICTABLE>

16d8: 447f4604 ldrbtmi r4, [pc], #-1540 ; 16e0 <free@plt+0xcc0>

16dc: 44fa468b ldrbtmi r4, [sl], #1675 ; 0x68b

16e0: 0900f04f stmdbeq r0, {r0, r1, r2, r3, r6, ip, sp, lr, pc}

16e4: 78fef640 ldmvc lr!, {r6, r9, sl, ip, sp, lr, pc}^

16e8: 46397823 ldrtmi r7, [r9], -r3, lsr #16

16ec: 466d4620 strbtmi r4, [sp], -r0, lsr #12

16f0: f010b1eb ; <UNDEFINED> instruction: 0xf010b1eb

16f4: 2200ffb3 andcs pc, r0, #716 ; 0x2cc

16f8: 46061e43 strmi r1, [r6], -r3, asr #28

16fc: bf884543 svclt 0x00884543

1700: d91a702a ldmdble sl, {r1, r3, r5, ip, sp, lr}

1704: 46284434 ; <UNDEFINED> instruction: 0x46284434

1708: b1037823 tstlt r3, r3, lsr #16

170c: f7ff3401 ; <UNDEFINED> instruction: 0xf7ff3401

1710: 2800fb9f stmdacs r0, {r0, r1, r2, r3, r4, r7, r8, r9, fp, ip, sp, lr, pc}

1714: 4628d0e8 strtmi sp, [r8], -r8, ror #1

1718: 46524659 ; <UNDEFINED> instruction: 0x46524659

171c: f7ff466d ; <UNDEFINED> instruction: 0xf7ff466d

1720: 7823fa6d stmdavc r3!, {r0, r2, r3, r5, r6, r9, fp, ip, sp, lr, pc}

1724: 44814639 strmi r4, [r1], #1593 ; 0x639

1728: 2b004620 blcs 12fb0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2248>

172c: 4648d1e1 strbmi sp, [r8], -r1, ror #3

1730: 5d80f50d cfstr32pl mvfx15, [r0, #52] ; 0x34

1734: e8bdb001 pop {r0, ip, sp, pc}

1738: 466d8ff0 uqsub8mi r8, sp, r0

173c: 46214602 strtmi r4, [r1], -r2, lsl #12

1740: f0114628 ; <UNDEFINED> instruction: 0xf0114628

1744: 2300ecde movwcs lr, #3294 ; 0xcde

1748: e7db55ab ldrb r5, [fp, fp, lsr #11]

174c: 000133ee andeq r3, r1, lr, ror #7

1750: 000133ee andeq r3, r1, lr, ror #7

1754: 4ff0e92d svcmi 0x00f0e92d

1758: f8dfb0df ; <UNDEFINED> instruction: 0xf8dfb0df

175c: af04cc20 svcge 0x0004cc20

1760: 5c1cf8df ldcpl 8, cr15, [ip], {223} ; 0xdf

1764: 0454f107 ldrbeq pc, [r4], #-263 ; 0xfffffef9 ; <UNPREDICTABLE>

1768: 63bc44fc ; <UNDEFINED> instruction: 0x63bc44fc

176c: 6c14f8df ldcvs 8, cr15, [r4], {223} ; 0xdf

1770: 6379447d cmnvs r9, #2097152000 ; 0x7d000000

1774: f8dc4692 ; <UNDEFINED> instruction: 0xf8dc4692

1778: 447e1000 ldrbtmi r1, [lr], #-0

177c: 460462bb ; <UNDEFINED> instruction: 0x460462bb

1780: f8df6bbb ; <UNDEFINED> instruction: 0xf8df6bbb

1784: 68282c04 stmdavs r8!, {r2, sl, fp, sp}

1788: 447a6019 ldrbtmi r6, [sl], #-25 ; 0xffffffe7

178c: 5bfcf8df blpl fff3fb10 <\_r\_debug@@GLIBC\_2.4+0xfff161b4>

1790: 282cf8c6 stmdacs ip!, {r1, r2, r6, r7, fp, ip, sp, lr, pc}

1794: 1bf8f8df blne ffe3fb18 <\_r\_debug@@GLIBC\_2.4+0xffe161bc>

1798: f8df447d ; <UNDEFINED> instruction: 0xf8df447d

179c: f8df2bf8 ; <UNDEFINED> instruction: 0xf8df2bf8

17a0: 44793bf8 ldrbtmi r3, [r9], #-3064 ; 0xfffff408

17a4: f8c6447a ; <UNDEFINED> instruction: 0xf8c6447a

17a8: f8c657f8 ; <UNDEFINED> instruction: 0xf8c657f8

17ac: 447b17fc ldrbtmi r1, [fp], #-2044 ; 0xfffff804

17b0: 2800f8c6 stmdacs r0, {r1, r2, r6, r7, fp, ip, sp, lr, pc}

17b4: 280062fb stmdacs r0, {r0, r1, r3, r4, r5, r6, r7, r9, sp, lr}

17b8: 8159f040 cmphi r9, r0, asr #32 ; <UNPREDICTABLE>

17bc: 3bdcf8df blcc ff73fb40 <\_r\_debug@@GLIBC\_2.4+0xff7161e4>

17c0: f503447b ; <UNDEFINED> instruction: 0xf503447b

17c4: f8df7315 ; <UNDEFINED> instruction: 0xf8df7315

17c8: 46a12bd8 ssatmi r2, #2, r8, asr #23

17cc: 1bd4f8df blne ff53fb50 <\_r\_debug@@GLIBC\_2.4+0xff5161f4>

17d0: 0b00f04f bleq 3d914 <\_r\_debug@@GLIBC\_2.4+0x13fb8>

17d4: 8bd0f8df blhi ff43fb58 <\_r\_debug@@GLIBC\_2.4+0xff4161fc>

17d8: 4479447a ldrbtmi r4, [r9], #-1146 ; 0xfffffb86

17dc: 44f86339 ldrbtmi r6, [r8], #825 ; 0x339

17e0: 1bc8f8df blne ff23fb64 <\_r\_debug@@GLIBC\_2.4+0xff216208>

17e4: b03cf8c7 eorslt pc, ip, r7, asr #17

17e8: f8c24479 ; <UNDEFINED> instruction: 0xf8c24479

17ec: 6279316c rsbsvs r3, r9, #108, 2

17f0: f00e6bb8 ; <UNDEFINED> instruction: 0xf00e6bb8

17f4: 4605ff79 ; <UNDEFINED> instruction: 0x4605ff79

17f8: d03b2800 eorsle r2, fp, r0, lsl #16

17fc: 2b3d7803 blcs f5f810 <\_r\_debug@@GLIBC\_2.4+0xf35eb4>

1800: 2b00bf18 blcs 31468 <\_r\_debug@@GLIBC\_2.4+0x7b0c>

1804: f1c0bf1c ; <UNDEFINED> instruction: 0xf1c0bf1c

1808: 462a0001 strtmi r0, [sl], -r1

180c: 1881d0f0 stmne r1, {r4, r5, r6, r7, ip, lr, pc}

1810: 3f01f812 svccc 0x0001f812

1814: bf182b00 svclt 0x00182b00

1818: d1f82b3d mvnsle r2, sp, lsr fp

181c: d1e72b3d mvnle r2, sp, lsr fp

1820: 29103904 ldmdbcs r0, {r2, r8, fp, ip, sp}

1824: a302d8e4 movwge sp, #10468 ; 0x28e4

1828: 2021f853 eorcs pc, r1, r3, asr r8 ; <UNPREDICTABLE>

182c: 47184413 ; <UNDEFINED> instruction: 0x47184413

1830: 0000043f andeq r0, r0, pc, lsr r4

1834: 00000409 andeq r0, r0, r9, lsl #8

1838: ffffffc1 ; <UNDEFINED> instruction: 0xffffffc1

183c: 000003d5 ldrdeq r0, [r0], -r5

1840: 0000039b muleq r0, fp, r3

1844: 00000373 andeq r0, r0, r3, ror r3

1848: ffffffc1 ; <UNDEFINED> instruction: 0xffffffc1

184c: 00000343 andeq r0, r0, r3, asr #6

1850: 00000315 andeq r0, r0, r5, lsl r3

1854: 000002c7 andeq r0, r0, r7, asr #5

1858: 0000028f andeq r0, r0, pc, lsl #5

185c: ffffffc1 ; <UNDEFINED> instruction: 0xffffffc1

1860: 00000261 andeq r0, r0, r1, ror #4

1864: ffffffc1 ; <UNDEFINED> instruction: 0xffffffc1

1868: ffffffc1 ; <UNDEFINED> instruction: 0xffffffc1

186c: ffffffc1 ; <UNDEFINED> instruction: 0xffffffc1

1870: 0000024b andeq r0, r0, fp, asr #4

1874: 3b38f8df blcc e3fbf8 <\_r\_debug@@GLIBC\_2.4+0xe1629c>

1878: 4681464c strmi r4, [r1], ip, asr #12

187c: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

1880: f0412b00 ; <UNDEFINED> instruction: 0xf0412b00

1884: f8df80ef ; <UNDEFINED> instruction: 0xf8df80ef

1888: 447b3b2c ldrbtmi r3, [fp], #-2860 ; 0xfffff4d4

188c: 2b00685b blcs 1ba00 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xac98>

1890: f1bbbf18 ; <UNDEFINED> instruction: 0xf1bbbf18

1894: f0400f00 ; <UNDEFINED> instruction: 0xf0400f00

1898: f8df86db ; <UNDEFINED> instruction: 0xf8df86db

189c: f8da3b1c ; <UNDEFINED> instruction: 0xf8da3b1c

18a0: 447b2000 ldrbtmi r2, [fp], #-0

18a4: f040429a ; <UNDEFINED> instruction: 0xf040429a

18a8: f8df81fd ; <UNDEFINED> instruction: 0xf8df81fd

18ac: f8df2b10 ; <UNDEFINED> instruction: 0xf8df2b10

18b0: 447a3b10 ldrbtmi r3, [sl], #-2832 ; 0xfffff4f0

18b4: 1b0cf8df blne 33fc38 <\_r\_debug@@GLIBC\_2.4+0x3162dc>

18b8: f8df447b ; <UNDEFINED> instruction: 0xf8df447b

18bc: 68125b0c ldmdavs r2, {r2, r3, r8, r9, fp, ip, lr}

18c0: f8d34479 ; <UNDEFINED> instruction: 0xf8d34479

18c4: 447db000 ldrbtmi fp, [sp], #-0

18c8: f8df6813 ; <UNDEFINED> instruction: 0xf8df6813

18cc: f8c12b00 ; <UNDEFINED> instruction: 0xf8c12b00

18d0: 447a351c ldrbtmi r3, [sl], #-1308 ; 0xfffffae4

18d4: f1bb633a ; <UNDEFINED> instruction: 0xf1bb633a

18d8: dd250f01 stcle 15, cr0, [r5, #-4]!

18dc: f8df682e ; <UNDEFINED> instruction: 0xf8df682e

18e0: f8d61af0 ; <UNDEFINED> instruction: 0xf8d61af0

18e4: 44798004 ldrbtmi r8, [r9], #-4

18e8: f0104640 ; <UNDEFINED> instruction: 0xf0104640

18ec: bb30fc31 bllt c409b8 <\_r\_debug@@GLIBC\_2.4+0xc1705c>

18f0: 1ae0f8df bne ff83fc74 <\_r\_debug@@GLIBC\_2.4+0xff816318>

18f4: 3bfff10b blcc ffffdd28 <\_r\_debug@@GLIBC\_2.4+0xfffd43cc>

18f8: 3adcf8df bcc ff73fc7c <\_r\_debug@@GLIBC\_2.4+0xff716320>

18fc: 0f01f1bb svceq 0x0001f1bb

1900: f8df4479 ; <UNDEFINED> instruction: 0xf8df4479

1904: 447b2ad8 ldrbtmi r2, [fp], #-2776 ; 0xfffff528

1908: 0604f106 streq pc, [r4], -r6, lsl #2

190c: 602e447a eorvs r4, lr, sl, ror r4

1910: b000f8c3 andlt pc, r0, r3, asr #17

1914: 30fff04f rscscc pc, pc, pc, asr #32

1918: 62d0680b sbcsvs r6, r0, #720896 ; 0xb0000

191c: 0201f04f andeq pc, r1, #79 ; 0x4f

1920: 441363fa ldrmi r6, [r3], #-1018 ; 0xfffffc06

1924: dcd9600b ldclle 0, cr6, [r9], {11}

1928: 4ab4f8df bmi fed3fcac <\_r\_debug@@GLIBC\_2.4+0xfed16350>

192c: 4621447c ; <UNDEFINED> instruction: 0x4621447c

1930: f00a2002 ; <UNDEFINED> instruction: 0xf00a2002

1934: 207ffa4b rsbscs pc, pc, fp, asr #20

1938: fa86f010 blx fe1bd980 <\_r\_debug@@GLIBC\_2.4+0xfe194024>

193c: f8dfe7f7 ; <UNDEFINED> instruction: 0xf8dfe7f7

1940: 46401aa4 strbmi r1, [r0], -r4, lsr #21

1944: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1948: b988fc03 stmiblt r8, {r0, r1, sl, fp, ip, sp, lr, pc}

194c: 1a98f8df bne fe63fcd0 <\_r\_debug@@GLIBC\_2.4+0xfe616374>

1950: f8df2302 ; <UNDEFINED> instruction: 0xf8df2302

1954: f10b2a98 ; <UNDEFINED> instruction: 0xf10b2a98

1958: 44793bff ldrbtmi r3, [r9], #-3071 ; 0xfffff401

195c: 447a63fb ldrbtmi r6, [sl], #-1019 ; 0xfffffc05

1960: 680b3604 stmdavs fp, {r2, r9, sl, ip, sp}

1964: f8c2602e ; <UNDEFINED> instruction: 0xf8c2602e

1968: 3301b000 movwcc fp, #4096 ; 0x1000

196c: e7b2600b ldr r6, [r2, fp]!

1970: 1a7cf8df bne 1f3fcf4 <\_r\_debug@@GLIBC\_2.4+0x1f16398>

1974: 44794640 ldrbtmi r4, [r9], #-1600 ; 0xfffff9c0

1978: fbeaf010 blx ffabd9c2 <\_r\_debug@@GLIBC\_2.4+0xffa94066>

197c: f8dfb9a0 ; <UNDEFINED> instruction: 0xf8dfb9a0

1980: f10b1a74 ; <UNDEFINED> instruction: 0xf10b1a74

1984: f8df3bff ; <UNDEFINED> instruction: 0xf8df3bff

1988: 20013a70 andcs r3, r1, r0, ror sl

198c: f8df4479 ; <UNDEFINED> instruction: 0xf8df4479

1990: 447b2a6c ldrbtmi r2, [fp], #-2668 ; 0xfffff594

1994: 447a3604 ldrbtmi r3, [sl], #-1540 ; 0xfffff9fc

1998: f8c3602e ; <UNDEFINED> instruction: 0xf8c3602e

199c: 680bb000 stmdavs fp, {ip, sp, pc}

19a0: 44036150 strmi r6, [r3], #-336 ; 0xfffffeb0

19a4: e796600b ldr r6, [r6, fp]

19a8: 1a54f8df bne 153fd2c <\_r\_debug@@GLIBC\_2.4+0x15163d0>

19ac: 44794640 ldrbtmi r4, [r9], #-1600 ; 0xfffff9c0

19b0: fbcef010 blx ff3bd9fa <\_r\_debug@@GLIBC\_2.4+0xff39409e>

19b4: 0302f1bb movweq pc, #8635 ; 0x21bb ; <UNPREDICTABLE>

19b8: 2301bf18 movwcs fp, #7960 ; 0x1f18

19bc: bf0c2800 svclt 0x000c2800

19c0: 20004618 andcs r4, r0, r8, lsl r6

19c4: f0402800 ; <UNDEFINED> instruction: 0xf0402800

19c8: f8df85fc ; <UNDEFINED> instruction: 0xf8df85fc

19cc: 46401a38 ; <UNDEFINED> instruction: 0x46401a38

19d0: 4479627b ldrbtmi r6, [r9], #-635 ; 0xfffffd85

19d4: fbbcf010 blx fef3da1e <\_r\_debug@@GLIBC\_2.4+0xfef140c2>

19d8: 28006a7b stmdacs r0, {r0, r1, r3, r4, r5, r6, r9, fp, sp, lr}

19dc: 2300bf18 movwcs fp, #3864 ; 0xf18

19e0: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

19e4: f8df85d7 ; <UNDEFINED> instruction: 0xf8df85d7

19e8: 46401a20 strbmi r1, [r0], -r0, lsr #20

19ec: f0104479 ; <UNDEFINED> instruction: 0xf0104479

19f0: 2800fbaf stmdacs r0, {r0, r1, r2, r3, r5, r7, r8, r9, fp, ip, sp, lr, pc}

19f4: 8264f042 rsbhi pc, r4, #66 ; 0x42

19f8: 0f02f1bb svceq 0x0002f1bb

19fc: 8191f002 orrshi pc, r1, r2

1a00: f10768b3 ; <UNDEFINED> instruction: 0xf10768b3

1a04: f8df0660 ; <UNDEFINED> instruction: 0xf8df0660

1a08: 60338a04 eorsvs r8, r3, r4, lsl #20

1a0c: e00244f8 strd r4, [r2], -r8

1a10: fa1ef7ff blx 7bfa14 <\_r\_debug@@GLIBC\_2.4+0x7960b8>

1a14: 4641b9d8 ; <UNDEFINED> instruction: 0x4641b9d8

1a18: f00f4630 ; <UNDEFINED> instruction: 0xf00f4630

1a1c: 4683f855 pkhtbmi pc, r3, r5, asr #16 ; <UNPREDICTABLE>

1a20: d1f52800 mvnsle r2, r0, lsl #16

1a24: 69e8f8df stmibvs r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1a28: 09e8f8df stmibeq r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1a2c: f8df447e ; <UNDEFINED> instruction: 0xf8df447e

1a30: 447819e8 ldrbtmi r1, [r8], #-2536 ; 0xfffff618

1a34: 44796833 ldrbtmi r6, [r9], #-2099 ; 0xfffff7cd

1a38: 33026802 movwcc r6, #10242 ; 0x2802

1a3c: 680b6033 stmdavs fp, {r0, r1, r4, r5, sp, lr}

1a40: 0b02f1a2 bleq be0d0 <\_r\_debug@@GLIBC\_2.4+0x94774>

1a44: b000f8c0 andlt pc, r0, r0, asr #17

1a48: 600b3308 andvs r3, fp, r8, lsl #6

1a4c: 2008e743 andcs lr, r8, r3, asr #14

1a50: efbcf7fe svc 0x00bcf7fe

1a54: 691b6b3b ldmdbvs fp, {r0, r1, r3, r4, r5, r8, r9, fp, sp, lr}

1a58: b000f8c0 andlt pc, r0, r0, asr #17

1a5c: f0002b00 ; <UNDEFINED> instruction: 0xf0002b00

1a60: 685a8594 ldmdavs sl, {r2, r4, r7, r8, sl, pc}^

1a64: 60426b39 subvs r6, r2, r9, lsr fp

1a68: 60586108 subsvs r6, r8, r8, lsl #2

1a6c: f8dfe7d3 ; <UNDEFINED> instruction: 0xf8dfe7d3

1a70: 447b39ac ldrbtmi r3, [fp], #-2476 ; 0xfffff654

1a74: 235df203 cmpcs sp, #805306368 ; 0x30000000 ; <UNPREDICTABLE>

1a78: 4628e6a5 strtmi lr, [r8], -r5, lsr #13

1a7c: 6b392214 blvs e4a2d4 <\_r\_debug@@GLIBC\_2.4+0xe20978>

1a80: ffdcf010 ; <UNDEFINED> instruction: 0xffdcf010

1a84: 28006bfb stmdacs r0, {r0, r1, r3, r4, r5, r6, r7, r8, r9, fp, sp, lr}

1a88: 2303bf08 movwcs fp, #16136 ; 0x3f08

1a8c: e6af63fb ; <UNDEFINED> instruction: 0xe6af63fb

1a90: 6a792210 bvs 1e4a2d8 <\_r\_debug@@GLIBC\_2.4+0x1e2097c>

1a94: f0104628 ; <UNDEFINED> instruction: 0xf0104628

1a98: 2800ffd1 stmdacs r0, {r0, r4, r6, r7, r8, r9, sl, fp, ip, sp, lr, pc}

1a9c: aea8f47f mcrge 4, 5, pc, cr8, cr15, {3} ; <UNPREDICTABLE>

1aa0: 3000f8d8 ldrdcc pc, [r0], -r8

1aa4: 22013511 andcs r3, r1, #71303168 ; 0x4400000

1aa8: 5170f8c8 cmnpl r0, r8, asr #17 ; <UNPREDICTABLE>

1aac: 6300f443 movwvs pc, #1091 ; 0x443 ; <UNPREDICTABLE>

1ab0: 2024f8c8 eorcs pc, r4, r8, asr #17

1ab4: 3000f8c8 andcc pc, r0, r8, asr #17

1ab8: 63fb2303 mvnsvs r2, #201326592 ; 0xc000000

1abc: f8dfe698 ; <UNDEFINED> instruction: 0xf8dfe698

1ac0: 447b3960 ldrbtmi r3, [fp], #-2400 ; 0xfffff6a0

1ac4: 2b00681b blcs 1bb38 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xadd0>

1ac8: ae92f47f mrcge 4, 4, APSR\_nzcv, cr2, cr15, {3}

1acc: 1954f8df ldmdbne r4, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1ad0: 4628220e strtmi r2, [r8], -lr, lsl #4

1ad4: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1ad8: 2800ffb1 stmdacs r0, {r0, r4, r5, r7, r8, r9, sl, fp, ip, sp, lr, pc}

1adc: ae88f47f mcrge 4, 4, pc, cr8, cr15, {3} ; <UNPREDICTABLE>

1ae0: 2b007beb blcs 20a94 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfd2c>

1ae4: ae84f43f mcrge 4, 4, pc, cr4, cr15, {1} ; <UNPREDICTABLE>

1ae8: 393cf8df ldmdbcc ip!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1aec: 447b350f ldrbtmi r3, [fp], #-1295 ; 0xfffffaf1

1af0: 516cf8c3 msrpl SPSR\_fs, r3, asr #17

1af4: f8dfe67c ; <UNDEFINED> instruction: 0xf8dfe67c

1af8: 220d1934 andcs r1, sp, #52, 18 ; 0xd0000

1afc: 44794628 ldrbtmi r4, [r9], #-1576 ; 0xfffff9d8

1b00: ff9cf010 ; <UNDEFINED> instruction: 0xff9cf010

1b04: f0002800 ; <UNDEFINED> instruction: 0xf0002800

1b08: f8df8769 ; <UNDEFINED> instruction: 0xf8df8769

1b0c: 447b3924 ldrbtmi r3, [fp], #-2340 ; 0xfffff6dc

1b10: 2b00681b blcs 1bb84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xae1c>

1b14: ae6cf47f mcrge 4, 3, pc, cr12, cr15, {3} ; <UNPREDICTABLE>

1b18: 1918f8df ldmdbne r8, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1b1c: 4628220d strtmi r2, [r8], -sp, lsl #4

1b20: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1b24: 2800ff8b stmdacs r0, {r0, r1, r3, r7, r8, r9, sl, fp, ip, sp, lr, pc}

1b28: ae62f47f mcrge 4, 3, pc, cr2, cr15, {3} ; <UNPREDICTABLE>

1b2c: f8df7bab ; <UNDEFINED> instruction: 0xf8df7bab

1b30: 2b312908 blcs c4bf58 <\_r\_debug@@GLIBC\_2.4+0xc225fc>

1b34: 33fff04f mvnscc pc, #79 ; 0x4f

1b38: bf18447a svclt 0x0018447a

1b3c: f8c22300 ; <UNDEFINED> instruction: 0xf8c22300

1b40: e6553164 ldrb r3, [r5], -r4, ror #2

1b44: 38f4f8df ldmcc r4!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1b48: 447b220c ldrbtmi r2, [fp], #-524 ; 0xfffffdf4

1b4c: 2b00681b blcs 1bbc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xae58>

1b50: 8771f040 ldrbhi pc, [r1, -r0, asr #32]! ; <UNPREDICTABLE>

1b54: 18e8f8df stmiane r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1b58: 44794628 ldrbtmi r4, [r9], #-1576 ; 0xfffff9d8

1b5c: ff6ef010 ; <UNDEFINED> instruction: 0xff6ef010

1b60: f0402800 ; <UNDEFINED> instruction: 0xf0402800

1b64: f8df8721 ; <UNDEFINED> instruction: 0xf8df8721

1b68: 350d38dc strcc r3, [sp, #-2268] ; 0xfffff724

1b6c: 611d447b tstvs sp, fp, ror r4

1b70: f8dfe63e ; <UNDEFINED> instruction: 0xf8dfe63e

1b74: 447b38d4 ldrbtmi r3, [fp], #-2260 ; 0xfffff72c

1b78: 2b00681b blcs 1bbec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xae84>

1b7c: ae38f47f mrcge 4, 1, APSR\_nzcv, cr8, cr15, {3}

1b80: 18c8f8df stmiane r8, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1b84: 4628220b strtmi r2, [r8], -fp, lsl #4

1b88: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1b8c: 2800ff57 stmdacs r0, {r0, r1, r2, r4, r6, r8, r9, sl, fp, ip, sp, lr, pc}

1b90: ae2ef47f mcrge 4, 1, pc, cr14, cr15, {3} ; <UNPREDICTABLE>

1b94: 38b8f8df ldmcc r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1b98: 447b350c ldrbtmi r3, [fp], #-1292 ; 0xfffffaf4

1b9c: 5160f8c3 msrpl SPSR\_, r3, asr #17

1ba0: f8dfe626 ; <UNDEFINED> instruction: 0xf8dfe626

1ba4: 447b38b0 ldrbtmi r3, [fp], #-2224 ; 0xfffff750

1ba8: 2b00681b blcs 1bc1c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xaeb4>

1bac: ae20f47f mcrge 4, 1, pc, cr0, cr15, {3} ; <UNPREDICTABLE>

1bb0: 18a4f8df stmiane r4!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1bb4: 22094628 andcs r4, r9, #40, 12 ; 0x2800000

1bb8: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1bbc: 2800ff3f stmdacs r0, {r0, r1, r2, r3, r4, r5, r8, r9, sl, fp, ip, sp, lr, pc}

1bc0: ae16f47f mrcge 4, 0, APSR\_nzcv, cr6, cr15, {3}

1bc4: fc36f00e ldc2 0, cr15, [r6], #-56 ; 0xffffffc8

1bc8: f8dfe612 ; <UNDEFINED> instruction: 0xf8dfe612

1bcc: 22081890 andcs r1, r8, #144, 16 ; 0x900000

1bd0: 44794628 ldrbtmi r4, [r9], #-1576 ; 0xfffff9d8

1bd4: ff32f010 ; <UNDEFINED> instruction: 0xff32f010

1bd8: f0002800 ; <UNDEFINED> instruction: 0xf0002800

1bdc: f8df8767 ; <UNDEFINED> instruction: 0xf8df8767

1be0: 22081880 andcs r1, r8, #128, 16 ; 0x800000

1be4: 44794628 ldrbtmi r4, [r9], #-1576 ; 0xfffff9d8

1be8: ff28f010 ; <UNDEFINED> instruction: 0xff28f010

1bec: f47f2800 ; <UNDEFINED> instruction: 0xf47f2800

1bf0: 7a6badff bvc 1aed3f4 <\_r\_debug@@GLIBC\_2.4+0x1ac3a98>

1bf4: 286cf8df stmdacs ip!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1bf8: 447a3300 ldrbtmi r3, [sl], #-768 ; 0xfffffd00

1bfc: 2301bf18 movwcs fp, #7960 ; 0x1f18

1c00: e5f56313 ldrb r6, [r5, #787]! ; 0x313

1c04: 1860f8df stmdane r0!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1c08: 46282207 strtmi r2, [r8], -r7, lsl #4

1c0c: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1c10: 2800ff15 stmdacs r0, {r0, r2, r4, r8, r9, sl, fp, ip, sp, lr, pc}

1c14: 8754f000 ldrbhi pc, [r4, -r0] ; <UNPREDICTABLE>

1c18: 1850f8df ldmdane r0, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1c1c: 46282207 strtmi r2, [r8], -r7, lsl #4

1c20: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1c24: 2800ff0b stmdacs r0, {r0, r1, r3, r8, r9, sl, fp, ip, sp, lr, pc}

1c28: 8128f041 msrhi CPSR\_f, r1, asr #32

1c2c: 3840f8df stmdacc r0, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

1c30: 447b3508 ldrbtmi r3, [fp], #-1288 ; 0xfffffaf8

1c34: e5db60dd ldrb r6, [fp, #221] ; 0xdd

1c38: 1838f8df ldmdane r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1c3c: 46282205 strtmi r2, [r8], -r5, lsl #4

1c40: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1c44: 2800fefb stmdacs r0, {r0, r1, r3, r4, r5, r6, r7, r9, sl, fp, ip, sp, lr, pc}

1c48: 86a9f000 strthi pc, [r9], r0

1c4c: 1828f8df stmdane r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1c50: 46282205 strtmi r2, [r8], -r5, lsl #4

1c54: f0104479 ; <UNDEFINED> instruction: 0xf0104479

1c58: 2800fef1 stmdacs r0, {r0, r4, r5, r6, r7, r9, sl, fp, ip, sp, lr, pc}

1c5c: adc8f47f cfstrdge mvd15, [r8, #508] ; 0x1fc

1c60: 3818f8df ldmdacc r8, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

1c64: 6afa3506 bvs ffe8f084 <\_r\_debug@@GLIBC\_2.4+0xffe65728>

1c68: 601d58d3 ; <UNDEFINED> instruction: 0x601d58d3

1c6c: 782be5c0 stmdavc fp!, {r6, r7, r8, sl, sp, lr, pc}

1c70: f47f2b57 ; <UNDEFINED> instruction: 0xf47f2b57

1c74: 786aadbd stmdavc sl!, {r0, r2, r3, r4, r5, r7, r8, sl, fp, sp, pc}^

1c78: 2a411c6b bcs 1048e2c <\_r\_debug@@GLIBC\_2.4+0x101f4d0>

1c7c: adb8f47f cfldrsge mvf15, [r8, #508]! ; 0x1fc

1c80: 2f01f813 svccs 0x0001f813

1c84: f47f2a52 ; <UNDEFINED> instruction: 0xf47f2a52

1c88: 785badb3 ldmdavc fp, {r0, r1, r4, r5, r7, r8, sl, fp, sp, pc}^

1c8c: f47f2b4e ; <UNDEFINED> instruction: 0xf47f2b4e

1c90: 796badaf stmdbvc fp!, {r0, r1, r2, r3, r5, r7, r8, sl, fp, sp, pc}^

1c94: 27e8f8df ubfxcs pc, pc, #17, #9

1c98: 447a3300 ldrbtmi r3, [sl], #-768 ; 0xfffffd00

1c9c: 2301bf18 movwcs fp, #7960 ; 0x1f18

1ca0: e5a56253 str r6, [r5, #595]! ; 0x253

1ca4: 17dcf8df ; <UNDEFINED> instruction: 0x17dcf8df

1ca8: 462b2500 strtmi r2, [fp], -r0, lsl #10

1cac: 44799501 ldrbtmi r9, [r9], #-1281 ; 0xfffffaff

1cb0: f04f462a ; <UNDEFINED> instruction: 0xf04f462a

1cb4: 96005600 strls r5, [r0], -r0, lsl #12

1cb8: f0064608 ; <UNDEFINED> instruction: 0xf0064608

1cbc: 4683fbb1 ; <UNDEFINED> instruction: 0x4683fbb1

1cc0: f0012800 ; <UNDEFINED> instruction: 0xf0012800

1cc4: f8da87e4 ; <UNDEFINED> instruction: 0xf8da87e4

1cc8: 46293000 strtmi r3, [r9], -r0

1ccc: f8c06b7a ; <UNDEFINED> instruction: 0xf8c06b7a

1cd0: f8c04154 ; <UNDEFINED> instruction: 0xf8c04154

1cd4: f8a03158 ; <UNDEFINED> instruction: 0xf8a03158

1cd8: f006215c ; <UNDEFINED> instruction: 0xf006215c

1cdc: f8dffb67 ; <UNDEFINED> instruction: 0xf8dffb67

1ce0: 447b37a8 ldrbtmi r3, [fp], #-1960 ; 0xfffff858

1ce4: 455b681b ldrbmi r6, [fp, #-2075] ; 0xfffff7e5

1ce8: 633dbf08 teqvs sp, #8, 30

1cec: 87dcf041 ldrbhi pc, [ip, r1, asr #32] ; <UNPREDICTABLE>

1cf0: 25006b7b strcs r6, [r0, #-2939] ; 0xfffff485

1cf4: 2194f8db ; <UNDEFINED> instruction: 0x2194f8db

1cf8: 31fff04f mvnscc pc, pc, asr #32

1cfc: 51b4f8cb ; <UNDEFINED> instruction: 0x51b4f8cb

1d00: 1343eb04 movtne lr, #15108 ; 0x3b04

1d04: 42a33201 adcmi r3, r3, #268435456 ; 0x10000000

1d08: 51b8f8cb ; <UNDEFINED> instruction: 0x51b8f8cb

1d0c: 2194f8cb orrscs pc, r4, fp, asr #17

1d10: 11b0f8cb asrsne pc, fp, #17 ; <UNPREDICTABLE>

1d14: 84fcf240 ldrbthi pc, [ip], #576 ; 0x240 ; <UNPREDICTABLE>

1d18: 2770f8df ; <UNDEFINED> instruction: 0x2770f8df

1d1c: f8df43e1 ; <UNDEFINED> instruction: 0xf8df43e1

1d20: 440b6770 strmi r6, [fp], #-1904 ; 0xfffff890

1d24: f8df447a ; <UNDEFINED> instruction: 0xf8df447a

1d28: f8df076c ; <UNDEFINED> instruction: 0xf8df076c

1d2c: f023876c ; <UNDEFINED> instruction: 0xf023876c

1d30: f104031f ; <UNDEFINED> instruction: 0xf104031f

1d34: 447e0140 ldrbtmi r0, [lr], #-320 ; 0xfffffec0

1d38: 6912440b ldmdbvs r2, {r0, r1, r3, sl, lr}

1d3c: 4478623e ldrbtmi r6, [r8], #-574 ; 0xfffffdc2

1d40: 6758f8df smmlsvs r8, pc, r8, pc ; <UNPREDICTABLE>

1d44: 627b44f8 rsbsvs r4, fp, #248, 8 ; 0xf8000000

1d48: 5e52f24e cdppl 2, 5, cr15, cr2, cr14, {2}

1d4c: 3750f8df smmlscc r0, pc, r8, pc ; <UNPREDICTABLE>

1d50: 61b8447e ; <UNDEFINED> instruction: 0x61b8447e

1d54: 4e74f2c6 cdpmi 2, 7, cr15, cr4, cr6, {6}

1d58: 61fe447b mvnsvs r4, fp, ror r4

1d5c: 46aa617b ; <UNDEFINED> instruction: 0x46aa617b

1d60: 637d4253 cmnvs sp, #805306373 ; 0x30000005

1d64: f10062bb ; <UNDEFINED> instruction: 0xf10062bb

1d68: f24e0314 vcge.s8 d16, d14, d4

1d6c: 613b5051 teqvs fp, r1, asr r0

1d70: 4074f2c6 rsbsmi pc, r4, r6, asr #5

1d74: 0320f108 msreq CPSR\_, #8, 2

1d78: f10460fb ; <UNDEFINED> instruction: 0xf10460fb

1d7c: e00c0320 and r0, ip, r0, lsr #6

1d80: f0002a02 ; <UNDEFINED> instruction: 0xf0002a02

1d84: 2a0383fa bcs e2d74 <\_r\_debug@@GLIBC\_2.4+0xb9418>

1d88: 83d6f000 bicshi pc, r6, #0

1d8c: f0002a01 ; <UNDEFINED> instruction: 0xf0002a01

1d90: 6a7a83af bvs 1ea2c54 <\_r\_debug@@GLIBC\_2.4+0x1e792f8>

1d94: 429a3320 addsmi r3, sl, #32, 6 ; 0x80000000

1d98: f853d02e ; <UNDEFINED> instruction: 0xf853d02e

1d9c: 2a062c20 bcs 18ce24 <\_r\_debug@@GLIBC\_2.4+0x1634c8>

1da0: 82c1f000 sbchi pc, r1, #0

1da4: 4282d9ec addmi sp, r2, #236, 18 ; 0x3b0000

1da8: 82b7f000 adcshi pc, r7, #0

1dac: f0004572 ; <UNDEFINED> instruction: 0xf0004572

1db0: 2a0782ab bcs 1e2864 <\_r\_debug@@GLIBC\_2.4+0x1b8f08>

1db4: f853d1ed ; <UNDEFINED> instruction: 0xf853d1ed

1db8: 2a002c0c bcs cdf0 <\_dl\_mcount@@GLIBC\_2.4+0x3b0>

1dbc: f8cbd0e9 ; <UNDEFINED> instruction: 0xf8cbd0e9

1dc0: f8532238 ; <UNDEFINED> instruction: 0xf8532238

1dc4: f8cb2c04 ; <UNDEFINED> instruction: 0xf8cb2c04

1dc8: f853223c ; <UNDEFINED> instruction: 0xf853223c

1dcc: 2a006c18 bcs 1ce34 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc0cc>

1dd0: 849af040 ldrhi pc, [sl], #64 ; 0x40

1dd4: 2240f8cb subcs pc, r0, #13303808 ; 0xcb0000

1dd8: f8532101 ; <UNDEFINED> instruction: 0xf8532101

1ddc: 33202c10 ; <UNDEFINED> instruction: 0x33202c10

1de0: 6230f8cb eorsvs pc, r0, #13303808 ; 0xcb0000

1de4: 1248f8cb subne pc, r8, #13303808 ; 0xcb0000

1de8: 2234f8cb eorscs pc, r4, #13303808 ; 0xcb0000

1dec: f8c269fa ; <UNDEFINED> instruction: 0xf8c269fa

1df0: 6a7a180c bvs 1e87e28 <\_r\_debug@@GLIBC\_2.4+0x1e5e4cc>

1df4: d1d0429a ; <UNDEFINED> instruction: 0xd1d0429a

1df8: 2230f8db eorscs pc, r0, #14352384 ; 0xdb0000

1dfc: f8dbb122 ; <UNDEFINED> instruction: 0xf8dbb122

1e00: 44133000 ldrmi r3, [r3], #-0

1e04: 3230f8cb eorscc pc, r0, #13303808 ; 0xcb0000

1e08: 2b006b7b blcs 1cbfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xbe94>

1e0c: 8291f000 addshi pc, r1, #0

1e10: 0f00f1ba svceq 0x0000f1ba

1e14: 8295f000 addshi pc, r5, #0

1e18: 3688f8df pkhtbcc pc, r8, pc, asr #17 ; <UNPREDICTABLE>

1e1c: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

1e20: 2a002534 bcs b2f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4664>

1e24: 8448f000 strbhi pc, [r8], #-0 ; <UNPREDICTABLE>

1e28: 367cf8df ; <UNDEFINED> instruction: 0x367cf8df

1e2c: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

1e30: b1ca2570 biclt r2, sl, r0, ror r5

1e34: 154cf8d3 strbne pc, [ip, #-2259] ; 0xfffff72d ; <UNPREDICTABLE>

1e38: f8d36852 ; <UNDEFINED> instruction: 0xf8d36852

1e3c: 684c6534 stmdavs ip, {r2, r4, r5, r8, sl, sp, lr}^

1e40: 68304414 ldmdavs r0!, {r2, r4, sl, lr}

1e44: f0104621 ; <UNDEFINED> instruction: 0xf0104621

1e48: b168f983 msrlt SPSR\_f, r3, lsl #19

1e4c: 365cf8df ; <UNDEFINED> instruction: 0x365cf8df

1e50: 21012200 mrscs r2, R9\_usr

1e54: 631a447b tstvs sl, #2063597568 ; 0x7b000000

1e58: 62dc6872 sbcsvs r6, ip, #7471104 ; 0x720000

1e5c: 2a006359 bcs 1abc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x9e60>

1e60: 869df041 ldrhi pc, [sp], r1, asr #32

1e64: 6073332c rsbsvs r3, r3, ip, lsr #6

1e68: 3644f8df ; <UNDEFINED> instruction: 0x3644f8df

1e6c: f893447b ; <UNDEFINED> instruction: 0xf893447b

1e70: 075b36b0 ; <UNDEFINED> instruction: 0x075b36b0

1e74: 86a0f141 strthi pc, [r0], r1, asr #2

1e78: 2b006b3b blcs 1cb6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xbe04>

1e7c: 80b4f040 adcshi pc, r4, r0, asr #32

1e80: 2008f8db ldrdcs pc, [r8], -fp

1e84: f0002a00 ; <UNDEFINED> instruction: 0xf0002a00

1e88: 681380ac ldmdavs r3, {r2, r3, r5, r7, pc}

1e8c: 0120f10b msreq CPSR\_, fp, lsl #2

1e90: f64fb33b ; <UNDEFINED> instruction: 0xf64fb33b

1e94: f64f5cff ; <UNDEFINED> instruction: 0xf64f5cff

1e98: 242266ff strtcs r6, [r2], #-1791 ; 0xfffff901

1e9c: 7cfff6c6 ldclvc 6, cr15, [pc], #792 ; 21bc <free@plt+0x179c>

1ea0: 76fff6c6 ldrbtvc pc, [pc], r6, asr #13 ; <UNPREDICTABLE>

1ea4: 0400f2c7 streq pc, [r0], #-711 ; 0xfffffd39

1ea8: 4e10f06f cdpmi 0, 1, cr15, cr0, cr15, {3}

1eac: 1ae3e005 bne ff8f9ec8 <\_r\_debug@@GLIBC\_2.4+0xff8d056c>

1eb0: 2023f841 eorcs pc, r3, r1, asr #16

1eb4: 3f08f852 svccc 0x0008f852

1eb8: 2b22b19b blcs 8ae52c <\_r\_debug@@GLIBC\_2.4+0x884bd0>

1ebc: ebaed9f8 bl febb86a4 <\_r\_debug@@GLIBC\_2.4+0xfeb8ed48>

1ec0: 280f0003 stmdacs pc, {r0, r1} ; <UNPREDICTABLE>

1ec4: f343d9f3 vmul.p8 <illegal reg q14.5>, <illegal reg q9.5>, <illegal reg q9.5>

1ec8: f110001e ; <UNDEFINED> instruction: 0xf110001e

1ecc: f2400f04 vmax.f32 d16, d0, d4

1ed0: eba18401 bl fe862edc <\_r\_debug@@GLIBC\_2.4+0xfe839580>

1ed4: f8c00080 ; <UNDEFINED> instruction: 0xf8c00080

1ed8: f85220c8 ; <UNDEFINED> instruction: 0xf85220c8

1edc: 2b003f08 blcs 11b04 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd9c>

1ee0: f8dbd1eb ; <UNDEFINED> instruction: 0xf8dbd1eb

1ee4: 2b003000 blcs deec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x4d4>

1ee8: f8dbd035 ; <UNDEFINED> instruction: 0xf8dbd035

1eec: b1122030 tstlt r2, r0, lsr r0

1ef0: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1ef4: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1ef8: b112202c tstlt r2, ip, lsr #32

1efc: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f00: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f04: b1122034 tstlt r2, r4, lsr r0

1f08: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f0c: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f10: b1122038 tstlt r2, r8, lsr r0

1f14: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f18: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f1c: b112203c tstlt r2, ip, lsr r0

1f20: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f24: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f28: b1122064 tstlt r2, r4, rrx

1f2c: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f30: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f34: b112207c tstlt r2, ip, ror r0

1f38: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f3c: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f40: b11220e8 tstlt r2, r8, ror #1

1f44: 44196851 ldrmi r6, [r9], #-2129 ; 0xfffff7af

1f48: f8db6051 ; <UNDEFINED> instruction: 0xf8db6051

1f4c: b1122150 tstlt r2, r0, asr r1

1f50: 440b6851 strmi r6, [fp], #-2129 ; 0xfffff7af

1f54: f8db6053 ; <UNDEFINED> instruction: 0xf8db6053

1f58: b12b3070 ; <UNDEFINED> instruction: 0xb12b3070

1f5c: 2b07685b blcs 1dc0d0 <\_r\_debug@@GLIBC\_2.4+0x1b2774>

1f60: 2b11bf18 blcs 471bc8 <\_r\_debug@@GLIBC\_2.4+0x44826c>

1f64: 87d1f040 ldrbhi pc, [r1, r0, asr #32] ; <UNPREDICTABLE>

1f68: 303cf8db ldrsbtcc pc, [ip], -fp ; <UNPREDICTABLE>

1f6c: f8dbb12b ; <UNDEFINED> instruction: 0xf8dbb12b

1f70: 685b3044 ldmdavs fp, {r2, r6, ip, sp}^

1f74: f0402b0c ; <UNDEFINED> instruction: 0xf0402b0c

1f78: f8db87d4 ; <UNDEFINED> instruction: 0xf8db87d4

1f7c: b12b3064 ; <UNDEFINED> instruction: 0xb12b3064

1f80: 306cf8db ldrdcc pc, [ip], #-139 ; 0xffffff75 ; <UNPREDICTABLE>

1f84: 2b08685b blcs 21c0f8 <\_r\_debug@@GLIBC\_2.4+0x1f279c>

1f88: 87d7f040 ldrbhi pc, [r7, r0, asr #32] ; <UNPREDICTABLE>

1f8c: 3098f8db ; <UNDEFINED> instruction: 0x3098f8db

1f90: 685ab173 ldmdavs sl, {r0, r1, r4, r5, r6, r8, ip, sp, pc}^

1f94: bf480790 svclt 0x00480790

1f98: 3060f8cb rsbcc pc, r0, fp, asr #17

1f9c: 2210f8cb andscs pc, r0, #13303808 ; 0xcb0000

1fa0: bf480751 svclt 0x00480751

1fa4: 3078f8cb rsbscc pc, r8, fp, asr #17

1fa8: bf480716 svclt 0x00480716

1fac: 3080f8cb addcc pc, r0, fp, asr #17

1fb0: 30bcf8db ldrsbtcc pc, [ip], fp ; <UNPREDICTABLE>

1fb4: f8dfb17b ; <UNDEFINED> instruction: 0xf8dfb17b

1fb8: 685b24fc ldmdavs fp, {r2, r3, r4, r5, r6, r7, sl, sp}^

1fbc: 6812447a ldmdavs r2, {r1, r3, r4, r5, r6, sl, lr}

1fc0: 320cf8cb andcc pc, ip, #13303808 ; 0xcb0000

1fc4: f1010654 ; <UNDEFINED> instruction: 0xf1010654

1fc8: 07d8875a ; <UNDEFINED> instruction: 0x07d8875a

1fcc: f8dbbf44 ; <UNDEFINED> instruction: 0xf8dbbf44

1fd0: f8cb30bc ; <UNDEFINED> instruction: 0xf8cb30bc

1fd4: f8db3080 ; <UNDEFINED> instruction: 0xf8db3080

1fd8: b1133094 ; <UNDEFINED> instruction: 0xb1133094

1fdc: f8cb2300 ; <UNDEFINED> instruction: 0xf8cb2300

1fe0: 4658305c ; <UNDEFINED> instruction: 0x4658305c

1fe4: f9a6f006 ; <UNDEFINED> instruction: 0xf9a6f006

1fe8: 2b026bfb blcs 9cfdc <\_r\_debug@@GLIBC\_2.4+0x73680>

1fec: 83a8f000 ; <UNDEFINED> instruction: 0x83a8f000

1ff0: 54c4f8df strbpl pc, [r4], #2271 ; 0x8df ; <UNPREDICTABLE>

1ff4: f8d5447d ; <UNDEFINED> instruction: 0xf8d5447d

1ff8: 2b00317c blcs e5f0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xbd8>

1ffc: 82f4f000 rscshi pc, r4, #0

2000: 14b8f8df ldrtne pc, [r8], #2271 ; 0x8df ; <UNPREDICTABLE>

2004: 0800f04f stmdaeq r0, {r0, r1, r2, r3, r6, ip, sp, lr, pc}

2008: e9cd4643 stmib sp, {r0, r1, r6, r9, sl, lr}^

200c: 44798800 ldrbtmi r8, [r9], #-2048 ; 0xfffff800

2010: 46082201 strmi r2, [r8], -r1, lsl #4

2014: fa04f006 blx 13e034 <\_r\_debug@@GLIBC\_2.4+0x1146d8>

2018: 28004604 stmdacs r0, {r2, r9, sl, lr}

201c: 82e4f000 rschi pc, r4, #0

2020: 517cf8d5 ldrsbpl pc, [ip, #-133]! ; 0xffffff7b ; <UNPREDICTABLE>

2024: 8dae4642 stchi 6, cr4, [lr, #264]! ; 0x108

2028: f8a069eb ; <UNDEFINED> instruction: 0xf8a069eb

202c: 442b615c strtmi r6, [fp], #-348 ; 0xfffffea4

2030: 3154f8c0 cmpcc r4, r0, asr #17 ; <UNPREDICTABLE>

2034: e018b956 ands fp, r8, r6, asr r9

2038: f0002901 ; <UNDEFINED> instruction: 0xf0002901

203c: 29078187 stmdbcs r7, {r0, r1, r2, r7, r8, pc}

2040: 865ef001 ldrbhi pc, [lr], -r1 ; <UNPREDICTABLE>

2044: 33203201 ; <UNDEFINED> instruction: 0x33203201

2048: d00e42b2 ; <UNDEFINED> instruction: 0xd00e42b2

204c: 29026819 stmdbcs r2, {r0, r3, r4, fp, sp, lr}

2050: 6959d1f2 ldmdbvs r9, {r1, r4, r5, r6, r7, r8, ip, lr, pc}^

2054: 68983201 ldmvs r8, {r0, r9, ip, sp}

2058: f10342b2 ; <UNDEFINED> instruction: 0xf10342b2

205c: ea4f0320 b 13c2ce4 <\_r\_debug@@GLIBC\_2.4+0x1399388>

2060: f8a401d1 ; <UNDEFINED> instruction: 0xf8a401d1

2064: 60a0115e adcvs r1, r0, lr, asr r1

2068: 6822d1f0 stmdavs r2!, {r4, r5, r6, r7, r8, ip, lr, pc}

206c: 1aaa68a6 bne fea9c30c <\_r\_debug@@GLIBC\_2.4+0xfea729b0>

2070: 01b4f8d4 ; <UNDEFINED> instruction: 0x01b4f8d4

2074: 31b8f8d4 ; <UNDEFINED> instruction: 0x31b8f8d4

2078: 441018b1 ldrmi r1, [r0], #-2225 ; 0xfffff74f

207c: 51b0f8c4 asrspl pc, r4, #17 ; <UNPREDICTABLE>

2080: 60224413 eorvs r4, r2, r3, lsl r4

2084: e9c460a1 stmib r4, {r0, r5, r7, sp, lr}^

2088: 2900036d stmdbcs r0, {r0, r2, r3, r5, r6, r8, r9}

208c: 80f9f000 rscshi pc, r9, r0

2090: f10458b3 ; <UNDEFINED> instruction: 0xf10458b3

2094: b33b0020 teqlt fp, #32

2098: 5efff64f cdppl 6, 15, cr15, cr15, cr15, {2}

209c: 6cfff64f ldclvs 6, cr15, [pc], #316 ; 21e0 <free@plt+0x17c0>

20a0: f6c62622 ; <UNDEFINED> instruction: 0xf6c62622

20a4: f6c67eff ; <UNDEFINED> instruction: 0xf6c67eff

20a8: f06f7cff ; <UNDEFINED> instruction: 0xf06f7cff

20ac: f2c74810 vmov.i16 d20, #112 ; 0x0070

20b0: e0050600 and r0, r5, r0, lsl #12

20b4: f8401af3 ; <UNDEFINED> instruction: 0xf8401af3

20b8: f8511023 ; <UNDEFINED> instruction: 0xf8511023

20bc: b19b3f08 orrslt r3, fp, r8, lsl #30

20c0: d9f82b22 ldmible r8!, {r1, r5, r8, r9, fp, sp}^

20c4: 0503eba8 streq lr, [r3, #-2984] ; 0xfffff458

20c8: d9f32d0f ldmible r3!, {r0, r1, r2, r3, r8, sl, fp, sp}^

20cc: 051ef343 ldreq pc, [lr, #-835] ; 0xfffffcbd

20d0: 0f04f115 svceq 0x0004f115

20d4: 82b0f240 adcshi pc, r0, #64, 4

20d8: 0585eba0 streq lr, [r5, #2976] ; 0xba0

20dc: 10c8f8c5 sbcne pc, r8, r5, asr #17

20e0: 3f08f851 svccc 0x0008f851

20e4: d1eb2b00 mvnle r2, r0, lsl #22

20e8: f0002a00 ; <UNDEFINED> instruction: 0xf0002a00

20ec: 6b20808d blvs 822328 <\_r\_debug@@GLIBC\_2.4+0x7f89cc>

20f0: b1504603 cmplt r0, r3, lsl #12

20f4: 23014df2 movwcs r4, #7666 ; 0x1df2

20f8: 1000e9d0 ldrdne lr, [r0], -r0

20fc: f105447d ; <UNDEFINED> instruction: 0xf105447d

2100: 44100614 ldrmi r0, [r0], #-1556 ; 0xfffff9ec

2104: 61696326 cmnvs r9, r6, lsr #6

2108: 6ae061a8 bvs ff81a7b0 <\_r\_debug@@GLIBC\_2.4+0xff7f0e54>

210c: 49edb160 stmibmi sp!, {r5, r6, r8, ip, sp, pc}^

2110: 44796805 ldrbtmi r6, [r9], #-2053 ; 0xfffff7fb

2114: 31146840 tstcc r4, r0, asr #16

2118: 06c3eb01 strbeq lr, [r3], r1, lsl #22

211c: f8414410 ; <UNDEFINED> instruction: 0xf8414410

2120: 33015033 movwcc r5, #4147 ; 0x1033

2124: 62e66070 rscvs r6, r6, #112 ; 0x70

2128: b1606b60 cmnlt r0, r0, ror #22

212c: 680549e6 stmdavs r5, {r1, r2, r5, r6, r7, r8, fp, lr}

2130: 68404479 stmdavs r0, {r0, r3, r4, r5, r6, sl, lr}^

2134: eb013114 bl 4e58c <\_r\_debug@@GLIBC\_2.4+0x24c30>

2138: 441006c3 ldrmi r0, [r0], #-1731 ; 0xfffff93d

213c: 5033f841 eorspl pc, r3, r1, asr #16

2140: 60703301 rsbsvs r3, r0, r1, lsl #6

2144: 6ba06366 blvs fe81aee4 <\_r\_debug@@GLIBC\_2.4+0xfe7f1588>

2148: 49e0b160 stmibmi r0!, {r5, r6, r8, ip, sp, pc}^

214c: 44796805 ldrbtmi r6, [r9], #-2053 ; 0xfffff7fb

2150: 31146840 tstcc r4, r0, asr #16

2154: 06c3eb01 strbeq lr, [r3], r1, lsl #22

2158: f8414410 ; <UNDEFINED> instruction: 0xf8414410

215c: 33015033 movwcc r5, #4147 ; 0x1033

2160: 63a66070 ; <UNDEFINED> instruction: 0x63a66070

2164: b1606be0 cmnlt r0, r0, ror #23

2168: 680549d9 stmdavs r5, {r0, r3, r4, r6, r7, r8, fp, lr}

216c: 68404479 stmdavs r0, {r0, r3, r4, r5, r6, sl, lr}^

2170: eb013114 bl 4e5c8 <\_r\_debug@@GLIBC\_2.4+0x24c6c>

2174: 441006c3 ldrmi r0, [r0], #-1731 ; 0xfffff93d

2178: 5033f841 eorspl pc, r3, r1, asr #16

217c: 60703301 rsbsvs r3, r0, r1, lsl #6

2180: 6e6063e6 cdpvs 3, 6, cr6, cr0, cr6, {7}

2184: 49d3b160 ldmibmi r3, {r5, r6, r8, ip, sp, pc}^

2188: 44796805 ldrbtmi r6, [r9], #-2053 ; 0xfffff7fb

218c: 31146840 tstcc r4, r0, asr #16

2190: 06c3eb01 strbeq lr, [r3], r1, lsl #22

2194: f8414410 ; <UNDEFINED> instruction: 0xf8414410

2198: 33015033 movwcc r5, #4147 ; 0x1033

219c: 66666070 ; <UNDEFINED> instruction: 0x66666070

21a0: b1606fe0 cmnlt r0, r0, ror #31

21a4: 680549cc stmdavs r5, {r2, r3, r6, r7, r8, fp, lr}

21a8: 68404479 stmdavs r0, {r0, r3, r4, r5, r6, sl, lr}^

21ac: eb013114 bl 4e604 <\_r\_debug@@GLIBC\_2.4+0x24ca8>

21b0: 441006c3 ldrmi r0, [r0], #-1731 ; 0xfffff93d

21b4: 5033f841 eorspl pc, r3, r1, asr #16

21b8: 60703301 rsbsvs r3, r0, r1, lsl #6

21bc: f8d467e6 ; <UNDEFINED> instruction: 0xf8d467e6

21c0: b16800e8 smulttlt r8, r8, r0

21c4: 680549c5 stmdavs r5, {r0, r2, r6, r7, r8, fp, lr}

21c8: 68404479 stmdavs r0, {r0, r3, r4, r5, r6, sl, lr}^

21cc: eb013114 bl 4e624 <\_r\_debug@@GLIBC\_2.4+0x24cc8>

21d0: 441006c3 ldrmi r0, [r0], #-1731 ; 0xfffff93d

21d4: 5033f841 eorspl pc, r3, r1, asr #16

21d8: 60703301 rsbsvs r3, r0, r1, lsl #6

21dc: 60e8f8c4 rscvs pc, r8, r4, asr #17

21e0: 0150f8d4 ldrsbeq pc, [r0, #-132] ; 0xffffff7c ; <UNPREDICTABLE>

21e4: 49beb180 ldmibmi lr!, {r7, r8, ip, sp, pc}

21e8: 68052b08 stmdavs r5, {r3, r8, r9, fp, sp}

21ec: 68404479 stmdavs r0, {r0, r3, r4, r5, r6, sl, lr}^

21f0: 0114f101 tsteq r4, r1, lsl #2 ; <UNPREDICTABLE>

21f4: 06c3eb01 strbeq lr, [r3], r1, lsl #22

21f8: f8414402 ; <UNDEFINED> instruction: 0xf8414402

21fc: 60725033 rsbsvs r5, r2, r3, lsr r0

2200: 6150f8c4 cmpvs r0, r4, asr #17 ; <UNPREDICTABLE>

2204: 8650f000 ldrbhi pc, [r0], -r0 ; <UNPREDICTABLE>

2208: b12b6f23 ; <UNDEFINED> instruction: 0xb12b6f23

220c: 2b07685b blcs 1dc380 <\_r\_debug@@GLIBC\_2.4+0x1b2a24>

2210: 2b11bf18 blcs 471e78 <\_r\_debug@@GLIBC\_2.4+0x44851c>

2214: 8679f040 ldrbthi pc, [r9], -r0, asr #32 ; <UNPREDICTABLE>

2218: b1236be3 ; <UNDEFINED> instruction: 0xb1236be3

221c: 685b6c63 ldmdavs fp, {r0, r1, r5, r6, sl, fp, sp, lr}^

2220: f0402b0c ; <UNDEFINED> instruction: 0xf0402b0c

2224: 6e63867e mcrvs 6, 3, r8, cr3, cr14, {3}

2228: 6ee3b123 urdvssp f3, f3

222c: 2b08685b blcs 21c3a0 <\_r\_debug@@GLIBC\_2.4+0x1f2a44>

2230: 8683f040 strhi pc, [r3], r0, asr #32

2234: 3098f8d4 ; <UNDEFINED> instruction: 0x3098f8d4

2238: 685ab163 ldmdavs sl, {r0, r1, r5, r6, r8, ip, sp, pc}^

223c: bf480795 svclt 0x00480795

2240: f8c46623 ; <UNDEFINED> instruction: 0xf8c46623

2244: 07502210 smmlaeq r0, r0, r2, r2

2248: 67a3bf48 strvs fp, [r3, r8, asr #30]!

224c: bf480711 svclt 0x00480711

2250: 3080f8c4 addcc pc, r0, r4, asr #17

2254: 30bcf8d4 ldrsbtcc pc, [ip], r4 ; <UNPREDICTABLE>

2258: 4aa2b173 bmi fe8ae82c <\_r\_debug@@GLIBC\_2.4+0xfe884ed0>

225c: 447a685b ldrbtmi r6, [sl], #-2139 ; 0xfffff7a5

2260: f8c46812 ; <UNDEFINED> instruction: 0xf8c46812

2264: 0652320c ldrbeq r3, [r2], -ip, lsl #4

2268: 8673f100 ldrbthi pc, [r3], -r0, lsl #2 ; <UNPREDICTABLE>

226c: bf4407de svclt 0x004407de

2270: 30bcf8d4 ldrsbtcc pc, [ip], r4 ; <UNPREDICTABLE>

2274: 3080f8c4 addcc pc, r0, r4, asr #17

2278: 3094f8d4 ; <UNDEFINED> instruction: 0x3094f8d4

227c: 2300b10b movwcs fp, #267 ; 0x10b

2280: 462065e3 strtmi r6, [r0], -r3, ror #11

2284: f856f006 ; <UNDEFINED> instruction: 0xf856f006

2288: 11d4f8d4 ldrsbne pc, [r4, #132] ; 0x84 ; <UNPREDICTABLE>

228c: 20016da2 andcs r6, r1, r2, lsr #27

2290: 3198f894 ; <UNDEFINED> instruction: 0x3198f894

2294: 0204f8c4 andeq pc, r4, #196, 16 ; 0xc40000

2298: 0304f043 movweq pc, #16451 ; 0x4043 ; <UNPREDICTABLE>

229c: 3198f884 orrscc pc, r8, r4, lsl #17

22a0: 0314f104 tsteq r4, #4, 2 ; <UNPREDICTABLE>

22a4: 600b6048 andvs r6, fp, r8, asr #32

22a8: 6b61b1a2 blvs 186e938 <\_r\_debug@@GLIBC\_2.4+0x1844fdc>

22ac: 684d6853 stmdavs sp, {r0, r1, r4, r6, fp, sp, lr}^

22b0: 4628441d ; <UNDEFINED> instruction: 0x4628441d

22b4: fa44f010 blx 113e2fc <\_r\_debug@@GLIBC\_2.4+0x11149a0>

22b8: 46301c46 ldrtmi r1, [r0], -r6, asr #24

22bc: eb86f7fe bl fe1c02bc <\_r\_debug@@GLIBC\_2.4+0xfe196960>

22c0: f0002800 ; <UNDEFINED> instruction: 0xf0002800

22c4: 463285c4 ldrtmi r8, [r2], -r4, asr #11

22c8: f0104629 ; <UNDEFINED> instruction: 0xf0104629

22cc: 69e3ef1a stmibvs r3!, {r1, r3, r4, r8, r9, sl, fp, sp, lr, pc}^

22d0: 60186060 andsvs r6, r8, r0, rrx

22d4: 46202100 strtmi r2, [r0], -r0, lsl #2

22d8: f868f006 ; <UNDEFINED> instruction: 0xf868f006

22dc: 2b0068e3 blcs 1c670 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb908>

22e0: 8607f040 strhi pc, [r7], -r0, asr #32

22e4: 459b6923 ldrmi r6, [fp, #2339] ; 0x923

22e8: 85eaf040 strbhi pc, [sl, #64]! ; 0x40 ; <UNPREDICTABLE>

22ec: 020cf104 andeq pc, ip, #4, 2

22f0: 627a4b7d rsbsvs r4, sl, #128000 ; 0x1f400

22f4: 447b4a7d ldrbtmi r4, [fp], #-2685 ; 0xfffff583

22f8: 63a3f503 ; <UNDEFINED> instruction: 0x63a3f503

22fc: 60dc447a sbcsvs r4, ip, sl, ror r4

2300: f8c26123 ; <UNDEFINED> instruction: 0xf8c26123

2304: e1754180 cmn r5, r0, lsl #3

2308: 2c18f853 ldccs 8, cr15, [r8], {83} ; 0x53

230c: 2250f8cb subscs pc, r0, #13303808 ; 0xcb0000

2310: 2c0cf853 stccs 8, cr15, [ip], {83} ; 0x53

2314: 2254f8cb subscs pc, r4, #13303808 ; 0xcb0000

2318: f853e53b ; <UNDEFINED> instruction: 0xf853e53b

231c: 6a392c08 bvs e4d344 <\_r\_debug@@GLIBC\_2.4+0xe239e8>

2320: 2804f8c1 stmdacs r4, {r0, r6, r7, fp, ip, sp, lr, pc}

2324: f853e535 ; <UNDEFINED> instruction: 0xf853e535

2328: 1aa22c18 bne fe88d390 <\_r\_debug@@GLIBC\_2.4+0xfe863a34>

232c: 2000f8cb andcs pc, r0, fp, asr #17

2330: f04fe52f ; <UNDEFINED> instruction: 0xf04fe52f

2334: f8cb33ff ; <UNDEFINED> instruction: 0xf8cb33ff

2338: f1ba31b4 ; <UNDEFINED> instruction: 0xf1ba31b4

233c: f47f0f00 ; <UNDEFINED> instruction: 0xf47f0f00

2340: f04fad6b ; <UNDEFINED> instruction: 0xf04fad6b

2344: f8cb33ff ; <UNDEFINED> instruction: 0xf8cb33ff

2348: e56531b8 strb r3, [r5, #-440]! ; 0xfffffe48

234c: 68996820 ldmvs r9, {r5, fp, sp, lr}

2350: 6021b900 eorvs fp, r1, r0, lsl #18

2354: 44086958 strmi r6, [r8], #-2392 ; 0xfffff6a8

2358: 11b4f8d4 ; <UNDEFINED> instruction: 0x11b4f8d4

235c: bf284288 svclt 0x00284288

2360: 01b4f8c4 ; <UNDEFINED> instruction: 0x01b4f8c4

2364: 07c96999 bfieq r6, r9, (invalid: 19:9)

2368: ae6cf57f mcrge 5, 3, pc, cr12, cr15, {3} ; <UNPREDICTABLE>

236c: 11b8f8d4 ; <UNDEFINED> instruction: 0x11b8f8d4

2370: bf284288 svclt 0x00284288

2374: 01b8f8c4 ; <UNDEFINED> instruction: 0x01b8f8c4

2378: bf00e664 svclt 0x0000e664

237c: 000281e4 andeq r8, r2, r4, ror #3

2380: 000277cc andeq r7, r2, ip, asr #15

2384: 000278e2 andeq r7, r2, r2, ror #17

2388: 0000705b andeq r7, r0, fp, asr r0

238c: fffff355 ; <UNDEFINED> instruction: 0xfffff355

2390: fffff353 ; <UNDEFINED> instruction: 0xfffff353

2394: 0000c081 andeq ip, r0, r1, lsl #1

2398: 0002784e andeq r7, r2, lr, asr #16

239c: 000125cc andeq r2, r1, ip, asr #11

23a0: 00027194 muleq r2, r4, r1

23a4: 000133e6 andeq r3, r1, r6, ror #7

23a8: 0002718e andeq r7, r2, lr, lsl #3

23ac: 000133c4 andeq r3, r1, r4, asr #7

23b0: 000276c0 andeq r7, r2, r0, asr #13

23b4: 00027082 andeq r7, r2, r2, lsl #1

23b8: fffff19a ; <UNDEFINED> instruction: 0xfffff19a

23bc: 00027fe6 andeq r7, r2, r6, ror #31

23c0: 00027054 andeq r7, r2, r4, asr r0

23c4: 0002779c muleq r2, ip, r7

23c8: 00027fd2 ldrdeq r7, [r2], -r2

23cc: 00027fc6 andeq r7, r2, r6, asr #31

23d0: 00013302 andeq r3, r1, r2, lsl #6

23d4: 00028050 andeq r8, r2, r0, asr r0

23d8: 00027006 andeq r7, r2, r6

23dc: 00027060 andeq r7, r2, r0, rrx

23e0: 0001330c andeq r3, r1, ip, lsl #6

23e4: 000132ac andeq r3, r1, ip, lsr #5

23e8: 00027ff6 strdeq r7, [r2], -r6

23ec: 00026fae andeq r6, r2, lr, lsr #31

23f0: 00013286 andeq r3, r1, r6, lsl #5

23f4: 00027fc4 andeq r7, r2, r4, asr #31

23f8: 00026f7a andeq r6, r2, sl, ror pc

23fc: 00026fd6 ldrdeq r6, [r2], -r6 ; <UNPREDICTABLE>

2400: 0001325e andeq r3, r1, lr, asr r2

2404: 0001324a andeq r3, r1, sl, asr #4

2408: 00013240 andeq r3, r1, r0, asr #4

240c: 00013228 andeq r3, r1, r8, lsr #4

2410: 00027f24 andeq r7, r2, r4, lsr #30

2414: 00026eda ldrdeq r6, [r2], -sl

2418: 00027e62 andeq r7, r2, r2, ror #28

241c: 0001231a andeq r2, r1, sl, lsl r3

2420: 0002747a andeq r7, r2, sl, ror r4

2424: 000130c8 andeq r3, r1, r8, asr #1

2428: 00026e7e andeq r6, r2, lr, ror lr

242c: 0001307e andeq r3, r1, lr, ror r0

2430: 0002742e andeq r7, r2, lr, lsr #8

2434: 0001306c andeq r3, r1, ip, rrx

2438: 00026e34 andeq r6, r2, r4, lsr lr

243c: 000273f2 strdeq r7, [r2], -r2

2440: 00013002 andeq r3, r1, r2

2444: 00026da0 andeq r6, r2, r0, lsr #27

2448: 000273c6 andeq r7, r2, r6, asr #7

244c: 00012fc8 andeq r2, r1, r8, asr #31

2450: 00026dd2 ldrdeq r6, [r2], -r2

2454: 00027396 muleq r2, r6, r3

2458: 00012f8c andeq r2, r1, ip, lsl #31

245c: 00012f5a andeq r2, r1, sl, asr pc

2460: 00012f52 andeq r2, r1, r2, asr pc

2464: 00026d72 andeq r6, r2, r2, ror sp

2468: 00012f08 andeq r2, r1, r8, lsl #30

246c: 00012efc strdeq r2, [r1], -ip

2470: 00026cda ldrdeq r6, [r2], -sl

2474: 00012ec4 andeq r2, r1, r4, asr #29

2478: 00012eb8 ; <UNDEFINED> instruction: 0x00012eb8

247c: 00000054 andeq r0, r0, r4, asr r0

2480: 00026cd2 ldrdeq r6, [r2], -r2

2484: 0001385a andeq r3, r1, sl, asr r8

2488: 0002737a andeq r7, r2, sl, ror r3

248c: 00026c48 andeq r6, r2, r8, asr #24

2490: 00027326 andeq r7, r2, r6, lsr #6

2494: 00027b5a andeq r7, r2, sl, asr fp

2498: 00027b54 andeq r7, r2, r4, asr fp

249c: 0002730c andeq r7, r2, ip, lsl #6

24a0: 00027304 andeq r7, r2, r4, lsl #6

24a4: 00027240 andeq r7, r2, r0, asr #4

24a8: 00027230 andeq r7, r2, r0, lsr r2

24ac: 00027a44 andeq r7, r2, r4, asr #20

24b0: 000271f0 strdeq r7, [r2], -r0

24b4: 000269b0 ; <UNDEFINED> instruction: 0x000269b0

24b8: 00026978 andeq r6, r2, r8, ror r9

24bc: 000134fa strdeq r3, [r1], -sl

24c0: 00026810 andeq r6, r2, r0, lsl r8

24c4: 000267fa strdeq r6, [r2], -sl

24c8: 000267dc ldrdeq r6, [r2], -ip

24cc: 000267be ; <UNDEFINED> instruction: 0x000267be

24d0: 000267a0 andeq r6, r2, r0, lsr #15

24d4: 00026782 andeq r6, r2, r2, lsl #15

24d8: 00026764 andeq r6, r2, r4, ror #14

24dc: 00026744 andeq r6, r2, r4, asr #14

24e0: 00026720 andeq r6, r2, r0, lsr #14

24e4: 0002670e andeq r6, r2, lr, lsl #14

24e8: 00026d66 andeq r6, r2, r6, ror #26

24ec: 00026670 andeq r6, r2, r0, ror r6

24f0: 1c18f853 ldcne 8, cr15, [r8], {83} ; 0x53

24f4: f8db6aba ; <UNDEFINED> instruction: 0xf8db6aba

24f8: ea016000 b 5a500 <\_r\_debug@@GLIBC\_2.4+0x30ba4>

24fc: f8db0c02 ; <UNDEFINED> instruction: 0xf8db0c02

2500: 44b421b0 ldrtmi r2, [r4], #432 ; 0x1b0

2504: bf884562 svclt 0x00884562

2508: c1b0f8cb asrsgt pc, fp, #17 ; <UNPREDICTABLE>

250c: 2c0cf853 stccs 8, cr15, [ip], {83} ; 0x53

2510: 4416440a ldrmi r4, [r6], #-1034 ; 0xfffffbf6

2514: 42b26b7a adcsmi r6, r2, #124928 ; 0x1e800

2518: f8cbbf38 ; <UNDEFINED> instruction: 0xf8cbbf38

251c: f85361b4 ; <UNDEFINED> instruction: 0xf85361b4

2520: bf382c08 svclt 0x00382c08

2524: 07d2637e ; <UNDEFINED> instruction: 0x07d2637e

2528: ac33f57f cfldr32ge mvfx15, [r3], #-508 ; 0xfffffe04

252c: bf3c45b2 svclt 0x003c45b2

2530: 61b8f8cb ; <UNDEFINED> instruction: 0x61b8f8cb

2534: e42c46b2 strt r4, [ip], #-1714 ; 0xfffff94e

2538: f8db697e ; <UNDEFINED> instruction: 0xf8db697e

253c: f8535000 ; <UNDEFINED> instruction: 0xf8535000

2540: 46b42c18 ssatmi r2, #21, r8, lsl #24

2544: 1520f8d6 strne pc, [r0, #-2262]! ; 0xfffff72a

2548: 693e442a ldmdbvs lr!, {r1, r3, r5, sl, lr}

254c: f8cc69bd ; <UNDEFINED> instruction: 0xf8cc69bd

2550: 616a6534 cmnvs sl, r4, lsr r5

2554: 2501b129 strcs fp, [r1, #-297] ; 0xfffffed7

2558: 3201e41b andcc lr, r1, #452984832 ; 0x1b000000

255c: bf082d2f svclt 0x00082d2f

2560: 78154611 ldmdavc r5, {r0, r4, r9, sl, lr}

2564: d1f82d00 mvnsle r2, r0, lsl #26

2568: d0f42900 rscsle r2, r4, r0, lsl #18

256c: 250168fa strcs r6, [r1, #-2298] ; 0xfffff706

2570: 1020f8c8 eorne pc, r0, r8, asr #17

2574: 2018f8c8 andscs pc, r8, r8, asr #17

2578: f853e40b ; <UNDEFINED> instruction: 0xf853e40b

257c: f8db2c18 ; <UNDEFINED> instruction: 0xf8db2c18

2580: 440a1000 strmi r1, [sl], #-0

2584: 2008f8cb andcs pc, r8, fp, asr #17

2588: 6b3be403 blvs efb59c <\_r\_debug@@GLIBC\_2.4+0xed1c40>

258c: 61186040 tstvs r8, r0, asr #32

2590: ba41f7ff blt 1080594 <\_r\_debug@@GLIBC\_2.4+0x1056c38>

2594: 1c4cf8df mcrrne 8, 13, pc, ip, cr15 ; <UNPREDICTABLE>

2598: 0b02f1ab bleq bec4c <\_r\_debug@@GLIBC\_2.4+0x952f0>

259c: 3c48f8df mcrrcc 8, 13, pc, r8, cr15 ; <UNPREDICTABLE>

25a0: f8df4479 ; <UNDEFINED> instruction: 0xf8df4479

25a4: 447b2c48 ldrbtmi r2, [fp], #-3144 ; 0xfffff3b8

25a8: 0f08f856 svceq 0x0008f856

25ac: f8c3447a ; <UNDEFINED> instruction: 0xf8c3447a

25b0: 680bb000 stmdavs fp, {ip, sp, pc}

25b4: 3302602e movwcc r6, #8238 ; 0x202e

25b8: 015cf8c2 cmpeq ip, r2, asr #17 ; <UNPREDICTABLE>

25bc: f7ff600b ; <UNDEFINED> instruction: 0xf7ff600b

25c0: f8dfb98a ; <UNDEFINED> instruction: 0xf8dfb98a

25c4: f1ab1c2c ; <UNDEFINED> instruction: 0xf1ab1c2c

25c8: f8df0b02 ; <UNDEFINED> instruction: 0xf8df0b02

25cc: 44793c28 ldrbtmi r3, [r9], #-3112 ; 0xfffff3d8

25d0: 0f08f856 svceq 0x0008f856

25d4: 680a447b stmdavs sl, {r0, r1, r3, r4, r5, r6, sl, lr}

25d8: 3202602e andcc r6, r2, #46 ; 0x2e

25dc: 600a6118 andvs r6, sl, r8, lsl r1

25e0: b000f8c3 andlt pc, r0, r3, asr #17

25e4: b977f7ff ldmdblt r7!, {r0, r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}^

25e8: 3c0cf8df stccc 8, cr15, [ip], {223} ; 0xdf

25ec: f203447b vqshl.s8 q2, <illegal reg q13.5>, <illegal reg q1.5>

25f0: 627b5324 rsbsvs r5, fp, #36, 6 ; 0x90000000

25f4: ffdcf00d ; <UNDEFINED> instruction: 0xffdcf00d

25f8: f2c02800 vmlal.s8 q9, d0, d0

25fc: f8df8096 ; <UNDEFINED> instruction: 0xf8df8096

2600: 447b3bfc ldrbtmi r3, [fp], #-3068 ; 0xfffff404

2604: 2b00685b blcs 1c778 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xba10>

2608: 8117f040 tsthi r7, r0, asr #32 ; <UNPREDICTABLE>

260c: 3bf0f8df blcc ffc40990 <\_r\_debug@@GLIBC\_2.4+0xffc17034>

2610: 6058447b subsvs r4, r8, fp, ror r4

2614: 13fff240 mvnsne pc, #64, 4

2618: 0303f2c0 movweq pc, #12992 ; 0x32c0 ; <UNPREDICTABLE>

261c: f3004298 vqsub.u8 d4, d16, d8

2620: f8df809b ; <UNDEFINED> instruction: 0xf8df809b

2624: 447c4be0 ldrbtmi r4, [ip], #-3040 ; 0xfffff420

2628: 20024621 andcs r4, r2, r1, lsr #12

262c: fbcef009 blx ff3be65a <\_r\_debug@@GLIBC\_2.4+0xff394cfe>

2630: f00f207f ; <UNDEFINED> instruction: 0xf00f207f

2634: e7f7fc09 ldrb pc, [r7, r9, lsl #24]! ; <UNPREDICTABLE>

2638: 0503ebae streq lr, [r3, #-2990] ; 0xfffff452

263c: d8572d0b ldmdale r7, {r0, r1, r3, r8, sl, fp, sp}^

2640: 0383eba0 orreq lr, r3, #160, 22 ; 0x28000

2644: 4340f103 movtmi pc, #259 ; 0x103 ; <UNPREDICTABLE>

2648: 732cf2a3 msrvc CPSR\_fs, #805306378 ; 0x3000000a

264c: e5346019 ldr r6, [r4, #-25]! ; 0xffffffe7

2650: 466e4658 ; <UNDEFINED> instruction: 0x466e4658

2654: f874f010 ; <UNDEFINED> instruction: 0xf874f010

2658: 0313f100 tsteq r3, #0, 2 ; <UNPREDICTABLE>

265c: 080bf100 stmdaeq fp, {r8, ip, sp, lr, pc}

2660: 0307f023 movweq pc, #28707 ; 0x7023 ; <UNPREDICTABLE>

2664: ebad4605 bl feb53e80 <\_r\_debug@@GLIBC\_2.4+0xfeb2a524>

2668: aa040d03 bge 105a7c <\_r\_debug@@GLIBC\_2.4+0xdc120>

266c: f802633a ; <UNDEFINED> instruction: 0xf802633a

2670: f00f9008 ; <UNDEFINED> instruction: 0xf00f9008

2674: 6b3afc0d blvs ec16b0 <\_r\_debug@@GLIBC\_2.4+0xe97d54>

2678: f8cd230a ; <UNDEFINED> instruction: 0xf8cd230a

267c: 44429000 strbmi r9, [r2], #-0

2680: f00e17c1 ; <UNDEFINED> instruction: 0xf00e17c1

2684: 43ebf9ef mvnmi pc, #3915776 ; 0x3bc000

2688: 222e4659 eorcs r4, lr, #93323264 ; 0x5900000

268c: 2c01f800 stccs 8, cr15, [r1], {-0}

2690: 462a4418 ; <UNDEFINED> instruction: 0x462a4418

2694: ed34f010 ldc 0, cr15, [r4, #-64]! ; 0xffffffc0

2698: 4141f248 cmpmi r1, r8, asr #4 ; <UNPREDICTABLE>

269c: 72dbf44f sbcsvc pc, fp, #1325400064 ; 0x4f000000

26a0: faacf00f blx feb3e6e4 <\_r\_debug@@GLIBC\_2.4+0xfeb14d88>

26a4: f0001c41 ; <UNDEFINED> instruction: 0xf0001c41

26a8: f8df83ca ; <UNDEFINED> instruction: 0xf8df83ca

26ac: 447b3b5c ldrbtmi r3, [fp], #-2908 ; 0xfffff4a4

26b0: 46b56298 ssatmi r6, #22, r8, lsl #5

26b4: b8f1f7ff ldmlt r1!, {r0, r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}^

26b8: 151cf8d3 ldrne pc, [ip, #-2259] ; 0xfffff72d

26bc: f0002900 ; <UNDEFINED> instruction: 0xf0002900

26c0: f8df840b ; <UNDEFINED> instruction: 0xf8df840b

26c4: 447a2b48 ldrbtmi r2, [sl], #-2888 ; 0xfffff4b8

26c8: 1f14f842 svcne 0x0014f842

26cc: 2534f8c3 ldrcs pc, [r4, #-2243]! ; 0xfffff73d

26d0: bbaaf7ff bllt feac06d4 <\_r\_debug@@GLIBC\_2.4+0xfea96d78>

26d4: 0003ebac andeq lr, r3, ip, lsr #23

26d8: f200280b vadd.i8 d2, d0, d11

26dc: eba183c3 bl fe8635f0 <\_r\_debug@@GLIBC\_2.4+0xfe839c94>

26e0: f1030383 ; <UNDEFINED> instruction: 0xf1030383

26e4: f2a34340 vqdmlal.s32 q2, d3, d0[0]

26e8: 601a732c andsvs r7, sl, ip, lsr #6

26ec: bbe2f7ff bllt ff8c06f0 <\_r\_debug@@GLIBC\_2.4+0xff896d94>

26f0: 0503ebac streq lr, [r3, #-2988] ; 0xfffff454

26f4: bf9f2d0a svclt 0x009f2d0a

26f8: 0383eba0 orreq lr, r3, #160, 22 ; 0x28000

26fc: 4340f103 movtmi pc, #259 ; 0x103 ; <UNPREDICTABLE>

2700: 733ff5a3 teqvc pc, #683671552 ; 0x28c00000 ; <UNPREDICTABLE>

2704: e4d86019 ldrb r6, [r8], #25

2708: 40323a01 eorsmi r3, r2, r1, lsl #20

270c: bb62f7ff bllt 18c0710 <\_r\_debug@@GLIBC\_2.4+0x1896db4>

2710: 2230f8db eorscs pc, r0, #14352384 ; 0xdb0000

2714: f0012a00 ; <UNDEFINED> instruction: 0xf0012a00

2718: f8db82ff ; <UNDEFINED> instruction: 0xf8db82ff

271c: f8cb3000 ; <UNDEFINED> instruction: 0xf8cb3000

2720: 441311b4 ldrmi r1, [r3], #-436 ; 0xfffffe4c

2724: 3230f8cb eorscc pc, r0, #13303808 ; 0xcb0000

2728: f8dfe60b ; <UNDEFINED> instruction: 0xf8dfe60b

272c: 447c4ae4 ldrbtmi r4, [ip], #-2788 ; 0xfffff51c

2730: 20024621 andcs r4, r2, r1, lsr #12

2734: fb4af009 blx 12be762 <\_r\_debug@@GLIBC\_2.4+0x1294e06>

2738: f00f207f ; <UNDEFINED> instruction: 0xf00f207f

273c: e7f7fb85 ldrb pc, [r7, r5, lsl #23]! ; <UNPREDICTABLE>

2740: 3008f8db ldrdcc pc, [r8], -fp

2744: f0012b00 ; <UNDEFINED> instruction: 0xf0012b00

2748: 2d0082d6 sfmcs f0, 1, [r0, #-856] ; 0xfffffca8

274c: 2002bf0c andcs fp, r2, ip, lsl #30

2750: f00f2000 ; <UNDEFINED> instruction: 0xf00f2000

2754: e44bfb79 strb pc, [fp], #-2937 ; 0xfffff487 ; <UNPREDICTABLE>

2758: 3ab8f8df bcc fee40adc <\_r\_debug@@GLIBC\_2.4+0xfee17180>

275c: 4ab8f8df bmi fee40ae0 <\_r\_debug@@GLIBC\_2.4+0xfee17184>

2760: 447c447b ldrbtmi r4, [ip], #-1147 ; 0xfffffb85

2764: f0036918 ; <UNDEFINED> instruction: 0xf0036918

2768: f8d4fd07 ; <UNDEFINED> instruction: 0xf8d4fd07

276c: 21000518 tstcs r0, r8, lsl r5

2770: f938f009 ; <UNDEFINED> instruction: 0xf938f009

2774: 351cf8d4 ldrcc pc, [ip, #-2260] ; 0xfffff72c

2778: 62b82200 adcsvs r2, r8, #0, 4

277c: 2b0060c2 blcs 1aa8c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x9d24>

2780: f8dfd055 ; <UNDEFINED> instruction: 0xf8dfd055

2784: 25013a98 strcs r3, [r1, #-2712] ; 0xfffff568

2788: 0a94f8df beq fe540b0c <\_r\_debug@@GLIBC\_2.4+0xfe5171b0>

278c: 4478447b ldrbtmi r4, [r8], #-1147 ; 0xfffffb85

2790: 64a3f503 strtvs pc, [r3], #1283 ; 0x503

2794: 16b0f893 ssatne pc, #17, r3, lsl #17 ; <UNPREDICTABLE>

2798: 64f8f8d3 ldrbtvs pc, [r8], #2259 ; 0x8d3 ; <UNPREDICTABLE>

279c: 0101f365 tsteq r1, r5, ror #6 ; <UNPREDICTABLE>

27a0: 16b0f883 ldrtne pc, [r0], r3, lsl #17 ; <UNPREDICTABLE>

27a4: 19766859 ldmdbne r6!, {r0, r3, r4, r6, fp, sp, lr}^

27a8: 400cf8cb andmi pc, ip, fp, asr #17

27ac: f8d34429 ; <UNDEFINED> instruction: 0xf8d34429

27b0: 605944fc ldrshvs r4, [r9], #-76 ; 0xffffffb4

27b4: 1164f8d0 ldrdne pc, [r4, #-128]! ; 0xffffff80

27b8: 0400f144 streq pc, [r0], #-324 ; 0xfffffebc

27bc: 64f8f8c3 ldrbtvs pc, [r8], #2243 ; 0x8c3 ; <UNPREDICTABLE>

27c0: f8c33102 ; <UNDEFINED> instruction: 0xf8c33102

27c4: f8c3b528 ; <UNDEFINED> instruction: 0xf8c3b528

27c8: d10744fc strdle r4, [r7, -ip]

27cc: 3000f8db ldrdcc pc, [r0], -fp

27d0: f383fab3 ; <UNDEFINED> instruction: 0xf383fab3

27d4: 425b095b subsmi r0, fp, #1490944 ; 0x16c000

27d8: 3164f8c0 msrcc SPSR\_s, r0, asr #17

27dc: 1a44f8df bne 1140b60 <\_r\_debug@@GLIBC\_2.4+0x1117204>

27e0: 8d0b4479 cfstrshi mvf4, [fp, #-484] ; 0xfffffe1c

27e4: f0412b34 ; <UNDEFINED> instruction: 0xf0412b34

27e8: 8d4b8279 sfmhi f0, 3, [fp, #-484] ; 0xfffffe1c

27ec: f0412b20 ; <UNDEFINED> instruction: 0xf0412b20

27f0: 8d8b8268 sfmhi f0, 1, [fp, #416] ; 0x1a0

27f4: 5452f24e ldrbpl pc, [r2], #-590 ; 0xfffffdb2 ; <UNPREDICTABLE>

27f8: 5a2cf8df bpl b40b7c <\_r\_debug@@GLIBC\_2.4+0xb17220>

27fc: 4474f2c6 ldrbtmi pc, [r4], #-710 ; 0xfffffd3a ; <UNPREDICTABLE>

2800: 6200f103 andvs pc, r0, #-1073741824 ; 0xc0000000

2804: 447d69c8 ldrbtmi r6, [sp], #-2504 ; 0xfffff638

2808: 44013a01 strmi r3, [r1], #-2561 ; 0xfffff5ff

280c: 3674f8a5 ldrbtcc pc, [r4], -r5, lsr #17 ; <UNPREDICTABLE>

2810: 1242eb01 subne lr, r2, #1024 ; 0x400

2814: 166cf8c5 strbtne pc, [ip], -r5, asr #17 ; <UNPREDICTABLE>

2818: e0053b01 and r3, r5, r1, lsl #22

281c: 46106811 ; <UNDEFINED> instruction: 0x46106811

2820: 3a203b01 bcc 81142c <\_r\_debug@@GLIBC\_2.4+0x7e7ad0>

2824: d00c42a1 andle r4, ip, r1, lsr #5

2828: d1f71c5d mvnsle r1, sp, asr ip

282c: f8d4e012 ; <UNDEFINED> instruction: 0xf8d4e012

2830: 681b3534 ldmdavs fp, {r2, r4, r5, r8, sl, ip, sp}

2834: 351cf8c4 ldrcc pc, [ip, #-2244] ; 0xfffff73c

2838: 4283e7a3 addmi lr, r3, #42729472 ; 0x28c0000

283c: aeeaf67f mcrge 6, 7, pc, cr10, cr15, {3} ; <UNPREDICTABLE>

2840: f8dfe6e4 ; <UNDEFINED> instruction: 0xf8dfe6e4

2844: 688139e8 stmvs r1, {r3, r5, r6, r7, r8, fp, ip, sp}

2848: 447b6942 ldrbtmi r6, [fp], #-2370 ; 0xfffff6be

284c: 1768f8c3 strbne pc, [r8, -r3, asr #17]! ; <UNPREDICTABLE>

2850: 276cf8c3 strbcs pc, [ip, -r3, asr #17]! ; <UNPREDICTABLE>

2854: 49d8f8df ldmibmi r8, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

2858: f8d4447c ; <UNDEFINED> instruction: 0xf8d4447c

285c: 2b003750 blcs 105a4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x764>

2860: 8098f040 addshi pc, r8, r0, asr #32

2864: 29ccf8df stmibcs ip, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

2868: f8df6af9 ; <UNDEFINED> instruction: 0xf8df6af9

286c: 588a39cc stmpl sl, {r2, r3, r6, r7, r8, fp, ip, sp}

2870: 691b447b ldmdbvs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

2874: 637a6812 cmnvs sl, #1179648 ; 0x120000

2878: f0412b00 ; <UNDEFINED> instruction: 0xf0412b00

287c: 2a008045 bcs 22998 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11c30>

2880: 8042f041 subhi pc, r2, r1, asr #32

2884: 0901f04f stmdbeq r1, {r0, r1, r2, r3, r6, ip, sp, lr, pc}

2888: fb78f00a blx 1e3e8ba <\_r\_debug@@GLIBC\_2.4+0x1e14f5e>

288c: 3074f8db ldrsbtcc pc, [r4], #-139 ; 0xffffff75 ; <UNPREDICTABLE>

2890: b10b6338 tstlt fp, r8, lsr r3

2894: 605a6aba ldrhvs r6, [sl], #-170 ; 0xffffff56

2898: 39a0f8df stmibcc r0!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

289c: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

28a0: b10b358c smlabblt fp, ip, r5, r3

28a4: 605a6aba ldrhvs r6, [sl], #-170 ; 0xffffff56

28a8: 23016aba movwcs r6, #6842 ; 0x1aba

28ac: 4990f8df ldmibmi r0, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

28b0: 60d3447c sbcsvs r4, r3, ip, ror r4

28b4: f894f009 ; <UNDEFINED> instruction: 0xf894f009

28b8: 31b0f8d4 asrscc pc, r4 ; <illegal shifter operand> ; <UNPREDICTABLE>

28bc: f0412b00 ; <UNDEFINED> instruction: 0xf0412b00

28c0: 6a7b800e bvs 1ee2900 <\_r\_debug@@GLIBC\_2.4+0x1eb8fa4>

28c4: 2b00681b blcs 1c938 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xbbd0>

28c8: 87fcf040 ldrbhi pc, [ip, r0, asr #32]! ; <UNPREDICTABLE>

28cc: 3974f8df ldmdbcc r4!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

28d0: 68d8447b ldmvs r8, {r0, r1, r3, r4, r5, r6, sl, lr}^

28d4: 28004605 stmdacs r0, {r0, r2, r9, sl, lr}

28d8: 87eef040 strbhi pc, [lr, r0, asr #32]! ; <UNPREDICTABLE>

28dc: 4968f8df stmdbmi r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

28e0: 447c2104 ldrbtmi r2, [ip], #-260 ; 0xfffffefc

28e4: f00f4620 ; <UNDEFINED> instruction: 0xf00f4620

28e8: 2800f8f3 stmdacs r0, {r0, r1, r4, r5, r6, r7, fp, ip, sp, lr, pc}

28ec: 8728f000 strhi pc, [r8, -r0]!

28f0: 68196a7b ldmdavs r9, {r0, r1, r3, r4, r5, r6, r9, fp, sp, lr}

28f4: f0402900 ; <UNDEFINED> instruction: 0xf0402900

28f8: 6bfb8702 blvs ffee4508 <\_r\_debug@@GLIBC\_2.4+0xffebabac>

28fc: 462a2000 strtmi r2, [sl], -r0

2900: f1a39000 ; <UNDEFINED> instruction: 0xf1a39000

2904: 46580403 ldrbmi r0, [r8], -r3, lsl #8

2908: f484fab4 vst3.32 ; <UNDEFINED> instruction: 0xf484fab4

290c: 46230964 strtmi r0, [r3], -r4, ror #18

2910: fad6f007 blx ff5be934 <\_r\_debug@@GLIBC\_2.4+0xff594fd8>

2914: 1164f8db ldrdne pc, [r4, #-139]! ; 0xffffff75

2918: f0012900 ; <UNDEFINED> instruction: 0xf0012900

291c: f8db82c3 ; <UNDEFINED> instruction: 0xf8db82c3

2920: eb000160 bl 2ea8 <free@plt+0x2488>

2924: f8530381 ; <UNDEFINED> instruction: 0xf8530381

2928: 42985d04 addsmi r5, r8, #4, 26 ; 0x100

292c: 2198f895 ; <UNDEFINED> instruction: 0x2198f895

2930: 0210f042 andseq pc, r0, #66 ; 0x42

2934: 2198f885 orrscs pc, r8, r5, lsl #17

2938: f8dfd1f5 ; <UNDEFINED> instruction: 0xf8dfd1f5

293c: 447b3910 ldrbtmi r3, [fp], #-2320 ; 0xfffff6f0

2940: 2528f8d3 strcs pc, [r8, #-2259]! ; 0xfffff72d

2944: 0524f8d3 streq pc, [r4, #-2259]! ; 0xfffff72d

2948: f8d360d0 ; <UNDEFINED> instruction: 0xf8d360d0

294c: b1033524 tstlt r3, r4, lsr #10

2950: 2901611a stmdbcs r1, {r1, r3, r4, r8, sp, lr}

2954: f8dbd91b ; <UNDEFINED> instruction: 0xf8dbd91b

2958: f8df0160 ; <UNDEFINED> instruction: 0xf8df0160

295c: 684238f4 stmdavs r2, {r2, r4, r5, r6, r7, fp, ip, sp}^

2960: f503447b ; <UNDEFINED> instruction: 0xf503447b

2964: 429a63a3 addsmi r6, sl, #-1946157054 ; 0x8c000002

2968: 8298f001 addshi pc, r8, #1

296c: 58e4f8df stmiapl r4!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

2970: 22011d06 andcs r1, r1, #384 ; 0x180

2974: f505447d ; <UNDEFINED> instruction: 0xf505447d

2978: e00565a3 and r6, r5, r3, lsr #11

297c: cf04f856 svcgt 0x0004f856

2980: f00045ac ; <UNDEFINED> instruction: 0xf00045ac

2984: 461a80a7 ldrmi r8, [sl], -r7, lsr #1

2988: 42991c53 addsmi r1, r9, #21248 ; 0x5300

298c: 2300d1f6 movwcs sp, #502 ; 0x1f6

2990: e0dc63bb ldrh r6, [ip], #59 ; 0x3b

2994: fa9cf00a blx fe73e9c4 <\_r\_debug@@GLIBC\_2.4+0xfe715068>

2998: 0760f8c4 strbeq pc, [r0, -r4, asr #17]! ; <UNPREDICTABLE>

299c: 1da8e762 stcne 7, cr14, [r8, #392]! ; 0x188

29a0: f97cf7fe ; <UNDEFINED> instruction: 0xf97cf7fe

29a4: bf24f7fe svclt 0x0024f7fe

29a8: 18acf8df stmiane ip!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

29ac: 4628220c strtmi r2, [r8], -ip, lsl #4

29b0: f0104479 ; <UNDEFINED> instruction: 0xf0104479

29b4: 2800f843 stmdacs r0, {r0, r1, r6, fp, ip, sp, lr, pc}

29b8: f8dfd046 ; <UNDEFINED> instruction: 0xf8dfd046

29bc: 462818a0 strtmi r1, [r8], -r0, lsr #17

29c0: 4479220c ldrbtmi r2, [r9], #-524 ; 0xfffffdf4

29c4: f83af010 ; <UNDEFINED> instruction: 0xf83af010

29c8: f47e2800 ; <UNDEFINED> instruction: 0xf47e2800

29cc: f8dfaf11 ; <UNDEFINED> instruction: 0xf8dfaf11

29d0: 22013890 andcs r3, r1, #144, 16 ; 0x900000

29d4: 635a447b cmpvs sl, #2063597568 ; 0x7b000000

29d8: bf0af7fe svclt 0x000af7fe

29dc: f107350e ; <UNDEFINED> instruction: 0xf107350e

29e0: 46060260 strmi r0, [r6], -r0, ror #4

29e4: 462b4604 strtmi r4, [fp], -r4, lsl #12

29e8: 46294615 ; <UNDEFINED> instruction: 0x46294615

29ec: 623b4618 eorsvs r4, fp, #24, 12 ; 0x1800000

29f0: fa44f009 blx 113ea1c <\_r\_debug@@GLIBC\_2.4+0x11150c0>

29f4: f63e28fe ; <UNDEFINED> instruction: 0xf63e28fe

29f8: 682aaefb stmdavs sl!, {r0, r1, r3, r4, r5, r6, r7, r9, sl, fp, sp, pc}

29fc: 429a6a3b addsmi r6, sl, #241664 ; 0x3b000

2a00: aef6f43e mrcge 4, 7, APSR\_nzcv, cr6, cr14, {1}

2a04: d0232c02 eorle r2, r3, r2, lsl #24

2a08: 2b2e7813 blcs ba0a5c <\_r\_debug@@GLIBC\_2.4+0xb77100>

2a0c: 2b00bf18 blcs 32674 <\_r\_debug@@GLIBC\_2.4+0x8d18>

2a10: aeeef47e mcrge 4, 7, pc, cr14, cr14, {3} ; <UNPREDICTABLE>

2a14: f1c100e1 ; <UNDEFINED> instruction: 0xf1c100e1

2a18: 40880110 addmi r0, r8, r0, lsl r1

2a1c: b1c34306 biclt r4, r3, r6, lsl #6

2a20: 46291c53 ; <UNDEFINED> instruction: 0x46291c53

2a24: 3401623b strcc r6, [r1], #-571 ; 0xfffffdc5

2a28: f0094618 ; <UNDEFINED> instruction: 0xf0094618

2a2c: 28fefa27 ldmcs lr!, {r0, r1, r2, r5, r9, fp, ip, sp, lr, pc}^

2a30: f7fed9e3 ; <UNDEFINED> instruction: 0xf7fed9e3

2a34: f8dfbedd ; <UNDEFINED> instruction: 0xf8dfbedd

2a38: 4628182c strtmi r1, [r8], -ip, lsr #16

2a3c: f00f4479 ; <UNDEFINED> instruction: 0xf00f4479

2a40: 2800fffd stmdacs r0, {r0, r2, r3, r4, r5, r6, r7, r8, r9, sl, fp, ip, sp, lr, pc}

2a44: aed4f47e mrcge 4, 6, APSR\_nzcv, cr4, cr14, {3}

2a48: 0b0df105 bleq 37ee64 <\_r\_debug@@GLIBC\_2.4+0x355508>

2a4c: bed0f7fe mrclt 7, 6, APSR\_nzcv, cr0, cr14, {7}

2a50: 2e004306 cdpcs 3, 0, cr4, cr0, cr6, {0}

2a54: aeccf43e mcrge 4, 6, pc, cr12, cr14, {1} ; <UNPREDICTABLE>

2a58: 380cf8df stmdacc ip, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

2a5c: 605e447b subsvs r4, lr, fp, ror r4

2a60: bec6f7fe mcrlt 7, 6, pc, cr6, cr14, {7} ; <UNPREDICTABLE>

2a64: 5804f8df stmdapl r4, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

2a68: f505447d ; <UNDEFINED> instruction: 0xf505447d

2a6c: 4628751b ; <UNDEFINED> instruction: 0x4628751b

2a70: fe50f00d cdp2 0, 5, cr15, cr0, cr13, {0}

2a74: f00f4628 ; <UNDEFINED> instruction: 0xf00f4628

2a78: 4428fe63 strtmi pc, [r8], #-3683 ; 0xfffff19d

2a7c: 78461c45 stmdavc r6, {r0, r2, r6, sl, fp, ip}^

2a80: d1f42e00 mvnsle r2, r0, lsl #28

2a84: 07e8f8df ubfxeq pc, pc, #17, #9

2a88: 44784631 ldrbtmi r4, [r8], #-1585 ; 0xfffff9cf

2a8c: f820f00f ; <UNDEFINED> instruction: 0xf820f00f

2a90: f8dfb118 ; <UNDEFINED> instruction: 0xf8dfb118

2a94: 447b37e0 ldrbtmi r3, [fp], #-2016 ; 0xfffff820

2a98: 6bfb601e blvs ffedab18 <\_r\_debug@@GLIBC\_2.4+0xffeb11bc>

2a9c: f43e2b00 ; <UNDEFINED> instruction: 0xf43e2b00

2aa0: 2005aefc strdcs sl, [r5], -ip

2aa4: f9d0f00f ; <UNDEFINED> instruction: 0xf9d0f00f

2aa8: bef7f7fe mrclt 7, 7, APSR\_nzcv, cr7, cr14, {7}

2aac: f8df7a6b ; <UNDEFINED> instruction: 0xf8df7a6b

2ab0: fab327c8 blx feccc9d8 <\_r\_debug@@GLIBC\_2.4+0xfeca307c>

2ab4: 447af383 ldrbtmi pc, [sl], #-899 ; 0xfffffc7d ; <UNPREDICTABLE>

2ab8: 62d3095b sbcsvs r0, r3, #1490944 ; 0x16c000

2abc: be98f7fe mrclt 7, 4, APSR\_nzcv, cr8, cr14, {7}

2ac0: 27b8f8df sbfxcs pc, pc, #17, #25

2ac4: 447a7a2b ldrbtmi r7, [sl], #-2603 ; 0xfffff5d5

2ac8: bf183300 svclt 0x00183300

2acc: 60932301 addsvs r2, r3, r1, lsl #6

2ad0: be8ef7fe mcrlt 7, 4, pc, cr14, cr14, {7} ; <UNPREDICTABLE>

2ad4: 0282eb00 addeq lr, r2, #0, 22

2ad8: 57a4f8df sbfxpl pc, pc, #17, #5

2adc: 6bfe6812 blvs fff9cb2c <\_r\_debug@@GLIBC\_2.4+0xfff731d0>

2ae0: f8c5447d ; <UNDEFINED> instruction: 0xf8c5447d

2ae4: 2e002528 cfsh32cs mvfx2, mvfx0, #24

2ae8: 8257f041 subshi pc, r7, #65 ; 0x41

2aec: f8df3301 ; <UNDEFINED> instruction: 0xf8df3301

2af0: 428b5794 addmi r5, fp, #148, 14 ; 0x2500000

2af4: 1790f8df ; <UNDEFINED> instruction: 0x1790f8df

2af8: 4479447d ldrbtmi r4, [r9], #-1149 ; 0xfffffb83

2afc: f850bf34 ; <UNDEFINED> instruction: 0xf850bf34

2b00: 6bfb3023 blvs ffeceb94 <\_r\_debug@@GLIBC\_2.4+0xffea5238>

2b04: 1180f8d1 ldrdne pc, [r0, r1]

2b08: 3524f8c5 strcc pc, [r4, #-2245]! ; 0xfffff73b

2b0c: b14168d0 ldrdlt r6, [r1, #-128] ; 0xffffff80

2b10: bf181ace svclt 0x00181ace

2b14: 42812601 addmi r2, r1, #1048576 ; 0x100000

2b18: 2600bf18 ; <UNDEFINED> instruction: 0x2600bf18

2b1c: f0412e00 ; <UNDEFINED> instruction: 0xf0412e00

2b20: 42988236 addsmi r8, r8, #1610612739 ; 0x60000003

2b24: 823ff041 eorshi pc, pc, #65 ; 0x41

2b28: 3760f8df ; <UNDEFINED> instruction: 0x3760f8df

2b2c: f503447b ; <UNDEFINED> instruction: 0xf503447b

2b30: 60d161a3 sbcsvs r6, r1, r3, lsr #3

2b34: 3524f8d3 strcc pc, [r4, #-2259]! ; 0xfffff72d

2b38: f0012b00 ; <UNDEFINED> instruction: 0xf0012b00

2b3c: 6918819c ldmdbvs r8, {r2, r3, r4, r7, r8, pc}

2b40: bf024290 svclt 0x00024290

2b44: 23016119 movwcs r6, #4377 ; 0x1119

2b48: f04163bb ; <UNDEFINED> instruction: 0xf04163bb

2b4c: 6bfb81c7 blvs ffee3270 <\_r\_debug@@GLIBC\_2.4+0xffeb9914>

2b50: 0a60f107 beq 183ef74 <\_r\_debug@@GLIBC\_2.4+0x1815618>

2b54: 1738f8df ; <UNDEFINED> instruction: 0x1738f8df

2b58: 0738f8df ; <UNDEFINED> instruction: 0x0738f8df

2b5c: f383fab3 ; <UNDEFINED> instruction: 0xf383fab3

2b60: 46524479 ; <UNDEFINED> instruction: 0x46524479

2b64: 095b4478 ldmdbeq fp, {r3, r4, r5, r6, sl, lr}^

2b68: 4004f8ca andmi pc, r4, sl, asr #17

2b6c: 3000f8ca andcc pc, r0, sl, asr #17

2b70: f910f00e ; <UNDEFINED> instruction: 0xf910f00e

2b74: 3720f8df ; <UNDEFINED> instruction: 0x3720f8df

2b78: 791b447b ldmdbvc fp, {r0, r1, r3, r4, r5, r6, sl, lr}

2b7c: 6b7b62bb blvs 1edb670 <\_r\_debug@@GLIBC\_2.4+0x1eb1d14>

2b80: f0012b00 ; <UNDEFINED> instruction: 0xf0012b00

2b84: f1b981a6 ; <UNDEFINED> instruction: 0xf1b981a6

2b88: d0120f00 andsle r0, r2, r0, lsl #30

2b8c: 270cf8df ; <UNDEFINED> instruction: 0x270cf8df

2b90: f8df2500 ; <UNDEFINED> instruction: 0xf8df2500

2b94: 447a370c ldrbtmi r3, [sl], #-1804 ; 0xfffff8f4

2b98: 68146af9 ldmdavs r4, {r0, r3, r4, r5, r6, r7, r9, fp, sp, lr}

2b9c: f8df58c8 ; <UNDEFINED> instruction: 0xf8df58c8

2ba0: 68231704 stmdavs r3!, {r2, r8, r9, sl, ip}

2ba4: 44796864 ldrbtmi r6, [r9], #-2148 ; 0xfffff79c

2ba8: 03fff023 mvnseq pc, #35 ; 0x23

2bac: 60036015 andvs r6, r3, r5, lsl r0

2bb0: 6bfb654c blvs ffedc0e8 <\_r\_debug@@GLIBC\_2.4+0xffeb278c>

2bb4: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

2bb8: f8db82aa ; <UNDEFINED> instruction: 0xf8db82aa

2bbc: 2a002140 bcs b0c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4430>

2bc0: f8dfd072 ; <UNDEFINED> instruction: 0xf8dfd072

2bc4: 447b36e4 ldrbtmi r3, [fp], #-1764 ; 0xfffff91c

2bc8: 1168f8d3 ldrdne pc, [r8, #-131]! ; 0xffffff7d

2bcc: f0402900 ; <UNDEFINED> instruction: 0xf0402900

2bd0: 6b59829a blvs 1663640 <\_r\_debug@@GLIBC\_2.4+0x1639ce4>

2bd4: f0402900 ; <UNDEFINED> instruction: 0xf0402900

2bd8: f8db8296 ; <UNDEFINED> instruction: 0xf8db8296

2bdc: f8db1034 ; <UNDEFINED> instruction: 0xf8db1034

2be0: f8d13118 ; <UNDEFINED> instruction: 0xf8d13118

2be4: 2b009004 blcs 26bfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15e94>

2be8: 8283f000 addhi pc, r3, #0

2bec: 6164f8db ldrdvs pc, [r4, #-139]! ; 0xffffff75

2bf0: f8db6855 ; <UNDEFINED> instruction: 0xf8db6855

2bf4: 685b8160 ldmdavs fp, {r5, r6, r8, pc}^

2bf8: 0686eb08 streq lr, [r6], r8, lsl #22

2bfc: 0a03eb05 beq fd818 <\_r\_debug@@GLIBC\_2.4+0xd3ebc>

2c00: bf384555 svclt 0x00384555

2c04: d22945b0 eorle r4, r9, #176, 10 ; 0x2c000000

2c08: 46a04644 strtmi r4, [r0], r4, asr #12

2c0c: 1b04f854 blne 140d64 <\_r\_debug@@GLIBC\_2.4+0x117408>

2c10: d019458b andsle r4, r9, fp, lsl #11

2c14: 2b00680b blcs 1cc48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xbee0>

2c18: 81e3f040 mvnhi pc, r0, asr #32

2c1c: 3114f8d1 ; <UNDEFINED> instruction: 0x3114f8d1

2c20: f0002b00 ; <UNDEFINED> instruction: 0xf0002b00

2c24: 685a8246 ldmdavs sl, {r1, r2, r6, r9, pc}^

2c28: 429a68ab addsmi r6, sl, #11206656 ; 0xab0000

2c2c: f8d1d116 ; <UNDEFINED> instruction: 0xf8d1d116

2c30: b19b3120 orrslt r3, fp, r0, lsr #2

2c34: 686b685a stmdavs fp!, {r1, r3, r4, r6, fp, sp, lr}^

2c38: d10f429a ; <UNDEFINED> instruction: 0xd10f429a

2c3c: 44486828 strbmi r6, [r8], #-2088 ; 0xfffff7d8

2c40: f8d4f009 ; <UNDEFINED> instruction: 0xf8d4f009

2c44: 3514b150 ldrcc fp, [r4, #-336] ; 0xfffffeb0

2c48: 46a042a6 strtmi r4, [r0], r6, lsr #5

2c4c: 2300bf94 movwcs fp, #3988 ; 0xf94

2c50: 45552301 ldrbmi r2, [r5, #-769] ; 0xfffffcff

2c54: 2300bf28 movwcs fp, #3880 ; 0xf28

2c58: d1d62b00 bicsle r2, r6, r0, lsl #22

2c5c: 964cf8df ; <UNDEFINED> instruction: 0x964cf8df

2c60: bf084546 svclt 0x00084546

2c64: f50b4555 ; <UNDEFINED> instruction: 0xf50b4555

2c68: 44f974b0 ldrbtmi r7, [r9], #1200 ; 0x4b0

2c6c: 2501bf0c strcs fp, [r1, #-3852] ; 0xfffff0f4

2c70: f8d92500 ; <UNDEFINED> instruction: 0xf8d92500

2c74: f0022000 ; <UNDEFINED> instruction: 0xf0022000

2c78: f0400301 ; <UNDEFINED> instruction: 0xf0400301

2c7c: 2b0081b9 blcs 23368 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12600>

2c80: 821ff040 andshi pc, pc, #64 ; 0x40

2c84: 3628f8df ; <UNDEFINED> instruction: 0x3628f8df

2c88: 0618f109 ldreq pc, [r8], -r9, lsl #2

2c8c: 0003e894 muleq r3, r4, r8

2c90: 609c447b addsvs r4, ip, fp, ror r4

2c94: f8d30594 ; <UNDEFINED> instruction: 0xf8d30594

2c98: e8863510 stm r6, {r4, r8, sl, ip, sp}

2c9c: f8c90003 ; <UNDEFINED> instruction: 0xf8c90003

2ca0: f1403178 ; <UNDEFINED> instruction: 0xf1403178

2ca4: e1c081d1 ldrd r8, [r0, #17]

2ca8: 3608f8df ; <UNDEFINED> instruction: 0x3608f8df

2cac: 74b0f50b ldrtvc pc, [r0], #1291 ; 0x50b ; <UNPREDICTABLE>

2cb0: 681a447b ldmdavs sl, {r0, r1, r3, r4, r5, r6, sl, lr}

2cb4: 5600f8df ; <UNDEFINED> instruction: 0x5600f8df

2cb8: 6600f8df ; <UNDEFINED> instruction: 0x6600f8df

2cbc: e894447d ldm r4, {r0, r2, r3, r4, r5, r6, sl, lr}

2cc0: 447e0003 ldrbtmi r0, [lr], #-3

2cc4: 3510f8d5 ldrcc pc, [r0, #-2261] ; 0xfffff72b

2cc8: f10660ac ; <UNDEFINED> instruction: 0xf10660ac

2ccc: 05950418 ldreq r0, [r5, #1048] ; 0x418

2cd0: 3178f8c6 cmncc r8, r6, asr #17 ; <UNPREDICTABLE>

2cd4: 0003e884 andeq lr, r3, r4, lsl #17

2cd8: 81a6f100 ; <UNDEFINED> instruction: 0x81a6f100

2cdc: 35e0f8df strbcc pc, [r0, #2271]! ; 0x8df ; <UNPREDICTABLE>

2ce0: 55e0f8df strbpl pc, [r0, #2271]! ; 0x8df ; <UNPREDICTABLE>

2ce4: f8df447b ; <UNDEFINED> instruction: 0xf8df447b

2ce8: 447d95e0 ldrbtmi r9, [sp], #-1504 ; 0xfffffa20

2cec: 8164f8db ldrdhi pc, [r4, #-139]! ; 0xffffff75

2cf0: 6168f8d3 ldrdvs pc, [r8, #-131]! ; 0xffffff7d

2cf4: 65a3f505 strvs pc, [r3, #1285]! ; 0x505

2cf8: 44f96ada ldrbtmi r6, [r9], #2778 ; 0xada

2cfc: bf183600 svclt 0x00183600

2d00: 43322601 teqmi r2, #1048576 ; 0x100000

2d04: f11862da ; <UNDEFINED> instruction: 0xf11862da

2d08: d32d38ff ; <UNDEFINED> instruction: 0xd32d38ff

2d0c: 31f8f8db ldrsbcc pc, [r8, #139]! ; 0x8b ; <UNPREDICTABLE>

2d10: f8532201 ; <UNDEFINED> instruction: 0xf8532201

2d14: 69e34028 stmibvs r3!, {r3, r5, lr}^

2d18: bb03685b bllt dce8c <\_r\_debug@@GLIBC\_2.4+0xb3530>

2d1c: 319af894 ; <UNDEFINED> instruction: 0x319af894

2d20: f36f42ac vhsub.u32 d20, d31, d28

2d24: f8840300 ; <UNDEFINED> instruction: 0xf8840300

2d28: d00a319a mulle sl, sl, r1

2d2c: 202cf8d9 ldrdcs pc, [ip], -r9 ; <UNPREDICTABLE>

2d30: f8d44633 ; <UNDEFINED> instruction: 0xf8d44633

2d34: 462011d0 ; <UNDEFINED> instruction: 0x462011d0

2d38: bf183200 svclt 0x00183200

2d3c: f0052201 ; <UNDEFINED> instruction: 0xf0052201

2d40: f8d4fdb3 ; <UNDEFINED> instruction: 0xf8d4fdb3

2d44: 2b003238 blcs f62c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x30c>

2d48: f8dfd0dd ; <UNDEFINED> instruction: 0xf8dfd0dd

2d4c: 447b3580 ldrbtmi r3, [fp], #-1408 ; 0xfffffa80

2d50: 2b00791b blcs 211c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1045c>

2d54: 4620d0d7 ; <UNDEFINED> instruction: 0x4620d0d7

2d58: fc40f00a mcrr2 0, 0, pc, r0, cr10 ; <UNPREDICTABLE>

2d5c: 609ae7d3 ; <UNDEFINED> instruction: 0x609ae7d3

2d60: 2b00685b blcs 1ced4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc16c>

2d64: e7fad0da ubfx sp, sl, #1, #27

2d68: 3564f8df strbcc pc, [r4, #-2271]! ; 0xfffff721 ; <UNPREDICTABLE>

2d6c: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

2d70: 2c004504 cfstr32cs mvfx4, [r0], {4}

2d74: 8131f040 teqhi r1, r0, asr #32 ; <UNPREDICTABLE>

2d78: b92b6abb stmdblt fp!, {r0, r1, r3, r4, r5, r7, r9, fp, sp, lr}

2d7c: 3554f8df ldrbcc pc, [r4, #-2271] ; 0xfffff721 ; <UNPREDICTABLE>

2d80: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

2d84: b923380c stmdblt r3!, {r2, r3, fp, ip, sp}

2d88: f8f8f00a ; <UNDEFINED> instruction: 0xf8f8f00a

2d8c: 42986b3b addsmi r6, r8, #60416 ; 0xec00

2d90: f8dfd007 ; <UNDEFINED> instruction: 0xf8dfd007

2d94: 447a2544 ldrbtmi r2, [sl], #-1348 ; 0xfffffabc

2d98: 3828f8d2 stmdacc r8!, {r1, r4, r6, r7, fp, ip, sp, lr, pc}

2d9c: f8c23301 ; <UNDEFINED> instruction: 0xf8c23301

2da0: 6b7d3828 blvs 1f50e48 <\_r\_debug@@GLIBC\_2.4+0x1f274ec>

2da4: f00a4628 ; <UNDEFINED> instruction: 0xf00a4628

2da8: f8dff9fb ; <UNDEFINED> instruction: 0xf8dff9fb

2dac: 447b3530 ldrbtmi r3, [fp], #-1328 ; 0xfffffad0

2db0: b953791b ldmdblt r3, {r0, r1, r3, r4, r8, fp, ip, sp, lr}^

2db4: f04f4628 ; <UNDEFINED> instruction: 0xf04f4628

2db8: f2c00c05 vmull.s8 q8, d0, d5

2dbc: f00e0c0f ; <UNDEFINED> instruction: 0xf00e0c0f

2dc0: f510f8ff pldw [r0, #-2303] ; 0xfffff701

2dc4: f2005f80 vmax.f32 d5, d16, d0

2dc8: f8df80fb ; <UNDEFINED> instruction: 0xf8df80fb

2dcc: f8df3514 ; <UNDEFINED> instruction: 0xf8df3514

2dd0: 447b1514 ldrbtmi r1, [fp], #-1300 ; 0xfffffaec

2dd4: f8d34479 ; <UNDEFINED> instruction: 0xf8d34479

2dd8: f8d12178 ; <UNDEFINED> instruction: 0xf8d12178

2ddc: 429a3510 addsmi r3, sl, #16, 10 ; 0x4000000

2de0: 80e4f040 rschi pc, r4, r0, asr #32

2de4: f0846bbb ; <UNDEFINED> instruction: 0xf0846bbb

2de8: 42230401 eormi r0, r3, #16777216 ; 0x1000000

2dec: f00dd118 ; <UNDEFINED> instruction: 0xf00dd118

2df0: f8dffb1f ; <UNDEFINED> instruction: 0xf8dffb1f

2df4: 447a24f4 ldrbtmi r2, [sl], #-1268 ; 0xfffffb0c

2df8: 31b0f8d2 asrscc pc, r2 ; <illegal shifter operand> ; <UNPREDICTABLE>

2dfc: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

2e00: 210080b7 strhcs r8, [r0, -r7]

2e04: f0084608 ; <UNDEFINED> instruction: 0xf0084608

2e08: 2300fded movwcs pc, #3565 ; 0xded ; <UNPREDICTABLE>

2e0c: f00860c3 ; <UNDEFINED> instruction: 0xf00860c3

2e10: f00cfde7 ; <UNDEFINED> instruction: 0xf00cfde7

2e14: f507fdb5 ; <UNDEFINED> instruction: 0xf507fdb5

2e18: 46bd77b6 ; <UNDEFINED> instruction: 0x46bd77b6

2e1c: 8ff0e8bd svchi 0x00f0e8bd

2e20: 46b0f891 ssatmi pc, #17, r1, lsl #17 ; <UNPREDICTABLE>

2e24: f5012300 ; <UNDEFINED> instruction: 0xf5012300

2e28: 461a60a3 ldrmi r6, [sl], -r3, lsr #1

2e2c: 0482f36f streq pc, [r2], #879 ; 0x36f

2e30: 46b0f881 ldrtmi pc, [r0], r1, lsl #17 ; <UNPREDICTABLE>

2e34: 11d0f8db ldrsbne pc, [r0, #139] ; 0x8b ; <UNPREDICTABLE>

2e38: fd36f005 ldc2 0, cr15, [r6, #-20]! ; 0xffffffec

2e3c: f8dfe7d7 ; <UNDEFINED> instruction: 0xf8dfe7d7

2e40: 220134ac andcs r3, r1, #172, 8 ; 0xac000000

2e44: 629a447b addsvs r4, sl, #2063597568 ; 0x7b000000

2e48: f7fe46b5 ; <UNDEFINED> instruction: 0xf7fe46b5

2e4c: f8dfbd26 ; <UNDEFINED> instruction: 0xf8dfbd26

2e50: 447c44a0 ldrbtmi r4, [ip], #-1184 ; 0xfffffb60

2e54: 20024621 andcs r4, r2, r1, lsr #12

2e58: ffb8f008 ; <UNDEFINED> instruction: 0xffb8f008

2e5c: f00e207f ; <UNDEFINED> instruction: 0xf00e207f

2e60: e7f7fff3 udf #32755 ; 0x7ff3

2e64: 280a1af0 stmdacs sl, {r4, r5, r6, r7, r9, fp, ip}

2e68: eba1bf9f bl fe872cec <\_r\_debug@@GLIBC\_2.4+0xfe849390>

2e6c: f1030383 ; <UNDEFINED> instruction: 0xf1030383

2e70: f5a34340 ; <UNDEFINED> instruction: 0xf5a34340

2e74: 601a733f andsvs r7, sl, pc, lsr r3

2e78: b81cf7ff ldmdalt ip, {r0, r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}

2e7c: 1474f8df ldrbtne pc, [r4], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2e80: 46282207 strtmi r2, [r8], -r7, lsl #4

2e84: f00f4479 ; <UNDEFINED> instruction: 0xf00f4479

2e88: 2800fdd9 stmdacs r0, {r0, r3, r4, r6, r7, r8, sl, fp, ip, sp, lr, pc}

2e8c: acb0f47e cfldrsge mvf15, [r0], #504 ; 0x1f8

2e90: 2b007a2b blcs 21744 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x109dc>

2e94: acacf43e cfstrsge mvf15, [ip], #248 ; 0xf8

2e98: 345cf8df ldrbcc pc, [ip], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2e9c: 447b3508 ldrbtmi r3, [fp], #-1288 ; 0xfffffaf8

2ea0: 5168f8c3 msrpl SPSR\_f, r3, asr #17

2ea4: bca4f7fe stclt 7, cr15, [r4], #1016 ; 0x3f8

2ea8: 3450f8df ldrbcc pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2eac: f8df2273 ; <UNDEFINED> instruction: 0xf8df2273

2eb0: f8df1450 ; <UNDEFINED> instruction: 0xf8df1450

2eb4: 447b0450 ldrbtmi r0, [fp], #-1104 ; 0xfffffbb0

2eb8: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

2ebc: fd66f00d stc2l 0, cr15, [r6, #-52]! ; 0xffffffcc

2ec0: 3444f8df strbcc pc, [r4], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2ec4: f8df2269 ; <UNDEFINED> instruction: 0xf8df2269

2ec8: f8df1444 ; <UNDEFINED> instruction: 0xf8df1444

2ecc: 447b0444 ldrbtmi r0, [fp], #-1092 ; 0xfffffbbc

2ed0: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

2ed4: fd5af00d ldc2l 0, cr15, [sl, #-52] ; 0xffffffcc

2ed8: 3438f8df ldrtcc pc, [r8], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2edc: 42f9f240 rscsmi pc, r9, #64, 4

2ee0: 1434f8df ldrtne pc, [r4], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2ee4: 0434f8df ldrteq pc, [r4], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

2ee8: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

2eec: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

2ef0: f8dffd4d ; <UNDEFINED> instruction: 0xf8dffd4d

2ef4: 2268342c rsbcs r3, r8, #44, 8 ; 0x2c000000

2ef8: 1428f8df strtne pc, [r8], #-2271 ; 0xfffff721

2efc: 0428f8df strteq pc, [r8], #-2271 ; 0xfffff721

2f00: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

2f04: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

2f08: f8dffd41 ; <UNDEFINED> instruction: 0xf8dffd41

2f0c: 227e3420 rsbscs r3, lr, #32, 8 ; 0x20000000

2f10: 141cf8df ldrne pc, [ip], #-2271 ; 0xfffff721

2f14: 041cf8df ldreq pc, [ip], #-2271 ; 0xfffff721

2f18: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

2f1c: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

2f20: f8dffd35 ; <UNDEFINED> instruction: 0xf8dffd35

2f24: 22833414 addcs r3, r3, #20, 8 ; 0x14000000

2f28: 1410f8df ldrne pc, [r0], #-2271 ; 0xfffff721

2f2c: 0410f8df ldreq pc, [r0], #-2271 ; 0xfffff721

2f30: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

2f34: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

2f38: f8dffd29 ; <UNDEFINED> instruction: 0xf8dffd29

2f3c: 22873408 addcs r3, r7, #8, 8 ; 0x8000000

2f40: 1404f8df strne pc, [r4], #-2271 ; 0xfffff721

2f44: 0404f8df streq pc, [r4], #-2271 ; 0xfffff721

2f48: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

2f4c: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

2f50: f423fd1d ; <UNDEFINED> instruction: 0xf423fd1d

2f54: f021610e ; <UNDEFINED> instruction: 0xf021610e

2f58: 29000109 stmdbcs r0, {r0, r3, r8}

2f5c: a986f43f stmibge r6, {r0, r1, r2, r3, r4, r5, sl, ip, sp, lr, pc}

2f60: 447848fb ldrbtmi r4, [r8], #-2299 ; 0xfffff705

2f64: ff06f008 ; <UNDEFINED> instruction: 0xff06f008

2f68: 320cf8d4 andcc pc, ip, #212, 16 ; 0xd40000

2f6c: b97ef7ff ldmdblt lr!, {r0, r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}^

2f70: 447b4bf8 ldrbtmi r4, [fp], #-3064 ; 0xfffff408

2f74: f895681d ; <UNDEFINED> instruction: 0xf895681d

2f78: f0133199 ; <UNDEFINED> instruction: 0xf0133199

2f7c: f47f0308 ; <UNDEFINED> instruction: 0xf47f0308

2f80: f8d2af40 ; <UNDEFINED> instruction: 0xf8d2af40

2f84: f50581ac ; <UNDEFINED> instruction: 0xf50581ac

2f88: 461c7518 ; <UNDEFINED> instruction: 0x461c7518

2f8c: f8d84616 ; <UNDEFINED> instruction: 0xf8d84616

2f90: b1133000 tstlt r3, r0

2f94: 46282100 strtmi r2, [r8], -r0, lsl #2

2f98: f8d64798 ; <UNDEFINED> instruction: 0xf8d64798

2f9c: 340131b0 strcc r3, [r1], #-432 ; 0xfffffe50

2fa0: 8020f8d8 ldrdhi pc, [r0], -r8 ; <UNPREDICTABLE>

2fa4: 42a33508 adcmi r3, r3, #8, 10 ; 0x2000000

2fa8: e72ad8f1 ; <UNDEFINED> instruction: 0xe72ad8f1

2fac: f44f4bea ; <UNDEFINED> instruction: 0xf44f4bea

2fb0: 49ea620c stmibmi sl!, {r2, r3, r9, sp, lr}^

2fb4: 447b48ea ldrbtmi r4, [fp], #-2282 ; 0xfffff716

2fb8: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

2fbc: fce6f00d stc2l 0, cr15, [r6], #52 ; 0x34

2fc0: 4ce94de8 stclmi 13, cr4, [r9], #928 ; 0x3a0

2fc4: 447c447d ldrbtmi r4, [ip], #-1149 ; 0xfffffb83

2fc8: 4621462a strtmi r4, [r1], -sl, lsr #12

2fcc: f0082002 ; <UNDEFINED> instruction: 0xf0082002

2fd0: 207ffefd ldrshtcs pc, [pc], #-237 ; <UNPREDICTABLE>

2fd4: ff38f00e ; <UNDEFINED> instruction: 0xff38f00e

2fd8: f009e7f6 ; <UNDEFINED> instruction: 0xf009e7f6

2fdc: 2400fa97 strcs pc, [r0], #-2711 ; 0xfffff569

2fe0: 4be2e6ca blmi ff8bcb10 <\_r\_debug@@GLIBC\_2.4+0xff8931b4>

2fe4: 74b0f50b ldrtvc pc, [r0], #1291 ; 0x50b ; <UNPREDICTABLE>

2fe8: 681a447b ldmdavs sl, {r0, r1, r3, r4, r5, r6, sl, lr}

2fec: 0301f002 movweq pc, #4098 ; 0x1002 ; <UNPREDICTABLE>

2ff0: f43f2b00 ; <UNDEFINED> instruction: 0xf43f2b00

2ff4: 49deae5f ldmibmi lr, {r0, r1, r2, r3, r4, r6, r9, sl, fp, sp, pc}^

2ff8: 447948de ldrbtmi r4, [r9], #-2270 ; 0xfffff722

2ffc: 44784dde ldrbtmi r4, [r8], #-3550 ; 0xfffff222

3000: feb8f008 cdp2 0, 11, cr15, cr8, cr8, {0}

3004: 447d4add ldrbtmi r4, [sp], #-2781 ; 0xfffff523

3008: 0003e894 muleq r3, r4, r8

300c: f8d2447a ; <UNDEFINED> instruction: 0xf8d2447a

3010: 60943510 addsvs r3, r4, r0, lsl r5

3014: 0218f105 andseq pc, r8, #1073741825 ; 0x40000001

3018: f8c5682c ; <UNDEFINED> instruction: 0xf8c5682c

301c: 05a33178 streq r3, [r3, #376]! ; 0x178

3020: 0003e882 andeq lr, r3, r2, lsl #17

3024: ae5af57f mrcge 5, 2, APSR\_nzcv, cr10, cr15, {3}

3028: 48d52500 ldmmi r5, {r8, sl, sp}^

302c: 4478465c ldrbtmi r4, [r8], #-1628 ; 0xfffff9a4

3030: fea0f008 cdp2 0, 10, cr15, cr0, cr8, {0}

3034: 21004620 tstcs r0, r0, lsr #12

3038: fe2af00a cdp2 0, 2, cr15, cr10, cr10, {0}

303c: 2c0068e4 stccs 8, cr6, [r0], {228} ; 0xe4

3040: 2d00d1f8 stfcsd f5, [r0, #-992] ; 0xfffffc20

3044: ae4af43f mcrge 4, 2, pc, cr10, cr15, {1} ; <UNPREDICTABLE>

3048: 2144f8db ldrdcs pc, [r4, #-139] ; 0xffffff75

304c: f8dbb142 ; <UNDEFINED> instruction: 0xf8dbb142

3050: b303311c movwlt r3, #12572 ; 0x311c

3054: 46586851 ; <UNDEFINED> instruction: 0x46586851

3058: 440a685a strmi r6, [sl], #-2138 ; 0xfffff7a6

305c: feaaf00d cdp2 0, 10, cr15, cr10, cr13, {0}

3060: 465c4dc8 ldrbmi r4, [ip], -r8, asr #27

3064: e006447d and r4, r6, sp, ror r4

3068: 3238f8d4 eorscc pc, r8, #212, 16 ; 0xd40000

306c: 792bb10b stmdbvc fp!, {r0, r1, r3, r8, ip, sp, pc}

3070: 68e4b9db stmiavs r4!, {r0, r1, r3, r4, r6, r7, r8, fp, ip, sp, pc}^

3074: f8d4b16c ; <UNDEFINED> instruction: 0xf8d4b16c

3078: f8942254 ; <UNDEFINED> instruction: 0xf8942254

307c: f0433198 ; <UNDEFINED> instruction: 0xf0433198

3080: f8840304 ; <UNDEFINED> instruction: 0xf8840304

3084: 2a003198 bcs f6ec <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3cc>

3088: 4620d0ee strtmi sp, [r0], -lr, ror #1

308c: fbc2f005 blx ff0bf0aa <\_r\_debug@@GLIBC\_2.4+0xff09574e>

3090: 2401e7ea strcs lr, [r1], #-2026 ; 0xfffff816

3094: 4bbce670 blmi fef3ca5c <\_r\_debug@@GLIBC\_2.4+0xfef13100>

3098: 0257f640 subseq pc, r7, #64, 12 ; 0x4000000

309c: 48bc49bb ldmmi ip!, {r0, r1, r3, r4, r5, r7, r8, fp, lr}

30a0: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

30a4: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

30a8: 4620fc71 ; <UNDEFINED> instruction: 0x4620fc71

30ac: fa96f00a blx fe5bf0dc <\_r\_debug@@GLIBC\_2.4+0xfe595780>

30b0: 4bb8e7df blmi fee3d034 <\_r\_debug@@GLIBC\_2.4+0xfee136d8>

30b4: 74b0f50b ldrtvc pc, [r0], #1291 ; 0x50b ; <UNPREDICTABLE>

30b8: 681a447b ldmdavs sl, {r0, r1, r3, r4, r5, r6, sl, lr}

30bc: 0301f002 movweq pc, #4098 ; 0x1002 ; <UNPREDICTABLE>

30c0: 49b5e796 ldmibmi r5!, {r1, r2, r4, r7, r8, r9, sl, sp, lr, pc}

30c4: 447948b5 ldrbtmi r4, [r9], #-2229 ; 0xfffff74b

30c8: f0084478 ; <UNDEFINED> instruction: 0xf0084478

30cc: 4bb4fe53 blmi fed42a20 <\_r\_debug@@GLIBC\_2.4+0xfed190c4>

30d0: 0003e894 muleq r3, r4, r8

30d4: 0218f109 andseq pc, r8, #1073741826 ; 0x40000002

30d8: f8d9447b ; <UNDEFINED> instruction: 0xf8d9447b

30dc: 609c6000 addsvs r6, ip, r0

30e0: 3510f8d3 ldrcc pc, [r0, #-2259] ; 0xfffff72d

30e4: 0003e882 andeq lr, r3, r2, lsl #17

30e8: f8c905b2 ; <UNDEFINED> instruction: 0xf8c905b2

30ec: d5ab3178 strle r3, [fp, #376]! ; 0x178

30f0: 4bace79b blmi feb3cf64 <\_r\_debug@@GLIBC\_2.4+0xfeb13608>

30f4: 0205f640 andeq pc, r5, #64, 12 ; 0x4000000

30f8: 48ac49ab stmiami ip!, {r0, r1, r3, r5, r7, r8, fp, lr}

30fc: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3100: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

3104: 681afc43 ldmdavs sl, {r0, r1, r6, sl, fp, ip, sp, lr, pc}

3108: 74b0f50b ldrtvc pc, [r0], #1291 ; 0x50b ; <UNPREDICTABLE>

310c: 4ba8e5d2 blmi fea3c85c <\_r\_debug@@GLIBC\_2.4+0xfea12f00>

3110: 681a447b ldmdavs sl, {r0, r1, r3, r4, r5, r6, sl, lr}

3114: f1400511 ; <UNDEFINED> instruction: 0xf1400511

3118: f8db819c ; <UNDEFINED> instruction: 0xf8db819c

311c: 2b003164 blcs f6b4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x394>

3120: 815ff000 cmphi pc, r0 ; <UNPREDICTABLE>

3124: 828cf8df addhi pc, ip, #14614528 ; 0xdf0000

3128: f8df2500 ; <UNDEFINED> instruction: 0xf8df2500

312c: 4ba3928c blmi fe8e7b64 <\_r\_debug@@GLIBC\_2.4+0xfe8be208>

3130: 44f944f8 ldrbtmi r4, [r9], #1272 ; 0x4f8

3134: 62fb447b rscsvs r4, fp, #2063597568 ; 0x7b000000

3138: f8d4e00e ; <UNDEFINED> instruction: 0xf8d4e00e

313c: 46334244 ldrtmi r4, [r3], -r4, asr #4

3140: 2001499f mulcs r1, pc, r9 ; <UNPREDICTABLE>

3144: 94004479 strls r4, [r0], #-1145 ; 0xfffffb87

3148: fe40f008 cdp2 0, 4, cr15, cr0, cr8, {0}

314c: 3164f8db ldrdcc pc, [r4, #-139]! ; 0xffffff75

3150: 42ab3501 adcmi r3, fp, #4194304 ; 0x400000

3154: 8145f240 cmphi r5, r0, asr #4 ; <UNPREDICTABLE>

3158: 3160f8db ldrdcc pc, [r0, #-139]! ; 0xffffff75

315c: 4025f853 eormi pc, r5, r3, asr r8 ; <UNPREDICTABLE>

3160: 3199f894 ; <UNDEFINED> instruction: 0x3199f894

3164: d431079e ldrtle r0, [r1], #-1950 ; 0xfffff862

3168: f8d84621 ; <UNDEFINED> instruction: 0xf8d84621

316c: f0080170 ; <UNDEFINED> instruction: 0xf0080170

3170: b108fe3d tstlt r8, sp, lsr lr ; <UNPREDICTABLE>

3174: 4174f8c8 cmnmi r4, r8, asr #17 ; <UNPREDICTABLE>

3178: 681a69e3 ldmdavs sl, {r0, r1, r5, r6, r7, r8, fp, sp, lr}

317c: b9237813 stmdblt r3!, {r0, r1, r4, fp, ip, sp, lr}

3180: 447b4b90 ldrbtmi r4, [fp], #-2960 ; 0xfffff470

3184: 681a681b ldmdavs sl, {r0, r1, r3, r4, fp, sp, lr}

3188: 6863b342 stmdavs r3!, {r1, r6, r8, r9, ip, sp, pc}^

318c: b9217819 stmdblt r1!, {r0, r3, r4, fp, ip, sp, lr}

3190: 447b4b8d ldrbtmi r4, [fp], #-2957 ; 0xfffff473

3194: 681b681b ldmdavs fp, {r0, r1, r3, r4, fp, sp, lr}

3198: 6820b1ab stmdavs r0!, {r0, r1, r3, r5, r7, r8, ip, sp, pc}

319c: 91022108 tstls r2, r8, lsl #2

31a0: 90034649 andls r4, r3, r9, asr #12

31a4: f8d42001 ; <UNDEFINED> instruction: 0xf8d42001

31a8: 960161b0 ; <UNDEFINED> instruction: 0x960161b0

31ac: 96002608 strls r2, [r0], -r8, lsl #12

31b0: fe0cf008 cdp2 0, 0, cr15, cr12, cr8, {0}

31b4: 2248f8d4 subcs pc, r8, #212, 16 ; 0xd40000

31b8: d1be2a00 ; <UNDEFINED> instruction: 0xd1be2a00

31bc: 20016af9 strdcs r6, [r1], -r9

31c0: fe04f008 cdp2 0, 0, cr15, cr4, cr8, {0}

31c4: 4b81e7c2 blmi fe07d0d4 <\_r\_debug@@GLIBC\_2.4+0xfe053778>

31c8: e7e6447b ; <UNDEFINED> instruction: 0xe7e6447b

31cc: 200169e3 andcs r6, r1, r3, ror #19

31d0: 681a497f ldmdavs sl, {r0, r1, r2, r3, r4, r5, r6, r8, fp, lr}

31d4: f0084479 ; <UNDEFINED> instruction: 0xf0084479

31d8: e7b7fdf9 ; <UNDEFINED> instruction: 0xe7b7fdf9

31dc: 447a4a7d ldrbtmi r4, [sl], #-2685 ; 0xfffff583

31e0: bf00e7d3 svclt 0x0000e7d3

31e4: 000273b0 ; <UNDEFINED> instruction: 0x000273b0

31e8: 00026366 andeq r6, r2, r6, ror #6

31ec: 000263c0 andeq r6, r2, r0, asr #7

31f0: 00027382 andeq r7, r2, r2, lsl #7

31f4: 00026338 andeq r6, r2, r8, lsr r3

31f8: 00026a70 andeq r6, r2, r0, ror sl

31fc: 0002636a andeq r6, r2, sl, ror #6

3200: 0002635c andeq r6, r2, ip, asr r3

3204: 00012d72 andeq r2, r1, r2, ror sp

3208: 000262be ; <UNDEFINED> instruction: 0x000262be

320c: 000271d2 ldrdeq r7, [r2], -r2

3210: 00012c82 andeq r2, r1, r2, lsl #25

3214: 000261ac andeq r6, r2, ip, lsr #3

3218: 000268fa strdeq r6, [r2], -sl

321c: 000268d0 ldrdeq r6, [r2], -r0

3220: 000261de ldrdeq r6, [r2], -lr

3224: ffffd81c ; <UNDEFINED> instruction: 0xffffd81c

3228: 00026856 andeq r6, r2, r6, asr r8

322c: 00026812 andeq r6, r2, r2, lsl r8

3230: 00026804 andeq r6, r2, r4, lsl #16

3234: 00000054 andeq r0, r0, r4, asr r0

3238: 00027028 andeq r7, r2, r8, lsr #32

323c: 000267c0 andeq r6, r2, r0, asr #15

3240: 000260bc strheq r6, [r2], -ip

3244: 0002603c andeq r6, r2, ip, lsr r0

3248: 00012ed6 ldrdeq r2, [r1], -r6

324c: 0002671e andeq r6, r2, lr, lsl r7

3250: 000266fc strdeq r6, [r2], -ip

3254: 000266e8 andeq r6, r2, r8, ror #13

3258: 00012da4 andeq r2, r1, r4, lsr #27

325c: 000121aa andeq r2, r1, sl, lsr #3

3260: 00025f98 muleq r2, r8, pc ; <UNPREDICTABLE>

3264: 00012d18 andeq r2, r1, r8, lsl sp

3268: 00025f10 andeq r5, r2, r0, lsl pc

326c: 00011324 andeq r1, r1, r4, lsr #6

3270: 0001214e andeq r2, r1, lr, asr #2

3274: 00025ed6 ldrdeq r5, [r2], -r6

3278: 00025eb6 ; <UNDEFINED> instruction: 0x00025eb6

327c: 00025e46 andeq r5, r2, r6, asr #28

3280: 0002657c andeq r6, r2, ip, ror r5

3284: 00026564 andeq r6, r2, r4, ror #10

3288: 00025e72 andeq r5, r2, r2, ror lr

328c: 00026530 andeq r6, r2, r0, lsr r5

3290: ffffe4c1 ; <UNDEFINED> instruction: 0xffffe4c1

3294: ffffe05d ; <UNDEFINED> instruction: 0xffffe05d

3298: 00026d20 andeq r6, r2, r0, lsr #26

329c: 000263a2 andeq r6, r2, r2, lsr #7

32a0: 00000038 andeq r0, r0, r8, lsr r0

32a4: 00025d66 andeq r5, r2, r6, ror #26

32a8: 00025da6 andeq r5, r2, r6, lsr #27

32ac: 00025d02 andeq r5, r2, r2, lsl #26

32b0: 000263cc andeq r6, r2, ip, asr #7

32b4: 00025cbc ; <UNDEFINED> instruction: 0x00025cbc

32b8: 000263a0 andeq r6, r2, r0, lsr #7

32bc: 00025caa andeq r5, r2, sl, lsr #25

32c0: 00025c88 andeq r5, r2, r8, lsl #25

32c4: 00026372 andeq r6, r2, r2, ror r3

32c8: 00025c72 andeq r5, r2, r2, ror ip

32cc: 00026b4a andeq r6, r2, sl, asr #22

32d0: 000262f0 strdeq r6, [r2], -r0

32d4: 000262dc ldrdeq r6, [r2], -ip

32d8: 000262c6 andeq r6, r2, r6, asr #5

32dc: 00026aea andeq r6, r2, sl, ror #21

32e0: 00025b9a muleq r2, sl, fp

32e4: 00026288 andeq r6, r2, r8, lsl #5

32e8: 00025b76 andeq r5, r2, r6, ror fp

32ec: 00025b28 andeq r5, r2, r8, lsr #22

32f0: 0001250a andeq r2, r1, sl, lsl #10

32f4: 00011ca0 andeq r1, r1, r0, lsr #25

32f8: 00025ace andeq r5, r2, lr, asr #21

32fc: 000128ea andeq r2, r1, sl, ror #17

3300: 00011a14 andeq r1, r1, r4, lsl sl

3304: 00012486 andeq r2, r1, r6, lsl #9

3308: 000128c6 andeq r2, r1, r6, asr #17

330c: 00012448 andeq r2, r1, r8, asr #8

3310: 000124ae andeq r2, r1, lr, lsr #9

3314: 000128e4 andeq r2, r1, r4, ror #17

3318: 00011896 muleq r1, r6, r8

331c: 0001230c andeq r2, r1, ip, lsl #6

3320: 00012894 muleq r1, r4, r8

3324: 00012416 andeq r2, r1, r6, lsl r4

3328: 00012468 andeq r2, r1, r8, ror #8

332c: 00012888 andeq r2, r1, r8, lsl #17

3330: 000119b2 ; <UNDEFINED> instruction: 0x000119b2

3334: 0001233c andeq r2, r1, ip, lsr r3

3338: 00012870 andeq r2, r1, r0, ror r8

333c: 0001199a muleq r1, sl, r9

3340: 00012374 andeq r2, r1, r4, ror r3

3344: 00012858 andeq r2, r1, r8, asr r8

3348: 00011982 andeq r1, r1, r2, lsl #19

334c: 000119bc ; <UNDEFINED> instruction: 0x000119bc

3350: 0001237a andeq r2, r1, sl, ror r3

3354: 000260ea andeq r6, r2, sl, ror #1

3358: 00012816 andeq r2, r1, r6, lsl r8

335c: 000117c8 andeq r1, r1, r8, asr #15

3360: 0001276e andeq r2, r1, lr, ror #14

3364: 00011848 andeq r1, r1, r8, asr #16

3368: 00011856 andeq r1, r1, r6, asr r8

336c: 00025984 andeq r5, r2, r4, lsl #19

3370: 00012786 andeq r2, r1, r6, lsl #15

3374: 0001276a andeq r2, r1, sl, ror #14

3378: 00025966 andeq r5, r2, r6, ror #18

337c: 00026050 andeq r6, r2, r0, asr r0

3380: 000126aa andeq r2, r1, sl, lsr #13

3384: 00026834 andeq r6, r2, r4, lsr r8

3388: 0001272c andeq r2, r1, ip, lsr #14

338c: 000116de ldrdeq r1, [r1], -lr

3390: 0001264c andeq r2, r1, ip, asr #12

3394: 000258b4 ; <UNDEFINED> instruction: 0x000258b4

3398: 0001269e muleq r1, lr, r6

339c: 000126a0 andeq r2, r1, r0, lsr #13

33a0: 00025f84 andeq r5, r2, r4, lsl #31

33a4: 000126d0 ldrdeq r2, [r1], -r0

33a8: 00011682 andeq r1, r1, r2, lsl #13

33ac: 000125a4 andeq r2, r1, r4, lsr #11

33b0: 0002585c andeq r5, r2, ip, asr r8

33b4: 0002583c andeq r5, r2, ip, lsr r8

33b8: 00012476 andeq r2, r1, r6, ror r4

33bc: 000131b4 ; <UNDEFINED> instruction: 0x000131b4

33c0: 00012484 andeq r2, r1, r4, lsl #9

33c4: 00026716 andeq r6, r2, r6, lsl r7

33c8: 00026706 andeq r6, r2, r6, lsl #14

33cc: 00011910 andeq r1, r1, r0, lsl r9

33d0: 000123c0 andeq r2, r1, r0, asr #7

33d4: 000118fa strdeq r1, [r1], -sl

33d8: f0802001 ; <UNDEFINED> instruction: 0xf0802001

33dc: f00e0001 ; <UNDEFINED> instruction: 0xf00e0001

33e0: 6bfbfd33 blvs fff028b4 <\_r\_debug@@GLIBC\_2.4+0xffed8f58>

33e4: f0402b03 ; <UNDEFINED> instruction: 0xf0402b03

33e8: f8df8583 ; <UNDEFINED> instruction: 0xf8df8583

33ec: 447a2bd8 ldrbtmi r2, [sl], #-3032 ; 0xfffff428

33f0: 2b006ad3 blcs 1df44 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd1dc>

33f4: 6a52db7a bvs 14ba1e4 <\_r\_debug@@GLIBC\_2.4+0x1490888>

33f8: d0772a00 rsbsle r2, r7, r0, lsl #20

33fc: 4bc8f8df blmi ff241780 <\_r\_debug@@GLIBC\_2.4+0xff217e24>

3400: f8df2b00 ; <UNDEFINED> instruction: 0xf8df2b00

3404: f04f6bc8 ; <UNDEFINED> instruction: 0xf04f6bc8

3408: 447c0301 ldrbtmi r0, [ip], #-769 ; 0xfffffcff

340c: 5164f8db ldrdpl pc, [r4, #-139]! ; 0xffffff75

3410: 64a3f504 strtvs pc, [r3], #1284 ; 0x504

3414: f2c0447e vmvn.i32 q10, #917504 ; 0x000e0000

3418: bf082300 svclt 0x00082300

341c: 7300f04f movwvc pc, #79 ; 0x4f ; <UNPREDICTABLE>

3420: 3004f8ca andcc pc, r4, sl, asr #17

3424: 35fff115 ldrbcc pc, [pc, #277]! ; 3541 <free@plt+0x2b21> ; <UNPREDICTABLE>

3428: f8dbd357 ; <UNDEFINED> instruction: 0xf8dbd357

342c: f85331f8 ; <UNDEFINED> instruction: 0xf85331f8

3430: 42a33025 adcmi r3, r3, #37 ; 0x25

3434: f893d0f6 ; <UNDEFINED> instruction: 0xf893d0f6

3438: 07922199 ; <UNDEFINED> instruction: 0x07922199

343c: f8dfd4f2 ; <UNDEFINED> instruction: 0xf8dfd4f2

3440: 46520b90 ; <UNDEFINED> instruction: 0x46520b90

3444: f8ca4631 ; <UNDEFINED> instruction: 0xf8ca4631

3448: 44783000 ldrbtmi r3, [r8], #-0

344c: fca2f00d stc2 0, cr15, [r2], #52 ; 0x34

3450: 05d0e7e8 ldrbeq lr, [r0, #2024] ; 0x7e8

3454: 8108f140 tsthi r8, r0, asr #2 ; <UNPREDICTABLE>

3458: 46526ad9 ; <UNDEFINED> instruction: 0x46526ad9

345c: 0b74f8df bleq 1d417e0 <\_r\_debug@@GLIBC\_2.4+0x1d17e84>

3460: 29002301 stmdbcs r0, {r0, r8, r9, sp}

3464: 1b70f8df blne 1c417e8 <\_r\_debug@@GLIBC\_2.4+0x1c17e8c>

3468: f2c04478 vmvn.i32 q10, #524288 ; 0x00080000

346c: 44792300 ldrbtmi r2, [r9], #-768 ; 0xfffffd00

3470: f04fbf08 ; <UNDEFINED> instruction: 0xf04fbf08

3474: e9ca7300 stmib sl, {r8, r9, ip, sp, lr}^

3478: f00db300 ; <UNDEFINED> instruction: 0xf00db300

347c: f8dbfc8b ; <UNDEFINED> instruction: 0xf8dbfc8b

3480: 68234008 stmdavs r3!, {r3, lr}

3484: d0a72b00 adcle r2, r7, r0, lsl #22

3488: 8b50f8df blhi 144180c <\_r\_debug@@GLIBC\_2.4+0x1417eb0>

348c: f8df46d9 ; <UNDEFINED> instruction: 0xf8df46d9

3490: 20016b50 andcs r6, r1, r0, asr fp

3494: 5b4cf8df blpl 1341818 <\_r\_debug@@GLIBC\_2.4+0x1317ebc>

3498: 447e44f8 ldrbtmi r4, [lr], #-1272 ; 0xfffffb08

349c: e003447d and r4, r3, sp, ror r4

34a0: 3f08f854 svccc 0x0008f854

34a4: d0982b00 addsle r2, r8, r0, lsl #22

34a8: d1f92b01 mvnsle r2, r1, lsl #22

34ac: 900cf8d9 ldrdls pc, [ip], -r9

34b0: 3180f8d8 ldrdcc pc, [r0, r8]

34b4: bf08454b svclt 0x0008454b

34b8: 900cf8d9 ldrdls pc, [ip], -r9

34bc: 3204f8d9 andcc pc, r4, #14221312 ; 0xd90000

34c0: d1ed2b00 mvnle r2, r0, lsl #22

34c4: f0402800 ; <UNDEFINED> instruction: 0xf0402800

34c8: 20018108 andcs r8, r1, r8, lsl #2

34cc: 2004f8d9 ldrdcs pc, [r4], -r9

34d0: f0084631 ; <UNDEFINED> instruction: 0xf0084631

34d4: 2000fc7b andcs pc, r0, fp, ror ip ; <UNPREDICTABLE>

34d8: f8dfe7e2 ; <UNDEFINED> instruction: 0xf8dfe7e2

34dc: 6bba3b0c blvs fee92114 <\_r\_debug@@GLIBC\_2.4+0xfee687b8>

34e0: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

34e4: 23d3ea12 bicscs lr, r3, #73728 ; 0x12000

34e8: 853ef040 ldrhi pc, [lr, #-64]! ; 0xffffffc0

34ec: 3afcf8df bcc fff41870 <\_r\_debug@@GLIBC\_2.4+0xfff17f14>

34f0: 689b447b ldmvs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

34f4: f0002b00 ; <UNDEFINED> instruction: 0xf0002b00

34f8: f8df80a7 ; <UNDEFINED> instruction: 0xf8df80a7

34fc: 465e3af4 ; <UNDEFINED> instruction: 0x465e3af4

3500: b020f8c7 eorlt pc, r0, r7, asr #17

3504: 62fb447b rscsvs r4, fp, #2063597568 ; 0x7b000000

3508: 3ae8f8df bcc ffa4188c <\_r\_debug@@GLIBC\_2.4+0xffa17f30>

350c: 627b447b rsbsvs r4, fp, #2063597568 ; 0x7b000000

3510: f8d62301 ; <UNDEFINED> instruction: 0xf8d62301

3514: 2a0020b0 bcs b7dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b48>

3518: 8090f000 addshi pc, r0, r0

351c: 68526b71 ldmdavs r2, {r0, r4, r5, r6, r8, r9, fp, sp, lr}^

3520: f8d16834 ; <UNDEFINED> instruction: 0xf8d16834

3524: 4414a004 ldrmi sl, [r4], #-4

3528: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

352c: 68728096 ldmdavs r2!, {r1, r2, r4, r7, pc}^

3530: b93b7813 ldmdblt fp!, {r0, r1, r4, fp, ip, sp, lr}

3534: 3ac0f8df bcc ff0418b8 <\_r\_debug@@GLIBC\_2.4+0xff017f5c>

3538: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

353c: 2a00681a bcs 1d5ac <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc844>

3540: 8087f000 addhi pc, r7, r0

3544: 1ab4f8df bne fed418c8 <\_r\_debug@@GLIBC\_2.4+0xfed17f6c>

3548: f8df2001 ; <UNDEFINED> instruction: 0xf8df2001

354c: 44793ab4 ldrbtmi r3, [r9], #-2740 ; 0xfffff54c

3550: 63fb447b mvnsvs r4, #2063597568 ; 0x7b000000

3554: fc3af008 ldc2 0, cr15, [sl], #-32 ; 0xffffffe0

3558: f8df61fe ; <UNDEFINED> instruction: 0xf8df61fe

355c: 68632aa8 stmdavs r3!, {r3, r5, r7, r9, fp, sp}^

3560: eb0a447a bl 294750 <\_r\_debug@@GLIBC\_2.4+0x26adf4>

3564: 68160803 ldmdavs r6, {r0, r1, fp}

3568: 5164f8d6 ldrdpl pc, [r4, #-134]! ; 0xffffff7a

356c: e00b3d01 and r3, fp, r1, lsl #26

3570: 3160f8d6 ldrdcc pc, [r0, #-134]! ; 0xffffff7a

3574: ea4f4640 b 13d4e7c <\_r\_debug@@GLIBC\_2.4+0x13ab520>

3578: f8530985 ; <UNDEFINED> instruction: 0xf8530985

357c: 3d011025 stccc 0, cr1, [r1, #-148] ; 0xffffff6c

3580: fc34f008 ldc2 0, cr15, [r4], #-32 ; 0xffffffe0

3584: d1532800 cmple r3, r0, lsl #16

3588: d1f11c6b mvnsle r1, fp, ror #24

358c: 68a52600 stmiavs r5!, {r9, sl, sp}

3590: 68ab4425 stmiavs fp!, {r0, r2, r5, sl, lr}

3594: 0903eb0a stmdbeq r3, {r1, r3, r8, r9, fp, sp, lr, pc}

3598: d0332e00 eorsle r2, r3, r0, lsl #28

359c: f8d66b72 ; <UNDEFINED> instruction: 0xf8d66b72

35a0: f8d230b8 ; <UNDEFINED> instruction: 0xf8d230b8

35a4: b36bb004 cmnlt fp, #4

35a8: 6833685a ldmdavs r3!, {r1, r3, r4, r6, fp, sp, lr}

35ac: 0803eb02 stmdaeq r3, {r1, r8, r9, fp, sp, lr, pc}

35b0: f8d8e003 ; <UNDEFINED> instruction: 0xf8d8e003

35b4: b32b3010 ; <UNDEFINED> instruction: 0xb32b3010

35b8: f8d84498 ; <UNDEFINED> instruction: 0xf8d84498

35bc: 4648300c strbmi r3, [r8], -ip

35c0: 1003f858 andne pc, r3, r8, asr r8 ; <UNPREDICTABLE>

35c4: f00e4459 ; <UNDEFINED> instruction: 0xf00e4459

35c8: 2800fdc3 stmdacs r0, {r0, r1, r6, r7, r8, sl, fp, ip, sp, lr, pc}

35cc: 88a9d1f1 stmiahi r9!, {r0, r4, r5, r6, r7, r8, ip, lr, pc}

35d0: 07896862 streq r6, [r9, r2, ror #16]

35d4: 44526873 ldrbmi r6, [r2], #-2163 ; 0xfffff78d

35d8: f8dfd522 ; <UNDEFINED> instruction: 0xf8dfd522

35dc: 44791a2c ldrbtmi r1, [r9], #-2604 ; 0xfffff5d4

35e0: 9100b1bb ; <UNDEFINED> instruction: 0x9100b1bb

35e4: f8df2001 ; <UNDEFINED> instruction: 0xf8df2001

35e8: 93011a24 movwls r1, #6692 ; 0x1a24

35ec: 4479464b ldrbtmi r4, [r9], #-1611 ; 0xfffff9b5

35f0: fbecf008 blx ffb3f61a <\_r\_debug@@GLIBC\_2.4+0xffb15cbe>

35f4: b1bb68eb ; <UNDEFINED> instruction: 0xb1bb68eb

35f8: 68ab441d stmiavs fp!, {r0, r2, r3, r4, sl, lr}

35fc: 0903eb0a stmdbeq r3, {r1, r3, r8, r9, fp, sp, lr, pc}

3600: d1cb2e00 bicle r2, fp, r0, lsl #28

3604: 686288ab stmdavs r2!, {r0, r1, r3, r5, r7, fp, pc}^

3608: 44520798 ldrbmi r0, [r2], #-1944 ; 0xfffff868

360c: 6af9bf58 bvs ffe73374 <\_r\_debug@@GLIBC\_2.4+0xffe49a18>

3610: f8dfd403 ; <UNDEFINED> instruction: 0xf8dfd403

3614: 447b39fc ldrbtmi r3, [fp], #-2556 ; 0xfffff604

3618: 6a79e7e3 bvs 1e7d5ac <\_r\_debug@@GLIBC\_2.4+0x1e53c50>

361c: e7e06bfb ; <UNDEFINED> instruction: 0xe7e06bfb

3620: 19f0f8df ldmibne r0!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

3624: e7db4479 ; <UNDEFINED> instruction: 0xe7db4479

3628: b13368e3 teqlt r3, r3, ror #17

362c: e794441c ; <UNDEFINED> instruction: 0xe794441c

3630: 3160f8d6 ldrdcc pc, [r0, #-134]! ; 0xffffff7a

3634: 6009f853 andvs pc, r9, r3, asr r8 ; <UNPREDICTABLE>

3638: 69fee7a9 ldmibvs lr!, {r0, r3, r5, r7, r8, r9, sl, sp, lr, pc}^

363c: 2e0068f6 mcrcs 8, 0, r6, cr0, cr6, {7}

3640: af67f47f svcge 0x0067f47f

3644: b020f8d7 ldrdlt pc, [r0], -r7 ; <UNPREDICTABLE>

3648: f00e2000 ; <UNDEFINED> instruction: 0xf00e2000

364c: f7fffbfd ; <UNDEFINED> instruction: 0xf7fffbfd

3650: f8dfbab4 ; <UNDEFINED> instruction: 0xf8dfbab4

3654: 447a29c4 ldrbtmi r2, [sl], #-2500 ; 0xfffff63c

3658: f8dfe774 ; <UNDEFINED> instruction: 0xf8dfe774

365c: 200119c0 andcs r1, r1, r0, asr #19

3660: f0084479 ; <UNDEFINED> instruction: 0xf0084479

3664: e762fbb3 ; <UNDEFINED> instruction: 0xe762fbb3

3668: 3024f8db ldrdcc pc, [r4], -fp ; <UNPREDICTABLE>

366c: d03f2b00 eorsle r2, pc, r0, lsl #22

3670: 400cf8db ldrdmi pc, [ip], -fp

3674: f43f2c00 ; <UNDEFINED> instruction: 0xf43f2c00

3678: f8dfaeb4 ; <UNDEFINED> instruction: 0xf8dfaeb4

367c: f8df89a4 ; <UNDEFINED> instruction: 0xf8df89a4

3680: f8df69a4 ; <UNDEFINED> instruction: 0xf8df69a4

3684: 44f859a4 ldrbtmi r5, [r8], #2468 ; 0x9a4

3688: 447d447e ldrbtmi r4, [sp], #-1150 ; 0xfffffb82

368c: 9200e00a andls lr, r0, #10

3690: 464a2308 strbmi r2, [sl], -r8, lsl #6

3694: 20014631 andcs r4, r1, r1, lsr r6

3698: fb98f008 blx fe63f6c2 <\_r\_debug@@GLIBC\_2.4+0xfe615d66>

369c: 2c0068e4 stccs 8, cr6, [r0], {228} ; 0xe4

36a0: ae9ff43f mrcge 4, 4, APSR\_nzcv, cr15, cr15, {1}

36a4: 3199f894 ; <UNDEFINED> instruction: 0x3199f894

36a8: 079969e2 ldreq r6, [r9, r2, ror #19]

36ac: 9000f8d2 ldrdls pc, [r0], -r2

36b0: 6863d418 stmdavs r3!, {r3, r4, sl, ip, lr, pc}^

36b4: 46194648 ldrmi r4, [r9], -r8, asr #12

36b8: f00e62fb ; <UNDEFINED> instruction: 0xf00e62fb

36bc: f8d4fd49 ; <UNDEFINED> instruction: 0xf8d4fd49

36c0: 6afb21b0 bvs ffecbd88 <\_r\_debug@@GLIBC\_2.4+0xffea242c>

36c4: d0e22800 rscle r2, r2, r0, lsl #16

36c8: 20089201 andcs r9, r8, r1, lsl #4

36cc: 9000464a andls r4, r0, sl, asr #12

36d0: 20014641 andcs r4, r1, r1, asr #12

36d4: fb7af008 blx 1ebf6fe <\_r\_debug@@GLIBC\_2.4+0x1e95da2>

36d8: 4629e7e0 strtmi lr, [r9], -r0, ror #15

36dc: f0082001 ; <UNDEFINED> instruction: 0xf0082001

36e0: e6f2fb75 ; <UNDEFINED> instruction: 0xe6f2fb75

36e4: 4629464a strtmi r4, [r9], -sl, asr #12

36e8: f0082001 ; <UNDEFINED> instruction: 0xf0082001

36ec: e7d5fb6f ldrb pc, [r5, pc, ror #22] ; <UNPREDICTABLE>

36f0: 1938f8df ldmdbne r8!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

36f4: 44792001 ldrbtmi r2, [r9], #-1

36f8: fb68f008 blx 1a3f722 <\_r\_debug@@GLIBC\_2.4+0x1a15dc6>

36fc: 00abe671 adceq lr, fp, r1, ror r6

3700: 33072200 movwcc r2, #29184 ; 0x7200

3704: 0307f023 movweq pc, #28707 ; 0x7023 ; <UNPREDICTABLE>

3708: 0d03ebad vstreq d14, [r3, #-692] ; 0xfffffd4c

370c: 1f18ab04 svcne 0x0018ab04

3710: 1f04f840 svcne 0x0004f840

3714: 68c93201 stmiavs r9, {r0, r9, ip, sp}^

3718: d1f92900 mvnsle r2, r0, lsl #18

371c: bf084295 svclt 0x00084295

3720: f43f4619 ; <UNDEFINED> instruction: 0xf43f4619

3724: f8dfa8ea ; <UNDEFINED> instruction: 0xf8dfa8ea

3728: f2403908 vmla.i8 d19, d0, d8

372c: f8df62be ; <UNDEFINED> instruction: 0xf8df62be

3730: f8df1904 ; <UNDEFINED> instruction: 0xf8df1904

3734: 447b0904 ldrbtmi r0, [fp], #-2308 ; 0xfffff6fc

3738: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

373c: f926f00d ; <UNDEFINED> instruction: 0xf926f00d

3740: 22034620 andcs r4, r3, #32, 12 ; 0x2000000

3744: f0086bb9 ; <UNDEFINED> instruction: 0xf0086bb9

3748: 4604fae9 strmi pc, [r4], -r9, ror #21

374c: f43f3001 ; <UNDEFINED> instruction: 0xf43f3001

3750: 6bbba8cf blvs feeeda94 <\_r\_debug@@GLIBC\_2.4+0xfeec4138>

3754: 0a60f107 beq 183fb78 <\_r\_debug@@GLIBC\_2.4+0x181621c>

3758: 4000f8ca andmi pc, r0, sl, asr #17

375c: 8000f8d3 ldrdhi pc, [r0], -r3

3760: 0f00f1b8 svceq 0x0000f1b8

3764: 4626d06f strtmi sp, [r6], -pc, rrx

3768: 21234642 ; <UNDEFINED> instruction: 0x21234642

376c: f00f4630 ; <UNDEFINED> instruction: 0xf00f4630

3770: b198f917 orrslt pc, r8, r7, lsl r9 ; <UNPREDICTABLE>

3774: 1e411b83 vmlane.f64 d17, d17, d3

3778: 0303eba8 movweq lr, #15272 ; 0x3ba8

377c: 1e5e4602 cdpne 6, 5, cr4, cr14, cr2, {0}

3780: 4430440b ldrtmi r4, [r0], #-1035 ; 0xfffffbf5

3784: f812e003 ; <UNDEFINED> instruction: 0xf812e003

3788: 290a1f01 stmdbcs sl, {r0, r8, r9, sl, fp, ip}

378c: 4282d058 addmi sp, r2, #88 ; 0x58

3790: 0120f04f msreq CPSR\_, pc, asr #32

3794: 0802eba3 stmdaeq r2, {r0, r1, r5, r7, r8, r9, fp, sp, lr, pc}

3798: d1f47011 mvnsle r7, r1, lsl r0

379c: 681e6bbb ldmdavs lr, {r0, r1, r3, r4, r5, r7, r8, r9, fp, sp, lr}

37a0: 18e21e73 stmiane r2!, {r0, r1, r4, r5, r6, r9, sl, fp, ip}^

37a4: 2b205ce3 blcs 81ab38 <\_r\_debug@@GLIBC\_2.4+0x7f11dc>

37a8: 2b3abf18 blcs eb3410 <\_r\_debug@@GLIBC\_2.4+0xe89ab4>

37ac: 0309f1a3 movweq pc, #37283 ; 0x91a3 ; <UNPREDICTABLE>

37b0: 2101bf14 tstcs r1, r4, lsl pc

37b4: 2b012100 blcs 4bbbc <\_r\_debug@@GLIBC\_2.4+0x22260>

37b8: 2300bf94 movwcs fp, #3988 ; 0xf94

37bc: 0301f001 movweq pc, #4097 ; 0x1001 ; <UNPREDICTABLE>

37c0: d1542b00 cmple r4, r0, lsl #22

37c4: 70162600 andsvc r2, r6, r0, lsl #12

37c8: f8dfb39c ; <UNDEFINED> instruction: 0xf8dfb39c

37cc: f8df8870 ; <UNDEFINED> instruction: 0xf8df8870

37d0: f8ca3870 ; <UNDEFINED> instruction: 0xf8ca3870

37d4: 44f84000 ldrbtmi r4, [r8], #0

37d8: 62bb447b adcsvs r4, fp, #2063597568 ; 0x7b000000

37dc: 781be002 ldmdavc fp, {r1, sp, lr, pc}

37e0: d13e2b00 teqle lr, r0, lsl #22

37e4: 46504641 ldrbmi r4, [r0], -r1, asr #12

37e8: f96ef00d ; <UNDEFINED> instruction: 0xf96ef00d

37ec: 28004603 stmdacs r0, {r0, r1, r9, sl, lr}

37f0: b1f6d1f5 ldrshlt sp, [r6, #21]!

37f4: 46306bbb ; <UNDEFINED> instruction: 0x46306bbb

37f8: 1b336819 blne cdd864 <\_r\_debug@@GLIBC\_2.4+0xcb3f08>

37fc: f00f1ac9 ; <UNDEFINED> instruction: 0xf00f1ac9

3800: 4631f80d ldrtmi pc, [r1], -sp, lsl #16 ; <UNPREDICTABLE>

3804: f1002600 ; <UNDEFINED> instruction: 0xf1002600

3808: 46020308 strmi r0, [r2], -r8, lsl #6

380c: 0307f023 movweq pc, #28707 ; 0x7023 ; <UNPREDICTABLE>

3810: 0d03ebad vstreq d14, [r3, #-692] ; 0xfffffd4c

3814: 0317f10d tsteq r7, #1073741827 ; 0x40000003 ; <UNPREDICTABLE>

3818: f80008db ; <UNDEFINED> instruction: 0xf80008db

381c: 00d86033 sbcseq r6, r8, r3, lsr r0

3820: ec6ef00f stcl 0, cr15, [lr], #-60 ; 0xffffffc4

3824: 281cf8df ldmdacs ip, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

3828: 447a4659 ldrbtmi r4, [sl], #-1625 ; 0xfffff9a7

382c: f9e6f7fd ; <UNDEFINED> instruction: 0xf9e6f7fd

3830: 6bbb4405 blvs feed484c <\_r\_debug@@GLIBC\_2.4+0xfeeaaef0>

3834: 68194620 ldmdavs r9, {r5, r9, sl, lr}

3838: fac2f00e blx ff0bf878 <\_r\_debug@@GLIBC\_2.4+0xff095f1c>

383c: b858f7ff ldmdalt r8, {r0, r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}^

3840: 6000f8da ldrdvs pc, [r0], -sl

3844: f814e790 ; <UNDEFINED> instruction: 0xf814e790

3848: 1e623c01 cdpne 12, 6, cr3, cr2, cr1, {0}

384c: bf182b3a svclt 0x00182b3a

3850: d0b72b20 adcsle r2, r7, r0, lsr #22

3854: 2b013b09 blcs 52480 <\_r\_debug@@GLIBC\_2.4+0x28b24>

3858: 4626d9b4 ; <UNDEFINED> instruction: 0x4626d9b4

385c: d0c842a6 sbcle r4, r8, r6, lsr #5

3860: 6abae7b3 bvs feebd734 <\_r\_debug@@GLIBC\_2.4+0xfee93dd8>

3864: f7fd4659 ; <UNDEFINED> instruction: 0xf7fd4659

3868: 4405f9c9 strmi pc, [r5], #-2505 ; 0xfffff637

386c: 4426e7ba strtmi lr, [r6], #-1978 ; 0xfffff846

3870: d2f342b4 rscsle r4, r3, #180, 4 ; 0x4000000b

3874: 3c01f816 stccc 8, cr15, [r1], {22}

3878: bf182b20 svclt 0x00182b20

387c: d0172b3a andsle r2, r7, sl, lsr fp

3880: 2b013b09 blcs 524ac <\_r\_debug@@GLIBC\_2.4+0x28b50>

3884: 1e72d914 ; <UNDEFINED> instruction: 0x1e72d914

3888: 46164294 ; <UNDEFINED> instruction: 0x46164294

388c: f812d0e6 ; <UNDEFINED> instruction: 0xf812d0e6

3890: 2b203d01 blcs 812c9c <\_r\_debug@@GLIBC\_2.4+0x7e9340>

3894: 2b3abf18 blcs eb34fc <\_r\_debug@@GLIBC\_2.4+0xe89ba0>

3898: 0309f1a3 movweq pc, #37283 ; 0x91a3 ; <UNPREDICTABLE>

389c: 2101bf14 tstcs r1, r4, lsl pc

38a0: 2b012100 blcs 4bca8 <\_r\_debug@@GLIBC\_2.4+0x2234c>

38a4: 2300bf94 movwcs fp, #3988 ; 0xf94

38a8: 0301f001 movweq pc, #4097 ; 0x1001 ; <UNPREDICTABLE>

38ac: d1eb2b00 mvnle r2, r0, lsl #22

38b0: f8062300 ; <UNDEFINED> instruction: 0xf8062300

38b4: e7883c01 str r3, [r8, r1, lsl #24]

38b8: f7fd4659 ; <UNDEFINED> instruction: 0xf7fd4659

38bc: 4605ff05 strmi pc, [r5], -r5, lsl #30

38c0: b80cf7ff stmdalt ip, {r0, r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}

38c4: 3780f8df ; <UNDEFINED> instruction: 0x3780f8df

38c8: 6252f240 subsvs pc, r2, #64, 4

38cc: 177cf8df ; <UNDEFINED> instruction: 0x177cf8df

38d0: 077cf8df ; <UNDEFINED> instruction: 0x077cf8df

38d4: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

38d8: f00d4478 ; <UNDEFINED> instruction: 0xf00d4478

38dc: f8d4f857 ; <UNDEFINED> instruction: 0xf8d4f857

38e0: f50b81ac ; <UNDEFINED> instruction: 0xf50b81ac

38e4: 25007618 strcs r7, [r0, #-1560] ; 0xfffff9e8

38e8: 3000f8d8 ldrdcc pc, [r0], -r8

38ec: 2101b113 tstcs r1, r3, lsl r1

38f0: 47984630 ; <UNDEFINED> instruction: 0x47984630

38f4: 31b0f8d4 asrscc pc, r4 ; <illegal shifter operand> ; <UNPREDICTABLE>

38f8: f8d83501 ; <UNDEFINED> instruction: 0xf8d83501

38fc: 36088020 strcc r8, [r8], -r0, lsr #32

3900: d8f142ab ldmle r1!, {r0, r1, r3, r5, r7, r9, lr}^

3904: bfddf7fe svclt 0x00ddf7fe

3908: f1076b7b ; <UNDEFINED> instruction: 0xf1076b7b

390c: f8df0a60 ; <UNDEFINED> instruction: 0xf8df0a60

3910: 24005744 strcs r5, [r0], #-1860 ; 0xfffff8bc

3914: 3400e9ca strcc lr, [r0], #-2506 ; 0xfffff636

3918: 60bd447d adcsvs r4, sp, sp, ror r4

391c: fab2f7fd blx fecc1918 <\_r\_debug@@GLIBC\_2.4+0xfec97fbc>

3920: 2734f8df ; <UNDEFINED> instruction: 0x2734f8df

3924: 61eff505 mvnvs pc, r5, lsl #10

3928: 3730f8df ; <UNDEFINED> instruction: 0x3730f8df

392c: 6079447a rsbsvs r4, r9, sl, ror r4

3930: 68156af9 ldmdavs r5, {r0, r3, r4, r5, r6, r7, r9, fp, sp, lr}

3934: 58c96378 stmiapl r9, {r3, r4, r5, r6, r8, r9, sp, lr}^

3938: 0724f8df ; <UNDEFINED> instruction: 0x0724f8df

393c: 686d682b stmdavs sp!, {r0, r1, r3, r5, fp, sp, lr}^

3940: f0234478 ; <UNDEFINED> instruction: 0xf0234478

3944: 601403ff ; <UNDEFINED> instruction: 0x601403ff

3948: f8df600b ; <UNDEFINED> instruction: 0xf8df600b

394c: f8df2718 ; <UNDEFINED> instruction: 0xf8df2718

3950: 447a3718 ldrbtmi r3, [sl], #-1816 ; 0xfffff8e8

3954: 447b6545 ldrbtmi r6, [fp], #-1349 ; 0xfffffabb

3958: 60fb613a rscsvs r6, fp, sl, lsr r1

395c: b907e9c7 stmdblt r7, {r0, r1, r2, r6, r7, r8, fp, sp, lr, pc}

3960: 4000f8da ldrdmi pc, [r0], -sl

3964: 2600b374 ; <UNDEFINED> instruction: 0x2600b374

3968: 0568f107 strbeq pc, [r8, #-263]! ; 0xfffffef9 ; <UNPREDICTABLE>

396c: 9010f8d7 ; <UNDEFINED> instruction: 0x9010f8d7

3970: eb04e012 bl 13b9c0 <\_r\_debug@@GLIBC\_2.4+0x112064>

3974: f8ca0308 ; <UNDEFINED> instruction: 0xf8ca0308

3978: f8143000 ; <UNDEFINED> instruction: 0xf8143000

397c: 46282008 strtmi r2, [r8], -r8

3980: bf042a3a svclt 0x00042a3a

3984: f8ca3301 ; <UNDEFINED> instruction: 0xf8ca3301

3988: f7fd3000 ; <UNDEFINED> instruction: 0xf7fd3000

398c: 2800fa61 stmdacs r0, {r0, r5, r6, r9, fp, ip, sp, lr, pc}

3990: 8089f040 addhi pc, r9, r0, asr #32

3994: 4000f8da ldrdmi pc, [r0], -sl

3998: b19b7823 orrslt r7, fp, r3, lsr #16

399c: 46204649 strtmi r4, [r0], -r9, asr #12

39a0: fe5cf00e cdp2 0, 5, cr15, cr12, cr14, {0}

39a4: 46801e43 strmi r1, [r0], r3, asr #28

39a8: bf882bfd svclt 0x00882bfd

39ac: 6008f88a andvs pc, r8, sl, lsl #17

39b0: 4602d8df ; <UNDEFINED> instruction: 0x4602d8df

39b4: 46284621 strtmi r4, [r8], -r1, lsr #12

39b8: eba2f00f bl fe8bf9fc <\_r\_debug@@GLIBC\_2.4+0xfe8960a0>

39bc: 0308eb0a movweq lr, #35594 ; 0x8b0a

39c0: e7d6721e bfi r7, lr, #4, #19

39c4: f8da68fa ; <UNDEFINED> instruction: 0xf8da68fa

39c8: 69123004 ldmdbvs r2, {r2, ip, sp}

39cc: d0612b00 rsble r2, r1, r0, lsl #22

39d0: f0004293 ; <UNDEFINED> instruction: 0xf0004293

39d4: 685b80fe ldmdavs fp, {r1, r2, r3, r4, r5, r6, r7, pc}^

39d8: f8ca681a ; <UNDEFINED> instruction: 0xf8ca681a

39dc: 633a3004 teqvs sl, #4

39e0: 2b006b3b blcs 1e6d4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd96c>

39e4: 80f5f000 rscshi pc, r5, r0

39e8: 3680f8df pkhtbcc pc, r0, pc, asr #17 ; <UNPREDICTABLE>

39ec: 064cf107 strbeq pc, [ip], -r7, lsl #2 ; <UNPREDICTABLE>

39f0: 0448f107 strbeq pc, [r8], #-263 ; 0xfffffef9 ; <UNPREDICTABLE>

39f4: 447b6b3a ldrbtmi r6, [fp], #-2874 ; 0xfffff4c6

39f8: 0943f107 stmdbeq r3, {r0, r1, r2, r8, ip, sp, lr, pc}^

39fc: 0044f107 subeq pc, r4, r7, lsl #2

3a00: f8d34621 ; <UNDEFINED> instruction: 0xf8d34621

3a04: 2300880c movwcs r8, #2060 ; 0x80c

3a08: 60236073 eorvs r6, r3, r3, ror r0

3a0c: 3660f8df ; <UNDEFINED> instruction: 0x3660f8df

3a10: 464a6032 ; <UNDEFINED> instruction: 0x464a6032

3a14: 447b61b8 ldrbtmi r6, [fp], #-440 ; 0xfffffe48

3a18: f7fc9600 ; <UNDEFINED> instruction: 0xf7fc9600

3a1c: 6823effc stmdavs r3!, {r2, r3, r4, r5, r6, r7, r8, r9, sl, fp, sp, lr, pc}

3a20: f8dfbb43 ; <UNDEFINED> instruction: 0xf8dfbb43

3a24: 464ab650 ; <UNDEFINED> instruction: 0x464ab650

3a28: 46216873 ; <UNDEFINED> instruction: 0x46216873

3a2c: 44fb6bbd ldrbtmi r6, [fp], #3005 ; 0xbbd

3a30: c644f8df ; <UNDEFINED> instruction: 0xc644f8df

3a34: 44fc69b8 ldrbtmi r6, [ip], #2488 ; 0x9b8

3a38: c300e9c5 movwgt lr, #2501 ; 0x9c5

3a3c: 9500465b strls r4, [r0, #-1627] ; 0xfffff9a5

3a40: efe8f7fc svc 0x00e8f7fc

3a44: 617b6823 cmnvs fp, r3, lsr #16

3a48: 6870b37b ldmdavs r0!, {r0, r1, r3, r4, r5, r6, r8, r9, ip, sp, pc}^

3a4c: f00b6986 ; <UNDEFINED> instruction: 0xf00b6986

3a50: 234cfac5 movtcs pc, #51909 ; 0xcac5 ; <UNPREDICTABLE>

3a54: 2624f8df ; <UNDEFINED> instruction: 0x2624f8df

3a58: f306fb03 vqrdmulh.s<illegal width 8> d15, d6, d3

3a5c: 18d1447a ldmne r1, {r1, r3, r4, r5, r6, sl, lr}^

3a60: 2b0058d3 blcs 19db4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x904c>

3a64: 684bd160 stmdavs fp, {r5, r6, r8, ip, lr, pc}^

3a68: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

3a6c: 682380be stmdavs r3!, {r1, r2, r3, r4, r5, r7, pc}

3a70: 880cf8c2 stmdahi ip, {r1, r6, r7, fp, ip, sp, lr, pc}

3a74: 1608f8df ; <UNDEFINED> instruction: 0x1608f8df

3a78: 6b3a2002 blvs e8ba88 <\_r\_debug@@GLIBC\_2.4+0xe6212c>

3a7c: f0084479 ; <UNDEFINED> instruction: 0xf0084479

3a80: f899f9a5 ; <UNDEFINED> instruction: 0xf899f9a5

3a84: 2b003000 blcs fa8c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x76c>

3a88: af6af43f svcge 0x006af43f

3a8c: f7fc6820 ; <UNDEFINED> instruction: 0xf7fc6820

3a90: e765efca strb lr, [r5, -sl, asr #31]!

3a94: f0002a00 ; <UNDEFINED> instruction: 0xf0002a00

3a98: 6853809c ldmdavs r3, {r2, r3, r4, r7, pc}^

3a9c: 3004f8ca andcc pc, r4, sl, asr #17

3aa0: 633b681b teqvs fp, #1769472 ; 0x1b0000

3aa4: 633de79c teqvs sp, #156, 14 ; 0x2700000

3aa8: 68aae79e stmiavs sl!, {r1, r2, r3, r4, r7, r8, r9, sl, sp, lr, pc}

3aac: d0cc2a00 sbcle r2, ip, r0, lsl #20

3ab0: 47902001 ldrmi r2, [r0, r1]

3ab4: 2801697b stmdacs r1, {r0, r1, r3, r4, r5, r6, r8, fp, sp, lr}

3ab8: 2024d1c7 eorcs sp, r4, r7, asr #3

3abc: f7fc461d ; <UNDEFINED> instruction: 0xf7fc461d

3ac0: f8dfef86 ; <UNDEFINED> instruction: 0xf8dfef86

3ac4: f8c735c0 ; <UNDEFINED> instruction: 0xf8c735c0

3ac8: 447bb014 ldrbtmi fp, [fp], #-20 ; 0xffffffec

3acc: 7360f503 msrvc SPSR\_, #12582912 ; 0xc00000

3ad0: 4680633b ; <UNDEFINED> instruction: 0x4680633b

3ad4: e00d4683 and r4, sp, r3, lsl #13

3ad8: f8cb2300 ; <UNDEFINED> instruction: 0xf8cb2300

3adc: 6b383000 blvs e0fae4 <\_r\_debug@@GLIBC\_2.4+0xde6188>

3ae0: f00f2100 ; <UNDEFINED> instruction: 0xf00f2100

3ae4: 3501fd1d strcc pc, [r1, #-3357] ; 0xfffff2e3

3ae8: 0b04f10b bleq 13ff1c <\_r\_debug@@GLIBC\_2.4+0x1165c0>

3aec: 1c427843 mcrrne 8, 4, r7, r2, cr3

3af0: b333633a teqlt r3, #-402653184 ; 0xe8000000

3af4: 6b396bba blvs e5e9e4 <\_r\_debug@@GLIBC\_2.4+0xe35088>

3af8: 6011697b andsvs r6, r1, fp, ror r9

3afc: 92004621 andls r4, r0, #34603008 ; 0x2100000

3b00: 69b8464a ldmibvs r8!, {r1, r3, r6, r9, sl, lr}

3b04: ef86f7fc svc 0x0086f7fc

3b08: 2b006823 blcs 1db9c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xce34>

3b0c: 6bbbd1e4 blvs feef82a4 <\_r\_debug@@GLIBC\_2.4+0xfeece948>

3b10: 2b00689b blcs 1dd84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd01c>

3b14: 687ad0e0 ldmdavs sl!, {r5, r6, r7, ip, lr, pc}^

3b18: 3000f8cb andcc pc, r0, fp, asr #17

3b1c: f50368bb ; <UNDEFINED> instruction: 0xf50368bb

3b20: f84263a3 ; <UNDEFINED> instruction: 0xf84263a3

3b24: e7da3035 ; <UNDEFINED> instruction: 0xe7da3035

3b28: 355cf8df ldrbcc pc, [ip, #-2271] ; 0xfffff721 ; <UNPREDICTABLE>

3b2c: 6203f240 andvs pc, r3, #64, 4

3b30: 1558f8df ldrbne pc, [r8, #-2271] ; 0xfffff721 ; <UNPREDICTABLE>

3b34: 0558f8df ldrbeq pc, [r8, #-2271] ; 0xfffff721 ; <UNPREDICTABLE>

3b38: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3b3c: f00c4478 ; <UNDEFINED> instruction: 0xf00c4478

3b40: 2d08ff25 stccs 15, cr15, [r8, #-148] ; 0xffffff6c

3b44: f8c8d11e ; <UNDEFINED> instruction: 0xf8c8d11e

3b48: 6a3b3020 bvs ecfbd0 <\_r\_debug@@GLIBC\_2.4+0xea6274>

3b4c: 6a3bb1a3 bvs ef01e0 <\_r\_debug@@GLIBC\_2.4+0xec6884>

3b50: 8020f8c3 eorhi pc, r0, r3, asr #17

3b54: 153cf8df ldrne pc, [ip, #-2271]! ; 0xfffff721

3b58: 44796872 ldrbtmi r6, [r9], #-2162 ; 0xfffff78e

3b5c: 8020f8c7 eorhi pc, r0, r7, asr #17

3b60: 31b0f8d1 asrscc pc, r1 ; <illegal shifter operand> ; <UNPREDICTABLE>

3b64: f8c13301 ; <UNDEFINED> instruction: 0xf8c13301

3b68: f89231b0 ; <UNDEFINED> instruction: 0xf89231b0

3b6c: f0433199 ; <UNDEFINED> instruction: 0xf0433199

3b70: f8820308 ; <UNDEFINED> instruction: 0xf8820308

3b74: e6f33199 usat r3, #19, r9, lsl #3

3b78: 351cf8df ldrcc pc, [ip, #-2271] ; 0xfffff721

3b7c: f8c3447b ; <UNDEFINED> instruction: 0xf8c3447b

3b80: e7e781ac strb r8, [r7, ip, lsr #3]!

3b84: 3514f8df ldrcc pc, [r4, #-2271] ; 0xfffff721

3b88: 52ebf240 rscpl pc, fp, #64, 4

3b8c: 1510f8df ldrne pc, [r0, #-2271] ; 0xfffff721

3b90: 0510f8df ldreq pc, [r0, #-2271] ; 0xfffff721

3b94: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3b98: f00c4478 ; <UNDEFINED> instruction: 0xf00c4478

3b9c: f8dffef7 ; <UNDEFINED> instruction: 0xf8dffef7

3ba0: f44f3508 vst3.8 {d19,d21,d23}, [pc], r8

3ba4: f8df62a1 ; <UNDEFINED> instruction: 0xf8df62a1

3ba8: f8df1504 ; <UNDEFINED> instruction: 0xf8df1504

3bac: 447b0504 ldrbtmi r0, [fp], #-1284 ; 0xfffffafc

3bb0: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

3bb4: feeaf00c cdp2 0, 14, cr15, cr10, cr12, {0}

3bb8: 34f8f8df ldrbtcc pc, [r8], #2271 ; 0x8df ; <UNPREDICTABLE>

3bbc: 520df240 andpl pc, sp, #64, 4

3bc0: 14f4f8df ldrbtne pc, [r4], #2271 ; 0x8df ; <UNPREDICTABLE>

3bc4: 04f4f8df ldrbteq pc, [r4], #2271 ; 0x8df ; <UNPREDICTABLE>

3bc8: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3bcc: f00c4478 ; <UNDEFINED> instruction: 0xf00c4478

3bd0: f8dffedd ; <UNDEFINED> instruction: 0xf8dffedd

3bd4: f8d734ec ; <UNDEFINED> instruction: 0xf8d734ec

3bd8: 447bb01c ldrbtmi fp, [fp], #-28 ; 0xffffffe4

3bdc: 21b0f8d3 asrscs pc, r3 ; <illegal shifter operand> ; <UNPREDICTABLE>

3be0: f04fb982 ; <UNDEFINED> instruction: 0xf04fb982

3be4: f7fe0900 ; <UNDEFINED> instruction: 0xf7fe0900

3be8: f8dfbe4f ; <UNDEFINED> instruction: 0xf8dfbe4f

3bec: f24034d8 vqshl.s8 <illegal reg q9.5>, q4, q8

3bf0: f8df6204 ; <UNDEFINED> instruction: 0xf8df6204

3bf4: f8df14d4 ; <UNDEFINED> instruction: 0xf8df14d4

3bf8: 447b04d4 ldrbtmi r0, [fp], #-1236 ; 0xfffffb2c

3bfc: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

3c00: fec4f00c cdp2 0, 12, cr15, cr4, cr12, {0}

3c04: f04f633b ; <UNDEFINED> instruction: 0xf04f633b

3c08: f8df0a00 ; <UNDEFINED> instruction: 0xf8df0a00

3c0c: 6bb934c4 blvs fee50f24 <\_r\_debug@@GLIBC\_2.4+0xfee275c8>

3c10: 94c0f8df strbls pc, [r0], #2271 ; 0x8df ; <UNPREDICTABLE>

3c14: 623b447b eorsvs r4, fp, #2063597568 ; 0x7b000000

3c18: 34bcf8df ldrtcc pc, [ip], #2271 ; 0x8df ; <UNPREDICTABLE>

3c1c: 468844f9 ; <UNDEFINED> instruction: 0x468844f9

3c20: b000f8c1 andlt pc, r0, r1, asr #17

3c24: f503447b ; <UNDEFINED> instruction: 0xf503447b

3c28: 604b63a3 subvs r6, fp, r3, lsr #7

3c2c: 24006b3b strcs r6, [r0], #-2875 ; 0xfffff4c5

3c30: 61acf8d3 ldrdvs pc, [ip, r3]!

3c34: 68b3b302 ldmvs r3!, {r1, r8, r9, ip, sp, pc}

3c38: f8d8b1c3 ; <UNDEFINED> instruction: 0xf8d8b1c3

3c3c: 21005000 mrscs r5, (UNDEF: 0)

3c40: 02c4eb05 sbceq lr, r4, #5120 ; 0x1400

3c44: 7218f502 andsvc pc, r8, #8388608 ; 0x800000

3c48: 47984628 ldrmi r4, [r8, r8, lsr #12]

3c4c: 03c4eb05 biceq lr, r4, #5120 ; 0x1400

3c50: 0264f8c3 rsbeq pc, r4, #12779520 ; 0xc30000

3c54: 3199f895 ; <UNDEFINED> instruction: 0x3199f895

3c58: 1200f3c3 andne pc, r0, #201326595 ; 0xc000003

3c5c: bf182800 svclt 0x00182800

3c60: 0201f042 andeq pc, r1, #66 ; 0x42

3c64: 1304f362 movwne pc, #17250 ; 0x4362 ; <UNPREDICTABLE>

3c68: 3199f885 orrscc pc, r9, r5, lsl #17

3c6c: 31b0f8d9 asrscc pc, r9 ; <illegal shifter operand> ; <UNPREDICTABLE>

3c70: 6a363401 bvs d90c7c <\_r\_debug@@GLIBC\_2.4+0xd67320>

3c74: d8de42a3 ldmle lr, {r0, r1, r5, r7, r9, lr}^

3c78: 0804f108 stmdaeq r4, {r3, r8, ip, sp, lr, pc}

3c7c: 0f00f1ba svceq 0x0000f1ba

3c80: 6a3bd1af bvs ef8344 <\_r\_debug@@GLIBC\_2.4+0xece9e8>

3c84: 0a01f04f beq 7fdc8 <\_r\_debug@@GLIBC\_2.4+0x5646c>

3c88: 21b0f8d3 asrscs pc, r3 ; <illegal shifter operand> ; <UNPREDICTABLE>

3c8c: f8dfe7ce ; <UNDEFINED> instruction: 0xf8dfe7ce

3c90: f240344c vshl.s8 <illegal reg q9.5>, q6, q0

3c94: f8df4257 ; <UNDEFINED> instruction: 0xf8df4257

3c98: f8df1448 ; <UNDEFINED> instruction: 0xf8df1448

3c9c: 447b0448 ldrbtmi r0, [fp], #-1096 ; 0xfffffbb8

3ca0: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

3ca4: fe72f00c cdp2 0, 7, cr15, cr2, cr12, {0}

3ca8: 343cf8df ldrtcc pc, [ip], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

3cac: 425ff240 subsmi pc, pc, #64, 4

3cb0: 1438f8df ldrtne pc, [r8], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

3cb4: 0438f8df ldrteq pc, [r8], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

3cb8: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3cbc: f00c4478 ; <UNDEFINED> instruction: 0xf00c4478

3cc0: f8dffe65 ; <UNDEFINED> instruction: 0xf8dffe65

3cc4: f2403430 vqshl.s8 d19, d16, d0

3cc8: f8df525c ; <UNDEFINED> instruction: 0xf8df525c

3ccc: f8df142c ; <UNDEFINED> instruction: 0xf8df142c

3cd0: 447b042c ldrbtmi r0, [fp], #-1068 ; 0xfffffbd4

3cd4: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

3cd8: fe58f00c cdp2 0, 5, cr15, cr8, cr12, {0}

3cdc: 3420f8df strtcc pc, [r0], #-2271 ; 0xfffff721

3ce0: 525bf240 subspl pc, fp, #64, 4

3ce4: 141cf8df ldrne pc, [ip], #-2271 ; 0xfffff721

3ce8: 041cf8df ldreq pc, [ip], #-2271 ; 0xfffff721

3cec: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3cf0: f00c4478 ; <UNDEFINED> instruction: 0xf00c4478

3cf4: 2001fe4b andcs pc, r1, fp, asr #28

3cf8: f8a6f00e ; <UNDEFINED> instruction: 0xf8a6f00e

3cfc: bd25f7fe stclt 7, cr15, [r5, #-1016]! ; 0xfffffc08

3d00: 3408f8df strcc pc, [r8], #-2271 ; 0xfffff721

3d04: f8df223d ; <UNDEFINED> instruction: 0xf8df223d

3d08: f8df1408 ; <UNDEFINED> instruction: 0xf8df1408

3d0c: 447b0408 ldrbtmi r0, [fp], #-1032 ; 0xfffffbf8

3d10: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

3d14: fe3af00c cdp2 0, 3, cr15, cr10, cr12, {0}

3d18: 11b4f8cb ; <UNDEFINED> instruction: 0x11b4f8cb

3d1c: f7fe4615 ; <UNDEFINED> instruction: 0xf7fe4615

3d20: 2501bb10 strcs fp, [r1, #-2832] ; 0xfffff4f0

3d24: 36046b7b ; <UNDEFINED> instruction: 0x36046b7b

3d28: 49fc48fb ldmibmi ip!, {r0, r1, r3, r4, r5, r6, r7, fp, lr}^

3d2c: 1243eb04 subne lr, r3, #4, 22 ; 0x1000

3d30: 44784bfb ldrbtmi r4, [r8], #-3067 ; 0xfffff405

3d34: 447b4479 ldrbtmi r4, [fp], #-1145 ; 0xfffffb87

3d38: 600d42a2 andvs r4, sp, r2, lsr #5

3d3c: 6803601e stmdavs r3, {r1, r2, r3, r4, sp, lr}

3d40: 0301f103 movweq pc, #4355 ; 0x1103 ; <UNPREDICTABLE>

3d44: d90e6003 stmdble lr, {r0, r1, sp, lr}

3d48: f24e6821 vadd.i8 d22, d14, d17

3d4c: f2c65351 vorr.i32 <illegal reg q10.5>, #24832 ; 0x00006100

3d50: 42994374 addsmi r4, r9, #116, 6 ; 0xd0000001

3d54: e0b6d104 adcs sp, r6, r4, lsl #2

3d58: 42996821 addsmi r6, r9, #2162688 ; 0x210000

3d5c: 80b3f000 adcshi pc, r3, r0

3d60: 42a23420 adcmi r3, r2, #32, 8 ; 0x20000000

3d64: 6bfbd8f8 blvs ffefa14c <\_r\_debug@@GLIBC\_2.4+0xffed07f0>

3d68: d02f2b02 eorle r2, pc, r2, lsl #22

3d6c: 46412300 strbmi r2, [r1], -r0, lsl #6

3d70: 9301461a movwls r4, #5658 ; 0x161a

3d74: f04f4618 ; <UNDEFINED> instruction: 0xf04f4618

3d78: 94005400 strls r5, [r0], #-1024 ; 0xfffffc00

3d7c: fb56f002 blx 15bfd8e <\_r\_debug@@GLIBC\_2.4+0x1596432>

3d80: 6bfa4be8 blvs ffe96d28 <\_r\_debug@@GLIBC\_2.4+0xffe6d3cc>

3d84: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

3d88: 2a00b000 bcs 2fd90 <\_r\_debug@@GLIBC\_2.4+0x6434>

3d8c: f8d3d13b ; <UNDEFINED> instruction: 0xf8d3d13b

3d90: 2a002570 bcs d358 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0xc>

3d94: f8dbd037 ; <UNDEFINED> instruction: 0xf8dbd037

3d98: 29001058 stmdbcs r0, {r3, r4, r6, ip}

3d9c: f8d3d033 ; <UNDEFINED> instruction: 0xf8d3d033

3da0: f8db054c ; <UNDEFINED> instruction: 0xf8db054c

3da4: 684b4034 stmdavs fp, {r2, r4, r5, lr}^

3da8: 68526840 ldmdavs r2, {r6, fp, sp, lr}^

3dac: 44106861 ldrmi r6, [r0], #-2145 ; 0xfffff79f

3db0: f00e4419 ; <UNDEFINED> instruction: 0xf00e4419

3db4: bb30f9cd bllt c424f0 <\_r\_debug@@GLIBC\_2.4+0xc18b94>

3db8: 447c4cdb ldrbtmi r4, [ip], #-3291 ; 0xfffff325

3dbc: 20024621 andcs r4, r2, r1, lsr #12

3dc0: f804f008 ; <UNDEFINED> instruction: 0xf804f008

3dc4: f00e207f ; <UNDEFINED> instruction: 0xf00e207f

3dc8: e7f7f83f ; <UNDEFINED> instruction: 0xe7f7f83f

3dcc: 20006bbc ; <UNDEFINED> instruction: 0x20006bbc

3dd0: 5100f04f tstpl r0, pc, asr #32 ; <UNPREDICTABLE>

3dd4: 66b96678 ; <UNDEFINED> instruction: 0x66b96678

3dd8: 0360f107 msreq SPSR\_, #-1073741823 ; 0xc0000001

3ddc: f1076020 ; <UNDEFINED> instruction: 0xf1076020

3de0: f8c70248 ; <UNDEFINED> instruction: 0xf8c70248

3de4: 46218060 strtmi r8, [r1], -r0, rrx

3de8: f1079300 ; <UNDEFINED> instruction: 0xf1079300

3dec: 4bcf004c blmi ff3c3f24 <\_r\_debug@@GLIBC\_2.4+0xff39a5c8>

3df0: f7fc447b ; <UNDEFINED> instruction: 0xf7fc447b

3df4: 6823ee10 stmdavs r3!, {r4, r9, sl, fp, sp, lr, pc}

3df8: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

3dfc: 4bcc80de blmi ff32417c <\_r\_debug@@GLIBC\_2.4+0xff2fa820>

3e00: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

3e04: e9dbb000 ldmib fp, {ip, sp, pc}^

3e08: 4bca4255 blmi ff294764 <\_r\_debug@@GLIBC\_2.4+0xff26ae08>

3e0c: f8cb447b ; <UNDEFINED> instruction: 0xf8cb447b

3e10: f8bb3004 ; <UNDEFINED> instruction: 0xf8bb3004

3e14: f8ca315c ; <UNDEFINED> instruction: 0xf8ca315c

3e18: 6aba2000 bvs fee8be20 <\_r\_debug@@GLIBC\_2.4+0xfee624c4>

3e1c: 6813637b ldmdavs r3, {r0, r1, r3, r4, r5, r6, r8, r9, sp, lr}

3e20: 49c5b1e3 stmibmi r5, {r0, r1, r5, r6, r7, r8, ip, sp, pc}^

3e24: 68094479 stmdavs r9, {r0, r3, r4, r5, r6, sl, lr}

3e28: 2b1c3b03 blcs 712a3c <\_r\_debug@@GLIBC\_2.4+0x6e90e0>

3e2c: e8dfd812 ldm pc, {r1, r4, fp, ip, lr, pc}^ ; <UNPREDICTABLE>

3e30: 1120f003 msrne CPSR\_, r3

3e34: 1111111d tstne r1, sp, lsl r1

3e38: 11111119 tstne r1, r9, lsl r1

3e3c: 11111111 tstne r1, r1, lsl r1

3e40: 11111111 tstne r1, r1, lsl r1

3e44: 11111111 tstne r1, r1, lsl r1

3e48: 11111111 tstne r1, r1, lsl r1

3e4c: 000f1111 andeq r1, pc, r1, lsl r1 ; <UNPREDICTABLE>

3e50: 6053680b subsvs r6, r3, fp, lsl #16

3e54: 3f08f852 svccc 0x0008f852

3e58: d1e52b00 mvnle r2, r0, lsl #22

3e5c: 633b2301 teqvs fp, #67108864 ; 0x4000000

3e60: bf46f7fd svclt 0x0046f7fd

3e64: 3000f8da ldrdcc pc, [r0], -sl

3e68: e7f36053 ubfx r6, r3, #0, #20

3e6c: 60536b7b subsvs r6, r3, fp, ror fp

3e70: 6054e7f0 ldrshvs lr, [r4], #-112 ; 0xffffff90

3e74: 2301e7ee movwcs lr, #6126 ; 0x17ee

3e78: f7fe63bb ; <UNDEFINED> instruction: 0xf7fe63bb

3e7c: f423be68 ; <UNDEFINED> instruction: 0xf423be68

3e80: f021610e ; <UNDEFINED> instruction: 0xf021610e

3e84: 29000109 stmdbcs r0, {r0, r3, r8}

3e88: a89ff43e ldmge pc, {r1, r2, r3, r4, r5, sl, ip, sp, lr, pc} ; <UNPREDICTABLE>

3e8c: 447848ab ldrbtmi r4, [r8], #-2219 ; 0xfffff755

3e90: ff70f007 ; <UNDEFINED> instruction: 0xff70f007

3e94: 320cf8db andcc pc, ip, #14352384 ; 0xdb0000

3e98: b897f7fe ldmlt r7, {r1, r2, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}

3e9c: 23014602 movwcs r4, #5634 ; 0x1602

3ea0: be1af7fe mrclt 7, 0, APSR\_nzcv, cr10, cr14, {7}

3ea4: 447b4ba6 ldrbtmi r4, [fp], #-2982 ; 0xfffff45a

3ea8: 2528f8d3 strcs pc, [r8, #-2259]! ; 0xfffff72d

3eac: 0524f8d3 streq pc, [r4, #-2259]! ; 0xfffff72d

3eb0: f8d360d0 ; <UNDEFINED> instruction: 0xf8d360d0

3eb4: 2b003524 blcs 1134c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5e4>

3eb8: ad4af47e cfstrdge mvd15, [sl, #-504] ; 0xfffffe08

3ebc: bd67f7fe stcllt 7, cr15, [r7, #-1016]! ; 0xfffffc08

3ec0: 35fff10b ldrbcc pc, [pc, #267]! ; 3fd3 <free@plt+0x35b3> ; <UNPREDICTABLE>

3ec4: 4b9fe72e blmi fe7fdb84 <\_r\_debug@@GLIBC\_2.4+0xfe7d4228>

3ec8: 447b69a2 ldrbtmi r6, [fp], #-2466 ; 0xfffff65e

3ecc: 2804f8c3 stmdacs r4, {r0, r1, r6, r7, fp, ip, sp, lr, pc}

3ed0: f7fce749 ; <UNDEFINED> instruction: 0xf7fce749

3ed4: 6378ffd7 cmnvs r8, #860 ; 0x35c ; <UNPREDICTABLE>

3ed8: be55f7fe mrclt 7, 2, APSR\_nzcv, cr5, cr14, {7}

3edc: f2404b9a vpadd.i8 d20, d16, d10

3ee0: 499a62f9 ldmibmi sl, {r0, r3, r4, r5, r6, r7, r9, sp, lr}

3ee4: 447b489a ldrbtmi r4, [fp], #-2202 ; 0xfffff766

3ee8: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

3eec: fd4ef00c stc2l 0, cr15, [lr, #-48] ; 0xffffffd0

3ef0: 447b4b98 ldrbtmi r4, [fp], #-2968 ; 0xfffff468

3ef4: 2b01681b blcs 5df68 <\_r\_debug@@GLIBC\_2.4+0x3460c>

3ef8: aba6f67f blge fe9c18fc <\_r\_debug@@GLIBC\_2.4+0xfe997fa0>

3efc: 24014d96 strcs r4, [r1], #-3478 ; 0xfffff26a

3f00: 46a8447d sxtabmi r4, r8, sp, ror #8

3f04: 2000682e andcs r6, r0, lr, lsr #16

3f08: 31d0f8db ldrsbcc pc, [r0, #139] ; 0x8b ; <UNPREDICTABLE>

3f0c: f8ca4659 ; <UNDEFINED> instruction: 0xf8ca4659

3f10: 46520000 ldrbmi r0, [r2], -r0

3f14: c024f856 eorgt pc, r4, r6, asr r8 ; <UNPREDICTABLE>

3f18: 90032601 andls r2, r3, r1, lsl #12

3f1c: 0984ea4f stmibeq r4, {r0, r1, r2, r3, r6, r9, fp, sp, lr, pc}

3f20: e9cd9000 stmib sp, {ip, pc}^

3f24: 46606601 strbtmi r6, [r0], -r1, lsl #12

3f28: fbb6f003 blx fedbff3e <\_r\_debug@@GLIBC\_2.4+0xfed965e2>

3f2c: b1004601 tstlt r0, r1, lsl #12

3f30: f8d86801 ; <UNDEFINED> instruction: 0xf8d86801

3f34: f04f2000 ; <UNDEFINED> instruction: 0xf04f2000

3f38: 46630c08 strbtmi r0, [r3], -r8, lsl #24

3f3c: 34012001 strcc r2, [r1], #-1

3f40: 2009f852 andcs pc, r9, r2, asr r8 ; <UNPREDICTABLE>

3f44: c101e9cd smlabtgt r1, sp, r9, lr

3f48: c000f8da ldrdgt pc, [r0], -sl

3f4c: f8dc4983 ; <UNDEFINED> instruction: 0xf8dc4983

3f50: 44796004 ldrbtmi r6, [r9], #-4

3f54: f0079600 ; <UNDEFINED> instruction: 0xf0079600

3f58: 4b81ff39 blmi fe083c44 <\_r\_debug@@GLIBC\_2.4+0xfe05a2e8>

3f5c: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

3f60: d8cf42a3 stmiale pc, {r0, r1, r5, r7, r9, lr}^ ; <UNPREDICTABLE>

3f64: bb70f7ff bllt 1c41f68 <\_r\_debug@@GLIBC\_2.4+0x1c1860c>

3f68: 2300497e movwcs r4, #2430 ; 0x97e

3f6c: 7200f04f andvc pc, r0, #79 ; 0x4f

3f70: f5014479 ; <UNDEFINED> instruction: 0xf5014479

3f74: f89160a3 ; <UNDEFINED> instruction: 0xf89160a3

3f78: f36f46b0 vmin.u32 d20, d31, d16

3f7c: f8810482 ; <UNDEFINED> instruction: 0xf8810482

3f80: f8db46b0 ; <UNDEFINED> instruction: 0xf8db46b0

3f84: f00411d0 ; <UNDEFINED> instruction: 0xf00411d0

3f88: f7fffc8f ; <UNDEFINED> instruction: 0xf7fffc8f

3f8c: f8c5baaf ; <UNDEFINED> instruction: 0xf8c5baaf

3f90: 460a1528 strmi r1, [sl], -r8, lsr #10

3f94: f7fe68c8 ; <UNDEFINED> instruction: 0xf7fe68c8

3f98: 68d3bdc4 ldmvs r3, {r2, r6, r7, r8, sl, fp, ip, sp, pc}^

3f9c: 3524f8c5 strcc pc, [r4, #-2245]! ; 0xfffff73b

3fa0: f7fe68d0 ; <UNDEFINED> instruction: 0xf7fe68d0

3fa4: 4b70bdbe blmi 1c336a4 <\_r\_debug@@GLIBC\_2.4+0x1c09d48>

3fa8: 62f5f240 rscsvs pc, r5, #64, 4

3fac: 4870496f ldmdami r0!, {r0, r1, r2, r3, r5, r6, r8, fp, lr}^

3fb0: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

3fb4: f00c4478 ; <UNDEFINED> instruction: 0xf00c4478

3fb8: 2001fce9 andcs pc, r1, r9, ror #25

3fbc: ff44f00d ; <UNDEFINED> instruction: 0xff44f00d

3fc0: bf00e71d svclt 0x0000e71d

3fc4: 0002557e andeq r5, r2, lr, ror r5

3fc8: 00025c52 andeq r5, r2, r2, asr ip

3fcc: ffffd875 ; <UNDEFINED> instruction: 0xffffd875

3fd0: ffffd74b ; <UNDEFINED> instruction: 0xffffd74b

3fd4: ffffd72d ; <UNDEFINED> instruction: 0xffffd72d

3fd8: ffffd81b ; <UNDEFINED> instruction: 0xffffd81b

3fdc: 000254d4 ldrdeq r5, [r2], -r4

3fe0: 00012166 andeq r2, r1, r6, ror #2

3fe4: 00012144 andeq r2, r1, r4, asr #2

3fe8: 0002548c andeq r5, r2, ip, lsl #9

3fec: 0002541c andeq r5, r2, ip, lsl r4

3ff0: 00012004 andeq r2, r1, r4

3ff4: 000115dc ldrdeq r1, [r1], -ip

3ff8: 00026360 andeq r6, r2, r0, ror #6

3ffc: 0001213a andeq r2, r1, sl, lsr r1

4000: 000115a0 andeq r1, r1, r0, lsr #11

4004: 00025afc strdeq r5, [r2], -ip

4008: 0001150a andeq r1, r1, sl, lsl #10

400c: 000120a2 andeq r2, r1, r2, lsr #1

4010: 000114da ldrdeq r1, [r1], -sl

4014: 00011ee4 andeq r1, r1, r4, ror #29

4018: 00011482 andeq r1, r1, r2, lsl #9

401c: 00012010 andeq r2, r1, r0, lsl r0

4020: 00011fa6 andeq r1, r1, r6, lsr #31

4024: 00011f94 muleq r1, r4, pc ; <UNPREDICTABLE>

4028: 00011f0a andeq r1, r1, sl, lsl #30

402c: 00011f12 andeq r1, r1, r2, lsl pc

4030: 00012096 muleq r1, r6, r0

4034: 00011048 andeq r1, r1, r8, asr #32

4038: 00011dd2 ldrdeq r1, [r1], -r2

403c: 00011d2e andeq r1, r1, lr, lsr #26

4040: 00011fe0 andeq r1, r1, r0, ror #31

4044: 00011f8e andeq r1, r1, lr, lsl #31

4048: 00011ef8 strdeq r1, [r1], -r8

404c: 00010eaa andeq r0, r1, sl, lsr #29

4050: 00011c14 andeq r1, r1, r4, lsl ip

4054: 00025744 andeq r5, r2, r4, asr #14

4058: 0002560c andeq r5, r2, ip, lsl #12

405c: 00000038 andeq r0, r0, r8, lsr r0

4060: 00024fcc andeq r4, r2, ip, asr #31

4064: 000112e2 andeq r1, r1, r2, ror #5

4068: 00025f42 andeq r5, r2, r2, asr #30

406c: 00025666 andeq r5, r2, r6, ror #12

4070: ffffd12f ; <UNDEFINED> instruction: 0xffffd12f

4074: ffffd0cf ; <UNDEFINED> instruction: 0xffffd0cf

4078: 00011a4e andeq r1, r1, lr, asr #20

407c: 00025600 andeq r5, r2, r0, lsl #12

4080: 000119b8 ; <UNDEFINED> instruction: 0x000119b8

4084: 000102c2 andeq r0, r1, r2, asr #5

4088: 00011c94 muleq r1, r4, ip

408c: 00010c46 andeq r0, r1, r6, asr #24

4090: 0001196c andeq r1, r1, ip, ror #18

4094: 00024e12 andeq r4, r2, r2, lsl lr

4098: 00024df0 strdeq r4, [r2], -r0

409c: 00011c38 andeq r1, r1, r8, lsr ip

40a0: 00010bea andeq r0, r1, sl, ror #23

40a4: 000118f8 strdeq r1, [r1], -r8

40a8: 00011c1e andeq r1, r1, lr, lsl ip

40ac: 00010bd0 ldrdeq r0, [r1], -r0 ; <UNPREDICTABLE>

40b0: 00011662 andeq r1, r1, r2, ror #12

40b4: 00011c04 andeq r1, r1, r4, lsl #24

40b8: 00010bb6 ; <UNDEFINED> instruction: 0x00010bb6

40bc: 00011670 andeq r1, r1, r0, ror r6

40c0: 00024d92 muleq r2, r2, sp

40c4: 00011bd2 ldrdeq r1, [r1], -r2

40c8: 00010b84 andeq r0, r1, r4, lsl #23

40cc: 000118ce andeq r1, r1, lr, asr #17

40d0: 00024d58 andeq r4, r2, r8, asr sp

40d4: 00024d50 andeq r4, r2, r0, asr sp

40d8: 00025438 andeq r5, r2, r8, lsr r4

40dc: 00011b2e andeq r1, r1, lr, lsr #22

40e0: 00010ae0 andeq r0, r1, r0, ror #21

40e4: 00011512 andeq r1, r1, r2, lsl r5

40e8: 00011b14 andeq r1, r1, r4, lsl fp

40ec: 00010ac6 andeq r0, r1, r6, asr #21

40f0: 0001150c andeq r1, r1, ip, lsl #10

40f4: 00011afa strdeq r1, [r1], -sl

40f8: 00010aac andeq r0, r1, ip, lsr #21

40fc: 0001172e andeq r1, r1, lr, lsr #14

4100: 00011ae0 andeq r1, r1, r0, ror #21

4104: 00010a92 muleq r1, r2, sl

4108: 000116e8 andeq r1, r1, r8, ror #13

410c: 00011a86 andeq r1, r1, r6, lsl #21

4110: 00011608 andeq r1, r1, r8, lsl #12

4114: 00011616 andeq r1, r1, r6, lsl r6

4118: 00025c1e andeq r5, r2, lr, lsl ip

411c: 00024bd8 ldrdeq r4, [r2], -r8

4120: 00025b62 andeq r5, r2, r2, ror #22

4124: 000252d8 ldrdeq r5, [r2], -r8

4128: 000113de ldrdeq r1, [r1], -lr

412c: ffffce71 ; <UNDEFINED> instruction: 0xffffce71

4130: 0002525c andeq r5, r2, ip, asr r2

4134: 000116fc strdeq r1, [r1], -ip

4138: 00025a74 andeq r5, r2, r4, ror sl

413c: 0001144e andeq r1, r1, lr, asr #8

4140: 000251b6 ; <UNDEFINED> instruction: 0x000251b6

4144: 00025192 muleq r2, r2, r1

4148: 000118e6 andeq r1, r1, r6, ror #17

414c: 00010898 muleq r1, r8, r8

4150: 0001166e andeq r1, r1, lr, ror #12

4154: 00024a1a andeq r4, r2, sl, lsl sl

4158: 00025998 muleq r2, r8, r9

415c: 000116f2 strdeq r1, [r1], -r2

4160: 000249b0 ; <UNDEFINED> instruction: 0x000249b0

4164: 000250ec andeq r5, r2, ip, ror #1

4168: 0001181c andeq r1, r1, ip, lsl r8

416c: 000107ce andeq r0, r1, lr, asr #15

4170: 00011568 andeq r1, r1, r8, ror #10

4174: 4605b570 ; <UNDEFINED> instruction: 0x4605b570

4178: 460e4608 strmi r4, [lr], -r8, lsl #12

417c: fae0f00e blx ff8401bc <\_r\_debug@@GLIBC\_2.4+0xff816860>

4180: 2b7b782b blcs 1ee2234 <\_r\_debug@@GLIBC\_2.4+0x1eb88d8>

4184: d0174604 andsle r4, r7, r4, lsl #12

4188: 46314602 ldrtmi r4, [r1], -r2, lsl #12

418c: f00e4628 ; <UNDEFINED> instruction: 0xf00e4628

4190: b110fb97 ; <UNDEFINED> instruction: 0xb110fb97

4194: 46202400 strtmi r2, [r0], -r0, lsl #8

4198: 5d2abd70 stcpl 13, cr11, [sl, #-448]! ; 0xfffffe40

419c: 0320f022 nopeq {34} ; 0x22

41a0: 2b193b41 blcs 652eac <\_r\_debug@@GLIBC\_2.4+0x629550>

41a4: f1a2d9f6 ; <UNDEFINED> instruction: 0xf1a2d9f6

41a8: 2a5f0330 bcs 17c4e70 <\_r\_debug@@GLIBC\_2.4+0x179b514>

41ac: 2b09bf18 blcs 273e14 <\_r\_debug@@GLIBC\_2.4+0x24a4b8>

41b0: 2400bf98 strcs fp, [r0], #-3992 ; 0xfffff068

41b4: bd704620 ldcllt 6, cr4, [r0, #-128]! ; 0xffffff80

41b8: 46023501 strmi r3, [r2], -r1, lsl #10

41bc: 46284631 ; <UNDEFINED> instruction: 0x46284631

41c0: fb7ef00e blx 1fc0202 <\_r\_debug@@GLIBC\_2.4+0x1f968a6>

41c4: d1e52800 mvnle r2, r0, lsl #16

41c8: 2b7d5d2b blcs 1f5b67c <\_r\_debug@@GLIBC\_2.4+0x1f31d20>

41cc: 3402bf08 strcc fp, [r2], #-3848 ; 0xfffff0f8

41d0: 4620d1e0 strtmi sp, [r0], -r0, ror #3

41d4: bf00bd70 svclt 0x0000bd70

41d8: d0732900 rsbsle r2, r3, r0, lsl #18

41dc: f0213109 ; <UNDEFINED> instruction: 0xf0213109

41e0: e92d0107 push {r0, r1, r2, r8}

41e4: af0047f0 svcge 0x000047f0

41e8: 0d01ebad vstreq d14, [r1, #-692] ; 0xfffffd4c

41ec: 466d7802 strbtmi r7, [sp], -r2, lsl #16

41f0: b14a462b cmplt sl, fp, lsr #12

41f4: 78412a2f stmdavc r1, {r0, r1, r2, r3, r5, r9, fp, sp}^

41f8: 3001bf18 andcc fp, r1, r8, lsl pc

41fc: f803d034 ; <UNDEFINED> instruction: 0xf803d034

4200: 460a2b01 strmi r2, [sl], -r1, lsl #22

4204: d1f52a00 mvnsle r2, r0, lsl #20

4208: d00342ab andle r4, r3, fp, lsr #5

420c: 2c01f813 stccs 8, cr15, [r1], {19}

4210: d0022a2f andle r2, r2, pc, lsr #20

4214: f803222f ; <UNDEFINED> instruction: 0xf803222f

4218: f8df2b01 ; <UNDEFINED> instruction: 0xf8df2b01

421c: 1b5c80b0 blne 17244e4 <\_r\_debug@@GLIBC\_2.4+0x16fab88>

4220: 42a62619 adcmi r2, r6, #26214400 ; 0x1900000

4224: 463244f8 ; <UNDEFINED> instruction: 0x463244f8

4228: 094cf108 stmdbeq ip, {r3, r8, ip, sp, lr, pc}^

422c: 0a58f108 beq 1640654 <\_r\_debug@@GLIBC\_2.4+0x1616cf8>

4230: 46404629 strbmi r4, [r0], -r9, lsr #12

4234: f00ed803 ; <UNDEFINED> instruction: 0xf00ed803

4238: 2800fc01 stmdacs r0, {r0, sl, fp, ip, sp, lr, pc}

423c: 45d1d044 ldrbmi sp, [r1, #68] ; 0x44

4240: 0601f106 streq pc, [r1], -r6, lsl #2

4244: d00b44b0 ; <UNDEFINED> instruction: 0xd00b44b0

4248: 6b04f859 blvs 1423b4 <\_r\_debug@@GLIBC\_2.4+0x118a58>

424c: 46404629 strbmi r4, [r0], -r9, lsr #12

4250: 463242a6 ldrtmi r4, [r2], -r6, lsr #5

4254: 45d1d9ef ldrbmi sp, [r1, #2543] ; 0x9ef

4258: 0601f106 streq pc, [r1], -r6, lsl #2

425c: d1f344b0 ldrhle r4, [r3, #64]! ; 0x40

4260: 46bd2000 ldrtmi r2, [sp], r0

4264: 87f0e8bd ; <UNDEFINED> instruction: 0x87f0e8bd

4268: d009292e andle r2, r9, lr, lsr #18

426c: f10042ab ; <UNDEFINED> instruction: 0xf10042ab

4270: d9c40001 stmible r4, {r0}^

4274: 4c01f813 stcmi 8, cr15, [r1], {19}

4278: d1c02c2f bicle r2, r0, pc, lsr #24

427c: e7b8460a ldr r4, [r8, sl, lsl #12]!

4280: 2c2e7884 stccs 8, cr7, [lr], #-528 ; 0xfffffdf0

4284: 2c00d007 stccs 0, cr13, [r0], {7}

4288: 2c2fbf18 stccs 15, cr11, [pc], #-96 ; 4230 <free@plt+0x3810>

428c: 3002bf04 andcc fp, r2, r4, lsl #30

4290: d1eb4622 mvnle r4, r2, lsr #12

4294: 78c4e7ad stmiavc r4, {r0, r2, r3, r5, r7, r8, r9, sl, sp, lr, pc}^

4298: bf182c00 svclt 0x00182c00

429c: d1e52c2f mvnle r2, pc, lsr #24

42a0: d90c42ab stmdble ip, {r0, r1, r3, r5, r7, r9, lr}

42a4: 2c01f813 stccs 8, cr15, [r1], {19}

42a8: 2a2f3b01 bcs bd2eb4 <\_r\_debug@@GLIBC\_2.4+0xba9558>

42ac: e006d104 and sp, r6, r4, lsl #2

42b0: 2d01f813 stccs 8, cr15, [r1, #-76] ; 0xffffffb4

42b4: d0022a2f andle r2, r2, pc, lsr #20

42b8: d1f9429d ; <UNDEFINED> instruction: 0xd1f9429d

42bc: 3003462b andcc r4, r3, fp, lsr #12

42c0: e7964622 ldr r4, [r6, r2, lsr #12]

42c4: 47702000 ldrbmi r2, [r0, -r0]!

42c8: e7ca2001 strb r2, [sl, r1]

42cc: 0000ff58 andeq pc, r0, r8, asr pc ; <UNPREDICTABLE>

42d0: 41f0e92d mvnsmi lr, sp, lsr #18

42d4: 4604460e strmi r4, [r4], -lr, lsl #12

42d8: e021b918 eor fp, r1, r8, lsl r9

42dc: b1456865 cmplt r5, r5, ror #16

42e0: 6821462c stmdavs r1!, {r2, r3, r5, r9, sl, lr}

42e4: f00d4630 ; <UNDEFINED> instruction: 0xf00d4630

42e8: 2800ff33 stmdacs r0, {r0, r1, r4, r5, r8, r9, sl, fp, ip, sp, lr, pc}

42ec: e8bdd1f6 pop {r1, r2, r4, r5, r6, r7, r8, ip, lr, pc}

42f0: 463081f0 ; <UNDEFINED> instruction: 0x463081f0

42f4: fa24f00e blx 940334 <\_r\_debug@@GLIBC\_2.4+0x9169d8>

42f8: 300d4680 andcc r4, sp, r0, lsl #13

42fc: eb66f7fc bl 19c22f4 <\_r\_debug@@GLIBC\_2.4+0x1998998>

4300: 0801f108 stmdaeq r1, {r3, r8, ip, sp, lr, pc}

4304: b1904607 orrslt r4, r0, r7, lsl #12

4308: 46314642 ldrtmi r4, [r1], -r2, asr #12

430c: 000cf107 andeq pc, ip, r7, lsl #2

4310: eef6f00e cdp 0, 15, cr15, cr6, cr14, {0}

4314: 5501e9c7 strpl lr, [r1, #-2503] ; 0xfffff639

4318: 60676038 rsbvs r6, r7, r8, lsr r0

431c: 81f0e8bd ldrhhi lr, [r0, #141]! ; 0x8d

4320: f00e4608 ; <UNDEFINED> instruction: 0xf00e4608

4324: 300dfa0d andcc pc, sp, sp, lsl #20

4328: eb50f7fc bl 1442320 <\_r\_debug@@GLIBC\_2.4+0x14189c4>

432c: 4b08b930 blmi 2327f4 <\_r\_debug@@GLIBC\_2.4+0x208e98>

4330: 22004631 andcs r4, r0, #51380224 ; 0x3100000

4334: 447b200c ldrbtmi r2, [fp], #-12

4338: eb66f7fc bl 19c2330 <\_r\_debug@@GLIBC\_2.4+0x19989d4>

433c: f2404b05 vqdmulh.s<illegal width 8> d20, d0, d5

4340: 490512af stmdbmi r5, {r0, r1, r2, r3, r5, r7, r9, ip}

4344: 447b4805 ldrbtmi r4, [fp], #-2053 ; 0xfffff7fb

4348: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

434c: fb1ef00c blx 7c0386 <\_r\_debug@@GLIBC\_2.4+0x796a2a>

4350: 000114c2 andeq r1, r1, r2, asr #9

4354: 00011d42 andeq r1, r1, r2, asr #26

4358: 000114cc andeq r1, r1, ip, asr #9

435c: 000114d6 ldrdeq r1, [r1], -r6

4360: 4617b580 ldrmi fp, [r7], -r0, lsl #11

4364: 46061c4a strmi r1, [r6], -sl, asr #24

4368: 9c024698 stcls 6, cr4, [r2], {152} ; 0x98

436c: d1179d04 tstle r7, r4, lsl #26

4370: f8d4b12c ; <UNDEFINED> instruction: 0xf8d4b12c

4374: 1c4301ac stfnee f0, [r3], {172} ; 0xac

4378: f7fcd001 ; <UNDEFINED> instruction: 0xf7fcd001

437c: 4620eb54 ; <UNDEFINED> instruction: 0x4620eb54

4380: eb50f7fc bl 1442378 <\_r\_debug@@GLIBC\_2.4+0x1418a1c>

4384: f7fc4640 ; <UNDEFINED> instruction: 0xf7fc4640

4388: b11deb4e tstlt sp, lr, asr #22

438c: 60eb2300 rscvs r2, fp, r0, lsl #6

4390: fb26f007 blx 9c03b6 <\_r\_debug@@GLIBC\_2.4+0x996a5a>

4394: 46304639 ; <UNDEFINED> instruction: 0x46304639

4398: 22009b03 andcs r9, r0, #3072 ; 0xc00

439c: eb34f7fc bl d42394 <\_r\_debug@@GLIBC\_2.4+0xd18a38>

43a0: f00d4608 ; <UNDEFINED> instruction: 0xf00d4608

43a4: e7e3fba9 strb pc, [r3, r9, lsr #23]! ; <UNPREDICTABLE>

43a8: 4ff0e92d svcmi 0x00f0e92d

43ac: af0ab0b9 svcge 0x000ab0b9

43b0: 64384614 ldrtvs r4, [r8], #-1556 ; 0xfffff9ec

43b4: 60f92000 rscsvs r2, r9, r0

43b8: 10f4f8d7 ldrsbtne pc, [r4], #135 ; 0x87 ; <UNPREDICTABLE>

43bc: 63fb63ba mvnsvs r6, #-402653182 ; 0xe8000002

43c0: fb10f007 blx 4403e6 <\_r\_debug@@GLIBC\_2.4+0x416a8a>

43c4: f1074621 ; <UNDEFINED> instruction: 0xf1074621

43c8: 64780250 ldrbtvs r0, [r8], #-592 ; 0xfffffdb0

43cc: f00d2003 ; <UNDEFINED> instruction: 0xf00d2003

43d0: 2800faf5 stmdacs r0, {r0, r2, r4, r5, r6, r7, r9, fp, ip, sp, lr, pc}

43d4: 822af2c0 eorhi pc, sl, #192, 4

43d8: 10f4f8d7 ldrsbtne pc, [r4], #135 ; 0x87 ; <UNPREDICTABLE>

43dc: f8df234c ; <UNDEFINED> instruction: 0xf8df234c

43e0: fb032b88 blx cf20a <\_r\_debug@@GLIBC\_2.4+0xa58ae>

43e4: e9d7f301 ldmib r7, {r0, r8, r9, ip, sp, lr, pc}^

43e8: 447a0114 ldrbtmi r0, [sl], #-276 ; 0xfffffeec

43ec: 010ae9c7 smlabteq sl, r7, r9, lr

43f0: 012ce9d7 ldrdeq lr, [ip, -r7]!

43f4: e9c758d4 stmib r7, {r2, r4, r6, r7, fp, ip, lr}^

43f8: e9d70108 ldmib r7, {r3, r8}^

43fc: e9d78908 ldmib r7, {r3, r8, fp, pc}^

4400: b914010a ldmdblt r4, {r1, r3, r8}

4404: 68e4e021 stmiavs r4!, {r0, r5, sp, lr, pc}^

4408: f894b1fc ; <UNDEFINED> instruction: 0xf894b1fc

440c: 069d3199 ; <UNDEFINED> instruction: 0x069d3199

4410: e9d4d4f9 ldmib r4, {r0, r3, r4, r5, r6, r7, sl, ip, lr, pc}^

4414: 428b2378 addmi r2, fp, #120, 6 ; 0xe0000001

4418: 4282bf08 addmi fp, r2, #8, 30

441c: e9d4d1f3 ldmib r4, {r0, r1, r4, r5, r6, r7, r8, ip, lr, pc}^

4420: 454b237a strbmi r2, [fp, #-890] ; 0xfffffc86

4424: 4542bf08 strbmi fp, [r2, #-3848] ; 0xfffff0f8

4428: 6bb8d1ed blvs fee38be4 <\_r\_debug@@GLIBC\_2.4+0xfee0f288>

442c: fb64f00d blx 194046a <\_r\_debug@@GLIBC\_2.4+0x1916b0e>

4430: 00e0f8d7 ldrdeq pc, [r0], #135 ; 0x87 ; <UNPREDICTABLE>

4434: eaf6f7fc b ffdc242c <\_r\_debug@@GLIBC\_2.4+0xffd98ad0>

4438: 69e06c39 stmibvs r0!, {r0, r3, r4, r5, sl, fp, sp, lr}^

443c: ff48f7ff ; <UNDEFINED> instruction: 0xff48f7ff

4440: 37bc4620 ldrcc r4, [ip, r0, lsr #12]!

4444: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

4448: f8d78ff0 ; <UNDEFINED> instruction: 0xf8d78ff0

444c: 2b0030f4 blcs 10824 <malloc@@GLIBC\_2.4+0xc0>

4450: 8374f040 cmnhi r4, #64 ; 0x40 ; <UNPREDICTABLE>

4454: 30ecf8d7 ldrdcc pc, [ip], #135 ; 0x87 ; <UNPREDICTABLE>

4458: 0304f013 movweq pc, #16403 ; 0x4013 ; <UNPREDICTABLE>

445c: f040613b ; <UNDEFINED> instruction: 0xf040613b

4460: f8df83c5 ; <UNDEFINED> instruction: 0xf8df83c5

4464: 447b3b08 ldrbtmi r3, [fp], #-2824 ; 0xfffff4f8

4468: 065c681b ; <UNDEFINED> instruction: 0x065c681b

446c: 81adf100 ; <UNDEFINED> instruction: 0x81adf100

4470: 68dc6c7b ldmvs ip, {r0, r1, r3, r4, r5, r6, sl, fp, sp, lr}^

4474: f0002c00 ; <UNDEFINED> instruction: 0xf0002c00

4478: 2c018114 stfcsd f0, [r1], {20}

447c: 2300bf04 movwcs fp, #3844 ; 0xf04

4480: f04060bb ; <UNDEFINED> instruction: 0xf04060bb

4484: f8d7853c ; <UNDEFINED> instruction: 0xf8d7853c

4488: 930130f4 movwls r3, #4340 ; 0x10f4

448c: 20ecf8d7 ldrdcs pc, [ip], #135 ; 0x87 ; <UNPREDICTABLE>

4490: 30e4f8d7 ldrdcc pc, [r4], #135 ; 0x87 ; <UNPREDICTABLE>

4494: f8d79200 ; <UNDEFINED> instruction: 0xf8d79200

4498: 6c3920e8 ldcvs 0, cr2, [r9], #-928 ; 0xfffffc60

449c: 00e0f8d7 ldrdeq pc, [r0], #135 ; 0x87 ; <UNPREDICTABLE>

44a0: ffbef003 ; <UNDEFINED> instruction: 0xffbef003

44a4: 28004604 stmdacs r0, {r2, r9, sl, lr}

44a8: 83a9f000 ; <UNDEFINED> instruction: 0x83a9f000

44ac: 8e0a6bf9 vmovhi.16 d26[1], r6

44b0: 0e07e9d1 ; <UNDEFINED> instruction: 0x0e07e9d1

44b4: 0155680e cmpeq r5, lr, lsl #16

44b8: c014f8b1 ; <UNDEFINED> instruction: 0xc014f8b1

44bc: 0158f8c4 cmpeq r8, r4, asr #17 ; <UNPREDICTABLE>

44c0: 0005eb0e andeq lr, r5, lr, lsl #22

44c4: 461342b0 ; <UNDEFINED> instruction: 0x461342b0

44c8: c000f8c7 andgt pc, r0, r7, asr #17

44cc: 215cf8a4 cmpcs ip, r4, lsr #17 ; <UNPREDICTABLE>

44d0: 8184f200 orrhi pc, r4, r0, lsl #4

44d4: 1d0a6bf9 vstrne d6, [sl, #-996] ; 0xfffffc1c

44d8: 090eeb02 stmdbeq lr, {r1, r8, r9, fp, sp, lr, pc}

44dc: 22184669 andscs r4, r8, #110100480 ; 0x6900000

44e0: 1313fb02 tstne r3, #2048 ; 0x800 ; <UNPREDICTABLE>

44e4: 45a9444d strmi r4, [r9, #1101]! ; 0x44d

44e8: d004f8c7 andle pc, r4, r7, asr #17

44ec: f10d469d ; <UNDEFINED> instruction: 0xf10d469d

44f0: f0800a28 ; <UNDEFINED> instruction: 0xf0800a28

44f4: f24e842d vshl.s8 d24, d29, d14

44f8: f2c65851 vmov.i16 <illegal reg q10.5>, #97 ; 0x0061

44fc: 23004874 movwcs r4, #2164 ; 0x874

4500: 5652f24e ldrbpl pc, [r2], -lr, asr #4 ; <UNPREDICTABLE>

4504: f24661fb vand q11, q11, <illegal reg q13.5>

4508: 64fb2240 ldrbtvs r2, [fp], #576 ; 0x240

450c: 3251f2c7 subscc pc, r1, #1879048204 ; 0x7000000c

4510: f8c72307 ; <UNDEFINED> instruction: 0xf8c72307

4514: f2c6a048 vmla.i<illegal width 8> d26, d6, d0[2]

4518: 617a4674 cmnvs sl, r4, ror r6

451c: 61bb464d ; <UNDEFINED> instruction: 0x61bb464d

4520: e00f46c2 and r4, pc, r2, asr #13

4524: f0004553 ; <UNDEFINED> instruction: 0xf0004553

4528: 42b3814d adcsmi r8, r3, #1073741843 ; 0x40000013

452c: 8143f000 mrshi pc, (UNDEF: 67) ; <UNPREDICTABLE>

4530: f0002b07 ; <UNDEFINED> instruction: 0xf0002b07

4534: f8b48120 ; <UNDEFINED> instruction: 0xf8b48120

4538: 3520315c strcc r3, [r0, #-348]! ; 0xfffffea4

453c: 1343eb09 movtne lr, #15113 ; 0x3b09

4540: d21a429d andsle r4, sl, #-805306359 ; 0xd0000009

4544: 2b06682b blcs 19e5f8 <\_r\_debug@@GLIBC\_2.4+0x174c9c>

4548: 8111f000 tsthi r1, r0 ; <UNPREDICTABLE>

454c: 2b01d8ea blcs 7a8fc <\_r\_debug@@GLIBC\_2.4+0x50fa0>

4550: 80baf000 adcshi pc, sl, r0

4554: d1ee2b02 mvnle r2, r2, lsl #22

4558: 2b00692b blcs 1ea0c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdca4>

455c: 696bd0eb stmdbvs fp!, {r0, r1, r3, r5, r6, r7, ip, lr, pc}^

4560: f8553520 ; <UNDEFINED> instruction: 0xf8553520

4564: 08db2c18 ldmeq fp, {r3, r4, sl, fp, sp}^

4568: 315ef8a4 cmpcc lr, r4, lsr #17 ; <UNPREDICTABLE>

456c: 315cf8b4 ldrhcc pc, [ip, #-132] ; 0xffffff7c ; <UNPREDICTABLE>

4570: eb0960a2 bl 25c800 <\_r\_debug@@GLIBC\_2.4+0x232ea4>

4574: 429d1343 addsmi r1, sp, #201326593 ; 0xc000001

4578: e9d7d3e4 ldmib r7, {r2, r5, r6, r7, r8, r9, ip, lr, pc}^

457c: 2b00a312 blcs 2d1cc <\_r\_debug@@GLIBC\_2.4+0x3870>

4580: 83e6f000 mvnhi pc, #0

4584: 2b03683b blcs de678 <\_r\_debug@@GLIBC\_2.4+0xb4d1c>

4588: 8365f040 msrhi SPSR\_sc, #64 ; 0x40

458c: f04f6cfb ; <UNDEFINED> instruction: 0xf04f6cfb

4590: f8df0818 ; <UNDEFINED> instruction: 0xf8df0818

4594: f10329dc ; <UNDEFINED> instruction: 0xf10329dc

4598: f8da3cff ; <UNDEFINED> instruction: 0xf8da3cff

459c: 447a6010 ldrbtmi r6, [sl], #-16

45a0: 5000f8da ldrdpl pc, [r0], -sl

45a4: ac0cfb08 ; <UNDEFINED> instruction: 0xac0cfb08

45a8: 0164f8d2 ldrdeq pc, [r4, #-130]! ; 0xffffff7e

45ac: b014f8da ; <UNDEFINED> instruction: 0xb014f8da

45b0: 0302f640 movweq pc, #9792 ; 0x2640 ; <UNPREDICTABLE>

45b4: 465a4028 ldrbmi r4, [sl], -r8, lsr #32

45b8: 100cf8dc ldrdne pc, [ip], -ip

45bc: 6bbe9601 blvs fefa9dc8 <\_r\_debug@@GLIBC\_2.4+0xfef8046c>

45c0: 64b91b49 ldrtvs r1, [r9], #2889 ; 0xb49

45c4: f00d9600 ; <UNDEFINED> instruction: 0xf00d9600

45c8: 1c43fbd5 mcrrne 11, 13, pc, r3, cr5 ; <UNPREDICTABLE>

45cc: 01b0f8c4 asrseq pc, r4, #17 ; <UNPREDICTABLE>

45d0: 6cbbd063 ldcvs 0, cr13, [fp], #396 ; 0x18c

45d4: 60251b45 eorvs r1, r5, r5, asr #22

45d8: 69fb4418 ldmibvs fp!, {r3, r4, sl, lr}^

45dc: 01b4f8c4 ; <UNDEFINED> instruction: 0x01b4f8c4

45e0: 6cfbb173 ldfvsp f3, [fp], #460 ; 0x1cc

45e4: fb082200 blx 20cdee <\_r\_debug@@GLIBC\_2.4+0x1e3492>

45e8: f8daa603 ; <UNDEFINED> instruction: 0xf8daa603

45ec: 18e83004 stmiane r8!, {r2, ip, sp}^

45f0: 1c18f856 ldcne 8, cr15, [r8], {86} ; 0x56

45f4: f00d1ac9 ; <UNDEFINED> instruction: 0xf00d1ac9

45f8: 2800fbfb stmdacs r0, {r0, r1, r3, r4, r5, r6, r7, r8, r9, fp, ip, sp, lr, pc}

45fc: 83c1f2c0 bichi pc, r1, #192, 4

4600: 3199f894 ; <UNDEFINED> instruction: 0x3199f894

4604: f0434655 ; <UNDEFINED> instruction: 0xf0434655

4608: f8840340 ; <UNDEFINED> instruction: 0xf8840340

460c: f01b3199 ; <UNDEFINED> instruction: 0xf01b3199

4610: bf1f0f04 svclt 0x001f0f04

4614: 686a6823 stmdavs sl!, {r0, r1, r5, fp, sp, lr}^

4618: f8c4189b ; <UNDEFINED> instruction: 0xf8c4189b

461c: f8d431b8 ; <UNDEFINED> instruction: 0xf8d431b8

4620: 2b003154 blcs 10b78 <realloc@@GLIBC\_2.4+0x1b0>

4624: 8359f000 cmphi r9, #0 ; <UNPREDICTABLE>

4628: 0202e9d5 andeq lr, r2, #3489792 ; 0x354000

462c: d9164282 ldmdble r6, {r1, r7, r9, lr}

4630: 3940f8df stmdbcc r0, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}^

4634: 637b447b cmnvs fp, #2063597568 ; 0x7b000000

4638: 68234619 stmdavs r3!, {r0, r3, r4, r9, sl, lr}

463c: eb006909 bl 1ea68 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdd00>

4640: 44130803 ldrmi r0, [r3], #-2051 ; 0xfffff7fd

4644: 42481e4e submi r1, r8, #1248 ; 0x4e0

4648: 40064446 andmi r4, r6, r6, asr #8

464c: bf28429e svclt 0x0028429e

4650: 45b0461e ldrmi r4, [r0, #1566]! ; 0x61e

4654: 82daf0c0 sbcshi pc, sl, #192 ; 0xc0

4658: f20042b3 vqsub.s8 d4, d16, d19

465c: 35188352 ldrcc r8, [r8, #-850] ; 0xfffffcae

4660: 23186cfa tstcs r8, #64000 ; 0xfa00

4664: a302fb03 movwge pc, #11011 ; 0x2b03 ; <UNPREDICTABLE>

4668: f080429d ; <UNDEFINED> instruction: 0xf080429d

466c: e9d580ef ldmib r5, {r0, r1, r2, r3, r5, r6, r7, pc}^

4670: f8d52100 ; <UNDEFINED> instruction: 0xf8d52100

4674: 4291b014 addsmi fp, r1, #20

4678: 6bb8d9c9 blvs fee3ada4 <\_r\_debug@@GLIBC\_2.4+0xfee11448>

467c: 692e1a89 stmdbvs lr!, {r0, r3, r7, r9, fp, ip}

4680: 0312f640 tsteq r2, #64, 12 ; 0x4000000 ; <UNPREDICTABLE>

4684: c000f8d4 ldrdgt pc, [r0], -r4

4688: 0600e9cd streq lr, [r0], -sp, asr #19

468c: 0002eb0c andeq lr, r2, ip, lsl #22

4690: f00d465a ; <UNDEFINED> instruction: 0xf00d465a

4694: 3001fb6f andcc pc, r1, pc, ror #22

4698: f8dfd1b9 ; <UNDEFINED> instruction: 0xf8dfd1b9

469c: 447b38dc ldrbtmi r3, [fp], #-2268 ; 0xfffff724

46a0: f8d7e0b5 ; <UNDEFINED> instruction: 0xf8d7e0b5

46a4: 011830ec tsteq r8, ip, ror #1

46a8: f8dfd407 ; <UNDEFINED> instruction: 0xf8dfd407

46ac: 447d58d0 ldrbtmi r5, [sp], #-2256 ; 0xfffff730

46b0: 31b0f8d5 asrscc pc, r5 ; <illegal shifter operand> ; <UNPREDICTABLE>

46b4: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

46b8: 6c7b82f2 lfmvs f0, 3, [fp], #-968 ; 0xfffffc38

46bc: 60dc2401 sbcsvs r2, ip, r1, lsl #8

46c0: f98ef007 ; <UNDEFINED> instruction: 0xf98ef007

46c4: e6de60bc ; <UNDEFINED> instruction: 0xe6de60bc

46c8: 38b4f8df ldmcc r4!, {r0, r1, r2, r3, r4, r6, r7, fp, ip, sp, lr, pc}

46cc: 447b69e8 ldrbtmi r6, [fp], #-2536 ; 0xfffff618

46d0: 1e4b6919 ; <UNDEFINED> instruction: 0x1e4b6919

46d4: f0404218 ; <UNDEFINED> instruction: 0xf0404218

46d8: f8d58097 ; <UNDEFINED> instruction: 0xf8d58097

46dc: 38018008 stmdacc r1, {r3, pc}

46e0: eba8686a bl fea1e890 <\_r\_debug@@GLIBC\_2.4+0xfe9f4f34>

46e4: ea1c0c02 b 7076f4 <\_r\_debug@@GLIBC\_2.4+0x6ddd98>

46e8: f0400f00 ; <UNDEFINED> instruction: 0xf0400f00

46ec: 6cf880ab ldclvs 0, cr8, [r8], #684 ; 0x2ac

46f0: f04f4249 ; <UNDEFINED> instruction: 0xf04f4249

46f4: ea020c18 b 8775c <\_r\_debug@@GLIBC\_2.4+0x5de00>

46f8: ea080e01 b 207f04 <\_r\_debug@@GLIBC\_2.4+0x1de5a8>

46fc: 637a0201 cmnvs sl, #268435456 ; 0x10000000

4700: fc00fb0c stc2 11, cr15, [r0], {12} ; <UNPREDICTABLE>

4704: 6928696a stmdbvs r8!, {r1, r3, r5, r6, r8, fp, sp, lr}

4708: 633a4440 teqvs sl, #64, 8 ; 0x40000000

470c: 6cba4403 cfldrsvs mvf4, [sl], #12

4710: 6cb9400b ldcvs 0, cr4, [r9], #44 ; 0x2c

4714: 0b0ceb02 bleq 33f324 <\_r\_debug@@GLIBC\_2.4+0x3159c8>

4718: 44426b3a strbmi r6, [r2], #-2874 ; 0xfffff4c6

471c: 6b794688 blvs 1e56144 <\_r\_debug@@GLIBC\_2.4+0x1e2c7e8>

4720: 100cf848 andne pc, ip, r8, asr #16

4724: f8cb6cf9 ; <UNDEFINED> instruction: 0xf8cb6cf9

4728: 3101e010 tstcc r1, r0, lsl r0

472c: 3001e9cb andcc lr, r1, fp, asr #19

4730: f8cb2901 ; <UNDEFINED> instruction: 0xf8cb2901

4734: d908200c stmdble r8, {r2, r3, sp}

4738: f85b69f8 ; <UNDEFINED> instruction: 0xf85b69f8

473c: f8583c14 ; <UNDEFINED> instruction: 0xf8583c14

4740: 4293200c addsmi r2, r3, #12

4744: 2001bf18 andcs fp, r1, r8, lsl pc

4748: 6cf861f8 ldfvse f6, [r8], #992 ; 0x3e0

474c: 69ab2218 stmibvs fp!, {r3, r4, r9, sp}

4750: 468464f9 ; <UNDEFINED> instruction: 0x468464f9

4754: f0036cb8 ; <UNDEFINED> instruction: 0xf0036cb8

4758: 69790307 ldmdbvs r9!, {r0, r1, r2, r8, r9}^

475c: 020cfb02 andeq pc, ip, #2048 ; 0x800

4760: fa41009b blx 10449d4 <\_r\_debug@@GLIBC\_2.4+0x101b078>

4764: f003f303 ; <UNDEFINED> instruction: 0xf003f303

4768: 6153030f cmpvs r3, pc, lsl #6

476c: 68abe6e3 stmiavs fp!, {r0, r1, r5, r6, r7, r9, sl, sp, lr, pc}

4770: 3154f8c4 cmpcc r4, r4, asr #17 ; <UNPREDICTABLE>

4774: 696be6df stmdbvs fp!, {r0, r1, r2, r3, r4, r6, r7, r9, sl, sp, lr, pc}^

4778: f43f2b00 ; <UNDEFINED> instruction: 0xf43f2b00

477c: f8c4aedc ; <UNDEFINED> instruction: 0xf8c4aedc

4780: 69eb3238 stmibvs fp!, {r3, r4, r5, r9, ip, sp}^

4784: 323cf8c4 eorscc pc, ip, #196, 16 ; 0xc40000

4788: b10b68aa smlatblt fp, sl, r8, r6

478c: 40133b01 andsmi r3, r3, r1, lsl #22

4790: 1198f894 ; <UNDEFINED> instruction: 0x1198f894

4794: 3240f8c4 subcc pc, r0, #196, 16 ; 0xc40000

4798: f8c4692b ; <UNDEFINED> instruction: 0xf8c4692b

479c: f0012230 ; <UNDEFINED> instruction: 0xf0012230

47a0: 2a010203 bcs 44fb4 <\_r\_debug@@GLIBC\_2.4+0x1b658>

47a4: 3234f8c4 eorscc pc, r4, #196, 16 ; 0xc40000

47a8: 8202f040 andhi pc, r2, #64 ; 0x40

47ac: fb90f008 blx fe4407d6 <\_r\_debug@@GLIBC\_2.4+0xfe416e7a>

47b0: 0248f8c4 subeq pc, r8, #196, 16 ; 0xc40000

47b4: 68abe6bf stmiavs fp!, {r0, r1, r2, r3, r4, r5, r7, r9, sl, sp, lr, pc}

47b8: 3250f8c4 subscc pc, r0, #196, 16 ; 0xc40000

47bc: f8c4696b ; <UNDEFINED> instruction: 0xf8c4696b

47c0: e6b83254 ssat r3, #25, r4, asr #4

47c4: 61bb69ab ; <UNDEFINED> instruction: 0x61bb69ab

47c8: f8dfe6b5 ; <UNDEFINED> instruction: 0xf8dfe6b5

47cc: f8d707b8 ; <UNDEFINED> instruction: 0xf8d707b8

47d0: 6c3920f4 ldcvs 0, cr2, [r9], #-976 ; 0xfffffc30

47d4: f0074478 ; <UNDEFINED> instruction: 0xf0074478

47d8: e649facd strb pc, [r9], -sp, asr #21 ; <UNPREDICTABLE>

47dc: ebad6bbe bl feb5f6dc <\_r\_debug@@GLIBC\_2.4+0xfeb35d80>

47e0: f10d0d05 ; <UNDEFINED> instruction: 0xf10d0d05

47e4: 46710928 ldrbtmi r0, [r1], -r8, lsr #18

47e8: 46302200 ldrtmi r2, [r0], -r0, lsl #4

47ec: f926f00d ; <UNDEFINED> instruction: 0xf926f00d

47f0: 4649462a strbmi r4, [r9], -sl, lsr #12

47f4: f00d4630 ; <UNDEFINED> instruction: 0xf00d4630

47f8: 42a8fa55 adcmi pc, r8, #348160 ; 0x55000

47fc: 83adf040 ; <UNDEFINED> instruction: 0x83adf040

4800: 315cf8b4 ldrhcc pc, [ip, #-132] ; 0xffffff7c ; <UNPREDICTABLE>

4804: e669015d ; <UNDEFINED> instruction: 0xe669015d

4808: 377cf8df ; <UNDEFINED> instruction: 0x377cf8df

480c: f8d7447b ; <UNDEFINED> instruction: 0xf8d7447b

4810: 68bad004 ldmvs sl!, {r2, ip, lr, pc}

4814: 6c7ab19a ldfvsp f3, [sl], #-616 ; 0xfffffd98

4818: 6c3a9202 lfmvs f1, 1, [sl], #-8

481c: 6bb99301 blvs fee69428 <\_r\_debug@@GLIBC\_2.4+0xfee3facc>

4820: 69389400 ldmdbvs r8!, {sl, ip, pc}

4824: 30e0f8d7 ldrdcc pc, [r0], #135 ; 0x87 ; <UNPREDICTABLE>

4828: fd9af7ff ldc2 7, cr15, [sl, #1020] ; 0x3fc

482c: 275cf8df smmlscs ip, pc, r8, pc ; <UNPREDICTABLE>

4830: f8df2400 ; <UNDEFINED> instruction: 0xf8df2400

4834: 447a375c ldrbtmi r3, [sl], #-1884 ; 0xfffff8a4

4838: 6812447b ldmdavs r2, {r0, r1, r3, r4, r5, r6, sl, lr}

483c: 2200613a andcs r6, r0, #-2147483634 ; 0x8000000e

4840: e7e8647a ; <UNDEFINED> instruction: 0xe7e8647a

4844: 374cf8df ; <UNDEFINED> instruction: 0x374cf8df

4848: e7e0447b ; <UNDEFINED> instruction: 0xe7e0447b

484c: f8d768a3 ; <UNDEFINED> instruction: 0xf8d768a3

4850: 2b00d004 blcs 38868 <\_r\_debug@@GLIBC\_2.4+0xef0c>

4854: 8266f000 rsbhi pc, r6, #0

4858: 8000f8d4 ldrdhi pc, [r0], -r4

485c: 0208eb03 andeq lr, r8, #3072 ; 0xc00

4860: 2a0060a2 bcs 1caf0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xbd88>

4864: 8262f000 rsbhi pc, r2, #0

4868: 3008f853 andcc pc, r8, r3, asr r8 ; <UNPREDICTABLE>

486c: 0120f104 msreq CPSR\_, r4, lsl #2

4870: f64fb37b ; <UNDEFINED> instruction: 0xf64fb37b

4874: f64f5cff ; <UNDEFINED> instruction: 0xf64f5cff

4878: 26226eff ; <UNDEFINED> instruction: 0x26226eff

487c: 7cfff6c6 ldclvc 6, cr15, [pc], #792 ; 4b9c <free@plt+0x417c>

4880: 7efff6c6 cdpvc 6, 15, cr15, cr15, cr6, {6}

4884: 4510f06f ldrmi pc, [r0, #-111] ; 0xffffff91

4888: 0600f2c7 streq pc, [r0], -r7, asr #5

488c: 1af3e005 bne ffcfc8a8 <\_r\_debug@@GLIBC\_2.4+0xffcd2f4c>

4890: 2023f841 eorcs pc, r3, r1, asr #16

4894: 3f08f852 svccc 0x0008f852

4898: 2b22b1db blcs 8b100c <\_r\_debug@@GLIBC\_2.4+0x8876b0>

489c: 1ae8d9f8 bne ffa3b084 <\_r\_debug@@GLIBC\_2.4+0xffa11728>

48a0: d9f4280f ldmible r4!, {r0, r1, r2, r3, fp, sp}^

48a4: 001ef343 andseq pc, lr, r3, asr #6

48a8: 0f04f110 svceq 0x0004f110

48ac: eba1d904 bl fe87acc4 <\_r\_debug@@GLIBC\_2.4+0xfe851368>

48b0: f8c00080 ; <UNDEFINED> instruction: 0xf8c00080

48b4: e7ed20c8 strb r2, [sp, r8, asr #1]!

48b8: 0003ebac andeq lr, r3, ip, lsr #23

48bc: f200280b vadd.i8 d2, d0, d11

48c0: eba181b3 bl fe864f94 <\_r\_debug@@GLIBC\_2.4+0xfe83b638>

48c4: f1030383 ; <UNDEFINED> instruction: 0xf1030383

48c8: f2a34340 vqdmlal.s32 q2, d3, d0[0]

48cc: 601a732c andsvs r7, sl, ip, lsr #6

48d0: f1b8e7e0 ; <UNDEFINED> instruction: 0xf1b8e7e0

48d4: d02e0f00 eorle r0, lr, r0, lsl #30

48d8: b1136b23 tstlt r3, r3, lsr #22

48dc: 4442685a strbmi r6, [r2], #-2138 ; 0xfffff7a6

48e0: 6ae3605a bvs ff8dca50 <\_r\_debug@@GLIBC\_2.4+0xff8b30f4>

48e4: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

48e8: 605a4442 subsvs r4, sl, r2, asr #8

48ec: b1136b63 tstlt r3, r3, ror #22

48f0: 4442685a strbmi r6, [r2], #-2138 ; 0xfffff7a6

48f4: 6ba3605a blvs fe8dca64 <\_r\_debug@@GLIBC\_2.4+0xfe8b3108>

48f8: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

48fc: 605a4442 subsvs r4, sl, r2, asr #8

4900: b1136be3 tstlt r3, r3, ror #23

4904: 4442685a strbmi r6, [r2], #-2138 ; 0xfffff7a6

4908: 6e63605a mcrvs 0, 3, r6, cr3, cr10, {2}

490c: 685ab113 ldmdavs sl, {r0, r1, r4, r8, ip, sp, pc}^

4910: 605a4442 subsvs r4, sl, r2, asr #8

4914: b1136fe3 tstlt r3, r3, ror #31

4918: 4442685a strbmi r6, [r2], #-2138 ; 0xfffff7a6

491c: f8d4605a ; <UNDEFINED> instruction: 0xf8d4605a

4920: b11330e8 tstlt r3, r8, ror #1

4924: 4442685a strbmi r6, [r2], #-2138 ; 0xfffff7a6

4928: f8d4605a ; <UNDEFINED> instruction: 0xf8d4605a

492c: b1133150 tstlt r3, r0, asr r1

4930: 4442685a strbmi r6, [r2], #-2138 ; 0xfffff7a6

4934: 6f23605a svcvs 0x0023605a

4938: 685bb183 ldmdavs fp, {r0, r1, r7, r8, ip, sp, pc}^

493c: bf182b07 svclt 0x00182b07

4940: d00b2b11 andle r2, fp, r1, lsl fp

4944: 3650f8df ; <UNDEFINED> instruction: 0x3650f8df

4948: f8df227e ; <UNDEFINED> instruction: 0xf8df227e

494c: f8df1650 ; <UNDEFINED> instruction: 0xf8df1650

4950: 447b0650 ldrbtmi r0, [fp], #-1616 ; 0xfffff9b0

4954: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

4958: f818f00c ; <UNDEFINED> instruction: 0xf818f00c

495c: b17b6be3 cmnlt fp, r3, ror #23

4960: 685b6c63 ldmdavs fp, {r0, r1, r5, r6, sl, fp, sp, lr}^

4964: d00b2b0c andle r2, fp, ip, lsl #22

4968: 3638f8df ; <UNDEFINED> instruction: 0x3638f8df

496c: f8df2283 ; <UNDEFINED> instruction: 0xf8df2283

4970: f8df1638 ; <UNDEFINED> instruction: 0xf8df1638

4974: 447b0638 ldrbtmi r0, [fp], #-1592 ; 0xfffff9c8

4978: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

497c: f806f00c ; <UNDEFINED> instruction: 0xf806f00c

4980: b17b6e63 cmnlt fp, r3, ror #28

4984: 685b6ee3 ldmdavs fp, {r0, r1, r5, r6, r7, r9, sl, fp, sp, lr}^

4988: d00b2b08 andle r2, fp, r8, lsl #22

498c: 3620f8df ; <UNDEFINED> instruction: 0x3620f8df

4990: f8df2287 ; <UNDEFINED> instruction: 0xf8df2287

4994: f8df1620 ; <UNDEFINED> instruction: 0xf8df1620

4998: 447b0620 ldrbtmi r0, [fp], #-1568 ; 0xfffff9e0

499c: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

49a0: fff4f00b ; <UNDEFINED> instruction: 0xfff4f00b

49a4: 3098f8d4 ; <UNDEFINED> instruction: 0x3098f8d4

49a8: 685ab163 ldmdavs sl, {r0, r1, r5, r6, r8, ip, sp, pc}^

49ac: bf480790 svclt 0x00480790

49b0: f8c46623 ; <UNDEFINED> instruction: 0xf8c46623

49b4: 07512210 smmlaeq r1, r0, r2, r2

49b8: 67a3bf48 strvs fp, [r3, r8, asr #30]!

49bc: bf480716 svclt 0x00480716

49c0: 3080f8c4 addcc pc, r0, r4, asr #17

49c4: 30bcf8d4 ldrsbtcc pc, [ip], r4 ; <UNPREDICTABLE>

49c8: f0002b00 ; <UNDEFINED> instruction: 0xf0002b00

49cc: f8df8165 ; <UNDEFINED> instruction: 0xf8df8165

49d0: 685b25ec ldmdavs fp, {r2, r3, r5, r6, r7, r8, sl, sp}^

49d4: 6812447a ldmdavs r2, {r1, r3, r4, r5, r6, sl, lr}

49d8: 320cf8c4 andcc pc, ip, #196, 16 ; 0xc40000

49dc: f1000655 ; <UNDEFINED> instruction: 0xf1000655

49e0: 07d88235 ; <UNDEFINED> instruction: 0x07d88235

49e4: f8d4bf44 ; <UNDEFINED> instruction: 0xf8d4bf44

49e8: f8c420bc ; <UNDEFINED> instruction: 0xf8c420bc

49ec: f8d42080 ; <UNDEFINED> instruction: 0xf8d42080

49f0: b10a2094 swplt r2, r4, [sl]

49f4: 65e22200 strbvs r2, [r2, #512]! ; 0x200

49f8: 20ecf8d7 ldrdcs pc, [ip], #135 ; 0x87 ; <UNPREDICTABLE>

49fc: ea120fd2 b 48894c <\_r\_debug@@GLIBC\_2.4+0x45eff0>

4a00: f0401393 ; <UNDEFINED> instruction: 0xf0401393

4a04: f8d48289 ; <UNDEFINED> instruction: 0xf8d48289

4a08: 2b003154 blcs 10f60 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f8>

4a0c: 81a3f000 ; <UNDEFINED> instruction: 0x81a3f000

4a10: 44136822 ldrmi r6, [r3], #-2082 ; 0xfffff7de

4a14: 3154f8c4 cmpcc r4, r4, asr #17 ; <UNPREDICTABLE>

4a18: 25a4f8df strcs pc, [r4, #2271]! ; 0x8df

4a1c: 447a69b9 ldrbtmi r6, [sl], #-2489 ; 0xfffff647

4a20: 3804f8d2 stmdacc r4, {r1, r4, r6, r7, fp, ip, sp, lr, pc}

4a24: 0303ea21 movweq lr, #14881 ; 0x3a21

4a28: f10007d9 ; <UNDEFINED> instruction: 0xf10007d9

4a2c: f8d481ad ; <UNDEFINED> instruction: 0xf8d481ad

4a30: b11b3230 tstlt fp, r0, lsr r2

4a34: 44136822 ldrmi r6, [r3], #-2082 ; 0xfffff7de

4a38: 3230f8c4 eorscc pc, r0, #196, 16 ; 0xc40000

4a3c: f00d6bb8 ; <UNDEFINED> instruction: 0xf00d6bb8

4a40: 2800f85b stmdacs r0, {r0, r1, r3, r4, r6, fp, ip, sp, lr, pc}

4a44: 81bcf040 ; <UNDEFINED> instruction: 0x81bcf040

4a48: 2b02683b blcs 9eb3c <\_r\_debug@@GLIBC\_2.4+0x751e0>

4a4c: 8171f000 cmnhi r1, r0 ; <UNPREDICTABLE>

4a50: 3570f8df ldrbcc pc, [r0, #-2271]! ; 0xfffff721 ; <UNPREDICTABLE>

4a54: 0158f8d4 ldrsbeq pc, [r8, #-132] ; 0xffffff7c ; <UNPREDICTABLE>

4a58: 6825447b stmdavs r5!, {r0, r1, r3, r4, r5, r6, sl, lr}

4a5c: 4428681b strtmi r6, [r8], #-2075 ; 0xfffff7e5

4a60: 0158f8c4 cmpeq r8, r4, asr #17 ; <UNPREDICTABLE>

4a64: f100065e ; <UNDEFINED> instruction: 0xf100065e

4a68: 462081ae strtmi r8, [r0], -lr, lsr #3

4a6c: fc62f003 stc2l 0, cr15, [r2], #-12

4a70: 30ecf8d7 ldrdcc pc, [ip], #135 ; 0x87 ; <UNPREDICTABLE>

4a74: d4030719 strle r0, [r3], #-1817 ; 0xfffff8e7

4a78: 2b006e23 blcs 2030c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf5a4>

4a7c: 81f4f040 mvnshi pc, r0, asr #32

4a80: 320cf8d4 andcc pc, ip, #212, 16 ; 0xd40000

4a84: d504069a strle r0, [r4, #-1690] ; 0xfffff966

4a88: 353cf8df ldrcc pc, [ip, #-2271]! ; 0xfffff721

4a8c: f8c3447b ; <UNDEFINED> instruction: 0xf8c3447b

4a90: e9d74500 ldmib r7, {r8, sl, lr}^

4a94: e9c4230a stmib r4, {r1, r3, r8, r9, sp}^

4a98: e9d72378 ldmib r7, {r3, r4, r5, r6, r8, r9, sp}^

4a9c: e9c42308 stmib r4, {r3, r8, r9, sp}^

4aa0: 68fb237a ldmvs fp!, {r1, r3, r4, r5, r6, r8, r9, sp}^

4aa4: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

4aa8: f8df81b1 ; <UNDEFINED> instruction: 0xf8df81b1

4aac: 447b3520 ldrbtmi r3, [fp], #-1312 ; 0xfffffae0

4ab0: 3168f8d3 ldrdcc pc, [r8, #-131]! ; 0xffffff7d

4ab4: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

4ab8: f8d7819d ; <UNDEFINED> instruction: 0xf8d7819d

4abc: 462010f4 ; <UNDEFINED> instruction: 0x462010f4

4ac0: fc74f003 ldc2l 0, cr15, [r4], #-12

4ac4: 3508f8df strcc pc, [r8, #-2271] ; 0xfffff721

4ac8: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

4acc: 2a0021b0 bcs d194 <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE+0x70>

4ad0: acb6f43f cfldrsge mvf15, [r6], #252 ; 0xfc

4ad4: 224c69a0 subcs r6, ip, #160, 18 ; 0x280000

4ad8: 14f8f8df ldrbtne pc, [r8], #2271 ; 0x8df ; <UNPREDICTABLE>

4adc: f200fb02 vqdmulh.s<illegal width 8> d15, d0, d2

4ae0: 588a4479 stmpl sl, {r0, r3, r4, r5, r6, sl, lr}

4ae4: 2199f892 ; <UNDEFINED> instruction: 0x2199f892

4ae8: 0208f012 andeq pc, r8, #18

4aec: aca8f47f cfstrsge mvf15, [r8], #508 ; 0x1fc

4af0: 91acf8d3 ldrdls pc, [ip, r3]!

4af4: 7818f504 ldmdavc r8, {r2, r8, sl, ip, sp, lr, pc}

4af8: 461d4616 ; <UNDEFINED> instruction: 0x461d4616

4afc: 3008f8d9 ldrdcc pc, [r8], -r9

4b00: 4642b193 ; <UNDEFINED> instruction: 0x4642b193

4b04: 10f4f8d7 ldrsbtne pc, [r4], #135 ; 0x87 ; <UNPREDICTABLE>

4b08: 47984620 ldrmi r4, [r8, r0, lsr #12]

4b0c: 0004f8c8 andeq pc, r4, r8, asr #17

4b10: 3199f894 ; <UNDEFINED> instruction: 0x3199f894

4b14: 1200f3c3 andne pc, r0, #201326595 ; 0xc000003

4b18: bf182800 svclt 0x00182800

4b1c: 0201f042 andeq pc, r1, #66 ; 0x42

4b20: 1304f362 movwne pc, #17250 ; 0x4362 ; <UNPREDICTABLE>

4b24: 3199f884 orrscc pc, r9, r4, lsl #17

4b28: 31b0f8d5 asrscc pc, r5 ; <illegal shifter operand> ; <UNPREDICTABLE>

4b2c: f8d93601 ; <UNDEFINED> instruction: 0xf8d93601

4b30: f1089020 ; <UNDEFINED> instruction: 0xf1089020

4b34: 42b30808 adcsmi r0, r3, #8, 16 ; 0x80000

4b38: e481d8e0 str sp, [r1], #2272 ; 0x8e0

4b3c: 3498f8df ldrcc pc, [r8], #2271 ; 0x8df

4b40: 450ae9d7 strmi lr, [sl, #-2519] ; 0xfffff629

4b44: f503447b ; <UNDEFINED> instruction: 0xf503447b

4b48: e9d262df ldmib r2, {r0, r1, r2, r3, r4, r6, r7, r9, sp, lr}^

4b4c: 42a90100 adcmi r0, r9, #0, 2

4b50: 42a0bf08 adcmi fp, r0, #8, 30

4b54: f8dfd074 ; <UNDEFINED> instruction: 0xf8dfd074

4b58: 6c381484 cfldrsvs mvf1, [r8], #-528 ; 0xfffffdf0

4b5c: f5014479 ; <UNDEFINED> instruction: 0xf5014479

4b60: f00761a3 ; <UNDEFINED> instruction: 0xf00761a3

4b64: 2800f943 stmdacs r0, {r0, r1, r6, r8, fp, ip, sp, lr, pc}

4b68: ac74f43f cfldrdge mvd15, [r4], #-252 ; 0xffffff04

4b6c: 30f4f8d7 ldrsbtcc pc, [r4], #135 ; 0x87 ; <UNPREDICTABLE>

4b70: f8d79301 ; <UNDEFINED> instruction: 0xf8d79301

4b74: f8d720ec ; <UNDEFINED> instruction: 0xf8d720ec

4b78: 920030e4 andls r3, r0, #228 ; 0xe4

4b7c: 20e8f8d7 ldrdcs pc, [r8], #135 ; 0x87 ; <UNPREDICTABLE>

4b80: f8d76c39 ; <UNDEFINED> instruction: 0xf8d76c39

4b84: f00300e0 ; <UNDEFINED> instruction: 0xf00300e0

4b88: 4604fc4b strmi pc, [r4], -fp, asr #24

4b8c: f0002800 ; <UNDEFINED> instruction: 0xf0002800

4b90: f8df81d2 ; <UNDEFINED> instruction: 0xf8df81d2

4b94: 6bb8344c blvs fee11ccc <\_r\_debug@@GLIBC\_2.4+0xfede8370>

4b98: f503447b ; <UNDEFINED> instruction: 0xf503447b

4b9c: 616363a3 cmnvs r3, r3, lsr #7

4ba0: ffaaf00c ; <UNDEFINED> instruction: 0xffaaf00c

4ba4: 10f4f8d7 ldrsbtne pc, [r4], #135 ; 0x87 ; <UNPREDICTABLE>

4ba8: f0034620 ; <UNDEFINED> instruction: 0xf0034620

4bac: e447fbff strb pc, [r7], #-3071 ; 0xfffff401 ; <UNPREDICTABLE>

4bb0: 3430f8df ldrtcc pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

4bb4: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

4bb8: 2b003810 blcs 12c00 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e98>

4bbc: adf6f47f cfldrdge mvd15, [r6, #508]! ; 0x1fc

4bc0: 2b006923 blcs 1f054 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe2ec>

4bc4: acb7f43f cfldrsge mvf15, [r7], #252 ; 0xfc

4bc8: 30ecf8d7 ldrdcc pc, [ip], #135 ; 0x87 ; <UNPREDICTABLE>

4bcc: f53f011a ; <UNDEFINED> instruction: 0xf53f011a

4bd0: f8dfacb2 ; <UNDEFINED> instruction: 0xf8dfacb2

4bd4: f2403414 vqshl.s8 d19, d4, d0

4bd8: f8df4277 ; <UNDEFINED> instruction: 0xf8df4277

4bdc: f8df1410 ; <UNDEFINED> instruction: 0xf8df1410

4be0: 447b0410 ldrbtmi r0, [fp], #-1040 ; 0xfffffbf0

4be4: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

4be8: fed0f00b cdp2 0, 13, cr15, cr0, cr11, {0}

4bec: 00e0f8d7 ldrdeq pc, [r0], #135 ; 0x87 ; <UNPREDICTABLE>

4bf0: f7fb2400 ; <UNDEFINED> instruction: 0xf7fb2400

4bf4: 6bb8ef18 blvs fee4085c <\_r\_debug@@GLIBC\_2.4+0xfee16f00>

4bf8: ff7ef00c ; <UNDEFINED> instruction: 0xff7ef00c

4bfc: 4bfde420 blmi fff7dc84 <\_r\_debug@@GLIBC\_2.4+0xfff54328>

4c00: 4afd447b bmi fff55df4 <\_r\_debug@@GLIBC\_2.4+0xfff2c498>

4c04: 6812447a ldmdavs r2, {r1, r3, r4, r5, r6, sl, lr}

4c08: e602613a ; <UNDEFINED> instruction: 0xe602613a

4c0c: 0202f01b andeq pc, r2, #27

4c10: f000633a ; <UNDEFINED> instruction: 0xf000633a

4c14: eba68100 bl fe9a501c <\_r\_debug@@GLIBC\_2.4+0xfe97b6c0>

4c18: 46400208 strbmi r0, [r0], -r8, lsl #4

4c1c: 637b2100 cmnvs fp, #0, 2

4c20: ea16f00e b 5c0c60 <\_r\_debug@@GLIBC\_2.4+0x597304>

4c24: e5176b7b ldr r6, [r7, #-2939] ; 0xfffff485

4c28: 0003ebae andeq lr, r3, lr, lsr #23

4c2c: bf9f280a svclt 0x009f280a

4c30: 0383eba1 orreq lr, r3, #164864 ; 0x28400

4c34: 4340f103 movtmi pc, #259 ; 0x103 ; <UNPREDICTABLE>

4c38: 733ff5a3 teqvc pc, #683671552 ; 0x28c00000 ; <UNPREDICTABLE>

4c3c: e629601a ; <UNDEFINED> instruction: 0xe629601a

4c40: 63e0f503 mvnvs pc, #12582912 ; 0xc00000

4c44: 0108e9d7 ldrdeq lr, [r8, -r7]

4c48: 2300e9d3 movwcs lr, #2515 ; 0x9d3

4c4c: bf08428b svclt 0x0008428b

4c50: d1804282 orrle r4, r0, r2, lsl #5

4c54: f8d7e78a ; <UNDEFINED> instruction: 0xf8d7e78a

4c58: 009b30ec addseq r3, fp, ip, ror #1

4c5c: 814cf140 cmphi ip, r0, asr #2 ; <UNPREDICTABLE>

4c60: 22186cfb andscs r6, r8, #64256 ; 0xfb00

4c64: 465569f8 ; <UNDEFINED> instruction: 0x465569f8

4c68: f8943b01 ; <UNDEFINED> instruction: 0xf8943b01

4c6c: f8da1199 ; <UNDEFINED> instruction: 0xf8da1199

4c70: f0806000 ; <UNDEFINED> instruction: 0xf0806000

4c74: fb020001 blx 84c82 <\_r\_debug@@GLIBC\_2.4+0x5b326>

4c78: 6822a303 stmdavs r2!, {r0, r1, r8, r9, sp, pc}

4c7c: 1186f360 orrne pc, r6, r0, ror #6

4c80: f8c41990 ; <UNDEFINED> instruction: 0xf8c41990

4c84: 68db01b0 ldmvs fp, {r4, r5, r7, r8}^

4c88: 1199f884 orrsne pc, r9, r4, lsl #17

4c8c: 1b9b441a blne fe6d5cfc <\_r\_debug@@GLIBC\_2.4+0xfe6ac3a0>

4c90: 21b4f8c4 ; <UNDEFINED> instruction: 0x21b4f8c4

4c94: e4e364bb strbt r6, [r3], #1211 ; 0x4bb

4c98: 320cf8d4 andcc pc, ip, #212, 16 ; 0xd40000

4c9c: f8d7e6a7 ; <UNDEFINED> instruction: 0xf8d7e6a7

4ca0: 234c10f4 movtcs r1, #49396 ; 0xc0f4

4ca4: fb034ad5 blx d7802 <\_r\_debug@@GLIBC\_2.4+0xadea6>

4ca8: 447af301 ldrbtmi pc, [sl], #-769 ; 0xfffffcff ; <UNPREDICTABLE>

4cac: f89358d3 ; <UNDEFINED> instruction: 0xf89358d3

4cb0: 07112199 ; <UNDEFINED> instruction: 0x07112199

4cb4: ad01f53f cfstr32ge mvfx15, [r1, #-252] ; 0xffffff04

4cb8: 61acf8d5 ldrdvs pc, [ip, r5]!

4cbc: 7818f503 ldmdavc r8, {r0, r1, r8, sl, ip, sp, lr, pc}

4cc0: b11b6833 tstlt fp, r3, lsr r8

4cc4: eb082101 bl 20d0d0 <\_r\_debug@@GLIBC\_2.4+0x1e3774>

4cc8: 479800c4 ldrmi r0, [r8, r4, asr #1]

4ccc: 31b0f8d5 asrscc pc, r5 ; <illegal shifter operand> ; <UNPREDICTABLE>

4cd0: 6a363401 bvs d91cdc <\_r\_debug@@GLIBC\_2.4+0xd68380>

4cd4: d8f342a3 ldmle r3!, {r0, r1, r5, r7, r9, lr}^

4cd8: 6bf8e4ef blvs ffe3e09c <\_r\_debug@@GLIBC\_2.4+0xffe14740>

4cdc: 6a036929 bvs df188 <\_r\_debug@@GLIBC\_2.4+0xb582c>

4ce0: f63f4299 ; <UNDEFINED> instruction: 0xf63f4299

4ce4: 8e00aca1 cdphi 12, 0, cr10, cr0, cr1, {5}

4ce8: 682e686a stmdavs lr!, {r1, r3, r5, r6, fp, sp, lr}

4cec: eb03440a bl d5d1c <\_r\_debug@@GLIBC\_2.4+0xac3c0>

4cf0: 1b921040 blne fe488df8 <\_r\_debug@@GLIBC\_2.4+0xfe45f49c>

4cf4: bf224282 svclt 0x00224282

4cf8: 1a5b199b bne 16cb36c <\_r\_debug@@GLIBC\_2.4+0x16a1a10>

4cfc: 3154f8c4 cmpcc r4, r4, asr #17 ; <UNPREDICTABLE>

4d00: 1b99e492 blne fe67df50 <\_r\_debug@@GLIBC\_2.4+0xfe6545f4>

4d04: f04f4630 ; <UNDEFINED> instruction: 0xf04f4630

4d08: 465a33ff ; <UNDEFINED> instruction: 0x465a33ff

4d0c: e9cd2600 stmib sp, {r9, sl, sp}^

4d10: 23323600 teqcs r2, #0, 12

4d14: f82ef00d ; <UNDEFINED> instruction: 0xf82ef00d

4d18: f47f3001 ; <UNDEFINED> instruction: 0xf47f3001

4d1c: 4bb8aca0 blmi fee2ffa4 <\_r\_debug@@GLIBC\_2.4+0xfee06648>

4d20: e574447b ldrb r4, [r4, #-1147]! ; 0xfffffb85

4d24: 2b03683b blcs dee18 <\_r\_debug@@GLIBC\_2.4+0xb54bc>

4d28: 80e3f000 rschi pc, r3, r0

4d2c: 320cf8d4 andcc pc, ip, #212, 16 ; 0xd40000

4d30: f894e662 ; <UNDEFINED> instruction: 0xf894e662

4d34: 079b3198 ; <UNDEFINED> instruction: 0x079b3198

4d38: ae8af43f mcrge 4, 4, pc, cr10, cr15, {1} ; <UNPREDICTABLE>

4d3c: f2404bb1 vpadd.i8 d20, d16, d17

4d40: 49b1521f ldmibmi r1!, {r0, r1, r2, r3, r4, r9, ip, lr}

4d44: 447b48b1 ldrbtmi r4, [fp], #-2225 ; 0xfffff74f

4d48: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

4d4c: fe1ef00b cdp2 0, 1, cr15, cr14, cr11, {0}

4d50: 447b4baf ldrbtmi r4, [fp], #-2991 ; 0xfffff451

4d54: 6bfbe55b blvs ffefe2c8 <\_r\_debug@@GLIBC\_2.4+0xffed496c>

4d58: 016d8e1d cmneq sp, sp, lsl lr

4d5c: f7fb4628 ; <UNDEFINED> instruction: 0xf7fb4628

4d60: 2800ee36 stmdacs r0, {r1, r2, r4, r5, r9, sl, fp, sp, lr, pc}

4d64: 80fcf000 rscshi pc, ip, r0

4d68: 4649462a strbmi r4, [r9], -sl, lsr #12

4d6c: e9c8f00e stmib r8, {r1, r2, r3, ip, sp, lr, pc}^

4d70: 3198f894 ; <UNDEFINED> instruction: 0x3198f894

4d74: 0380f043 orreq pc, r0, #67 ; 0x43

4d78: 3198f884 orrscc pc, r8, r4, lsl #17

4d7c: 0154f8c4 cmpeq r4, r4, asr #17 ; <UNPREDICTABLE>

4d80: 4ba4e64a blmi fe93e6b0 <\_r\_debug@@GLIBC\_2.4+0xfe914d54>

4d84: e542447b strb r4, [r2, #-1147] ; 0xfffffb85

4d88: 30ecf8d7 ldrdcc pc, [ip], #135 ; 0x87 ; <UNPREDICTABLE>

4d8c: 4308f003 movwmi pc, #32771 ; 0x8003 ; <UNPREDICTABLE>

4d90: 4f00f1b3 svcmi 0x0000f1b3

4d94: 8085f000 addhi pc, r5, r0

4d98: 447a4a9f ldrbtmi r4, [sl], #-2719 ; 0xfffff561

4d9c: f0436813 ; <UNDEFINED> instruction: 0xf0436813

4da0: 60130307 andsvs r0, r3, r7, lsl #6

4da4: f8d74b9d ; <UNDEFINED> instruction: 0xf8d74b9d

4da8: 447b00f0 ldrbtmi r0, [fp], #-240 ; 0xffffff10

4dac: 3800f8d3 stmdacc r0, {r0, r1, r4, r6, r7, fp, ip, sp, lr, pc}

4db0: 61384798 teqvs r8, r8 ; <illegal shifter operand>

4db4: f43f2800 ; <UNDEFINED> instruction: 0xf43f2800

4db8: 4b99ae3a blmi fe6706a8 <\_r\_debug@@GLIBC\_2.4+0xfe646d4c>

4dbc: e528447b str r4, [r8, #-1147]! ; 0xfffffb85

4dc0: 447b4b98 ldrbtmi r4, [fp], #-2968 ; 0xfffff468

4dc4: f8b4e71d ; <UNDEFINED> instruction: 0xf8b4e71d

4dc8: 2608115c ; <UNDEFINED> instruction: 0x2608115c

4dcc: 463368a2 ldrtmi r6, [r3], -r2, lsr #17

4dd0: 6107e9cd smlabtvs r7, sp, r9, lr

4dd4: 1154f8d4 ldrsbne pc, [r4, #-132] ; 0xffffff7c ; <UNPREDICTABLE>

4dd8: 96039605 strls r9, [r3], -r5, lsl #12

4ddc: 46319106 ldrtmi r9, [r1], -r6, lsl #2

4de0: 90049601 andls r9, r4, r1, lsl #12

4de4: 95006cb8 strls r6, [r0, #-3256] ; 0xfffff348

4de8: 488f9002 stmmi pc, {r1, ip, pc} ; <UNPREDICTABLE>

4dec: f0064478 ; <UNDEFINED> instruction: 0xf0064478

4df0: e63affc1 ldrt pc, [sl], -r1, asr #31 ; <UNPREDICTABLE>

4df4: 2b006da3 blcs 20488 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf720>

4df8: ae5ff43f mrcge 4, 2, APSR\_nzcv, cr15, cr15, {1}

4dfc: 685b6b62 ldmdavs fp, {r1, r5, r6, r8, r9, fp, sp, lr}^

4e00: 685169e0 ldmdavs r1, {r5, r6, r7, r8, fp, sp, lr}^

4e04: f7ff4419 ; <UNDEFINED> instruction: 0xf7ff4419

4e08: e656fa63 ldrb pc, [r6], -r3, ror #20 ; <UNPREDICTABLE>

4e0c: 69e04619 stmibvs r0!, {r0, r3, r4, r9, sl, lr}^

4e10: fa5ef7ff blx 17c2e14 <\_r\_debug@@GLIBC\_2.4+0x17994b8>

4e14: ea08e649 b 23e740 <\_r\_debug@@GLIBC\_2.4+0x214de4>

4e18: f04b0000 ; <UNDEFINED> instruction: 0xf04b0000

4e1c: 61fb0202 mvnsvs r0, r2, lsl #4

4e20: ffe6f00c ; <UNDEFINED> instruction: 0xffe6f00c

4e24: 280069fb stmdacs r0, {r0, r1, r3, r4, r5, r6, r7, r8, fp, sp, lr}

4e28: 6b39dbab blvs e7bcdc <\_r\_debug@@GLIBC\_2.4+0xe52380>

4e2c: 0208eba6 andeq lr, r8, #169984 ; 0x29800

4e30: 61fb4640 mvnsvs r4, r0, asr #12

4e34: e90cf00e stmdb ip, {r1, r2, r3, ip, sp, lr, pc}

4e38: 465a6b7b ; <UNDEFINED> instruction: 0x465a6b7b

4e3c: 42486919 submi r6, r8, #409600 ; 0x64000

4e40: 0008ea00 andeq lr, r8, r0, lsl #20

4e44: ffd4f00c ; <UNDEFINED> instruction: 0xffd4f00c

4e48: e40569fb str r6, [r5], #-2555 ; 0xfffff605

4e4c: 610ef423 tstvs lr, r3, lsr #8 ; <UNPREDICTABLE>

4e50: 0109f021 tsteq r9, r1, lsr #32 ; <UNPREDICTABLE>

4e54: f43f2900 ; <UNDEFINED> instruction: 0xf43f2900

4e58: 4874adc4 ldmdami r4!, {r2, r6, r7, r8, sl, fp, sp, pc}^

4e5c: f0064478 ; <UNDEFINED> instruction: 0xf0064478

4e60: f8d4ff89 ; <UNDEFINED> instruction: 0xf8d4ff89

4e64: e5bc320c ldr r3, [ip, #524]! ; 0x20c

4e68: 11d0f8d4 ldrsbne pc, [r0, #132] ; 0x84 ; <UNPREDICTABLE>

4e6c: 73b0f504 movsvc pc, #4, 10 ; 0x1000000

4e70: 429a680a addsmi r6, sl, #655360 ; 0xa0000

4e74: ae04f43f cfmvdhrge mvd4, pc

4e78: 21ccf8d4 ldrdcs pc, [ip, #132] ; 0x84

4e7c: f8d42301 ; <UNDEFINED> instruction: 0xf8d42301

4e80: f1020168 ; <UNDEFINED> instruction: 0xf1020168

4e84: 3a014280 bcc 5588c <\_r\_debug@@GLIBC\_2.4+0x2bf30>

4e88: 1d086004 stcne 0, cr6, [r8, #-16]

4e8c: f8c40092 ; <UNDEFINED> instruction: 0xf8c40092

4e90: f00d316c ; <UNDEFINED> instruction: 0xf00d316c

4e94: f8d4ef3e ; <UNDEFINED> instruction: 0xf8d4ef3e

4e98: f50431d0 ; <UNDEFINED> instruction: 0xf50431d0

4e9c: 601a72b4 ; <UNDEFINED> instruction: 0x601a72b4

4ea0: 4963e5ee stmdbmi r3!, {r1, r2, r3, r5, r6, r7, r8, sl, sp, lr, pc}^

4ea4: 5768f8d2 ; <UNDEFINED> instruction: 0x5768f8d2

4ea8: f8d24479 ; <UNDEFINED> instruction: 0xf8d24479

4eac: f8d23518 ; <UNDEFINED> instruction: 0xf8d23518

4eb0: 6909076c stmdbvs r9, {r2, r3, r5, r6, r8, r9, sl}

4eb4: 4e5f442b cdpmi 4, 5, cr4, cr15, cr11, {1}

4eb8: 424a4403 submi r4, sl, #50331648 ; 0x3000000

4ebc: 4013447e andsmi r4, r3, lr, ror r4

4ec0: 0806ea02 stmdaeq r6, {r1, r9, fp, sp, lr, pc}

4ec4: 429a1d32 addsmi r1, sl, #3200 ; 0xc80

4ec8: 0508eba2 streq lr, [r8, #-2978] ; 0xfffff45e

4ecc: af64f63f svcge 0x0064f63f

4ed0: 46292203 strtmi r2, [r9], -r3, lsl #4

4ed4: f00c4640 ; <UNDEFINED> instruction: 0xf00c4640

4ed8: 2800ff8b stmdacs r0, {r0, r1, r3, r7, r8, r9, sl, fp, ip, sp, lr, pc}

4edc: 6833db19 ldmdavs r3!, {r0, r3, r4, r8, r9, fp, ip, lr, pc}

4ee0: 46404629 strbmi r4, [r0], -r9, lsr #12

4ee4: f0432201 ; <UNDEFINED> instruction: 0xf0432201

4ee8: 60330307 eorsvs r0, r3, r7, lsl #6

4eec: ff80f00c ; <UNDEFINED> instruction: 0xff80f00c

4ef0: 4b51e758 blmi 147ec58 <\_r\_debug@@GLIBC\_2.4+0x14552fc>

4ef4: e48c447b str r4, [ip], #1147 ; 0x47b

4ef8: 447b4b50 ldrbtmi r4, [fp], #-2896 ; 0xfffff4b0

4efc: 4b50e487 blmi 143e120 <\_r\_debug@@GLIBC\_2.4+0x14147c4>

4f00: 32e6f240 rsccc pc, r6, #64, 4

4f04: 4850494f ldmdami r0, {r0, r1, r2, r3, r6, r8, fp, lr}^

4f08: 4479447b ldrbtmi r4, [r9], #-1147 ; 0xfffffb85

4f0c: f00b4478 ; <UNDEFINED> instruction: 0xf00b4478

4f10: 4b4efd3d blmi 13c440c <\_r\_debug@@GLIBC\_2.4+0x139aab0>

4f14: e674447b ; <UNDEFINED> instruction: 0xe674447b

4f18: 016ce9d4 ldrdeq lr, [ip, #-148]! ; 0xffffff6c

4f1c: f00c1a09 ; <UNDEFINED> instruction: 0xf00c1a09

4f20: 69e0ff4f stmibvs r0!, {r0, r1, r2, r3, r6, r8, r9, sl, fp, ip, sp, lr, pc}^

4f24: b1ab6883 ; <UNDEFINED> instruction: 0xb1ab6883

4f28: 3198f994 ; <UNDEFINED> instruction: 0x3198f994

4f2c: db0a2b00 blle 28fb34 <\_r\_debug@@GLIBC\_2.4+0x2661d8>

4f30: 447b4b47 ldrbtmi r4, [fp], #-2887 ; 0xfffff4b9

4f34: 4a47e46d bmi 11fe0f0 <\_r\_debug@@GLIBC\_2.4+0x11d4794>

4f38: 447a4b47 ldrbtmi r4, [sl], #-2887 ; 0xfffff4b9

4f3c: 447b6478 ldrbtmi r6, [fp], #-1144 ; 0xfffffb88

4f40: 613a6812 teqvs sl, r2, lsl r8

4f44: f8d4e467 ; <UNDEFINED> instruction: 0xf8d4e467

4f48: f7fb0154 ; <UNDEFINED> instruction: 0xf7fb0154

4f4c: 4b43ed6c blmi 1100504 <\_r\_debug@@GLIBC\_2.4+0x10d6ba8>

4f50: e45e447b ldrb r4, [lr], #-1147 ; 0xfffffb85

4f54: ed66f7fb stcl 7, cr15, [r6, #-1004]! ; 0xfffffc14

4f58: 4b41e7e6 blmi 107eef8 <\_r\_debug@@GLIBC\_2.4+0x105559c>

4f5c: e650447b ; <UNDEFINED> instruction: 0xe650447b

4f60: 447b4b40 ldrbtmi r4, [fp], #-2880 ; 0xfffff4c0

4f64: bf00e64d svclt 0x0000e64d

4f68: 00024c72 andeq r4, r2, r2, ror ip

4f6c: 00024506 andeq r4, r2, r6, lsl #10

4f70: 000243ce andeq r4, r2, lr, asr #7

4f74: 00024338 andeq r4, r2, r8, lsr r3

4f78: 00011392 muleq r1, r2, r3

4f7c: 000242be ; <UNDEFINED> instruction: 0x000242be

4f80: 0002429e muleq r2, lr, r2

4f84: 000112a4 andeq r1, r1, r4, lsr #5

4f88: 000111f8 strdeq r1, [r1], -r8

4f8c: 0002511e andeq r5, r2, lr, lsl r1

4f90: 0001112c andeq r1, r1, ip, lsr #2

4f94: 00011138 andeq r1, r1, r8, lsr r1

4f98: 00010e4e andeq r0, r1, lr, asr #28

4f9c: 0000ff78 andeq pc, r0, r8, ror pc ; <UNPREDICTABLE>

4fa0: 00010902 andeq r0, r1, r2, lsl #18

4fa4: 00010e2a andeq r0, r1, sl, lsr #28

4fa8: 0000ff54 andeq pc, r0, r4, asr pc ; <UNPREDICTABLE>

4fac: 0001092e andeq r0, r1, lr, lsr #18

4fb0: 00010e06 andeq r0, r1, r6, lsl #28

4fb4: 0000ff30 andeq pc, r0, r0, lsr pc ; <UNPREDICTABLE>

4fb8: 0000ff6a andeq pc, r0, sl, ror #30

4fbc: 00023f98 muleq r2, r8, pc ; <UNPREDICTABLE>

4fc0: 0002463e andeq r4, r2, lr, lsr r6

4fc4: 00023f14 andeq r3, r2, r4, lsl pc

4fc8: 000245d0 ldrdeq r4, [r2], -r0

4fcc: 00023ebe ; <UNDEFINED> instruction: 0x00023ebe

4fd0: 00023ea4 andeq r3, r2, r4, lsr #29

4fd4: 0002457c andeq r4, r2, ip, ror r5

4fd8: 00024518 andeq r4, r2, r8, lsl r5

4fdc: 00024500 andeq r4, r2, r0, lsl #10

4fe0: 000244c4 andeq r4, r2, r4, asr #9

4fe4: 000244a8 andeq r4, r2, r8, lsr #9

4fe8: 0001148e andeq r1, r1, lr, lsl #9

4fec: 00010c30 andeq r0, r1, r0, lsr ip

4ff0: 00010ed2 ldrdeq r0, [r1], -r2

4ff4: 00010c54 andeq r0, r1, r4, asr ip

4ff8: 00024d50 andeq r4, r2, r0, asr sp

4ffc: 000243b2 ; <UNDEFINED> instruction: 0x000243b2

5000: 00010d3c andeq r0, r1, ip, lsr sp

5004: 0001132a andeq r1, r1, sl, lsr #6

5008: 00010acc andeq r0, r1, ip, asr #21

500c: 00010d9e muleq r1, lr, sp

5010: 00010c66 andeq r0, r1, r6, ror #24

5014: 00010aac andeq r0, r1, ip, lsr #21

5018: 00023d8a andeq r3, r2, sl, lsl #27

501c: 000242b2 ; <UNDEFINED> instruction: 0x000242b2

5020: 00010b6c andeq r0, r1, ip, ror #22

5024: 00010afe strdeq r0, [r1], -lr

5028: 00010d2c andeq r0, r1, ip, lsr #26

502c: 00010480 andeq r0, r1, r0, lsl #9

5030: 00023ac4 andeq r3, r2, r4, asr #21

5034: 00023c68 andeq r3, r2, r8, ror #24

5038: 000109ec andeq r0, r1, ip, ror #19

503c: 00010ae6 andeq r0, r1, r6, ror #21

5040: 00011168 andeq r1, r1, r8, ror #2

5044: 0001090a andeq r0, r1, sl, lsl #18

5048: 00010b94 muleq r1, r4, fp

504c: 0001091c andeq r0, r1, ip, lsl r9

5050: 000109d2 ldrdeq r0, [r1], -r2

5054: 00024a1a andeq r4, r2, sl, lsl sl

5058: 00010916 andeq r0, r1, r6, lsl r9

505c: 000109b4 ; <UNDEFINED> instruction: 0x000109b4

5060: 00010920 andeq r0, r1, r0, lsr #18

5064: 00010932 andeq r0, r1, r2, lsr r9

5068: 4ff0e92d svcmi 0x00f0e92d

506c: 8dcbb08b stclhi 0, cr11, [fp, #556] ; 0x22c

5070: 8e0caf00 cdphi 15, 0, cr10, cr12, cr0, {0}

5074: 62794606 rsbsvs r4, r9, #6291456 ; 0x600000

5078: 2200617a andcs r6, r0, #-2147483618 ; 0x8000001e

507c: f403fb04 ; <UNDEFINED> instruction: 0xf403fb04

5080: 62386a09 eorsvs r6, r8, #36864 ; 0x9000

5084: f0231de3 ; <UNDEFINED> instruction: 0xf0231de3

5088: ebad0307 bl feb45cac <\_r\_debug@@GLIBC\_2.4+0xfeb1c350>

508c: f00c0d03 ; <UNDEFINED> instruction: 0xf00c0d03

5090: 4622fcd5 ; <UNDEFINED> instruction: 0x4622fcd5

5094: 4630466d ldrtmi r4, [r0], -sp, ror #12

5098: f00c4629 ; <UNDEFINED> instruction: 0xf00c4629

509c: 4284fcbb addmi pc, r4, #47872 ; 0xbb00

50a0: 80e7f040 rschi pc, r7, r0, asr #32

50a4: 8e196a7b ; <UNDEFINED> instruction: 0x8e196a7b

50a8: d0332900 eorsle r2, r3, r0, lsl #18

50ac: f04f4b75 ; <UNDEFINED> instruction: 0xf04f4b75

50b0: 447b0a00 ldrbtmi r0, [fp], #-2560 ; 0xfffff600

50b4: 4b7461bb blmi 1d1d7a8 <\_r\_debug@@GLIBC\_2.4+0x1cf3e4c>

50b8: 60fb447b rscsvs r4, fp, fp, ror r4

50bc: f10ae004 ; <UNDEFINED> instruction: 0xf10ae004

50c0: 35280a01 strcc r0, [r8, #-2561]! ; 0xfffff5ff

50c4: d225458a eorle r4, r5, #578813952 ; 0x22800000

50c8: 2303686a movwcs r6, #14442 ; 0x386a

50cc: 0300f2c7 movweq pc, #711 ; 0x2c7 ; <UNPREDICTABLE>

50d0: d1f4429a ; <UNDEFINED> instruction: 0xd1f4429a

50d4: 2200696b andcs r6, r0, #1753088 ; 0x1ac000

50d8: 33076a3e movwcc r6, #31294 ; 0x7a3e

50dc: f0236929 ; <UNDEFINED> instruction: 0xf0236929

50e0: ebad0307 bl feb45d04 <\_r\_debug@@GLIBC\_2.4+0xfeb1c3a8>

50e4: 46300d03 ldrtmi r0, [r0], -r3, lsl #26

50e8: fca8f00c stc2 0, cr15, [r8], #48 ; 0x30

50ec: 466c696a strbtmi r6, [ip], -sl, ror #18

50f0: 46214630 ; <UNDEFINED> instruction: 0x46214630

50f4: fc8ef00c stc2 0, cr15, [lr], {12}

50f8: 4298696b addsmi r6, r8, #1753088 ; 0x1ac000

50fc: 80b9f040 adcshi pc, r9, r0, asr #32

5100: 2b417823 blcs 1063194 <\_r\_debug@@GLIBC\_2.4+0x1039838>

5104: 6a7bd00d bvs 1ef9140 <\_r\_debug@@GLIBC\_2.4+0x1ecf7e4>

5108: 0a01f10a beq 81538 <\_r\_debug@@GLIBC\_2.4+0x57bdc>

510c: 8e193528 cfmul64hi mvdx3, mvdx9, mvdx8

5110: d3d9458a bicsle r4, r9, #578813952 ; 0x22800000

5114: 61fb2300 mvnsvs r2, r0, lsl #6

5118: 372c69f8 ; <UNDEFINED> instruction: 0x372c69f8

511c: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

5120: 1e438ff0 mcrne 15, 2, r8, cr3, cr0, {7}

5124: 0401f104 streq pc, [r1], #-260 ; 0xfffffefc

5128: 4a58d0ed bmi 16394e4 <\_r\_debug@@GLIBC\_2.4+0x160fb88>

512c: f8c74698 ; <UNDEFINED> instruction: 0xf8c74698

5130: f8d7a000 ; <UNDEFINED> instruction: 0xf8d7a000

5134: 447aa00c ldrbtmi sl, [sl], #-12

5138: 4a5560ba bmi 155d428 <\_r\_debug@@GLIBC\_2.4+0x1533acc>

513c: 447a607d ldrbtmi r6, [sl], #-125 ; 0xffffff83

5140: 6825613a stmdavs r5!, {r1, r3, r4, r5, r8, sp, lr}

5144: bf984545 svclt 0x00984545

5148: 0805eba8 stmdaeq r5, {r3, r5, r7, r8, r9, fp, sp, lr, pc}

514c: 8088f200 addhi pc, r8, r0, lsl #4

5150: 69391d26 ldmdbvs r9!, {r1, r2, r5, r8, sl, fp, ip}

5154: f00c4630 ; <UNDEFINED> instruction: 0xf00c4630

5158: 61f8fffb ldrshvs pc, [r8, #251]! ; 0xfb ; <UNPREDICTABLE>

515c: d1742800 cmnle r4, r0, lsl #16

5160: 3d044630 stccc 6, cr4, [r4, #-192] ; 0xffffff40

5164: faecf00d blx ffb411a0 <\_r\_debug@@GLIBC\_2.4+0xffb17844>

5168: 1a2d3001 bne b51174 <\_r\_debug@@GLIBC\_2.4+0xb27818>

516c: 0400eb06 streq lr, [r0], #-2822 ; 0xfffff4fa

5170: f8d4d013 ; <UNDEFINED> instruction: 0xf8d4d013

5174: f8942001 ; <UNDEFINED> instruction: 0xf8942001

5178: 42aab000 adcmi fp, sl, #0

517c: 1aadbf98 bne feb74fe4 <\_r\_debug@@GLIBC\_2.4+0xfeb4b688>

5180: f1bbd85b ; <UNDEFINED> instruction: 0xf1bbd85b

5184: eb040f01 bl 108d90 <\_r\_debug@@GLIBC\_2.4+0xdf434>

5188: d00c0602 andle r0, ip, r2, lsl #12

518c: 46504659 ; <UNDEFINED> instruction: 0x46504659

5190: f0064634 ; <UNDEFINED> instruction: 0xf0064634

5194: 2d00fe05 stccs 14, cr15, [r0, #-20] ; 0xffffffec

5198: f1b8d1eb ; <UNDEFINED> instruction: 0xf1b8d1eb

519c: d1d00f00 bicsle r0, r0, r0, lsl #30

51a0: a500e9d7 strge lr, [r0, #-2519] ; 0xfffff629

51a4: 3405e7af strcc lr, [r5], #-1967 ; 0xfffff851

51a8: bf3842b4 svclt 0x003842b4

51ac: 0900f04f stmdbeq r0, {r0, r1, r2, r3, r6, ip, sp, lr, pc}

51b0: f04fd2f1 ; <UNDEFINED> instruction: 0xf04fd2f1

51b4: 465a0b00 ldrbmi r0, [sl], -r0, lsl #22

51b8: 1b01f814 blne 83210 <\_r\_debug@@GLIBC\_2.4+0x598b4>

51bc: 037ff001 cmneq pc, #1 ; <UNPREDICTABLE>

51c0: fa030609 blx c69ec <\_r\_debug@@GLIBC\_2.4+0x9d090>

51c4: f102f302 ; <UNDEFINED> instruction: 0xf102f302

51c8: ea4b0207 b 12c59ec <\_r\_debug@@GLIBC\_2.4+0x129c090>

51cc: d4f30b03 ldrbtle r0, [r3], #2819 ; 0xb03

51d0: 0304f1ab movweq pc, #16811 ; 0x41ab ; <UNPREDICTABLE>

51d4: 0f43f1bb svceq 0x0043f1bb

51d8: 2b01bf18 blcs 74e40 <\_r\_debug@@GLIBC\_2.4+0x4b4e4>

51dc: 2301bf94 movwcs fp, #8084 ; 0x1f94

51e0: d8102300 ldmdale r0, {r8, r9, sp}

51e4: f00d4620 ; <UNDEFINED> instruction: 0xf00d4620

51e8: f1b9faab ; <UNDEFINED> instruction: 0xf1b9faab

51ec: bf080f01 svclt 0x00080f01

51f0: 0f1cf1bb svceq 0x001cf1bb

51f4: 0001f100 andeq pc, r1, r0, lsl #2

51f8: d0164404 andsle r4, r6, r4, lsl #8

51fc: d8d842a6 ldmle r8, {r1, r2, r5, r7, r9, lr}^

5200: d1b62d00 ; <UNDEFINED> instruction: 0xd1b62d00

5204: 4699e7c9 ldrmi lr, [r9], r9, asr #15

5208: f814461a ; <UNDEFINED> instruction: 0xf814461a

520c: f0011b01 ; <UNDEFINED> instruction: 0xf0011b01

5210: 4093037f addsmi r0, r3, pc, ror r3

5214: ea493207 b 1251a38 <\_r\_debug@@GLIBC\_2.4+0x12280dc>

5218: 060b0903 streq r0, [fp], -r3, lsl #18

521c: f1b9d4f5 ; <UNDEFINED> instruction: 0xf1b9d4f5

5220: bf080f01 svclt 0x00080f01

5224: 0f1cf1bb svceq 0x001cf1bb

5228: 697ad1e8 ldmdbvs sl!, {r3, r5, r6, r7, r8, ip, lr, pc}^

522c: 69f82301 ldmibvs r8!, {r0, r8, r9, sp}^

5230: 7013372c andsvc r3, r3, ip, lsr #14

5234: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

5238: 46118ff0 ssub8mi r8, r1, r0

523c: 462a69b8 ; <UNDEFINED> instruction: 0x462a69b8

5240: fdaef006 stc2 0, cr15, [lr, #24]!

5244: 2500462a strcs r4, [r0, #-1578] ; 0xfffff9d6

5248: 4812e79b ldmdami r2, {r0, r1, r3, r4, r7, r8, r9, sl, sp, lr, pc}

524c: 442c4631 strtmi r4, [ip], #-1585 ; 0xfffff9cf

5250: f0064478 ; <UNDEFINED> instruction: 0xf0064478

5254: f1b8fda5 ; <UNDEFINED> instruction: 0xf1b8fda5

5258: f47f0f00 ; <UNDEFINED> instruction: 0xf47f0f00

525c: e79faf72 ; <UNDEFINED> instruction: 0xe79faf72

5260: 46424629 strbmi r4, [r2], -r9, lsr #12

5264: 464568b8 ; <UNDEFINED> instruction: 0x464568b8

5268: fd9af006 ldc2 0, cr15, [sl, #24]

526c: 0800f04f stmdaeq r0, {r0, r1, r2, r3, r6, ip, sp, lr, pc}

5270: f04fe76e ; <UNDEFINED> instruction: 0xf04fe76e

5274: 61fb33ff ldrshvs r3, [fp, #63]! ; 0x3f

5278: 372c69f8 ; <UNDEFINED> instruction: 0x372c69f8

527c: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

5280: bf008ff0 svclt 0x00008ff0

5284: 00010b2a andeq r0, r1, sl, lsr #22

5288: 00010b58 andeq r0, r1, r8, asr fp

528c: 00010a46 andeq r0, r1, r6, asr #20

5290: 00010a6e andeq r0, r1, lr, ror #20

5294: 00010964 andeq r0, r1, r4, ror #18

5298: c49cf8df ldrgt pc, [ip], #2271 ; 0x8df

529c: 4ff0e92d svcmi 0x00f0e92d

52a0: b09144fc ; <UNDEFINED> instruction: 0xb09144fc

52a4: 460eaf04 strmi sl, [lr], -r4, lsl #30

52a8: 11b0f8dc asrsne pc, ip ; <illegal shifter operand> ; <UNPREDICTABLE>

52ac: f8976238 ; <UNDEFINED> instruction: 0xf8976238

52b0: 29000060 stmdbcs r0, {r5, r6}

52b4: 2b00bf18 blcs 34f1c <\_r\_debug@@GLIBC\_2.4+0xb5c0>

52b8: f0406178 ; <UNDEFINED> instruction: 0xf0406178

52bc: f44f81c1 vst4.<illegal width 64> {d24,d26,d28,d30}, [pc], r1

52c0: 6a382100 bvs e0d6c8 <\_r\_debug@@GLIBC\_2.4+0xde3d6c>

52c4: fc9af00c ldc2 0, cr15, [sl], {12}

52c8: 3ffff1b0 svccc 0x00fff1b0

52cc: f0004680 ; <UNDEFINED> instruction: 0xf0004680

52d0: f8df8119 ; <UNDEFINED> instruction: 0xf8df8119

52d4: 23002468 movwcs r2, #1128 ; 0x468

52d8: 461946b3 ; <UNDEFINED> instruction: 0x461946b3

52dc: 6013447a andsvs r4, r3, sl, ror r4

52e0: 3b04f84b blcc 143414 <\_r\_debug@@GLIBC\_2.4+0x119ab8>

52e4: 7200f5c1 andvc pc, r0, #809500672 ; 0x30400000

52e8: 44594640 ldrbmi r4, [r9], #-1600 ; 0xfffff9c0

52ec: fcdaf00c ldc2l 0, cr15, [sl], {12}

52f0: 28006835 stmdacs r0, {r0, r2, r4, r5, fp, sp, lr}

52f4: 1941dd04 stmdbne r1, {r2, r8, sl, fp, ip, lr, pc}^

52f8: 29336031 ldmdbcs r3!, {r0, r4, r5, sp, lr}

52fc: 460dd9f2 ; <UNDEFINED> instruction: 0x460dd9f2

5300: f3402d33 vmul.f32 d18, d0, d19

5304: f8df816b ; <UNDEFINED> instruction: 0xf8df816b

5308: 22084438 andcs r4, r8, #56, 8 ; 0x38000000

530c: 447c4658 ldrbtmi r4, [ip], #-1624 ; 0xfffff9a8

5310: 0158f104 cmpeq r8, r4, lsl #2 ; <UNPREDICTABLE>

5314: fb92f00d blx fe4c1352 <\_r\_debug@@GLIBC\_2.4+0xfe4979f6>

5318: f0402800 ; <UNDEFINED> instruction: 0xf0402800

531c: 6ab3817e bvs fece591c <\_r\_debug@@GLIBC\_2.4+0xfecbbfc0>

5320: 7200f44f andvc pc, r0, #1325400064 ; 0x4f000000

5324: 5200f2c0 andpl pc, r0, #192, 4

5328: 23fff003 mvnscs pc, #3

532c: 437df423 cmnmi sp, #587202560 ; 0x23000000 ; <UNPREDICTABLE>

5330: f0004293 ; <UNDEFINED> instruction: 0xf0004293

5334: 7b3380f4 blvc ce570c <\_r\_debug@@GLIBC\_2.4+0xcbbdb0>

5338: f0402b00 ; <UNDEFINED> instruction: 0xf0402b00

533c: f8df80ea ; <UNDEFINED> instruction: 0xf8df80ea

5340: 22071404 andcs r1, r7, #4, 8 ; 0x4000000

5344: 000df106 andeq pc, sp, r6, lsl #2

5348: 31614479 smccc 5193 ; 0x1449

534c: fb76f00d blx 1dc138a <\_r\_debug@@GLIBC\_2.4+0x1d97a2e>

5350: 280060f8 stmdacs r0, {r3, r4, r5, r6, r7, sp, lr}

5354: 80e3f040 rschi pc, r3, r0, asr #32

5358: 2b0169b3 blcs 5fa2c <\_r\_debug@@GLIBC\_2.4+0x360d0>

535c: 8198f040 orrshi pc, r8, r0, asr #32

5360: 2b288af3 blcs a27f34 <\_r\_debug@@GLIBC\_2.4+0x9fe5d8>

5364: 8197f040 orrshi pc, r7, r0, asr #32

5368: 1e9a8ab3 ; <UNDEFINED> instruction: 0x1e9a8ab3

536c: f2002a01 vpmax.s8 d2, d0, d1

5370: 2b028195 blcs a59cc <\_r\_debug@@GLIBC\_2.4+0x7c070>

5374: 8195f000 orrshi pc, r5, r0

5378: 2b208df3 blcs 828b4c <\_r\_debug@@GLIBC\_2.4+0x7ff1f0>

537c: 8198f040 orrshi pc, r8, r0, asr #32

5380: 6a318e32 bvs c68c50 <\_r\_debug@@GLIBC\_2.4+0xc3f2f4>

5384: 18630154 stmdane r3!, {r2, r4, r6, r8}^

5388: f20042ab vhsub.s8 d4, d16, d27

538c: eb0b8103 bl 2e57a0 <\_r\_debug@@GLIBC\_2.4+0x2bbe44>

5390: 627b0301 rsbsvs r0, fp, #67108864 ; 0x4000000

5394: 440c6a79 strmi r6, [ip], #-2681 ; 0xfffff587

5398: f08042a1 ; <UNDEFINED> instruction: 0xf08042a1

539c: 4bea8194 blmi ffaa59f4 <\_r\_debug@@GLIBC\_2.4+0xffa7c098>

53a0: 49ea460c stmibmi sl!, {r2, r3, r9, sl, lr}^

53a4: 61be447b ; <UNDEFINED> instruction: 0x61be447b

53a8: 33804479 orrcc r4, r0, #2030043136 ; 0x79000000

53ac: 210061f9 strdcs r6, [r0, -r9]

53b0: 6139603b teqvs r9, fp, lsr r0

53b4: 2b046823 blcs 11f448 <\_r\_debug@@GLIBC\_2.4+0xf5aec>

53b8: f8d4d172 ; <UNDEFINED> instruction: 0xf8d4d172

53bc: f1baa010 ; <UNDEFINED> instruction: 0xf1baa010

53c0: d96d0f1f stmdble sp!, {r0, r1, r2, r3, r4, r8, r9, sl, fp}^

53c4: 2e0369e6 vmlscs.f16 s12, s7, s13 ; <UNPREDICTABLE>

53c8: 1f31d96a svcne 0x0031d96a

53cc: 0304f031 movweq pc, #16433 ; 0x4031 ; <UNPREDICTABLE>

53d0: 69bbd17c ldmibvs fp!, {r2, r3, r4, r5, r6, r8, ip, lr, pc}

53d4: 9004f8d4 ldrdls pc, [r4], -r4

53d8: eb0a681a bl 29f448 <\_r\_debug@@GLIBC\_2.4+0x275aec>

53dc: 42910109 addsmi r0, r1, #1073741826 ; 0x40000002

53e0: eb0bbf98 bl 2f5248 <\_r\_debug@@GLIBC\_2.4+0x2cb8ec>

53e4: d9170509 ldmdble r7, {r0, r3, r8, sl}

53e8: 3f80f5ba svccc 0x0080f5ba

53ec: 80edf080 rschi pc, sp, r0, lsl #1

53f0: 0207f10a andeq pc, r7, #-2147483646 ; 0x80000002

53f4: 0207f022 andeq pc, r7, #34 ; 0x22

53f8: 0d02ebad vstreq d14, [r2, #-692] ; 0xfffffd4c

53fc: 4649ad04 strbmi sl, [r9], -r4, lsl #26

5400: 46402200 strbmi r2, [r0], -r0, lsl #4

5404: fb1af00c blx 6c143e <\_r\_debug@@GLIBC\_2.4+0x697ae2>

5408: 46294652 ; <UNDEFINED> instruction: 0x46294652

540c: f00c4640 ; <UNDEFINED> instruction: 0xf00c4640

5410: 4582fc49 strmi pc, [r2, #3145] ; 0xc49

5414: 818bf040 orrhi pc, fp, r0, asr #32

5418: 030bf106 movweq pc, #45318 ; 0xb106 ; <UNPREDICTABLE>

541c: 0900f1c6 stmdbeq r0, {r1, r2, r6, r7, r8, ip, sp, lr, pc}

5420: e9c73e01 stmib r7, {r0, r9, sl, fp, ip, sp}^

5424: 46d08401 ldrbmi r8, [r0], r1, lsl #8

5428: 46b2683c ; <UNDEFINED> instruction: 0x46b2683c

542c: e010461e ands r4, r0, lr, lsl r6

5430: f1a8682b ; <UNDEFINED> instruction: 0xf1a8682b

5434: 68690020 stmdavs r9!, {r5}^

5438: ea0218f2 b 8b808 <\_r\_debug@@GLIBC\_2.4+0x61eac>

543c: 44510209 ldrbmi r0, [r1], #-521 ; 0xfffffdf7

5440: ea02440a b 96470 <\_r\_debug@@GLIBC\_2.4+0x6cb14>

5444: 42820209 addmi r0, r2, #-1879048192 ; 0x90000000

5448: 8098f200 addshi pc, r8, r0, lsl #4

544c: 0802eba8 stmdaeq r2, {r3, r5, r7, r8, r9, fp, sp, lr, pc}

5450: 22104415 andscs r4, r0, #352321536 ; 0x15000000

5454: 46284621 strtmi r4, [r8], -r1, lsr #12

5458: faf0f00d blx ffc41494 <\_r\_debug@@GLIBC\_2.4+0xffc17b38>

545c: d1e72800 mvnle r2, r0, lsl #16

5460: 68bc46c2 ldmvs ip!, {r1, r6, r7, r9, sl, lr}

5464: 8004f8d7 ldrdhi pc, [r4], -r7

5468: 0f00f1ba svceq 0x0000f1ba

546c: 692bd02c stmdbvs fp!, {r2, r3, r5, ip, lr, pc}

5470: d1642b00 cmnle r4, r0, lsl #22

5474: 49b67d2a ldmibmi r6!, {r1, r3, r5, r8, sl, fp, ip, sp, lr}

5478: 44797e2b ldrbtmi r7, [r9], #-3627 ; 0xfffff1d5

547c: 2202eb03 andcs lr, r2, #3072 ; 0xc00

5480: 68497f2b stmdavs r9, {r0, r1, r3, r5, r8, r9, sl, fp, ip, sp, lr}^

5484: 2302eb03 movwcs lr, #11011 ; 0x2b03

5488: bf182900 svclt 0x00182900

548c: d856428b ldmdale r6, {r0, r1, r3, r7, r9, lr}^

5490: f7fb6938 ; <UNDEFINED> instruction: 0xf7fb6938

5494: 4640eac8 strbmi lr, [r0], -r8, asr #21

5498: 46bd3734 ; <UNDEFINED> instruction: 0x46bd3734

549c: 8ff0e8bd svchi 0x00f0e8bd

54a0: 022ff107 eoreq pc, pc, #-1073741823 ; 0xc0000001

54a4: 46404659 ; <UNDEFINED> instruction: 0x46404659

54a8: f8872300 ; <UNDEFINED> instruction: 0xf8872300

54ac: f7ff302f ; <UNDEFINED> instruction: 0xf7ff302f

54b0: b998fddb ldmiblt r8, {r0, r1, r3, r4, r6, r7, r8, sl, fp, ip, sp, lr, pc}

54b4: 302ff897 mlacc pc, r7, r8, pc ; <UNPREDICTABLE>

54b8: d0402b00 suble r2, r0, r0, lsl #22

54bc: 681b69fb ldmdavs fp, {r0, r1, r3, r4, r5, r6, r7, r8, fp, sp, lr}

54c0: d01b1c5a andsle r1, fp, sl, asr ip

54c4: d03a2b00 eorsle r2, sl, r0, lsl #22

54c8: 8e1a69bb ; <UNDEFINED> instruction: 0x8e1a69bb

54cc: 34206a7b strtcc r6, [r0], #-2683 ; 0xfffff585

54d0: 1342eb03 movtne lr, #11011 ; 0x2b03

54d4: f4ff429c ; <UNDEFINED> instruction: 0xf4ff429c

54d8: e7d9af6d ldrb sl, [r9, sp, ror #30]

54dc: d02e2816 eorle r2, lr, r6, lsl r8

54e0: 447c4c9c ldrbtmi r4, [ip], #-3228 ; 0xfffff364

54e4: 2b00697b blcs 1fad8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xed70>

54e8: 2300d133 movwcs sp, #307 ; 0x133

54ec: 6a3a9401 bvs eaa4f8 <\_r\_debug@@GLIBC\_2.4+0xe80b9c>

54f0: 68f84641 ldmvs r8!, {r0, r6, r9, sl, lr}^

54f4: 93009302 movwls r9, #770 ; 0x302

54f8: ff32f7fe ; <UNDEFINED> instruction: 0xff32f7fe

54fc: 230169fa movwcs r6, #6650 ; 0x19fa

5500: e7e16013 ; <UNDEFINED> instruction: 0xe7e16013

5504: 38fff04f ldmcc pc!, {r0, r1, r2, r3, r6, ip, sp, lr, pc}^ ; <UNPREDICTABLE>

5508: 46403734 ; <UNDEFINED> instruction: 0x46403734

550c: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

5510: 7af28ff0 bvc ffca94d8 <\_r\_debug@@GLIBC\_2.4+0xffc7fb7c>

5514: bf982b02 svclt 0x00982b02

5518: f43f2a03 ; <UNDEFINED> instruction: 0xf43f2a03

551c: 6872af10 ldmdavs r2!, {r4, r8, r9, sl, fp, sp, pc}^

5520: 537ff244 cmnpl pc, #68, 4 ; 0x40000004 ; <UNPREDICTABLE>

5524: 634cf2c4 movtvs pc, #49860 ; 0xc2c4 ; <UNPREDICTABLE>

5528: f040429a ; <UNDEFINED> instruction: 0xf040429a

552c: 7a3380fb bvc ce5920 <\_r\_debug@@GLIBC\_2.4+0xcbbfc4>

5530: d05c2b01 subsle r2, ip, r1, lsl #22

5534: 613a2200 teqvs sl, r0, lsl #4

5538: 23016dfa movwcs r6, #7674 ; 0x1dfa

553c: 46407013 ; <UNDEFINED> instruction: 0x46407013

5540: 38fff04f ldmcc pc!, {r0, r1, r2, r3, r6, ip, sp, lr, pc}^ ; <UNPREDICTABLE>

5544: fad8f00c blx ff64157c <\_r\_debug@@GLIBC\_2.4+0xff617c20>

5548: 22024b83 andcs r4, r2, #134144 ; 0x20c00

554c: 601a447b andsvs r4, sl, fp, ror r4

5550: 6a3de79e bvs f7f3d0 <\_r\_debug@@GLIBC\_2.4+0xf55a74>

5554: f00d4628 ; <UNDEFINED> instruction: 0xf00d4628

5558: 4629f8f3 ; <UNDEFINED> instruction: 0x4629f8f3

555c: 0308f100 movweq pc, #33024 ; 0x8100 ; <UNPREDICTABLE>

5560: f0231c42 ; <UNDEFINED> instruction: 0xf0231c42

5564: ebad0307 bl feb46188 <\_r\_debug@@GLIBC\_2.4+0xfeb1c82c>

5568: a8040d03 stmdage r4, {r0, r1, r8, sl, fp}

556c: edc8f00d stcl 0, cr15, [r8, #52] ; 0x34

5570: 46284603 strtmi r4, [r8], -r3, lsl #12

5574: f7fb623b ; <UNDEFINED> instruction: 0xf7fb623b

5578: e7b6ea56 sbfx lr, r6, #20, #23

557c: e9d769bb ldmib r7, {r0, r1, r3, r4, r5, r7, r8, fp, sp, lr}^

5580: 8e1a8401 cfmulshi mvf8, mvf10, mvf1

5584: 6a7b3420 bvs 1ed260c <\_r\_debug@@GLIBC\_2.4+0x1ea8cb0>

5588: 1342eb03 movtne lr, #11011 ; 0x2b03

558c: f4ff429c ; <UNDEFINED> instruction: 0xf4ff429c

5590: e77daf11 ; <UNDEFINED> instruction: 0xe77daf11

5594: 0d04ebad vstreq d14, [r4, #-692] ; 0xfffffd4c

5598: ab042200 blge 10dda0 <\_r\_debug@@GLIBC\_2.4+0xe4444>

559c: 627b4640 rsbsvs r4, fp, #64, 12 ; 0x4000000

55a0: f00c461d ; <UNDEFINED> instruction: 0xf00c461d

55a4: 4622fa4b strtmi pc, [r2], -fp, asr #20

55a8: 46404629 strbmi r4, [r0], -r9, lsr #12

55ac: fb7af00c blx 1ec15e6 <\_r\_debug@@GLIBC\_2.4+0x1e97c8a>

55b0: bf0442a0 svclt 0x000442a0

55b4: 01548e32 cmpeq r4, r2, lsr lr

55b8: aeecf43f mcrge 4, 7, pc, cr12, cr15, {1} ; <UNPREDICTABLE>

55bc: 4c684b67 ; <UNDEFINED> instruction: 0x4c684b67

55c0: 447c447b ldrbtmi r4, [ip], #-1147 ; 0xfffffb85

55c4: 60fb681b rscsvs r6, fp, fp, lsl r8

55c8: 6938e78c ldmdbvs r8!, {r2, r3, r7, r8, r9, sl, sp, lr, pc}

55cc: f7fb4651 ; <UNDEFINED> instruction: 0xf7fb4651

55d0: 4605ea16 ; <UNDEFINED> instruction: 0x4605ea16

55d4: d0f12800 rscsle r2, r1, r0, lsl #16

55d8: e7106138 ; <UNDEFINED> instruction: 0xe7106138

55dc: 447b4b61 ldrbtmi r4, [fp], #-2913 ; 0xfffff49f

55e0: 60fb681b rscsvs r6, fp, fp, lsl r8

55e4: d14d2b00 cmple sp, r0, lsl #22

55e8: 447c4c5f ldrbtmi r4, [ip], #-3167 ; 0xfffff3a1

55ec: 6ab3e77a bvs fecff3dc <\_r\_debug@@GLIBC\_2.4+0xfecd5a80>

55f0: 7200f44f andvc pc, r0, #1325400064 ; 0x4f000000

55f4: 5200f2c0 andpl pc, r0, #192, 4

55f8: 23fff003 mvnscs pc, #3

55fc: 437df423 cmnmi sp, #587202560 ; 0x23000000 ; <UNPREDICTABLE>

5600: d0424293 umaalle r4, r2, r3, r2

5604: 2b288af3 blcs a281d8 <\_r\_debug@@GLIBC\_2.4+0x9fe87c>

5608: 7a73d13f bvc 1cf9b0c <\_r\_debug@@GLIBC\_2.4+0x1cd01b0>

560c: d0522b01 subsle r2, r2, r1, lsl #22

5610: 23004c56 movwcs r4, #3158 ; 0xc56

5614: 447c60fb ldrbtmi r6, [ip], #-251 ; 0xffffff05

5618: 2208e764 andcs lr, r8, #100, 14 ; 0x1900000

561c: 0168f104 msreq SPSR\_f, r4, lsl #2

5620: f00d4658 ; <UNDEFINED> instruction: 0xf00d4658

5624: 2800fa0b stmdacs r0, {r0, r1, r3, r9, fp, ip, sp, lr, pc}

5628: ae79f43f mrcge 4, 3, APSR\_nzcv, cr9, cr15, {1}

562c: 0174f104 cmneq r4, r4, lsl #2 ; <UNPREDICTABLE>

5630: 46582208 ldrbmi r2, [r8], -r8, lsl #4

5634: fa02f00d blx c1670 <\_r\_debug@@GLIBC\_2.4+0x97d14>

5638: f43f2800 ; <UNDEFINED> instruction: 0xf43f2800

563c: e76eae70 ; <UNDEFINED> instruction: 0xe76eae70

5640: 1199f892 ; <UNDEFINED> instruction: 0x1199f892

5644: 0508f011 streq pc, [r8, #-17] ; 0xffffffef

5648: ae39f47f mrcge 4, 1, APSR\_nzcv, cr9, cr15, {3}

564c: 81acf8dc ldrdhi pc, [ip, ip]!

5650: 7418f502 ldrvc pc, [r8], #-1282 ; 0xfffffafe

5654: a118f8df ; <UNDEFINED> instruction: 0xa118f8df

5658: 46a16a38 ; <UNDEFINED> instruction: 0x46a16a38

565c: 464444fa ; <UNDEFINED> instruction: 0x464444fa

5660: 68634698 stmdavs r3!, {r3, r4, r7, r9, sl, lr}^

5664: 4642b133 ; <UNDEFINED> instruction: 0x4642b133

5668: 01c5eb09 biceq lr, r5, r9, lsl #22

566c: 28004798 stmdacs r0, {r3, r4, r7, r8, r9, sl, lr}

5670: af48f43f svcge 0x0048f43f

5674: 31b0f8da asrscc pc, sl ; <illegal shifter operand> ; <UNPREDICTABLE>

5678: 6a243501 bvs 912a84 <\_r\_debug@@GLIBC\_2.4+0x8e9128>

567c: d3f0429d mvnsle r4, #-805306359 ; 0xd0000009

5680: e61c6238 ; <UNDEFINED> instruction: 0xe61c6238

5684: 447c4c3b ldrbtmi r4, [ip], #-3131 ; 0xfffff3c5

5688: 2300e72c movwcs lr, #1836 ; 0x72c

568c: e756613b smmlar r6, fp, r1, r6

5690: 447c4c39 ldrbtmi r4, [ip], #-3129 ; 0xfffff3c7

5694: 68fbe726 ldmvs fp!, {r1, r2, r5, r8, r9, sl, sp, lr, pc}^

5698: e750613b smmlar r0, fp, r1, r6

569c: 447c4c37 ldrbtmi r4, [ip], #-3127 ; 0xfffff3c9

56a0: 6dbbe720 ldcvs 7, cr14, [fp, #128]! ; 0x80

56a4: f53f0099 ; <UNDEFINED> instruction: 0xf53f0099

56a8: 4c35ae67 ldcmi 14, cr10, [r5], #-412 ; 0xfffffe64

56ac: e719447c ; <UNDEFINED> instruction: 0xe719447c

56b0: 447c4c34 ldrbtmi r4, [ip], #-3124 ; 0xfffff3cc

56b4: 7ab3e716 bvc fecff314 <\_r\_debug@@GLIBC\_2.4+0xfecd59b8>

56b8: d0072b01 andle r2, r7, r1, lsl #22

56bc: 23004c32 movwcs r4, #3122 ; 0xc32

56c0: 447c60fb ldrbtmi r6, [ip], #-251 ; 0xffffff05

56c4: 2300e70e movwcs lr, #1806 ; 0x70e

56c8: e6e1613b ; <UNDEFINED> instruction: 0xe6e1613b

56cc: 1ed37af2 ; <UNDEFINED> instruction: 0x1ed37af2

56d0: 2301bf18 movwcs fp, #7960 ; 0x1f18

56d4: 0fbff012 svceq 0x00bff012

56d8: 4619bf14 sadd16mi fp, r9, r4

56dc: b9b92100 ldmiblt r9!, {r8, sp}

56e0: b1427b32 cmplt r2, r2, lsr fp

56e4: bf882a02 svclt 0x00882a02

56e8: 0301f043 movweq pc, #4163 ; 0x1043 ; <UNPREDICTABLE>

56ec: 4c27b11b stfmid f3, [r7], #-108 ; 0xffffff94

56f0: 447c60f9 ldrbtmi r6, [ip], #-249 ; 0xffffff07

56f4: 4926e6f6 stmdbmi r6!, {r1, r2, r4, r5, r6, r7, r9, sl, sp, lr, pc}

56f8: 000df106 andeq pc, sp, r6, lsl #2

56fc: 44792207 ldrbtmi r2, [r9], #-519 ; 0xfffffdf9

5700: f00d3161 ; <UNDEFINED> instruction: 0xf00d3161

5704: 60f8f99b smlalsvs pc, r8, fp, r9 ; <UNPREDICTABLE>

5708: 4c22b938 ; <UNDEFINED> instruction: 0x4c22b938

570c: e6e9447c uxtab r4, r9, ip, ror #8

5710: 23004c21 movwcs r4, #3105 ; 0xc21

5714: 447c60fb ldrbtmi r6, [ip], #-251 ; 0xffffff05

5718: 4c20e6e4 stcmi 6, cr14, [r0], #-912 ; 0xfffffc70

571c: 60fb2300 rscsvs r2, fp, r0, lsl #6

5720: e6df447c ; <UNDEFINED> instruction: 0xe6df447c

5724: 23004c1e movwcs r4, #3102 ; 0xc1e

5728: 447c60fb ldrbtmi r6, [ip], #-251 ; 0xffffff05

572c: 6938e6da ldmdbvs r8!, {r1, r3, r4, r6, r7, r9, sl, sp, lr, pc}

5730: e978f7fb ldmdb r8!, {r0, r1, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}^

5734: bf00e742 svclt 0x0000e742

5738: 000236cc andeq r3, r2, ip, asr #13

573c: 00024678 andeq r4, r2, r8, ror r6

5740: 0000ee6e andeq lr, r0, lr, ror #28

5744: 0000ee34 andeq lr, r0, r4, lsr lr

5748: 0000edd8 ldrdeq lr, [r0], -r8

574c: 000244ec andeq r4, r2, ip, ror #9

5750: 000234f2 strdeq r3, [r2], -r2

5754: 000108d2 ldrdeq r0, [r1], -r2

5758: 00024408 andeq r4, r2, r8, lsl #8

575c: 00024394 muleq r2, r4, r3

5760: 000102ba ; <UNDEFINED> instruction: 0x000102ba

5764: 00024376 andeq r4, r2, r6, ror r3

5768: 000107ba ; <UNDEFINED> instruction: 0x000107ba

576c: 0001074e andeq r0, r1, lr, asr #14

5770: 00023310 andeq r3, r2, r0, lsl r3

5774: 000101f6 strdeq r0, [r1], -r6

5778: 00010672 andeq r0, r1, r2, ror r6

577c: 000105ae andeq r0, r1, lr, lsr #11

5780: 00010334 andeq r0, r1, r4, lsr r3

5784: 000105c2 andeq r0, r1, r2, asr #11

5788: 0001066e andeq r0, r1, lr, ror #12

578c: 000105c6 andeq r0, r1, r6, asr #11

5790: 0000ea7e andeq lr, r0, lr, ror sl

5794: 000105e8 andeq r0, r1, r8, ror #11

5798: 0001058a andeq r0, r1, sl, lsl #11

579c: 000105b8 ; <UNDEFINED> instruction: 0x000105b8

57a0: 00010666 andeq r0, r1, r6, ror #12

57a4: 4ff0e92d svcmi 0x00f0e92d

57a8: 681cb0ad ldmdavs ip, {r0, r2, r3, r5, r7, ip, sp, pc}

57ac: 61fcaf04 mvnsvs sl, r4, lsl #30

57b0: f0002c00 ; <UNDEFINED> instruction: 0xf0002c00

57b4: 637a80c5 cmnvs sl, #197 ; 0xc5

57b8: e9c72200 stmib r7, {r9, sp}^

57bc: 46100108 ldrmi r0, [r0], -r8, lsl #2

57c0: 61ba603b ; <UNDEFINED> instruction: 0x61ba603b

57c4: 4adc4bdb bmi ff718738 <\_r\_debug@@GLIBC\_2.4+0xff6eeddc>

57c8: 60b8447b adcsvs r4, r8, fp, ror r4

57cc: 69f8447a ldmibvs r8!, {r1, r3, r4, r5, r6, sl, lr}^

57d0: 6852681b ldmdavs r2, {r0, r1, r3, r4, fp, sp, lr}^

57d4: 8000f8d0 ldrdhi pc, [r0], -r0

57d8: 48d84413 ldmmi r8, {r0, r1, r4, sl, lr}^

57dc: 4ad8440b bmi ff616810 <\_r\_debug@@GLIBC\_2.4+0xff5eceb4>

57e0: 44783307 ldrbtmi r3, [r8], #-775 ; 0xfffffcf9

57e4: 0307f023 movweq pc, #28707 ; 0x7023 ; <UNPREDICTABLE>

57e8: ebad447a bl feb569d8 <\_r\_debug@@GLIBC\_2.4+0xfeb2d07c>

57ec: 60780d03 rsbsvs r0, r8, r3, lsl #26

57f0: 0b10f10d bleq 441c2c <\_r\_debug@@GLIBC\_2.4+0x4182d0>

57f4: 4bd3617a blmi ff4ddde4 <\_r\_debug@@GLIBC\_2.4+0xff4b4488>

57f8: 681d447b ldmdavs sp, {r0, r1, r3, r4, r5, r6, sl, lr}

57fc: 0501f015 streq pc, [r1, #-21] ; 0xffffffeb

5800: 80e1f040 rschi pc, r1, r0, asr #32

5804: 2010f8d8 ; <UNDEFINED> instruction: 0x2010f8d8

5808: f8d84658 ; <UNDEFINED> instruction: 0xf8d84658

580c: 2600100c strcs r1, [r0], -ip

5810: fc5cf00d mrrc2 0, 0, pc, ip, cr13 ; <UNPREDICTABLE>

5814: a330f8df teqge r0, #14614528 ; 0xdf0000 ; <UNPREDICTABLE>

5818: 46344bcc ldrtmi r4, [r4], -ip, asr #23

581c: 9330f8df teqls r0, #14614528 ; 0xdf0000 ; <UNPREDICTABLE>

5820: 447b44fa ldrbtmi r4, [fp], #-1274 ; 0xfffffb06

5824: 44f9633b ldrbtmi r6, [r9], #827 ; 0x33b

5828: 447b4bca ldrbtmi r4, [fp], #-3018 ; 0xfffff436

582c: 62b8613b adcsvs r6, r8, #-1073741810 ; 0xc000000e

5830: 689a6b3b ldmvs sl, {r0, r1, r3, r4, r5, r8, r9, fp, sp, lr}

5834: d96d42a2 stmdble sp!, {r1, r5, r7, r9, lr}^

5838: 0514f108 ldreq pc, [r4, #-264] ; 0xfffffef8

583c: 3024f855 eorcc pc, r4, r5, asr r8 ; <UNPREDICTABLE>

5840: d0642b01 rsble r2, r4, r1, lsl #22

5844: 300cf8da ldrdcc pc, [ip], -sl

5848: eb036ab8 bl e0330 <\_r\_debug@@GLIBC\_2.4+0xb69d4>

584c: f85302c4 ; <UNDEFINED> instruction: 0xf85302c4

5850: 68521034 ldmdavs r2, {r2, r4, r5, ip}^

5854: fc3af00d ldc2 0, cr15, [sl], #-52 ; 0xffffffcc

5858: 1208e9d7 andne lr, r8, #3522560 ; 0x35c000

585c: fc36f00d ldc2 0, cr15, [r6], #-52 ; 0xffffffcc

5860: 3000f8d9 ldrdcc pc, [r0], -r9

5864: eba007d9 bl fe8077d0 <\_r\_debug@@GLIBC\_2.4+0xfe7dde74>

5868: 62fa020b rscsvs r0, sl, #-1342177280 ; 0xb0000000

586c: 80a3f100 adchi pc, r3, r0, lsl #2

5870: 30d8f8d7 ldrsbcc pc, [r8], #135 ; 0x87 ; <UNPREDICTABLE>

5874: 46582200 ldrbmi r2, [r8], -r0, lsl #4

5878: 6b7b9301 blvs 1eea484 <\_r\_debug@@GLIBC\_2.4+0x1ec0b28>

587c: f8d79300 ; <UNDEFINED> instruction: 0xf8d79300

5880: 920230d4 andls r3, r2, #212 ; 0xd4

5884: 1233e9d7 eorsne lr, r3, #3522560 ; 0x35c000

5888: fd06f7ff stc2 7, cr15, [r6, #-1020] ; 0xfffffc04

588c: 2024f855 eorcs pc, r4, r5, asr r8 ; <UNPREDICTABLE>

5890: 2a004603 bcs 170a4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x633c>

5894: 1c42d15b stfnep f5, [r2], {91} ; 0x5b

5898: 2601d061 strcs sp, [r1], -r1, rrx

589c: f8452202 ; <UNDEFINED> instruction: 0xf8452202

58a0: 6b7a2024 blvs 1e8d938 <\_r\_debug@@GLIBC\_2.4+0x1e63fdc>

58a4: d5060152 strle r0, [r6, #-338] ; 0xfffffeae

58a8: 6812693a ldmdavs r2, {r1, r3, r4, r5, r8, fp, sp, lr}

58ac: 1c59b9ca mrrcne 9, 12, fp, r9, cr10 ; <UNPREDICTABLE>

58b0: 0401f104 streq pc, [r1], #-260 ; 0xfffffefc

58b4: 6af8d0bc bvs ffe39bac <\_r\_debug@@GLIBC\_2.4+0xffe10250>

58b8: f7fb637b ; <UNDEFINED> instruction: 0xf7fb637b

58bc: f8d7e888 ; <UNDEFINED> instruction: 0xf8d7e888

58c0: 601830c8 andsvs r3, r8, r8, asr #1

58c4: 28006b7b stmdacs r0, {r0, r1, r3, r4, r5, r6, r8, r9, fp, sp, lr}

58c8: 812bf000 msrhi CPSR\_fxc, r0

58cc: 46596afa ; <UNDEFINED> instruction: 0x46596afa

58d0: f00d637b ; <UNDEFINED> instruction: 0xf00d637b

58d4: 6b7bec16 blvs 1f00934 <\_r\_debug@@GLIBC\_2.4+0x1ed6fd8>

58d8: 37a44618 ; <UNDEFINED> instruction: 0x37a44618

58dc: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

58e0: 46198ff0 ssub8mi r8, r9, r0

58e4: 0238f107 eorseq pc, r8, #-1073741823 ; 0xc0000001

58e8: 60fb2003 rscsvs r2, fp, r3

58ec: f866f00c ; <UNDEFINED> instruction: 0xf866f00c

58f0: b91068fb ldmdblt r0, {r0, r1, r3, r4, r5, r6, r7, fp, sp, lr}

58f4: 05106cba ldreq r6, [r0, #-3258] ; 0xfffff346

58f8: 4618d4d9 ; <UNDEFINED> instruction: 0x4618d4d9

58fc: f8fcf00c ; <UNDEFINED> instruction: 0xf8fcf00c

5900: 4b964a95 blmi fe59835c <\_r\_debug@@GLIBC\_2.4+0xfe56ea00>

5904: 447a2102 ldrbtmi r2, [sl], #-258 ; 0xfffffefe

5908: 6011447b andsvs r4, r1, fp, ror r4

590c: 3401689a strcc r6, [r1], #-2202 ; 0xfffff766

5910: d89142a2 ldmle r1, {r1, r5, r7, r9, lr}

5914: 4b92b136 blmi fe4b1df4 <\_r\_debug@@GLIBC\_2.4+0xfe488498>

5918: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

591c: bf182b0d svclt 0x00182b0d

5920: d10d2b02 tstle sp, r2, lsl #22

5924: f85369fb ; <UNDEFINED> instruction: 0xf85369fb

5928: 61fb8f04 mvnsvs r8, r4, lsl #30

592c: 433369bb teqmi r3, #3063808 ; 0x2ec000

5930: f1b861bb ; <UNDEFINED> instruction: 0xf1b861bb

5934: f47f0f00 ; <UNDEFINED> instruction: 0xf47f0f00

5938: 2b00af5e blcs 316b8 <\_r\_debug@@GLIBC\_2.4+0x7d5c>

593c: 80daf000 sbcshi pc, sl, r0

5940: 33fff04f mvnscc pc, #79 ; 0x4f

5944: 461837a4 ldrmi r3, [r8], -r4, lsr #15

5948: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

594c: 2a018ff0 bcs 69914 <\_r\_debug@@GLIBC\_2.4+0x3ffb8>

5950: f046bf18 ; <UNDEFINED> instruction: 0xf046bf18

5954: 1c450601 mcrrne 6, 0, r0, r5, cr1

5958: 3401d1a3 strcc sp, [r1], #-419 ; 0xfffffe5d

595c: f8d7e768 ; <UNDEFINED> instruction: 0xf8d7e768

5960: b16330d0 ldrdlt r3, [r3, #-0]!

5964: 234c6999 movtcs r6, #51609 ; 0xc999

5968: fb034a7e blx d836a <\_r\_debug@@GLIBC\_2.4+0xaea0e>

596c: 447af301 ldrbtmi pc, [sl], #-769 ; 0xfffffcff ; <UNPREDICTABLE>

5970: f89358d3 ; <UNDEFINED> instruction: 0xf89358d3

5974: 071b3199 ; <UNDEFINED> instruction: 0x071b3199

5978: 2601bf48 strcs fp, [r1], -r8, asr #30

597c: 6afbd4ed bvs ffefad38 <\_r\_debug@@GLIBC\_2.4+0xffed13dc>

5980: f1072000 ; <UNDEFINED> instruction: 0xf1072000

5984: 445b0238 ldrbmi r0, [fp], #-568 ; 0xfffffdc8

5988: 6a7b4619 bvs 1ed71f4 <\_r\_debug@@GLIBC\_2.4+0x1ead898>

598c: 54c843db strbpl r4, [r8], #987 ; 0x3db

5990: 20034659 andcs r4, r3, r9, asr r6

5994: fffcf00b ; <UNDEFINED> instruction: 0xfffcf00b

5998: 6cbbb998 ; <UNDEFINED> instruction: 0x6cbbb998

599c: 4370f403 cmnmi r0, #50331648 ; 0x3000000 ; <UNPREDICTABLE>

59a0: 4f80f5b3 svcmi 0x0080f5b3

59a4: 2202bf0a andcs fp, r2, #10, 30 ; 0x28

59a8: 26012201 strcs r2, [r1], -r1, lsl #4

59ac: 0384eb08 orreq lr, r4, #8, 22 ; 0x2000

59b0: 615a3401 cmpvs sl, r1, lsl #8

59b4: 486ce73c stmdami ip!, {r2, r3, r4, r5, r8, r9, sl, sp, lr, pc}^

59b8: 44784659 ldrbtmi r4, [r8], #-1625 ; 0xfffff9a7

59bc: f9daf006 ; <UNDEFINED> instruction: 0xf9daf006

59c0: 2201e756 andcs lr, r1, #22544384 ; 0x1580000

59c4: f8d8e7f2 ; <UNDEFINED> instruction: 0xf8d8e7f2

59c8: 68ba3004 ldmvs sl!, {r2, ip, sp}

59cc: 633b4293 teqvs fp, #805306377 ; 0x30000009

59d0: af18f43f svcge 0x0018f43f

59d4: 4c664b65 ; <UNDEFINED> instruction: 0x4c664b65

59d8: f8d8447b ; <UNDEFINED> instruction: 0xf8d8447b

59dc: 447c1008 ldrbtmi r1, [ip], #-8

59e0: 681b4864 ldmdavs fp, {r2, r5, r6, fp, lr}

59e4: 44786862 ldrbtmi r6, [r8], #-2146 ; 0xfffff79e

59e8: d010f8c7 andsle pc, r0, r7, asr #17

59ec: 62b94413 adcsvs r4, r9, #318767104 ; 0x13000000

59f0: f0233307 ; <UNDEFINED> instruction: 0xf0233307

59f4: ebad0307 bl feb46618 <\_r\_debug@@GLIBC\_2.4+0xfeb1ccbc>

59f8: f0060d03 ; <UNDEFINED> instruction: 0xf0060d03

59fc: 69fbf9bb ldmibvs fp!, {r0, r1, r3, r4, r5, r7, r8, fp, ip, sp, lr, pc}^

5a00: 681bae04 ldmdavs fp, {r2, r9, sl, fp, sp, pc}

5a04: d04f2b00 suble r2, pc, r0, lsl #22

5a08: 6b39685a blvs e5fb78 <\_r\_debug@@GLIBC\_2.4+0xe3621c>

5a0c: d14b4291 ; <UNDEFINED> instruction: 0xd14b4291

5a10: 46194a59 ; <UNDEFINED> instruction: 0x46194a59

5a14: b802e9c7 stmdalt r2, {r0, r1, r2, r6, r7, r8, fp, sp, lr, pc}

5a18: f8d7447a ; <UNDEFINED> instruction: 0xf8d7447a

5a1c: f8d7901c ; <UNDEFINED> instruction: 0xf8d7901c

5a20: 4692b004 ldrmi fp, [r2], r4

5a24: 690a62fc stmdbvs sl, {r2, r3, r4, r5, r6, r7, r9, sp, lr}

5a28: 68c94630 stmiavs r9, {r4, r5, r9, sl, lr}^

5a2c: fb4ef00d blx 13c1a6a <\_r\_debug@@GLIBC\_2.4+0x139810e>

5a30: 689a6afb ldmvs sl, {r0, r1, r3, r4, r5, r6, r7, r9, fp, sp, lr}

5a34: b3724680 cmnlt r2, #128, 12 ; 0x8000000

5a38: e0132400 ands r2, r3, r0, lsl #8

5a3c: d0494290 umaalle r4, r9, r0, r2

5a40: 1c01f800 stcne 8, cr15, [r1], {-0}

5a44: 2d00697b vstrcs.16 s12, [r0, #-246] ; 0xffffff0a ; <UNPREDICTABLE>

5a48: f04f4631 ; <UNDEFINED> instruction: 0xf04f4631

5a4c: bf0c0500 svclt 0x000c0500

5a50: 46184650 ; <UNDEFINED> instruction: 0x46184650

5a54: f9a4f006 ; <UNDEFINED> instruction: 0xf9a4f006

5a58: 34014a48 strcc r4, [r1], #-2632 ; 0xfffff5b8

5a5c: 6892447a ldmvs r2, {r1, r3, r4, r5, r6, sl, lr}

5a60: d2184294 andsle r4, r8, #148, 4 ; 0x40000009

5a64: 2000f8d9 ldrdcs pc, [r0], -r9

5a68: eb024640 bl 97370 <\_r\_debug@@GLIBC\_2.4+0x6da14>

5a6c: 69520284 ldmdbvs r2, {r2, r7, r9}^

5a70: d0f12a01 rscsle r2, r1, r1, lsl #20

5a74: 200cf8db ldrdcs pc, [ip], -fp

5a78: 0cc4eb02 vstmiaeq r4, {d30}

5a7c: 1034f852 eorsne pc, r4, r2, asr r8 ; <UNPREDICTABLE>

5a80: 2004f8dc ldrdcs pc, [r4], -ip

5a84: fb22f00d blx 8c1ac2 <\_r\_debug@@GLIBC\_2.4+0x898166>

5a88: 21001c72 tstcs r0, r2, ror ip

5a8c: d1d54286 bicsle r4, r5, r6, lsl #5

5a90: 70022200 andvc r2, r2, r0, lsl #4

5a94: f859e7d6 ; <UNDEFINED> instruction: 0xf859e7d6

5a98: b1191f04 tstlt r9, r4, lsl #30

5a9c: 6b3b684a blvs edfbcc <\_r\_debug@@GLIBC\_2.4+0xeb6270>

5aa0: d0c04293 smullle r4, r0, r3, r2

5aa4: b802e9d7 stmdalt r2, {r0, r1, r2, r4, r6, r7, r8, fp, sp, lr, pc}

5aa8: b1cb6abb strhlt r6, [fp, #171] ; 0xab

5aac: 781b6abb ldmdavc fp, {r0, r1, r3, r4, r5, r7, r9, fp, sp, lr}

5ab0: 4b33b92b blmi cf3f64 <\_r\_debug@@GLIBC\_2.4+0xcca608>

5ab4: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

5ab8: 62bb681b adcsvs r6, fp, #1769472 ; 0x1b0000

5abc: 4831b1b3 ldmdami r1!, {r0, r1, r4, r5, r7, r8, ip, sp, pc}

5ac0: 6b396aba blvs e605b0 <\_r\_debug@@GLIBC\_2.4+0xe36c54>

5ac4: f0064478 ; <UNDEFINED> instruction: 0xf0064478

5ac8: 6b3bf96b blvs f0407c <\_r\_debug@@GLIBC\_2.4+0xeda720>

5acc: f8d760bb ; <UNDEFINED> instruction: 0xf8d760bb

5ad0: e697d010 ; <UNDEFINED> instruction: 0xe697d010

5ad4: 2a2f7832 bcs be3ba4 <\_r\_debug@@GLIBC\_2.4+0xbba248>

5ad8: 2200d1b2 andcs sp, r0, #-2147483604 ; 0x8000002c

5adc: e7b17002 ldr r7, [r1, r2]!

5ae0: 6b394829 blvs e57b8c <\_r\_debug@@GLIBC\_2.4+0xe2e230>

5ae4: f0064478 ; <UNDEFINED> instruction: 0xf0064478

5ae8: e7eef95b ubfx pc, fp, #18, #15

5aec: 447b4b27 ldrbtmi r4, [fp], #-2855 ; 0xfffff4d9

5af0: e7e462bb ; <UNDEFINED> instruction: 0xe7e462bb

5af4: 685b683b ldmdavs fp, {r0, r1, r3, r4, r5, fp, sp, lr}^

5af8: 4b25b973 blmi 9740cc <\_r\_debug@@GLIBC\_2.4+0x94a770>

5afc: 447b6839 ldrbtmi r6, [fp], #-2105 ; 0xfffff7c7

5b00: 0210f103 andseq pc, r0, #-1073741824 ; 0xc0000000

5b04: 42913318 addsmi r3, r1, #24, 6 ; 0x60000000

5b08: 4299bf18 addsmi fp, r9, #24, 30 ; 0x60

5b0c: af18f43f svcge 0x0018f43f

5b10: 33fff04f mvnscc pc, #79 ; 0x4f

5b14: e6df600b ldrb r6, [pc], fp

5b18: 6818683b ldmdavs r8, {r0, r1, r3, r4, r5, fp, sp, lr}

5b1c: ef82f7fa svc 0x0082f7fa

5b20: 4618e7eb ldrmi lr, [r8], -fp, ror #15

5b24: 33fff04f mvnscc pc, #79 ; 0x4f

5b28: f00b637b ; <UNDEFINED> instruction: 0xf00b637b

5b2c: 6b7bffe5 blvs 1f05ac8 <\_r\_debug@@GLIBC\_2.4+0x1edc16c>

5b30: bf00e6d2 svclt 0x0000e6d2

5b34: 00024108 andeq r4, r2, r8, lsl #2

5b38: 00023358 andeq r3, r2, r8, asr r3

5b3c: 00023342 andeq r3, r2, r2, asr #6

5b40: 00010a7c andeq r0, r1, ip, ror sl

5b44: 00023174 andeq r3, r2, r4, ror r1

5b48: 00023304 andeq r3, r2, r4, lsl #6

5b4c: 00023302 andeq r3, r2, r2, lsl #6

5b50: 00023146 andeq r3, r2, r6, asr #2

5b54: 00023712 andeq r3, r2, r2, lsl r7

5b58: 0002404e andeq r4, r2, lr, asr #32

5b5c: 0002321c andeq r3, r2, ip, lsl r2

5b60: 0002403c andeq r4, r2, ip, lsr r0

5b64: 000236ee andeq r3, r2, lr, ror #13

5b68: 00010452 andeq r0, r1, r2, asr r4

5b6c: 00023ef8 strdeq r3, [r2], -r8

5b70: 00023146 andeq r3, r2, r6, asr #2

5b74: 000103f6 strdeq r0, [r1], -r6

5b78: 000103c0 andeq r0, r1, r0, asr #7

5b7c: 000230c8 andeq r3, r2, r8, asr #1

5b80: 00023de4 andeq r3, r2, r4, ror #27

5b84: 00010328 andeq r0, r1, r8, lsr #6

5b88: 00010320 andeq r0, r1, r0, lsr #6

5b8c: 0000efea andeq lr, r0, sl, ror #31

5b90: 00023026 andeq r3, r2, r6, lsr #32

5b94: d0411c4b suble r1, r1, fp, asr #24

5b98: 41f0e92d mvnsmi lr, sp, lsr #18

5b9c: 680d4604 stmdavs sp, {r2, r9, sl, lr}

5ba0: f04f460f ; <UNDEFINED> instruction: 0xf04f460f

5ba4: e00b0800 and r0, fp, r0, lsl #16

5ba8: 5f04f857 svcpl 0x0004f857

5bac: 2000e9d3 ldrdcs lr, [r0], -r3

5bb0: 29023001 stmdbcs r2, {r0, ip, sp}

5bb4: 1851bf2c ldmdane r1, {r2, r3, r5, r8, r9, sl, fp, ip, sp, pc}^

5bb8: 60581c91 ; <UNDEFINED> instruction: 0x60581c91

5bbc: b33d6019 teqlt sp, #25

5bc0: 68a37822 stmiavs r3!, {r1, r5, fp, ip, sp, lr}

5bc4: 2a006929 bcs 20070 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf308>

5bc8: 6866d1ee stmdavs r6!, {r1, r2, r3, r5, r6, r7, r8, ip, lr, pc}^

5bcc: f8d42901 ; <UNDEFINED> instruction: 0xf8d42901

5bd0: f101c00c ; <UNDEFINED> instruction: 0xf101c00c

5bd4: f10632ff ; <UNDEFINED> instruction: 0xf10632ff

5bd8: 60600001 rsbvs r0, r0, r1

5bdc: 06c6eb03 strbeq lr, [r6], r3, lsl #22

5be0: 0301f10c movweq pc, #4364 ; 0x110c ; <UNPREDICTABLE>

5be4: f8c64660 ; <UNDEFINED> instruction: 0xf8c64660

5be8: d813c008 ldmdale r3, {r3, lr, pc}

5bec: 60e32900 rscvs r2, r3, r0, lsl #18

5bf0: 232fbf14 ; <UNDEFINED> instruction: 0x232fbf14

5bf4: f88c232e ; <UNDEFINED> instruction: 0xf88c232e

5bf8: 68e03000 stmiavs r0!, {ip, sp}^

5bfc: 60e31c43 rscvs r1, r3, r3, asr #24

5c00: 8000f880 andhi pc, r0, r0, lsl #17

5c04: 5f04f857 svcpl 0x0004f857

5c08: 800cf8c6 andhi pc, ip, r6, asr #17

5c0c: d1d72d00 bicsle r2, r7, r0, lsl #26

5c10: 81f0e8bd ldrhhi lr, [r0, #141]! ; 0x8d

5c14: f00d68e9 ; <UNDEFINED> instruction: 0xf00d68e9

5c18: e7effa59 ubfx pc, r9, #20, #16

5c1c: bf004770 svclt 0x00004770

5c20: 41f0e92d mvnsmi lr, sp, lsr #18

5c24: f00c2124 ; <UNDEFINED> instruction: 0xf00c2124

5c28: 4605fa2b strmi pc, [r5], -fp, lsr #20

5c2c: 4628b910 ; <UNDEFINED> instruction: 0x4628b910

5c30: 81f0e8bd ldrhhi lr, [r0, #141]! ; 0x8d

5c34: 25004f10 strcs r4, [r0, #-3856] ; 0xfffff0f0

5c38: f8df4e10 ; <UNDEFINED> instruction: 0xf8df4e10

5c3c: 447f8044 ldrbtmi r8, [pc], #-68 ; 5c44 <free@plt+0x5224>

5c40: 44f8447e ldrbtmi r4, [r8], #1150 ; 0x47e

5c44: 46391c44 ldrtmi r1, [r9], -r4, asr #24

5c48: f7fe4620 ; <UNDEFINED> instruction: 0xf7fe4620

5c4c: 4631fa93 ; <UNDEFINED> instruction: 0x4631fa93

5c50: 4620b940 strtmi fp, [r0], -r0, asr #18

5c54: fa8ef7fe blx fe3c3c54 <\_r\_debug@@GLIBC\_2.4+0xfe39a2f8>

5c58: b9184641 ldmdblt r8, {r0, r6, r9, sl, lr}

5c5c: f7fe4620 ; <UNDEFINED> instruction: 0xf7fe4620

5c60: b108fa89 smlabblt r8, r9, sl, pc ; <UNPREDICTABLE>

5c64: 44043501 strmi r3, [r4], #-1281 ; 0xfffffaff

5c68: 46202124 strtmi r2, [r0], -r4, lsr #2

5c6c: fa08f00c blx 241ca4 <\_r\_debug@@GLIBC\_2.4+0x218348>

5c70: d1e72800 mvnle r2, r0, lsl #16

5c74: bf00e7db svclt 0x0000e7db

5c78: 000101e2 andeq r0, r1, r2, ror #3

5c7c: 000101e8 andeq r0, r1, r8, ror #3

5c80: 000101f2 strdeq r0, [r1], -r2

5c84: 4ff0e92d svcmi 0x00f0e92d

5c88: f8dfb085 ; <UNDEFINED> instruction: 0xf8dfb085

5c8c: 4688a0e8 strmi sl, [r8], r8, ror #1

5c90: 90e4f8df ldrdls pc, [r4], #143 ; 0x8f ; <UNPREDICTABLE>

5c94: 44fa4617 ldrbtmi r4, [sl], #1559 ; 0x617

5c98: f10a780d ; <UNDEFINED> instruction: 0xf10a780d

5c9c: 44f933ff ldrbtmi r3, [r9], #1023 ; 0x3ff

5ca0: 0301e9cd movweq lr, #6605 ; 0x19cd

5ca4: 468b4614 pkhbtmi r4, fp, r4, lsl #12

5ca8: e0042600 and r2, r4, r0, lsl #12

5cac: 5b01f804 blpl 83cc4 <\_r\_debug@@GLIBC\_2.4+0x5a368>

5cb0: 5000f89b mulpl r0, fp, r8

5cb4: 2d24b1fd stfcsd f3, [r4, #-1012]! ; 0xfffffc0c

5cb8: 0b01f10b bleq 820ec <\_r\_debug@@GLIBC\_2.4+0x58790>

5cbc: 4649d1f6 ; <UNDEFINED> instruction: 0x4649d1f6

5cc0: f7fe4658 ; <UNDEFINED> instruction: 0xf7fe4658

5cc4: 4602fa57 ; <UNDEFINED> instruction: 0x4602fa57

5cc8: 492cb1e0 stmdbmi ip!, {r5, r6, r7, r8, ip, sp, pc}

5ccc: 680e4479 stmdavs lr, {r0, r3, r4, r5, r6, sl, lr}

5cd0: d1372e00 teqle r7, r0, lsl #28

5cd4: f8d39b01 ; <UNDEFINED> instruction: 0xf8d39b01

5cd8: 1e4811ac dvfne<illegal precision>p f1, f0, #4.0

5cdc: d82e3003 stmdale lr!, {r0, r1, ip, sp}

5ce0: 92034620 andls r4, r3, #32, 12 ; 0x2000000

5ce4: fbacf00c blx feb41d1e <\_r\_debug@@GLIBC\_2.4+0xfeb183c2>

5ce8: 44939a03 ldrmi r9, [r3], #2563 ; 0xa03

5cec: 5000f89b mulpl r0, fp, r8

5cf0: 2d004604 stccs 6, cr4, [r0, #-16]

5cf4: b9e6d1df stmiblt r6!, {r0, r1, r2, r3, r4, r6, r7, r8, ip, lr, pc}^

5cf8: 70232300 eorvc r2, r3, r0, lsl #6

5cfc: b0054638 andlt r4, r5, r8, lsr r6

5d00: 8ff0e8bd svchi 0x00f0e8bd

5d04: 4658491e ; <UNDEFINED> instruction: 0x4658491e

5d08: f7fe4479 ; <UNDEFINED> instruction: 0xf7fe4479

5d0c: 4602fa33 ; <UNDEFINED> instruction: 0x4602fa33

5d10: 491cb950 ldmdbmi ip, {r4, r6, r8, fp, ip, sp, pc}

5d14: 44794658 ldrbtmi r4, [r9], #-1624 ; 0xfffff9a8

5d18: fa2cf7fe blx b43d18 <\_r\_debug@@GLIBC\_2.4+0xb1a3bc>

5d1c: 28004602 stmdacs r0, {r1, r9, sl, lr}

5d20: 9802d0c4 stmdals r2, {r2, r6, r7, ip, lr, pc}

5d24: e7d94651 ; <UNDEFINED> instruction: 0xe7d94651

5d28: 44794917 ldrbtmi r4, [r9], #-2327 ; 0xfffff6e9

5d2c: 1e486889 cdpne 8, 4, cr6, cr8, cr9, {4}

5d30: 1be1e7d4 blne ff87fc88 <\_r\_debug@@GLIBC\_2.4+0xff85632c>

5d34: f7fe4638 ; <UNDEFINED> instruction: 0xf7fe4638

5d38: 2800fa4f stmdacs r0, {r0, r1, r2, r3, r6, r9, fp, ip, sp, lr, pc}

5d3c: 2300d1dc movwcs sp, #476 ; 0x1dc

5d40: e7db703b ; <UNDEFINED> instruction: 0xe7db703b

5d44: 0101f108 tsteq r1, r8, lsl #2 ; <UNPREDICTABLE>

5d48: d1f8458b mvnsle r4, fp, lsl #11

5d4c: 1000f81b andne pc, r0, fp, lsl r8 ; <UNPREDICTABLE>

5d50: bf182900 svclt 0x00182900

5d54: d1f2292f mvnsle r2, pc, lsr #18

5d58: f8939b01 ; <UNDEFINED> instruction: 0xf8939b01

5d5c: f8d30198 ; <UNDEFINED> instruction: 0xf8d30198

5d60: f01011ac ; <UNDEFINED> instruction: 0xf01011ac

5d64: f1010f03 ; <UNDEFINED> instruction: 0xf1010f03

5d68: bf0c30ff svclt 0x000c30ff

5d6c: 26002601 strcs r2, [r0], -r1, lsl #12

5d70: bf00e7b4 svclt 0x0000e7b4

5d74: 000101a2 andeq r0, r1, r2, lsr #3

5d78: 00010182 andeq r0, r1, r2, lsl #3

5d7c: 00023270 andeq r3, r2, r0, ror r2

5d80: 00010120 andeq r0, r1, r0, lsr #2

5d84: 0001011e andeq r0, r1, lr, lsl r1

5d88: 00022c42 andeq r2, r2, r2, asr #24

5d8c: 4606b5f8 ; <UNDEFINED> instruction: 0x4606b5f8

5d90: 460c4608 strmi r4, [ip], -r8, lsl #12

5d94: ff44f7ff ; <UNDEFINED> instruction: 0xff44f7ff

5d98: 4620b920 strtmi fp, [r0], -r0, lsr #18

5d9c: 40f8e8bd ldrhtmi lr, [r8], #141 ; 0x8d

5da0: bcb6f00c ldclt 0, cr15, [r6], #48 ; 0x30

5da4: 46204605 strtmi r4, [r0], -r5, lsl #12

5da8: fccaf00c stc2l 0, cr15, [sl], {12}

5dac: f8d64607 ; <UNDEFINED> instruction: 0xf8d64607

5db0: b1c801ac biclt r0, r8, ip, lsr #3

5db4: d0281c43 eorle r1, r8, r3, asr #24

5db8: fcc2f00c stc2l 0, cr15, [r2], {12}

5dbc: 37014a19 smladcc r1, r9, sl, r4

5dc0: 68d3447a ldmvs r3, {r1, r3, r4, r5, r6, sl, lr}^

5dc4: bf382b17 svclt 0x00382b17

5dc8: 42832317 addmi r2, r3, #1543503872 ; 0x5c000000

5dcc: 4603bf38 ; <UNDEFINED> instruction: 0x4603bf38

5dd0: fb053b04 blx 1549ea <\_r\_debug@@GLIBC\_2.4+0x12b08e>

5dd4: f7fa7003 ; <UNDEFINED> instruction: 0xf7fa7003

5dd8: 4602edfa ; <UNDEFINED> instruction: 0x4602edfa

5ddc: 4621b1b8 ; <UNDEFINED> instruction: 0x4621b1b8

5de0: e8bd4630 pop {r4, r5, r9, sl, lr}

5de4: e74d40f8 ; <UNDEFINED> instruction: 0xe74d40f8

5de8: 4b0f6872 blmi 3dffb8 <\_r\_debug@@GLIBC\_2.4+0x3b665c>

5dec: 447b7812 ldrbtmi r7, [fp], #-2066 ; 0xfffff7ee

5df0: 63a3f503 ; <UNDEFINED> instruction: 0x63a3f503

5df4: bf18429e svclt 0x0018429e

5df8: d1092a00 tstle r9, r0, lsl #20

5dfc: fc4ef007 mcrr2 0, 0, pc, lr, cr7 ; <UNPREDICTABLE>

5e00: f8c61e42 ; <UNDEFINED> instruction: 0xf8c61e42

5e04: 320301ac andcc r0, r3, #172, 2 ; 0x2b

5e08: 2000d9d6 ldrdcs sp, [r0], -r6

5e0c: bdf8e7d6 ldcllt 7, cr14, [r8, #856]! ; 0x358

5e10: f2404b06 vqdmulh.s<illegal width 8> d20, d0, d6

5e14: 4906128b stmdbmi r6, {r0, r1, r3, r7, r9, ip}

5e18: 447b4806 ldrbtmi r4, [fp], #-2054 ; 0xfffff7fa

5e1c: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

5e20: fdb4f00a ldc2 0, cr15, [r4, #40]! ; 0x28

5e24: 00022bac andeq r2, r2, ip, lsr #23

5e28: 0002326e andeq r3, r2, lr, ror #4

5e2c: 00010292 muleq r1, r2, r2

5e30: 0000f9f8 strdeq pc, [r0], -r8

5e34: 00010032 andeq r0, r1, r2, lsr r0

5e38: 4ff0e92d svcmi 0x00f0e92d

5e3c: f10db08b ; <UNDEFINED> instruction: 0xf10db08b

5e40: 46920b24 ldrmi r0, [r2], r4, lsr #22

5e44: 26009307 strcs r9, [r0], -r7, lsl #6

5e48: 90094b6e andls r4, r9, lr, ror #22

5e4c: 9300447b movwls r4, #1147 ; 0x47b

5e50: 91014b6d tstls r1, sp, ror #22

5e54: 9308447b movwls r4, #33915 ; 0x847b

5e58: 447b4b6c ldrbtmi r4, [fp], #-2924 ; 0xfffff494

5e5c: 46519306 ldrbmi r9, [r1], -r6, lsl #6

5e60: f00a4658 ; <UNDEFINED> instruction: 0xf00a4658

5e64: 4605fe31 ; <UNDEFINED> instruction: 0x4605fe31

5e68: d05c2800 subsle r2, ip, r0, lsl #16

5e6c: 2c00782c stccs 8, cr7, [r0], {44} ; 0x2c

5e70: 4629d060 strtmi sp, [r9], -r0, rrx

5e74: f7ff9815 ; <UNDEFINED> instruction: 0xf7ff9815

5e78: 4605ff89 strmi pc, [r5], -r9, lsl #31

5e7c: d0ee2800 rscle r2, lr, r0, lsl #16

5e80: fc5ef00c mrrc2 0, 0, pc, lr, cr12 ; <UNPREDICTABLE>

5e84: 28004604 stmdacs r0, {r2, r9, sl, lr}

5e88: 80a8f000 adchi pc, r8, r0

5e8c: d0132801 andsle r2, r3, r1, lsl #16

5e90: f813182b ; <UNDEFINED> instruction: 0xf813182b

5e94: 2b2f3c01 blcs bd4ea0 <\_r\_debug@@GLIBC\_2.4+0xbab544>

5e98: 80a5f040 adchi pc, r5, r0, asr #32

5e9c: f1c51e83 ; <UNDEFINED> instruction: 0xf1c51e83

5ea0: 442b0101 strtmi r0, [fp], #-257 ; 0xfffffeff

5ea4: f813e004 ; <UNDEFINED> instruction: 0xf813e004

5ea8: 2a2f2901 bcs bd02b4 <\_r\_debug@@GLIBC\_2.4+0xba6958>

5eac: 809bf040 addshi pc, fp, r0, asr #32

5eb0: eb01429d bl 5692c <\_r\_debug@@GLIBC\_2.4+0x2cfd0>

5eb4: d1f60403 mvnsle r0, r3, lsl #8

5eb8: 2b2f782b blcs be3f6c <\_r\_debug@@GLIBC\_2.4+0xbba610>

5ebc: 8092f040 addshi pc, r2, r0, asr #32

5ec0: 240146a9 strcs r4, [r1], #-1705 ; 0xfffff957

5ec4: f8d39b00 ; <UNDEFINED> instruction: 0xf8d39b00

5ec8: f1b88510 ; <UNDEFINED> instruction: 0xf1b88510

5ecc: d0380f00 eorsle r0, r8, r0, lsl #30

5ed0: e0024647 and r4, r2, r7, asr #12

5ed4: 2f00683f svccs 0x0000683f

5ed8: 693bd033 ldmdbvs fp!, {r0, r1, r4, r5, ip, lr, pc}

5edc: d1f942a3 mvnsle r4, r3, lsr #5

5ee0: 68f94622 ldmvs r9!, {r1, r5, r9, sl, lr}^

5ee4: f00c4628 ; <UNDEFINED> instruction: 0xf00c4628

5ee8: 2800fda9 stmdacs r0, {r0, r3, r5, r7, r8, sl, fp, ip, sp, lr, pc}

5eec: b166d1f2 strdlt sp, [r6, #-18]! ; 0xffffffee

5ef0: 68139a01 ldmdavs r3, {r0, r9, fp, ip, pc}

5ef4: d00c429f mulle ip, pc, r2 ; <UNPREDICTABLE>

5ef8: e0034613 and r4, r3, r3, lsl r6

5efc: 2f04f853 svccs 0x0004f853

5f00: d00642ba ; <UNDEFINED> instruction: 0xd00642ba

5f04: 42863001 addmi r3, r6, #1

5f08: 9b01d1f8 blls 7a6f0 <\_r\_debug@@GLIBC\_2.4+0x50d94>

5f0c: 7026f843 eorvc pc, r6, r3, asr #16

5f10: 46483601 strbmi r3, [r8], -r1, lsl #12

5f14: ed86f7fa stc 7, cr15, [r6, #1000] ; 0x3e8

5f18: 46584651 ; <UNDEFINED> instruction: 0x46584651

5f1c: fdd4f00a ldc2l 0, cr15, [r4, #40] ; 0x28

5f20: 28004605 stmdacs r0, {r0, r2, r9, sl, lr}

5f24: 9b01d1a2 blls 7a5b4 <\_r\_debug@@GLIBC\_2.4+0x50c58>

5f28: 0026f843 eoreq pc, r6, r3, asr #16

5f2c: b00b4618 andlt r4, fp, r8, lsl r6

5f30: 8ff0e8bd svchi 0x00f0e8bd

5f34: 46a19b00 strtmi r9, [r1], r0, lsl #22

5f38: 8510f8d3 ldrhi pc, [r0, #-2259] ; 0xfffff72d

5f3c: 0f00f1b8 svceq 0x0000f1b8

5f40: 9b14d1c6 blls 53a660 <\_r\_debug@@GLIBC\_2.4+0x510d04>

5f44: 9814b11b ldmdals r4, {r0, r1, r3, r4, r8, ip, sp, pc}

5f48: fbfaf00c blx ffec1f82 <\_r\_debug@@GLIBC\_2.4+0xffe98626>

5f4c: 93021c43 movwls r1, #11331 ; 0x2c43

5f50: 689b9b08 ldmvs fp, {r3, r8, r9, fp, ip, pc}

5f54: 93041d5a movwls r1, #19802 ; 0x4d5a

5f58: 92030092 andls r0, r3, #146 ; 0x92

5f5c: 93051c53 movwls r1, #23635 ; 0x5c53

5f60: 9b021918 blls 8c3c8 <\_r\_debug@@GLIBC\_2.4+0x62a6c>

5f64: f7fa4418 ; <UNDEFINED> instruction: 0xf7fa4418

5f68: e9dded32 ldmib sp, {r1, r4, r5, r8, sl, fp, sp, lr, pc}^

5f6c: 46072303 strmi r2, [r7], -r3, lsl #6

5f70: d0402800 suble r2, r0, r0, lsl #16

5f74: 46294410 ; <UNDEFINED> instruction: 0x46294410

5f78: 60f84622 rscsvs r4, r8, r2, lsr #12

5f7c: f00d9303 ; <UNDEFINED> instruction: 0xf00d9303

5f80: 9b06f8a5 blls 1c421c <\_r\_debug@@GLIBC\_2.4+0x19a8c0>

5f84: 681a2100 ldmdavs sl, {r8, sp}

5f88: 42a29b03 adcmi r9, r2, #3072 ; 0xc00

5f8c: 9a06bf3c bls 1b5c84 <\_r\_debug@@GLIBC\_2.4+0x18c328>

5f90: 70016014 andvc r6, r1, r4, lsl r0

5f94: 0110f107 tsteq r0, r7, lsl #2 ; <UNPREDICTABLE>

5f98: 782a613c stmdavc sl!, {r2, r3, r4, r5, r8, sp, lr}

5f9c: f04f2a2f ; <UNDEFINED> instruction: 0xf04f2a2f

5fa0: bf140200 svclt 0x00140200

5fa4: 20002002 andcs r2, r0, r2

5fa8: 3201b123 andcc fp, r1, #-1073741816 ; 0xc0000008

5fac: 0f04f841 svceq 0x0004f841

5fb0: d1fa4293 ; <UNDEFINED> instruction: 0xd1fa4293

5fb4: 607b9b07 rsbsvs r9, fp, r7, lsl #22

5fb8: b1cb9b14 biclt r9, fp, r4, lsl fp

5fbc: 9a029b05 bls acbd8 <\_r\_debug@@GLIBC\_2.4+0x8327c>

5fc0: 99144423 ldmdbls r4, {r0, r1, r5, sl, lr}

5fc4: 44384618 ldrtmi r4, [r8], #-1560 ; 0xfffff9e8

5fc8: e89af00d ldm sl, {r0, r2, r3, ip, sp, lr, pc}

5fcc: 4b1060b8 blmi 41e2b4 <\_r\_debug@@GLIBC\_2.4+0x3f4958>

5fd0: 8000f8c7 andhi pc, r0, r7, asr #17

5fd4: f8c3447b ; <UNDEFINED> instruction: 0xf8c3447b

5fd8: e7967510 ; <UNDEFINED> instruction: 0xe7967510

5fdc: f7fa4628 ; <UNDEFINED> instruction: 0xf7fa4628

5fe0: e73ced22 ldr lr, [ip, -r2, lsr #26]!

5fe4: 232f2401 ; <UNDEFINED> instruction: 0x232f2401

5fe8: 552b46a9 strpl r4, [fp, #-1705]! ; 0xfffff957

5fec: e7693401 strb r3, [r9, -r1, lsl #8]!

5ff0: 60bb9b14 adcsvs r9, fp, r4, lsl fp

5ff4: 4b07e7eb blmi 1fffa8 <\_r\_debug@@GLIBC\_2.4+0x1d664c>

5ff8: 46014602 strmi r4, [r1], -r2, lsl #12

5ffc: 447b200c ldrbtmi r2, [fp], #-12

6000: ed02f7fa stc 7, cr15, [r2, #-1000] ; 0xfffffc18

6004: 00023210 andeq r3, r2, r0, lsl r2

6008: 00022cd0 ldrdeq r2, [r2], -r0

600c: 00023a76 andeq r3, r2, r6, ror sl

6010: 00023088 andeq r3, r2, r8, lsl #1

6014: 0000fe7a andeq pc, r0, sl, ror lr ; <UNPREDICTABLE>

6018: 47f0e92d ldrbmi lr, [r0, sp, lsr #18]!

601c: 4c444684 mcrrmi 6, 8, r4, r4, cr4

6020: f8d3b082 ; <UNDEFINED> instruction: 0xf8d3b082

6024: 447c8004 ldrbtmi r8, [ip], #-4

6028: 415cf8d4 ldrsbmi pc, [ip, #-132] ; 0xffffff7c ; <UNPREDICTABLE>

602c: d13c2c00 teqle ip, r0, lsl #24

6030: b9387810 ldmdblt r8!, {r4, fp, ip, sp, lr}

6034: f04f2000 ; <UNDEFINED> instruction: 0xf04f2000

6038: f8cc33ff ; <UNDEFINED> instruction: 0xf8cc33ff

603c: b0023000 andlt r3, r2, r0

6040: 87f0e8bd ; <UNDEFINED> instruction: 0x87f0e8bd

6044: 468a4610 pkhbtmi r4, sl, r0, lsl #12

6048: 461e4667 ldrmi r4, [lr], -r7, ror #12

604c: fb60f00c blx 1842086 <\_r\_debug@@GLIBC\_2.4+0x181872a>

6050: 28004681 stmdacs r0, {r0, r7, r9, sl, lr}

6054: 7802d068 stmdavc r2, {r3, r5, r6, ip, lr, pc}

6058: d04d2a00 suble r2, sp, r0, lsl #20

605c: 25004604 strcs r4, [r0, #-1540] ; 0xfffff9fc

6060: f8142a3a ; <UNDEFINED> instruction: 0xf8142a3a

6064: bf082f01 svclt 0x00082f01

6068: 2a003501 bcs 13474 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x270c>

606c: 1ca8d1f8 stfned f5, [r8], #992 ; 0x3e0

6070: f7fa0080 ; <UNDEFINED> instruction: 0xf7fa0080

6074: 4605ecac strmi lr, [r5], -ip, lsr #25

6078: d04b2800 suble r2, fp, r0, lsl #16

607c: 46014a2d strmi r4, [r1], -sp, lsr #20

6080: 46489b0a strbmi r9, [r8], -sl, lsl #22

6084: 9601447a ; <UNDEFINED> instruction: 0x9601447a

6088: 8000f8cd andhi pc, r0, sp, asr #17

608c: fed4f7ff mrc2 7, 6, pc, cr4, cr15, {7}

6090: f7fa4648 ; <UNDEFINED> instruction: 0xf7fa4648

6094: 682cecc8 stmdavs ip!, {r3, r6, r7, sl, fp, sp, lr, pc}

6098: 2301b384 movwcs fp, #4996 ; 0x1384

609c: 4618603d ; <UNDEFINED> instruction: 0x4618603d

60a0: 3000f8ca andcc pc, r0, sl, asr #17

60a4: e8bdb002 pop {r1, ip, sp, pc}

60a8: 482387f0 stmdami r3!, {r4, r5, r6, r7, r8, r9, sl, pc}

60ac: 68004478 stmdavs r0, {r3, r4, r5, r6, sl, lr}

60b0: d1bd2800 ; <UNDEFINED> instruction: 0xd1bd2800

60b4: 0000f898 muleq r0, r8, r8

60b8: 42a87825 adcmi r7, r8, #2424832 ; 0x250000

60bc: 2800d126 stmdacs r0, {r1, r2, r5, r8, ip, lr, pc}

60c0: 4646d0b8 ; <UNDEFINED> instruction: 0x4646d0b8

60c4: b195e000 orrslt lr, r5, r0

60c8: 5f01f814 svcpl 0x0001f814

60cc: 7f01f816 svcvc 0x0001f816

60d0: d0f842bd ldrhtle r4, [r8], #45 ; 0x2d

60d4: 4625b15f ; <UNDEFINED> instruction: 0x4625b15f

60d8: 6b01f815 blvs 84134 <\_r\_debug@@GLIBC\_2.4+0x5a7d8>

60dc: d0a72e00 adcle r2, r7, r0, lsl #28

60e0: 462c2e3a ; <UNDEFINED> instruction: 0x462c2e3a

60e4: 782dd1f8 stmdavc sp!, {r3, r4, r5, r6, r7, r8, ip, lr, pc}

60e8: d1e62d00 mvnle r2, r0, lsl #26

60ec: 2d3ae7a0 ldccs 7, cr14, [sl, #-640]! ; 0xfffffd80

60f0: 2d00bf18 stccs 15, cr11, [r0, #-96] ; 0xffffffa0

60f4: e79dd1ef ldr sp, [sp, pc, ror #3]

60f8: e7ba2008 ldr r2, [sl, r8]!

60fc: f7fa4628 ; <UNDEFINED> instruction: 0xf7fa4628

6100: f04fec92 ; <UNDEFINED> instruction: 0xf04fec92

6104: 462033ff ; <UNDEFINED> instruction: 0x462033ff

6108: e798603b ; <UNDEFINED> instruction: 0xe798603b

610c: 2f004607 svccs 0x00004607

6110: e7ecd1e1 strb sp, [ip, r1, ror #3]!

6114: f7fa4648 ; <UNDEFINED> instruction: 0xf7fa4648

6118: 4b08ec86 blmi 241338 <\_r\_debug@@GLIBC\_2.4+0x2179dc>

611c: 2200447b andcs r4, r0, #2063597568 ; 0x7b000000

6120: 4611200c ldrmi r2, [r1], -ip

6124: ec70f7fa ldcl 7, cr15, [r0], #-1000 ; 0xfffffc18

6128: 447b4b05 ldrbtmi r4, [fp], #-2821 ; 0xfffff4fb

612c: bf00e7f7 svclt 0x0000e7f7

6130: 00022946 andeq r2, r2, r6, asr #18

6134: 0000ebb0 ; <UNDEFINED> instruction: 0x0000ebb0

6138: 00022e90 muleq r2, r0, lr

613c: 0000fd5c andeq pc, r0, ip, asr sp ; <UNPREDICTABLE>

6140: 0000fd72 andeq pc, r0, r2, ror sp ; <UNPREDICTABLE>

6144: f8503208 ; <UNDEFINED> instruction: 0xf8503208

6148: b17a2022 cmnlt sl, r2, lsr #32

614c: 460cb570 ; <UNDEFINED> instruction: 0x460cb570

6150: b0826b46 addlt r6, r2, r6, asr #22

6154: 6852461d ldmdavs r2, {r0, r2, r3, r4, r9, sl, lr}^

6158: 46204603 strtmi r4, [r0], -r3, lsl #12

615c: 31046874 tstcc r4, r4, ror r8

6160: 44229500 strtmi r9, [r2], #-1280 ; 0xfffffb00

6164: ff58f7ff ; <UNDEFINED> instruction: 0xff58f7ff

6168: bd70b002 ldcllt 0, cr11, [r0, #-8]!

616c: 33fff04f mvnscc pc, #79 ; 0x4f

6170: 600b4610 andvs r4, fp, r0, lsl r6

6174: bf004770 svclt 0x00004770

6178: e92d4a95 push {r0, r2, r4, r7, r9, fp, lr}

617c: b0874ff0 strdlt r4, [r7], r0

6180: af024d94 svcge 0x00024d94

6184: 447d447a ldrbtmi r4, [sp], #-1146 ; 0xfffffb86

6188: 68d16078 ldmvs r1, {r3, r4, r5, r6, sp, lr}^

618c: 68901d2b ldmvs r0, {r0, r1, r3, r5, r8, sl, fp, ip}

6190: 0208f105 andeq pc, r8, #1073741825 ; 0x40000001

6194: fbd0f004 blx ff4421ae <\_r\_debug@@GLIBC\_2.4+0xff418852>

6198: 201460e8 andscs r6, r4, r8, ror #1

619c: ec16f7fa ldc 7, cr15, [r6], {250} ; 0xfa

61a0: 280061a8 stmdacs r0, {r3, r5, r7, r8, sp, lr}

61a4: 8111f000 tsthi r1, r0 ; <UNPREDICTABLE>

61a8: f64c68ac ; <UNDEFINED> instruction: 0xf64c68ac

61ac: f6cc49cd ; <UNDEFINED> instruction: 0xf6cc49cd

61b0: 468049cc strmi r4, [r0], ip, asr #19

61b4: ea4f2050 b 13ce2fc <\_r\_debug@@GLIBC\_2.4+0x13a49a0>

61b8: f10a0a84 ; <UNDEFINED> instruction: 0xf10a0a84

61bc: fba90a27 blx fea48a62 <\_r\_debug@@GLIBC\_2.4+0xfea1f106>

61c0: ea4f3a0a b 13d49f0 <\_r\_debug@@GLIBC\_2.4+0x13ab094>

61c4: fb00191a blx c636 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xc56>

61c8: f7faf009 ; <UNDEFINED> instruction: 0xf7faf009

61cc: f8c8ec00 ; <UNDEFINED> instruction: 0xf8c8ec00

61d0: 28000000 stmdacs r0, {} ; <UNPREDICTABLE>

61d4: 80f2f000 rscshi pc, r2, r0

61d8: 0a14f04f beq 54231c <\_r\_debug@@GLIBC\_2.4+0x5189c0>

61dc: fb0a4a7e blx 298bde <\_r\_debug@@GLIBC\_2.4+0x26f282>

61e0: 497efa09 ldmdbmi lr!, {r0, r3, r9, fp, ip, sp, lr, pc}^

61e4: 4e7e447a mrcmi 4, 3, r4, cr14, cr10, {3}

61e8: f04f4479 ; <UNDEFINED> instruction: 0xf04f4479

61ec: 447e0c00 ldrbtmi r0, [lr], #-3072 ; 0xfffff400

61f0: 0510f8c2 ldreq pc, [r0, #-2242] ; 0xfffff73e

61f4: 22196046 andscs r6, r9, #70 ; 0x46

61f8: 0e4cf101 sqteqe f7, f1

61fc: 610260c1 smlabtvs r2, r1, r0, r6

6200: 4a78311a bmi 1e12670 <\_r\_debug@@GLIBC\_2.4+0x1de8d14>

6204: 0604f108 streq pc, [r4], -r8, lsl #2

6208: 0b10f108 bleq 442630 <\_r\_debug@@GLIBC\_2.4+0x418cd4>

620c: 447a4663 ldrbtmi r4, [sl], #-1635 ; 0xfffff99d

6210: c01cf8c5 andsgt pc, ip, r5, asr #17

6214: c008f8c0 andgt pc, r8, r0, asr #17

6218: f10060ba ; <UNDEFINED> instruction: 0xf10060ba

621c: 22000510 andcs r0, r0, #16, 10 ; 0x4000000

6220: 3201b124 andcc fp, r1, #36, 2

6224: 3f04f845 svccc 0x0004f845

6228: d1fa4294 ; <UNDEFINED> instruction: 0xd1fa4294

622c: d01945b3 ; <UNDEFINED> instruction: 0xd01945b3

6230: cb04f85e blgt 1443b0 <\_r\_debug@@GLIBC\_2.4+0x11aa54>

6234: 020aeb00 andeq lr, sl, #0, 22

6238: 9000f891 mulls r0, r1, r8

623c: 0501f10c streq pc, [r1, #-268] ; 0xfffffef4

6240: 18686002 stmdane r8!, {r1, sp, lr}^

6244: f1b968bd ; <UNDEFINED> instruction: 0xf1b968bd

6248: 60f80f2f rscsvs r0, r8, pc, lsr #30

624c: c010f8c2 andsgt pc, r0, r2, asr #17

6250: f8464610 ; <UNDEFINED> instruction: 0xf8464610

6254: e9c22b04 stmib r2, {r2, r8, r9, fp, sp}^

6258: 60d15301 sbcsvs r5, r1, r1, lsl #6

625c: 809af040 addshi pc, sl, r0, asr #32

6260: e7da68f9 ; <UNDEFINED> instruction: 0xe7da68f9

6264: 25004a60 strcs r4, [r0, #-2656] ; 0xfffff5a0

6268: 211d4b60 tstcs sp, r0, ror #22

626c: 6005447a andvs r4, r5, sl, ror r4

6270: f8c8447b ; <UNDEFINED> instruction: 0xf8c8447b

6274: 68145010 ldmdavs r4, {r4, ip, lr}

6278: b1ec6019 mvnlt r6, r9, lsl r0

627c: 3198f894 ; <UNDEFINED> instruction: 0x3198f894

6280: 0303f003 movweq pc, #12291 ; 0x3003 ; <UNPREDICTABLE>

6284: f0002b02 ; <UNDEFINED> instruction: 0xf0002b02

6288: f8d4808f ; <UNDEFINED> instruction: 0xf8d4808f

628c: b3066094 movwlt r6, #24724 ; 0x6094

6290: 46236872 ; <UNDEFINED> instruction: 0x46236872

6294: f5046b66 ; <UNDEFINED> instruction: 0xf5046b66

6298: 485571fa ldmdami r5, {r1, r3, r4, r5, r6, r7, r8, ip, sp, lr}^

629c: 44786876 ldrbtmi r6, [r8], #-2166 ; 0xfffff78a

62a0: f5049000 ; <UNDEFINED> instruction: 0xf5049000

62a4: 443270f8 ldrtmi r7, [r2], #-248 ; 0xffffff08

62a8: feb6f7ff mrc2 7, 5, pc, cr6, cr15, {7}

62ac: 33fff04f mvnscc pc, #79 ; 0x4f

62b0: 51f4f8c4 mvnspl pc, r4, asr #17

62b4: 319cf8c4 orrscc pc, ip, r4, asr #17

62b8: b10b687b tstlt fp, fp, ror r8

62bc: bb03781b bllt e4330 <\_r\_debug@@GLIBC\_2.4+0xba9d4>

62c0: f04f4b4c ; <UNDEFINED> instruction: 0xf04f4b4c

62c4: 371432ff ; <UNDEFINED> instruction: 0x371432ff

62c8: 611a447b tstvs sl, fp, ror r4

62cc: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

62d0: 6de28ff0 stclvs 15, cr8, [r2, #960]! ; 0x3c0

62d4: 33fff04f mvnscc pc, #79 ; 0x4f

62d8: 31f0f8c4 mvnscc pc, r4, asr #17

62dc: d04e2a00 suble r2, lr, r0, lsl #20

62e0: 46236b65 strtmi r6, [r3], -r5, ror #22

62e4: f5046852 ; <UNDEFINED> instruction: 0xf5046852

62e8: 484371d0 stmdami r3, {r4, r6, r7, r8, ip, sp, lr}^

62ec: 4478686d ldrbtmi r6, [r8], #-2157 ; 0xfffff793

62f0: 442a9000 strtmi r9, [sl], #-0

62f4: 70cef504 sbcvc pc, lr, r4, lsl #10

62f8: fe8ef7ff mcr2 7, 4, pc, cr14, cr15, {7} ; <UNPREDICTABLE>

62fc: 61a0f8c4 asrvs pc, r4, #17 ; <UNPREDICTABLE>

6300: 687de7da ldmdavs sp!, {r1, r3, r4, r6, r7, r8, r9, sl, sp, lr, pc}^

6304: f00c4628 ; <UNDEFINED> instruction: 0xf00c4628

6308: 4629fa1b ; <UNDEFINED> instruction: 0x4629fa1b

630c: 0308f100 movweq pc, #33024 ; 0x8100 ; <UNPREDICTABLE>

6310: f0231c42 ; <UNDEFINED> instruction: 0xf0231c42

6314: ebad0307 bl feb46f38 <\_r\_debug@@GLIBC\_2.4+0xfeb1d5dc>

6318: a8020d03 stmdage r2, {r0, r1, r8, sl, fp}

631c: eef0f00c cdp 0, 15, cr15, cr0, cr12, {0}

6320: 46057803 strmi r7, [r5], -r3, lsl #16

6324: d0332b00 eorsle r2, r3, r0, lsl #22

6328: 20014601 andcs r4, r1, r1, lsl #12

632c: 1c423b3a vmovne d26, r3, r2

6330: bf9e2b01 svclt 0x009e2b01

6334: 46101c83 ldrmi r1, [r0], -r3, lsl #25

6338: f811461a ; <UNDEFINED> instruction: 0xf811461a

633c: 2b003f01 blcs 15f48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x51e0>

6340: 0090d1f4 ; <UNDEFINED> instruction: 0x0090d1f4

6344: eb42f7fa bl 10c4334 <\_r\_debug@@GLIBC\_2.4+0x109a9d8>

6348: 447e4e2c ldrbtmi r4, [lr], #-3628 ; 0xfffff1d4

634c: 61304601 teqvs r0, r1, lsl #12

6350: d0332800 eorsle r2, r3, r0, lsl #16

6354: 4b2a2200 blmi a8eb5c <\_r\_debug@@GLIBC\_2.4+0xa65200>

6358: 46289200 strtmi r9, [r8], -r0, lsl #4

635c: 447b4a29 ldrbtmi r4, [fp], #-2601 ; 0xfffff5d7

6360: 447a9401 ldrbtmi r9, [sl], #-1025 ; 0xfffffbff

6364: fd68f7ff stc2l 7, cr15, [r8, #-1020]! ; 0xfffffc04

6368: 68036930 stmdavs r3, {r4, r5, r8, fp, sp, lr}

636c: 4b26b153 blmi 9b28c0 <\_r\_debug@@GLIBC\_2.4+0x988f64>

6370: 37142200 ldrcc r2, [r4, -r0, lsl #4]

6374: 615a447b cmpvs sl, fp, ror r4

6378: e8bd46bd pop {r0, r2, r3, r4, r5, r7, r9, sl, lr}

637c: f8c48ff0 ; <UNDEFINED> instruction: 0xf8c48ff0

6380: e799319c ; <UNDEFINED> instruction: 0xe799319c

6384: eb4ef7fa bl 13c4374 <\_r\_debug@@GLIBC\_2.4+0x139aa18>

6388: 33fff04f mvnscc pc, #79 ; 0x4f

638c: e7ee6133 ; <UNDEFINED> instruction: 0xe7ee6133

6390: e7d72008 ldrb r2, [r7, r8]

6394: f2404b1d vpadd.i8 d20, d0, d13

6398: 491d22e2 ldmdbmi sp, {r1, r5, r6, r7, r9, sp}

639c: 447b481d ldrbtmi r4, [fp], #-2077 ; 0xfffff7e3

63a0: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

63a4: faf2f00a blx ffcc23d4 <\_r\_debug@@GLIBC\_2.4+0xffc98a78>

63a8: f44f4b1b ; <UNDEFINED> instruction: 0xf44f4b1b

63ac: 491b723d ldmdbmi fp, {r0, r2, r3, r4, r5, r9, ip, sp, lr}

63b0: 447b481b ldrbtmi r4, [fp], #-2075 ; 0xfffff7e5

63b4: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

63b8: fae8f00a blx ffa423e8 <\_r\_debug@@GLIBC\_2.4+0xffa18a8c>

63bc: 447b4b19 ldrbtmi r4, [fp], #-2841 ; 0xfffff4e7

63c0: 200c2200 andcs r2, ip, r0, lsl #4

63c4: f7fa4611 ; <UNDEFINED> instruction: 0xf7fa4611

63c8: 4b17eb20 blmi 601050 <\_r\_debug@@GLIBC\_2.4+0x5d76f4>

63cc: e7f7447b ; <UNDEFINED> instruction: 0xe7f7447b

63d0: 000227e8 andeq r2, r2, r8, ror #15

63d4: 0002299e muleq r2, lr, r9

63d8: 00022e78 andeq r2, r2, r8, ror lr

63dc: 0000df94 muleq r0, r4, pc ; <UNPREDICTABLE>

63e0: 0000fcf2 strdeq pc, [r0], -r2

63e4: 0000fcd2 ldrdeq pc, [r0], -r2

63e8: 00022df0 strdeq r2, [r2], -r0

63ec: 00023660 andeq r3, r2, r0, ror #12

63f0: 0000fc8a andeq pc, r0, sl, lsl #25

63f4: 0002285c andeq r2, r2, ip, asr r8

63f8: 0000fc42 andeq pc, r0, r2, asr #24

63fc: 000227da ldrdeq r2, [r2], -sl

6400: 0000fbda ldrdeq pc, [r0], -sl

6404: 0000fbe6 andeq pc, r0, r6, ror #23

6408: 000227b0 ; <UNDEFINED> instruction: 0x000227b0

640c: 0000fd2a andeq pc, r0, sl, lsr #26

6410: 0000f474 andeq pc, r0, r4, ror r4 ; <UNPREDICTABLE>

6414: 0000fb52 andeq pc, r0, r2, asr fp ; <UNPREDICTABLE>

6418: 0000fd16 andeq pc, r0, r6, lsl sp ; <UNPREDICTABLE>

641c: 0000f460 andeq pc, r0, r0, ror #8

6420: 0000fb5a andeq pc, r0, sl, asr fp ; <UNPREDICTABLE>

6424: 0000faba ; <UNDEFINED> instruction: 0x0000faba

6428: 0000faf4 strdeq pc, [r0], -r4

642c: 4ff0e92d svcmi 0x00f0e92d

6430: 7d13f5ad cfldr32vc mvfx15, [r3, #-692] ; 0xfffffd4c

6434: b274f8dd rsbslt pc, r4, #14483456 ; 0xdd0000

6438: f1bb9307 ; <UNDEFINED> instruction: 0xf1bb9307

643c: f2c00f00 ; <UNDEFINED> instruction: 0xf2c00f00

6440: 469083bb ; <UNDEFINED> instruction: 0x469083bb

6444: 2784f8df ; <UNDEFINED> instruction: 0x2784f8df

6448: f8d2447a ; <UNDEFINED> instruction: 0xf8d2447a

644c: 455b34c0 ldrbmi r3, [fp, #-1216] ; 0xfffffb40

6450: 83a8f240 ; <UNDEFINED> instruction: 0x83a8f240

6454: 4606234c strmi r2, [r6], -ip, asr #6

6458: f30bfb03 vqrdmulh.s<illegal width 8> d15, d11, d3

645c: 58d5460c ldmpl r5, {r2, r3, r9, sl, lr}^

6460: d0412d00 suble r2, r1, r0, lsl #26

6464: 9768f8df ; <UNDEFINED> instruction: 0x9768f8df

6468: a768f8df ; <UNDEFINED> instruction: 0xa768f8df

646c: 44fa44f9 ldrbtmi r4, [sl], #1273 ; 0x4f9

6470: 68ede002 stmiavs sp!, {r1, sp, lr, pc}^

6474: d0372d00 eorsle r2, r7, r0, lsl #26

6478: 2199f895 ; <UNDEFINED> instruction: 0x2199f895

647c: 1340f3c2 movtne pc, #962 ; 0x3c2 ; <UNPREDICTABLE>

6480: 0352ea43 cmpeq r2, #274432 ; 0x43000

6484: d4f407db ldrbtle r0, [r4], #2011 ; 0x7db

6488: 46204629 strtmi r4, [r0], -r9, lsr #12

648c: fcaef005 stc2 0, cr15, [lr], #20

6490: f895bb28 ; <UNDEFINED> instruction: 0xf895bb28

6494: 07df3199 bfieq r3, r9, #3, #29

6498: 6dabd4eb cfstrsvs mvf13, [fp, #940]! ; 0x3ac

649c: d0e82b00 rscle r2, r8, r0, lsl #22

64a0: 46206b6a strtmi r6, [r0], -sl, ror #22

64a4: 6857685b ldmdavs r7, {r0, r1, r3, r4, r6, fp, sp, lr}^

64a8: 4639441f ; <UNDEFINED> instruction: 0x4639441f

64ac: fe50f00b cdp2 0, 5, cr15, cr0, cr11, {0}

64b0: 4649b158 ; <UNDEFINED> instruction: 0x4649b158

64b4: f00b4620 ; <UNDEFINED> instruction: 0xf00b4620

64b8: 2800fe4b stmdacs r0, {r0, r1, r3, r6, r9, sl, fp, ip, sp, lr, pc}

64bc: 4638d1d9 ; <UNDEFINED> instruction: 0x4638d1d9

64c0: f00b4651 ; <UNDEFINED> instruction: 0xf00b4651

64c4: 2800fe45 stmdacs r0, {r0, r2, r6, r9, sl, fp, ip, sp, lr, pc}

64c8: 4621d1d3 ; <UNDEFINED> instruction: 0x4621d1d3

64cc: f7fd69e8 ; <UNDEFINED> instruction: 0xf7fd69e8

64d0: f895feff ; <UNDEFINED> instruction: 0xf895feff

64d4: f0422199 ; <UNDEFINED> instruction: 0xf0422199

64d8: f8850201 ; <UNDEFINED> instruction: 0xf8850201

64dc: 46282199 ; <UNDEFINED> instruction: 0x46282199

64e0: 7d13f50d cfldr32vc mvfx15, [r3, #-52] ; 0xffffffcc

64e4: 8ff0e8bd svchi 0x00f0e8bd

64e8: 36ecf8df usatcc pc, #12, pc, asr #17 ; <UNPREDICTABLE>

64ec: 447b9a9c ldrbtmi r9, [fp], #-2716 ; 0xfffff564

64f0: 5280f002 addpl pc, r0, #2

64f4: 681f9208 ldmdavs pc, {r3, r9, ip, pc} ; <UNPREDICTABLE>

64f8: f100067a ; <UNDEFINED> instruction: 0xf100067a

64fc: f8d38163 ; <UNDEFINED> instruction: 0xf8d38163

6500: 2b0031b0 blcs 12bc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e60>

6504: 8207f040 andhi pc, r7, #64 ; 0x40

6508: f10d9309 ; <UNDEFINED> instruction: 0xf10d9309

650c: 212f053b ; <UNDEFINED> instruction: 0x212f053b

6510: 23004620 movwcs r4, #1568 ; 0x620

6514: f00b702b ; <UNDEFINED> instruction: 0xf00b702b

6518: 2800fdb3 stmdacs r0, {r0, r1, r4, r5, r7, r8, sl, fp, ip, sp, lr, pc}

651c: 2e00d03c mcrcs 0, 0, sp, cr0, cr12, {1}

6520: 80e8f000 rschi pc, r8, r0

6524: 46304621 ldrtmi r4, [r0], -r1, lsr #12

6528: fc30f7ff ldc2 7, cr15, [r0], #-1020 ; 0xfffffc04

652c: 093cf10d ldmdbeq ip!, {r0, r2, r3, r8, ip, sp, lr, pc}

6530: f8c94632 ; <UNDEFINED> instruction: 0xf8c94632

6534: 28000000 stmdacs r0, {} ; <UNPREDICTABLE>

6538: 8096f000 addshi pc, r6, r0

653c: af119b9c svcge 0x00119b9c

6540: 46399501 ldrtmi r9, [r9], -r1, lsl #10

6544: 23019300 movwcs r9, #4864 ; 0x1300

6548: 23009302 movwcs r9, #770 ; 0x302

654c: fea4f7fe mcr2 7, 5, pc, cr4, cr14, {7} ; <UNPREDICTABLE>

6550: 46021c43 strmi r1, [r2], -r3, asr #24

6554: 8084f000 addhi pc, r4, r0

6558: 2b009b08 blcs 2d180 <\_r\_debug@@GLIBC\_2.4+0x3824>

655c: 2600bf18 ; <UNDEFINED> instruction: 0x2600bf18

6560: c678f8df ; <UNDEFINED> instruction: 0xc678f8df

6564: f8d9463b ; <UNDEFINED> instruction: 0xf8d9463b

6568: 44fc0000 ldrbtmi r0, [ip], #0

656c: b014f8cd andslt pc, r4, sp, asr #17

6570: f8dc9909 ; <UNDEFINED> instruction: 0xf8dc9909

6574: 90005000 andls r5, r0, r0

6578: 9c9c4620 ldcls 6, cr4, [ip], {32}

657c: 95109601 ldrls r9, [r0, #-1537] ; 0xfffff9ff

6580: 8402e9cd strhi lr, [r2], #-2509 ; 0xfffff633

6584: 9404ac10 strls sl, [r4], #-3088 ; 0xfffff3f0

6588: ff0ef7fd ; <UNDEFINED> instruction: 0xff0ef7fd

658c: 46284605 strtmi r4, [r8], -r5, lsl #12

6590: 7d13f50d cfldr32vc mvfx15, [r3, #-52] ; 0xffffffcc

6594: 8ff0e8bd svchi 0x00f0e8bd

6598: f00c4620 ; <UNDEFINED> instruction: 0xf00c4620

659c: f8dff8d1 ; <UNDEFINED> instruction: 0xf8dff8d1

65a0: 447b3640 ldrbtmi r3, [fp], #-1600 ; 0xfffff9c0

65a4: 07d9681b bfieq r6, fp, #16, #10

65a8: 0201f100 andeq pc, r1, #0, 2

65ac: f100920b ; <UNDEFINED> instruction: 0xf100920b

65b0: 2e008101 mvfcss f0, f1

65b4: 82d7f000 sbcshi pc, r7, #0

65b8: 3094f8d6 ; <UNDEFINED> instruction: 0x3094f8d6

65bc: f0002b00 ; <UNDEFINED> instruction: 0xf0002b00

65c0: f8df80aa ; <UNDEFINED> instruction: 0xf8df80aa

65c4: f10d3620 ; <UNDEFINED> instruction: 0xf10d3620

65c8: 447b093c ldrbtmi r0, [fp], #-2364 ; 0xfffff6c4

65cc: 3301691b movwcc r6, #6427 ; 0x191b

65d0: f8dfd01b ; <UNDEFINED> instruction: 0xf8dfd01b

65d4: 21023614 tstcs r2, r4, lsl r6

65d8: af119a9c svcge 0x00119a9c

65dc: 9103447b tstls r3, fp, ror r4

65e0: 95043310 strls r3, [r4, #-784] ; 0xfffffcf0

65e4: 46209602 strtmi r9, [r0], -r2, lsl #12

65e8: 9000f8cd andls pc, r0, sp, asr #17

65ec: 9701990b strls r9, [r1, -fp, lsl #18]

65f0: f8d8f7ff ; <UNDEFINED> instruction: 0xf8d8f7ff

65f4: f1a02e00 ; <UNDEFINED> instruction: 0xf1a02e00

65f8: 460233ff ; <UNDEFINED> instruction: 0x460233ff

65fc: f383fab3 ; <UNDEFINED> instruction: 0xf383fab3

6600: 1353ea4f cmpne r3, #323584 ; 0x4f000

6604: 2300bf08 movwcs fp, #3848 ; 0xf08

6608: f8d6b1c3 ; <UNDEFINED> instruction: 0xf8d6b1c3

660c: f50631f0 ; <UNDEFINED> instruction: 0xf50631f0

6610: 1c5a77f8 mrrcne 7, 15, r7, sl, cr8

6614: 8108f000 mrshi pc, (UNDEF: 8) ; <UNPREDICTABLE>

6618: f0002b00 ; <UNDEFINED> instruction: 0xf0002b00

661c: 463b81e9 ldrtmi r8, [fp], -r9, ror #3

6620: 95042204 strls r2, [r4, #-516] ; 0xfffffdfc

6624: 9203af11 andls sl, r3, #17, 30 ; 0x44

6628: 9a9c4620 bls fe717eb0 <\_r\_debug@@GLIBC\_2.4+0xfe6ee554>

662c: f8cd9602 ; <UNDEFINED> instruction: 0xf8cd9602

6630: 97019000 strls r9, [r1, -r0]

6634: f7ff990b ; <UNDEFINED> instruction: 0xf7ff990b

6638: 4602f8b5 ; <UNDEFINED> instruction: 0x4602f8b5

663c: f0001c51 ; <UNDEFINED> instruction: 0xf0001c51

6640: f8df80f3 ; <UNDEFINED> instruction: 0xf8df80f3

6644: 447b35a8 ldrbtmi r3, [fp], #-1448 ; 0xfffffa58

6648: 07d9681b bfieq r6, fp, #16, #10

664c: 8236f100 eorshi pc, r6, #0, 2

6650: 2b009b08 blcs 2d278 <\_r\_debug@@GLIBC\_2.4+0x391c>

6654: 2600bf18 ; <UNDEFINED> instruction: 0x2600bf18

6658: d0091c57 andle r1, r9, r7, asr ip

665c: e77faf11 ; <UNDEFINED> instruction: 0xe77faf11

6660: 0000f8d9 ldrdeq pc, [r0], -r9

6664: e9def7fa ldmib lr, {r1, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}^

6668: 2b009b08 blcs 2d290 <\_r\_debug@@GLIBC\_2.4+0x3934>

666c: 2600bf18 ; <UNDEFINED> instruction: 0x2600bf18

6670: 2b009b07 blcs 2d294 <\_r\_debug@@GLIBC\_2.4+0x3938>

6674: 808ff000 addhi pc, pc, r0

6678: 3574f8df ldrbcc pc, [r4, #-2271]! ; 0xfffff721 ; <UNPREDICTABLE>

667c: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

6680: f100051d ; <UNDEFINED> instruction: 0xf100051d

6684: 46208088 strtmi r8, [r0], -r8, lsl #1

6688: f842f00c ; <UNDEFINED> instruction: 0xf842f00c

668c: 28004607 stmdacs r0, {r0, r1, r2, r9, sl, lr}

6690: 827ef000 rsbshi pc, lr, #0

6694: 46339a9c ; <UNDEFINED> instruction: 0x46339a9c

6698: b004f8cd andlt pc, r4, sp, asr #17

669c: 92004621 andls r4, r0, #34603008 ; 0x2100000

66a0: f0014642 ; <UNDEFINED> instruction: 0xf0014642

66a4: 4605febd ; <UNDEFINED> instruction: 0x4605febd

66a8: f0002800 ; <UNDEFINED> instruction: 0xf0002800

66ac: f8b08271 ; <UNDEFINED> instruction: 0xf8b08271

66b0: 23012198 movwcs r2, #4504 ; 0x1198

66b4: 453cf8df ldrmi pc, [ip, #-2271]! ; 0xfffff721

66b8: f4424659 vst1.16 {d20-d22}, [r2 :64], r9

66bc: f8c07201 ; <UNDEFINED> instruction: 0xf8c07201

66c0: 447c317c ldrbtmi r3, [ip], #-380 ; 0xfffffe84

66c4: 2198f8a0 orrscs pc, r8, r0, lsr #17

66c8: 4190f8c0 orrsmi pc, r0, r0, asr #17

66cc: fe6ef001 cdp2 0, 6, cr15, cr14, cr1, {0}

66d0: f8d3e705 ; <UNDEFINED> instruction: 0xf8d3e705

66d4: 2b0031b0 blcs 12d9c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2034>

66d8: 81f7f040 mvnshi pc, r0, asr #32

66dc: 053bf10d ldreq pc, [fp, #-269]! ; 0xfffffef3

66e0: 4620212f strtmi r2, [r0], -pc, lsr #2

66e4: f00b702e ; <UNDEFINED> instruction: 0xf00b702e

66e8: 9009fccb andls pc, r9, fp, asr #25

66ec: f0002800 ; <UNDEFINED> instruction: 0xf0002800

66f0: 96098232 ; <UNDEFINED> instruction: 0x96098232

66f4: f10d4620 ; <UNDEFINED> instruction: 0xf10d4620

66f8: f00c093c ; <UNDEFINED> instruction: 0xf00c093c

66fc: f8c9f809 ; <UNDEFINED> instruction: 0xf8c9f809

6700: 28000000 stmdacs r0, {} ; <UNPREDICTABLE>

6704: 234cd0b0 movtcs sp, #49328 ; 0xc0b0

6708: 24ecf8df strbtcs pc, [ip], #2271 ; 0x8df ; <UNPREDICTABLE>

670c: f30bfb03 vqrdmulh.s<illegal width 8> d15, d11, d3

6710: 58d2447a ldmpl r2, {r1, r3, r4, r5, r6, sl, lr}^

6714: f8dfe712 ; <UNDEFINED> instruction: 0xf8dfe712

6718: 469a24e4 ldrmi r2, [sl], r4, ror #9

671c: 14e0f8df strbtne pc, [r0], #2271 ; 0x8df ; <UNPREDICTABLE>

6720: 447a4637 ldrbtmi r4, [sl], #-1591 ; 0xfffff9c9

6724: b274f8cd rsbslt pc, r4, #13434880 ; 0xcd0000

6728: f10d4479 ; <UNDEFINED> instruction: 0xf10d4479

672c: 6812093c ldmdavs r2, {r2, r3, r4, r5, r8, fp}

6730: f8cd910c ; <UNDEFINED> instruction: 0xf8cd910c

6734: 920a8034 andls r8, sl, #52 ; 0x34

6738: b02cf8dd ldrdlt pc, [ip], -sp ; <UNPREDICTABLE>

673c: 4643e01a ; <UNDEFINED> instruction: 0x4643e01a

6740: a8112204 ldmdage r1, {r2, r9, sp}

6744: 90019203 andls r9, r1, r3, lsl #4

6748: 95044659 strls r4, [r4, #-1625] ; 0xfffff9a7

674c: 96024620 strls r4, [r2], -r0, lsr #12

6750: f8cd9a9c ; <UNDEFINED> instruction: 0xf8cd9a9c

6754: f7ff9000 ; <UNDEFINED> instruction: 0xf7ff9000

6758: 1c43f825 mcrrne 8, 2, pc, r3, cr5 ; <UNPREDICTABLE>

675c: 8153f040 cmphi r3, r0, asr #32 ; <UNPREDICTABLE>

6760: 42bb9b0a adcsmi r9, fp, #10240 ; 0x2800

6764: f04abf08 ; <UNDEFINED> instruction: 0xf04abf08

6768: f8d70a01 ; <UNDEFINED> instruction: 0xf8d70a01

676c: 2f007170 svccs 0x00007170

6770: 80f2f000 rscshi pc, r2, r0

6774: 319cf8d7 ; <UNDEFINED> instruction: 0x319cf8d7

6778: 78cef507 stmiavc lr, {r0, r1, r2, r8, sl, ip, sp, lr, pc}^

677c: d0f41c5a rscsle r1, r4, sl, asr ip

6780: d1dc2b00 bicsle r2, ip, r0, lsl #22

6784: 220f9b0c andcs r9, pc, #12, 22 ; 0x3000

6788: 46384641 ldrtmi r4, [r8], -r1, asr #12

678c: fcdaf7ff ldc2l 7, cr15, [sl], {255} ; 0xff

6790: d1d42800 bicsle r2, r4, r0, lsl #16

6794: f89de7e9 ; <UNDEFINED> instruction: 0xf89de7e9

6798: 2a00203b bcs e88c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe74>

679c: 819af000 orrshi pc, sl, r0

67a0: 3460f8df strbtcc pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

67a4: 46212200 strtmi r2, [r1], -r0, lsl #4

67a8: 447b4610 ldrbtmi r4, [fp], #-1552 ; 0xfffff9f0

67ac: e92cf7fa stmdb ip!, {r1, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}

67b0: 93092300 movwls r2, #37632 ; 0x9300

67b4: 0450f8df ldrbeq pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

67b8: 4621465a ; <UNDEFINED> instruction: 0x4621465a

67bc: f0054478 ; <UNDEFINED> instruction: 0xf0054478

67c0: e6f6fad9 usat pc, #22, r9, asr #21 ; <UNPREDICTABLE>

67c4: d0842e00 addle r2, r4, r0, lsl #28

67c8: 2b009b08 blcs 2d3f0 <\_r\_debug@@GLIBC\_2.4+0x3a94>

67cc: 8121f000 msrhi CPSR\_c, r0

67d0: 0438f8df ldrteq pc, [r8], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

67d4: 68734478 ldmdavs r3!, {r3, r4, r5, r6, sl, lr}^

67d8: b93a781a ldmdblt sl!, {r1, r3, r4, fp, ip, sp, lr}

67dc: 3430f8df ldrtcc pc, [r0], #-2271 ; 0xfffff721 ; <UNPREDICTABLE>

67e0: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

67e4: 2b00681b blcs 20858 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfaf0>

67e8: 81a7f000 ; <UNDEFINED> instruction: 0x81a7f000

67ec: 465a69b1 ; <UNDEFINED> instruction: 0x465a69b1

67f0: 7420f8df strtvc pc, [r0], #-2271 ; 0xfffff721

67f4: 9100447f tstls r0, pc, ror r4

67f8: f0054621 ; <UNDEFINED> instruction: 0xf0054621

67fc: f8d7fabb ; <UNDEFINED> instruction: 0xf8d7fabb

6800: 2b0031b0 blcs 12ec8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2160>

6804: 808af040 addhi pc, sl, r0, asr #32

6808: 053bf10d ldreq pc, [fp, #-269]! ; 0xfffffef3

680c: 4620212f strtmi r2, [r0], -pc, lsr #2

6810: f00b702b ; <UNDEFINED> instruction: 0xf00b702b

6814: 2800fc35 stmdacs r0, {r0, r2, r4, r5, sl, fp, ip, sp, lr, pc}

6818: 8171f000 cmnhi r1, r0 ; <UNPREDICTABLE>

681c: 93092300 movwls r2, #37632 ; 0x9300

6820: 2e00e680 cfmadd32cs mvax4, mvfx14, mvfx0, mvfx0

6824: aef1f47f mrcge 4, 7, APSR\_nzcv, cr1, cr15, {3}

6828: f8c92300 ; <UNDEFINED> instruction: 0xf8c92300

682c: 9b9c3000 blls fe712834 <\_r\_debug@@GLIBC\_2.4+0xfe6e8ed8>

6830: f1000158 cps #24

6834: 4bf88185 blmi ffe26e50 <\_r\_debug@@GLIBC\_2.4+0xffdfd4f4>

6838: 695b447b ldmdbvs fp, {r0, r1, r3, r4, r5, r6, sl, lr}^

683c: d14b2b00 cmple fp, r0, lsl #22

6840: f0084620 ; <UNDEFINED> instruction: 0xf0084620

6844: 4607fe81 strmi pc, [r7], -r1, lsl #29

6848: d0452800 suble r2, r5, r0, lsl #16

684c: f0002e00 ; <UNDEFINED> instruction: 0xf0002e00

6850: f8d68158 ; <UNDEFINED> instruction: 0xf8d68158

6854: 0518320c ldreq r3, [r8, #-524] ; 0xfffffdf4

6858: 4632d411 ; <UNDEFINED> instruction: 0x4632d411

685c: 21009b9c ; <UNDEFINED> instruction: 0x21009b9c

6860: 46389501 ldrtmi r9, [r8], -r1, lsl #10

6864: a9119102 ldmdbge r1, {r1, r8, ip, pc}

6868: 23089300 movwcs r9, #33536 ; 0x8300

686c: fd14f7fe ldc2 7, cr15, [r4, #-1016] ; 0xfffffc08

6870: 46021c43 strmi r1, [r2], -r3, asr #24

6874: 816bf000 msrhi SPSR\_fxc, r0

6878: 7000f8c9 andvc pc, r0, r9, asr #17

687c: 4ae7e6e1 bmi ffa00408 <\_r\_debug@@GLIBC\_2.4+0xff9d6aac>

6880: 930a2319 movwls r2, #41753 ; 0xa319

6884: 447a4638 ldrbtmi r4, [sl], #-1592 ; 0xfffff9c8

6888: 9030f8cd eorsls pc, r0, sp, asr #17

688c: 034cf102 movteq pc, #49410 ; 0xc102 ; <UNPREDICTABLE>

6890: 0158f102 cmpeq r8, r2, lsl #2 ; <UNPREDICTABLE>

6894: 910d4692 ; <UNDEFINED> instruction: 0x910d4692

6898: 46519a0a ldrbmi r9, [r1], -sl, lsl #20

689c: 462546a9 strtmi r4, [r5], -r9, lsr #13

68a0: f00c461c ; <UNDEFINED> instruction: 0xf00c461c

68a4: b180f8cb orrlt pc, r0, fp, asr #17

68a8: 429c9b0d addsmi r9, ip, #13312 ; 0x3400

68ac: 8168f000 msrhi SPSR\_f, r0

68b0: 46389a0a ldrtmi r9, [r8], -sl, lsl #20

68b4: 3b04f854 blcc 144a0c <\_r\_debug@@GLIBC\_2.4+0x11b0b0>

68b8: 44923201 ldrmi r3, [r2], #513 ; 0x201

68bc: 9a0a930a bls 2ab4ec <\_r\_debug@@GLIBC\_2.4+0x281b90>

68c0: f00c4651 ; <UNDEFINED> instruction: 0xf00c4651

68c4: 2800f8bb stmdacs r0, {r0, r1, r3, r4, r5, r7, fp, ip, sp, lr, pc}

68c8: 4638d1ee ldrtmi sp, [r8], -lr, ror #3

68cc: 464d462c strbmi r4, [sp], -ip, lsr #12

68d0: 9030f8dd ldrsbtls pc, [r0], -sp ; <UNPREDICTABLE>

68d4: e8a6f7fa stmia r6!, {r1, r3, r4, r5, r6, r7, r8, r9, sl, ip, sp, lr, pc}

68d8: 2e004631 mcrcs 6, 0, r4, cr0, cr1, {1}

68dc: 80e4f000 rschi pc, r4, r0

68e0: 320cf8d1 andcc pc, ip, #13697024 ; 0xd10000

68e4: f100051f ; <UNDEFINED> instruction: 0xf100051f

68e8: 4bcd8145 blmi ff366e04 <\_r\_debug@@GLIBC\_2.4+0xff33d4a8>

68ec: 699a447b ldmibvs sl, {r0, r1, r3, r4, r5, r6, sl, lr}

68f0: f43f1c50 ; <UNDEFINED> instruction: 0xf43f1c50

68f4: 9102aea6 smlatbls r2, r6, lr, sl

68f8: 95042240 strls r2, [r4, #-576] ; 0xfffffdc0

68fc: 92033318 andls r3, r3, #24, 6 ; 0x60000000

6900: 9a9caf11 bls fe73254c <\_r\_debug@@GLIBC\_2.4+0xfe708bf0>

6904: 990b4620 stmdbls fp, {r5, r9, sl, lr}

6908: 9000f8cd andls pc, r0, sp, asr #17

690c: f7fe9701 ; <UNDEFINED> instruction: 0xf7fe9701

6910: 4602ff49 strmi pc, [r2], -r9, asr #30

6914: 2e00e695 mcrcs 6, 0, lr, cr0, cr5, {4}

6918: 80d7f000 sbcshi pc, r7, r0

691c: 3199f896 ; <UNDEFINED> instruction: 0x3199f896

6920: d579071a ldrble r0, [r9, #-1818]! ; 0xfffff8e6

6924: 053bf10d ldreq pc, [fp, #-269]! ; 0xfffffef3

6928: 4620212f strtmi r2, [r0], -pc, lsr #2

692c: 702b2300 eorvc r2, fp, r0, lsl #6

6930: fba6f00b blx fe9c2966 <\_r\_debug@@GLIBC\_2.4+0xfe99900a>

6934: f47f2800 ; <UNDEFINED> instruction: 0xf47f2800

6938: 4bbaaf71 blmi feeb2704 <\_r\_debug@@GLIBC\_2.4+0xfee88da8>

693c: 681f447b ldmdavs pc, {r0, r1, r3, r4, r5, r6, sl, lr} ; <UNPREDICTABLE>

6940: f00b4620 ; <UNDEFINED> instruction: 0xf00b4620

6944: f017fefd ; <UNDEFINED> instruction: 0xf017fefd

6948: f1000701 ; <UNDEFINED> instruction: 0xf1000701

694c: 930b0301 movwls r0, #45825 ; 0xb301

6950: af2ef47f svcge 0x002ef47f

6954: e62f9709 strt r9, [pc], -r9, lsl #14

6958: f8dd4653 ; <UNDEFINED> instruction: 0xf8dd4653

695c: f8dd8034 ; <UNDEFINED> instruction: 0xf8dd8034

6960: 2b00b274 blcs 33338 <\_r\_debug@@GLIBC\_2.4+0x99dc>

6964: 80c4f040 sbchi pc, r4, r0, asr #32

6968: b32b9b0a ; <UNDEFINED> instruction: 0xb32b9b0a

696c: f8929a0a ; <UNDEFINED> instruction: 0xf8929a0a

6970: f0033198 ; <UNDEFINED> instruction: 0xf0033198

6974: 2b020303 blcs 87588 <\_r\_debug@@GLIBC\_2.4+0x5dc2c>

6978: f8d2d01e ; <UNDEFINED> instruction: 0xf8d2d01e

697c: f502319c ; <UNDEFINED> instruction: 0xf502319c

6980: 1c5877ce mrrcne 7, 12, r7, r8, cr14

6984: 2b00d018 blcs 3a9ec <\_r\_debug@@GLIBC\_2.4+0x11090>

6988: 80ccf000 sbchi pc, ip, r0

698c: 22049b0a andcs r9, r4, #10240 ; 0x2800

6990: 4620990b strtmi r9, [r0], -fp, lsl #18

6994: bf182e00 svclt 0x00182e00

6998: 92034633 andls r4, r3, #53477376 ; 0x3300000

699c: 463b9302 ldrtmi r9, [fp], -r2, lsl #6

69a0: af119a9c svcge 0x00119a9c

69a4: f8cd9504 ; <UNDEFINED> instruction: 0xf8cd9504

69a8: 97019000 strls r9, [r1, -r0]

69ac: fefaf7fe mrc2 7, 7, pc, cr10, cr14, {7}

69b0: 46021c41 strmi r1, [r2], -r1, asr #24

69b4: ae45f47f mcrge 4, 2, pc, cr5, cr15, {3} ; <UNPREDICTABLE>

69b8: 447b4b9b ldrbtmi r4, [fp], #-2971 ; 0xfffff465

69bc: 3201691a andcc r6, r1, #425984 ; 0x68000

69c0: af2ff43f svcge 0x002ff43f

69c4: f47f2e00 ; <UNDEFINED> instruction: 0xf47f2e00

69c8: 4a98ae04 bmi fe6321e0 <\_r\_debug@@GLIBC\_2.4+0xfe608884>

69cc: 95042102 strls r2, [r4, #-258] ; 0xfffffefe

69d0: 447a3310 ldrbtmi r3, [sl], #-784 ; 0xfffffcf0

69d4: 9000f8cd andls pc, r0, sp, asr #17

69d8: 4620af11 qadd16mi sl, r0, r1

69dc: 91036812 tstls r3, r2, lsl r8

69e0: 92029701 andls r9, r2, #262144 ; 0x40000

69e4: 9a9c990b bls fe72ce18 <\_r\_debug@@GLIBC\_2.4+0xfe7034bc>

69e8: fedcf7fe mrc2 7, 6, pc, cr12, cr14, {7}

69ec: e6254602 strt r4, [r5], -r2, lsl #12

69f0: 221d4b8f andscs r4, sp, #146432 ; 0x23c00

69f4: 46304639 ; <UNDEFINED> instruction: 0x46304639

69f8: f7ff447b ; <UNDEFINED> instruction: 0xf7ff447b

69fc: 2800fba3 stmdacs r0, {r0, r1, r5, r7, r8, r9, fp, ip, sp, lr, pc}

6a00: ae0df47f mcrge 4, 0, pc, cr13, cr15, {3} ; <UNPREDICTABLE>

6a04: 4602e710 ; <UNDEFINED> instruction: 0x4602e710

6a08: 8034f8dd ldrsbthi pc, [r4], -sp ; <UNPREDICTABLE>

6a0c: b274f8dd rsbslt pc, r4, #14483456 ; 0xdd0000

6a10: 4888e617 stmmi r8, {r0, r1, r2, r4, r9, sl, sp, lr, pc}

6a14: e6de4478 ; <UNDEFINED> instruction: 0xe6de4478

6a18: 447b4b87 ldrbtmi r4, [fp], #-2951 ; 0xfffff479

6a1c: 51acf8d3 ldrdpl pc, [ip, r3]!

6a20: 27004b86 strcs r4, [r0, -r6, lsl #23]

6a24: a218f8df andsge pc, r8, #14614528 ; 0xdf0000

6a28: 4a86447b bmi fe197c1c <\_r\_debug@@GLIBC\_2.4+0xfe16e2c0>

6a2c: f8cd44fa ; <UNDEFINED> instruction: 0xf8cd44fa

6a30: f8cd8028 ; <UNDEFINED> instruction: 0xf8cd8028

6a34: 46b8b274 ; <UNDEFINED> instruction: 0x46b8b274

6a38: 461c46a3 ldrmi r4, [ip], -r3, lsr #13

6a3c: 9209447a andls r4, r9, #2046820352 ; 0x7a000000

6a40: b1bb686b ; <UNDEFINED> instruction: 0xb1bb686b

6a44: 7118f506 tstvc r8, r6, lsl #10 ; <UNPREDICTABLE>

6a48: eb012201 bl 4f254 <\_r\_debug@@GLIBC\_2.4+0x258f8>

6a4c: 465801c7 ldrbmi r0, [r8], -r7, asr #3

6a50: 46814798 pkhbtmi r4, r1, r8, lsl #15

6a54: d0452800 suble r2, r5, r0, lsl #16

6a58: d00b4558 andle r4, fp, r8, asr r5

6a5c: 46584601 ldrbmi r4, [r8], -r1, lsl #12

6a60: fb76f00b blx 1dc2a96 <\_r\_debug@@GLIBC\_2.4+0x1d9913a>

6a64: 6822b128 stmdavs r2!, {r3, r5, r8, ip, sp, pc}

6a68: d4140650 ldrle r0, [r4], #-1616 ; 0xfffff9b0

6a6c: 0f00f1b8 svceq 0x0000f1b8

6a70: 46cbd00e strbmi sp, [fp], lr

6a74: 21b0f8da asrscs pc, sl ; <illegal shifter operand> ; <UNPREDICTABLE>

6a78: 6a2d3701 bvs b54684 <\_r\_debug@@GLIBC\_2.4+0xb2ad28>

6a7c: d8df42ba ldmle pc, {r1, r3, r4, r5, r7, r9, lr}^ ; <UNPREDICTABLE>

6a80: 8024f8cd eorhi pc, r4, sp, asr #17

6a84: f8dd465c ; <UNDEFINED> instruction: 0xf8dd465c

6a88: f8dd8028 ; <UNDEFINED> instruction: 0xf8dd8028

6a8c: e53cb274 ldr fp, [ip, #-628]! ; 0xfffffd8c

6a90: 46cb46d8 ; <UNDEFINED> instruction: 0x46cb46d8

6a94: 464ae7ee strbmi lr, [sl], -lr, ror #15

6a98: 98094659 stmdals r9, {r0, r3, r4, r6, r9, sl, lr}

6a9c: f96af005 ; <UNDEFINED> instruction: 0xf96af005

6aa0: 0f00f1b8 svceq 0x0000f1b8

6aa4: e7f3d1e5 ldrb sp, [r3, r5, ror #3]!

6aa8: 4a67234c bmi 19cf7e0 <\_r\_debug@@GLIBC\_2.4+0x19a5e84>

6aac: f30bfb03 vqrdmulh.s<illegal width 8> d15, d11, d3

6ab0: 58d1447a ldmpl r1, {r1, r3, r4, r5, r6, sl, lr}^

6ab4: f43f2900 ; <UNDEFINED> instruction: 0xf43f2900

6ab8: e711af18 ; <UNDEFINED> instruction: 0xe711af18

6abc: 920a4863 andls r4, sl, #6488064 ; 0x630000

6ac0: f0054478 ; <UNDEFINED> instruction: 0xf0054478

6ac4: 9a0af957 bls 2c5028 <\_r\_debug@@GLIBC\_2.4+0x29b6cc>

6ac8: 4b61e5c2 blmi 18801d8 <\_r\_debug@@GLIBC\_2.4+0x185687c>

6acc: f8d3447b ; <UNDEFINED> instruction: 0xf8d3447b

6ad0: e7a551ac str r5, [r5, ip, lsr #3]!

6ad4: 4621485f ; <UNDEFINED> instruction: 0x4621485f

6ad8: 44784b5f ldrbtmi r4, [r8], #-2911 ; 0xfffff4a1

6adc: 6800447b stmdavs r0, {r0, r1, r3, r4, r5, r6, sl, lr}

6ae0: ef92f7f9 svc 0x0092f7f9

6ae4: 8028f8dd ldrdhi pc, [r8], -sp ; <UNPREDICTABLE>

6ae8: f8dd4604 ; <UNDEFINED> instruction: 0xf8dd4604

6aec: e5bbb274 ldr fp, [fp, #628]! ; 0x274

6af0: 447b4b5a ldrbtmi r4, [fp], #-2906 ; 0xfffff4a6

6af4: 3301691b movwcc r6, #6427 ; 0x191b

6af8: ad87f43f cfstrsge mvf15, [r7, #252] ; 0xfc

6afc: 683fe569 ldmdavs pc!, {r0, r3, r5, r6, r8, sl, sp, lr, pc} ; <UNPREDICTABLE>

6b00: 4a57e71e bmi 1600780 <\_r\_debug@@GLIBC\_2.4+0x15d6e24>

6b04: 6813447a ldmdavs r3, {r1, r3, r4, r5, r6, sl, lr}

6b08: d0362b00 eorsle r2, r6, r0, lsl #22

6b0c: 320cf8d3 andcc pc, ip, #13828096 ; 0xd30000

6b10: f53f0519 ; <UNDEFINED> instruction: 0xf53f0519

6b14: 234caeb4 movtcs sl, #52916 ; 0xceb4

6b18: fb034a52 blx d946a <\_r\_debug@@GLIBC\_2.4+0xafb0e>

6b1c: 447af30b ldrbtmi pc, [sl], #-779 ; 0xfffffcf5 ; <UNPREDICTABLE>

6b20: e69b58d2 ; <UNDEFINED> instruction: 0xe69b58d2

6b24: 220f4b50 andcs r4, pc, #80, 22 ; 0x14000

6b28: 980a4639 stmdals sl, {r0, r3, r4, r5, r9, sl, lr}

6b2c: f7ff447b ; <UNDEFINED> instruction: 0xf7ff447b

6b30: 2800fb09 stmdacs r0, {r0, r3, r8, r9, fp, ip, sp, lr, pc}

6b34: af2af47f svcge 0x002af47f

6b38: 4b4ce73e blmi 1340838 <\_r\_debug@@GLIBC\_2.4+0x1316edc>

6b3c: e655447b ; <UNDEFINED> instruction: 0xe655447b

6b40: 447b4b4b ldrbtmi r4, [fp], #-2891 ; 0xfffff4b5

6b44: 2b00681b blcs 20bb8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfe50>

6b48: aec6f47f mcrge 4, 6, pc, cr6, cr15, {3} ; <UNPREDICTABLE>

6b4c: 4638e673 ; <UNDEFINED> instruction: 0x4638e673

6b50: ef68f7f9 svc 0x0068f7f9

6b54: 4620e6c0 strtmi lr, [r0], -r0, asr #13

6b58: fdf2f00b ldc2l 0, cr15, [r2, #44]! ; 0x2c

6b5c: 930b1c43 movwls r1, #48195 ; 0xbc43

6b60: f53f07fb ; <UNDEFINED> instruction: 0xf53f07fb

6b64: 4b43ae27 blmi 10f2408 <\_r\_debug@@GLIBC\_2.4+0x10c8aac>

6b68: 093cf10d ldmdbeq ip!, {r0, r2, r3, r8, ip, sp, lr, pc}

6b6c: 681b447b ldmdavs fp, {r0, r1, r3, r4, r5, r6, sl, lr}

6b70: e6f9930a ldrbt r9, [r9], sl, lsl #6

6b74: 32fff04f rscscc pc, pc, #79 ; 0x4f

6b78: f502e563 ; <UNDEFINED> instruction: 0xf502e563

6b7c: e7c563a3 strb r6, [r5, r3, lsr #7]

6b80: 464d462c strbmi r4, [sp], -ip, lsr #12

6b84: 9030f8dd ldrsbtls pc, [r0], -sp ; <UNPREDICTABLE>

6b88: f47f2e00 ; <UNDEFINED> instruction: 0xf47f2e00

6b8c: e7c2ae66 strb sl, [r2, r6, ror #28]

6b90: f7f94638 ; <UNDEFINED> instruction: 0xf7f94638

6b94: 4b38ef48 blmi e428bc <\_r\_debug@@GLIBC\_2.4+0xe18f60>

6b98: 22004621 andcs r4, r0, #34603008 ; 0x2100000

6b9c: 200c447b andcs r4, ip, fp, ror r4

6ba0: ef32f7f9 svc 0x0032f7f9

6ba4: f6404b35 ; <UNDEFINED> instruction: 0xf6404b35

6ba8: 49350276 ldmdbmi r5!, {r1, r2, r4, r5, r6, r9}

6bac: 447b4835 ldrbtmi r4, [fp], #-2101 ; 0xfffff7cb

6bb0: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

6bb4: feeaf009 cdp2 0, 14, cr15, cr10, cr9, {0}

6bb8: f6404b33 ; <UNDEFINED> instruction: 0xf6404b33

6bbc: 49330275 ldmdbmi r3!, {r0, r2, r4, r5, r6, r9}

6bc0: 447b4833 ldrbtmi r4, [fp], #-2099 ; 0xfffff7cd

6bc4: 44784479 ldrbtmi r4, [r8], #-1145 ; 0xfffffb87

6bc8: fee0f009 cdp2 0, 14, cr15, cr0, cr9, {0}

6bcc: 00022c14 andeq r2, r2, r4, lsl ip

6bd0: 0000fb5c andeq pc, r0, ip, asr fp ; <UNPREDICTABLE>

6bd4: 0000fb6a andeq pc, r0, sl, ror #22

6bd8: 0002247e andeq r2, r2, lr, ror r4

6bdc: 000229ca andeq r2, r2, sl, asr #19

6be0: 000223ca andeq r2, r2, sl, asr #7

6be4: 0002255a andeq r2, r2, sl, asr r5

6be8: 00022548 andeq r2, r2, r8, asr #10

6bec: 00022326 andeq r2, r2, r6, lsr #6

6bf0: 000222f0 strdeq r2, [r2], -r0

6bf4: 00011476 andeq r1, r1, r6, ror r4

6bf8: 0002294c andeq r2, r2, ip, asr #18

6bfc: 0002293a andeq r2, r2, sl, lsr r9

6c00: 0000f808 andeq pc, r0, r8, lsl #16

6c04: 0000f88a andeq pc, r0, sl, lsl #17

6c08: 0000f854 andeq pc, r0, r4, asr r8 ; <UNPREDICTABLE>

6c0c: 0000f7a0 andeq pc, r0, r0, lsr #15

6c10: 000230b8 strheq r3, [r2], -r8

6c14: 00022178 andeq r2, r2, r8, ror r1

6c18: 00022134 andeq r2, r2, r4, lsr r1

6c1c: 0000d8f6 strdeq sp, [r0], -r6

6c20: 00022238 andeq r2, r2, r8, lsr r2

6c24: 00022030 andeq r2, r2, r0, lsr r0

6c28: 0002216a andeq r2, r2, sl, ror #2

6c2c: 0002268a andeq r2, r2, sl, lsl #13

6c30: 0000f530 andeq pc, r0, r0, lsr r5 ; <UNPREDICTABLE>

6c34: 0000f538 andeq pc, r0, r8, lsr r5 ; <UNPREDICTABLE>

6c38: 00021f52 andeq r1, r2, r2, asr pc

6c3c: 00021f44 andeq r1, r2, r4, asr #30

6c40: 00021f40 andeq r1, r2, r0, asr #30

6c44: 0000f5b0 ; <UNDEFINED> instruction: 0x0000f5b0

6c48: 000225ac andeq r2, r2, ip, lsr #11

6c4c: 0000f828 andeq pc, r0, r8, lsr #16

6c50: 00021ea0 andeq r1, r2, r0, lsr #29

6c54: 00022e7a andeq r2, r2, sl, ror lr

6c58: 0000f574 andeq pc, r0, r4, ror r5 ; <UNPREDICTABLE>

6c5c: 00022032 andeq r2, r2, r2, lsr r0

6c60: 00022558 andeq r2, r2, r8, asr r5

6c64: 0002253e andeq r2, r2, lr, lsr r5

6c68: 0000f404 andeq pc, r0, r4, lsl #8

6c6c: 0000df9c muleq r0, ip, pc ; <UNPREDICTABLE>

6c70: 000223fa strdeq r2, [r2], -sl

6c74: 000224f0 strdeq r2, [r2], -r0

6c78: 0000ecb8 ; <UNDEFINED> instruction: 0x0000ecb8

6c7c: 0000f4ee andeq pc, r0, lr, ror #9

6c80: 0000ec64 andeq lr, r0, r4, ror #24

6c84: 0000f402 andeq pc, r0, r2, lsl #8

6c88: 0000f4da ldrdeq pc, [r0], -sl

6c8c: 0000ec50 andeq lr, r0, r0, asr ip

6c90: 0000f3e2 andeq pc, r0, r2, ror #7

00006c94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE>:

6c94: e92d 47f0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, lr}

6c98: 4607 mov r7, r0

6c9a: b084 sub sp, #16

6c9c: 460e mov r6, r1

6c9e: 4690 mov r8, r2

6ca0: bb62 cbnz r2, 6cfc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x68>

6ca2: 684b ldr r3, [r1, #4]

6ca4: 3301 adds r3, #1

6ca6: 00db lsls r3, r3, #3

6ca8: f8d7 2094 ldr.w r2, [r7, #148] ; 0x94

6cac: 4433 add r3, r6

6cae: 2100 movs r1, #0

6cb0: f88d 8000 strb.w r8, [sp]

6cb4: e9cd 6302 strd r6, r3, [sp, #8]

6cb8: 46e9 mov r9, sp

6cba: 9101 str r1, [sp, #4]

6cbc: 2a00 cmp r2, #0

6cbe: d036 beq.n 6d2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9a>

6cc0: 4b3d ldr r3, [pc, #244] ; (6db8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x124>)

6cc2: 4648 mov r0, r9

6cc4: 447b add r3, pc

6cc6: 6919 ldr r1, [r3, #16]

6cc8: f7fe ff64 bl 5b94 <free@plt+0x5174>

6ccc: f8d7 11f0 ldr.w r1, [r7, #496] ; 0x1f0

6cd0: 1c4a adds r2, r1, #1

6cd2: d003 beq.n 6cdc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48>

6cd4: b1b9 cbz r1, 6d06 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x72>

6cd6: 4648 mov r0, r9

6cd8: f7fe ff5c bl 5b94 <free@plt+0x5174>

6cdc: f8d7 320c ldr.w r3, [r7, #524] ; 0x20c

6ce0: 051b lsls r3, r3, #20

6ce2: d51d bpl.n 6d20 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8c>

6ce4: f1b8 0f00 cmp.w r8, #0

6ce8: d005 beq.n 6cf6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x62>

6cea: e9d6 3200 ldrd r3, r2, [r6]

6cee: 3201 adds r2, #1

6cf0: eb03 03c2 add.w r3, r3, r2, lsl #3

6cf4: 6033 str r3, [r6, #0]

6cf6: b004 add sp, #16

6cf8: e8bd 87f0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, pc}

6cfc: 2200 movs r2, #0

6cfe: 2308 movs r3, #8

6d00: e9c1 2200 strd r2, r2, [r1]

6d04: e7d0 b.n 6ca8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14>

6d06: 4b2d ldr r3, [pc, #180] ; (6dbc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x128>)

6d08: 221d movs r2, #29

6d0a: f507 71f8 add.w r1, r7, #496 ; 0x1f0

6d0e: 4638 mov r0, r7

6d10: 447b add r3, pc

6d12: f7ff fa17 bl 6144 <free@plt+0x5724>

6d16: 2800 cmp r0, #0

6d18: d0e0 beq.n 6cdc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48>

6d1a: f8d7 11f0 ldr.w r1, [r7, #496] ; 0x1f0

6d1e: e7da b.n 6cd6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x42>

6d20: 4b27 ldr r3, [pc, #156] ; (6dc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12c>)

6d22: 4648 mov r0, r9

6d24: 447b add r3, pc

6d26: 6999 ldr r1, [r3, #24]

6d28: f7fe ff34 bl 5b94 <free@plt+0x5174>

6d2c: e7da b.n 6ce4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x50>

6d2e: f8df a094 ldr.w sl, [pc, #148] ; 6dc4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x130>

6d32: 46e9 mov r9, sp

6d34: 463c mov r4, r7

6d36: 44fa add sl, pc

6d38: e006 b.n 6d48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb4>

6d3a: 4648 mov r0, r9

6d3c: 4629 mov r1, r5

6d3e: f7fe ff29 bl 5b94 <free@plt+0x5174>

6d42: f8d4 4170 ldr.w r4, [r4, #368] ; 0x170

6d46: b18c cbz r4, 6d6c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd8>

6d48: f8d4 519c ldr.w r5, [r4, #412] ; 0x19c

6d4c: 1c6b adds r3, r5, #1

6d4e: d0f8 beq.n 6d42 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xae>

6d50: f504 71ce add.w r1, r4, #412 ; 0x19c

6d54: 4653 mov r3, sl

6d56: 220f movs r2, #15

6d58: 4620 mov r0, r4

6d5a: 2d00 cmp r5, #0

6d5c: d1ed bne.n 6d3a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa6>

6d5e: f7ff f9f1 bl 6144 <free@plt+0x5724>

6d62: 2800 cmp r0, #0

6d64: d0ed beq.n 6d42 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xae>

6d66: f8d4 519c ldr.w r5, [r4, #412] ; 0x19c

6d6a: e7e6 b.n 6d3a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa6>

6d6c: 69bb ldr r3, [r7, #24]

6d6e: 2b00 cmp r3, #0

6d70: d1a6 bne.n 6cc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c>

6d72: 4b15 ldr r3, [pc, #84] ; (6dc8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x134>)

6d74: 447b add r3, pc

6d76: 681c ldr r4, [r3, #0]

6d78: 2c00 cmp r4, #0

6d7a: d0a1 beq.n 6cc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c>

6d7c: f894 3198 ldrb.w r3, [r4, #408] ; 0x198

6d80: f003 0303 and.w r3, r3, #3

6d84: 2b02 cmp r3, #2

6d86: bf18 it ne

6d88: 42a7 cmpne r7, r4

6d8a: d099 beq.n 6cc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c>

6d8c: f8d4 119c ldr.w r1, [r4, #412] ; 0x19c

6d90: 1c48 adds r0, r1, #1

6d92: d095 beq.n 6cc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c>

6d94: b119 cbz r1, 6d9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x10a>

6d96: 4648 mov r0, r9

6d98: f7fe fefc bl 5b94 <free@plt+0x5174>

6d9c: e790 b.n 6cc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c>

6d9e: 4b0b ldr r3, [pc, #44] ; (6dcc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x138>)

6da0: 220f movs r2, #15

6da2: f504 71ce add.w r1, r4, #412 ; 0x19c

6da6: 4620 mov r0, r4

6da8: 447b add r3, pc

6daa: f7ff f9cb bl 6144 <free@plt+0x5724>

6dae: 2800 cmp r0, #0

6db0: d086 beq.n 6cc0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c>

6db2: f8d4 119c ldr.w r1, [r4, #412] ; 0x19c

6db6: e7ee b.n 6d96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x102>

6db8: 1e60 subs r0, r4, #1

6dba: 0002 movs r2, r0

6dbc: f218 0000 ; <UNDEFINED> instruction: 0xf2180000

6dc0: 1e00 subs r0, r0, #0

6dc2: 0002 movs r2, r0

6dc4: f1fa 0000 ; <UNDEFINED> instruction: 0xf1fa0000

6dc8: 22e8 movs r2, #232 ; 0xe8

6dca: 0002 movs r2, r0

6dcc: f188 0000 ; <UNDEFINED> instruction: 0xf1880000

6dd0: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

6dd4: 9d07 ldr r5, [sp, #28]

6dd6: 9f0a ldr r7, [sp, #40] ; 0x28

6dd8: 686c ldr r4, [r5, #4]

6dda: 7b2e ldrb r6, [r5, #12]

6ddc: f8b5 c00e ldrh.w ip, [r5, #14]

6de0: f006 060f and.w r6, r6, #15

6de4: 2c00 cmp r4, #0

6de6: d050 beq.n 6e8a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f6>

6de8: 9c06 ldr r4, [sp, #24]

6dea: f1bc 0f00 cmp.w ip, #0

6dee: bf14 ite ne

6df0: 2400 movne r4, #0

6df2: f004 0401 andeq.w r4, r4, #1

6df6: 2c00 cmp r4, #0

6df8: d14d bne.n 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6dfa: f240 4c67 movw ip, #1127 ; 0x467

6dfe: fa4c f606 asr.w r6, ip, r6

6e02: 07f4 lsls r4, r6, #31

6e04: d547 bpl.n 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6e06: 4614 mov r4, r2

6e08: 460a mov r2, r1

6e0a: 4295 cmp r5, r2

6e0c: 461e mov r6, r3

6e0e: 4601 mov r1, r0

6e10: d006 beq.n 6e20 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x18c>

6e12: 6828 ldr r0, [r5, #0]

6e14: 9b09 ldr r3, [sp, #36] ; 0x24

6e16: 4418 add r0, r3

6e18: f00b f99a bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

6e1c: 2800 cmp r0, #0

6e1e: d13a bne.n 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6e20: f8d7 31a8 ldr.w r3, [r7, #424] ; 0x1a8

6e24: b1bc cbz r4, 6e56 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c2>

6e26: 2b00 cmp r3, #0

6e28: d03f beq.n 6eaa <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x216>

6e2a: 9a08 ldr r2, [sp, #32]

6e2c: 6861 ldr r1, [r4, #4]

6e2e: f833 8012 ldrh.w r8, [r3, r2, lsl #1]

6e32: f8d7 2174 ldr.w r2, [r7, #372] ; 0x174

6e36: f3c8 030e ubfx r3, r8, #0, #15

6e3a: 011b lsls r3, r3, #4

6e3c: 18d0 adds r0, r2, r3

6e3e: 6846 ldr r6, [r0, #4]

6e40: 428e cmp r6, r1

6e42: d02b beq.n 6e9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x208>

6e44: 68a3 ldr r3, [r4, #8]

6e46: 4333 orrs r3, r6

6e48: d125 bne.n 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6e4a: f418 4f00 tst.w r8, #32768 ; 0x8000

6e4e: d122 bne.n 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6e50: 4628 mov r0, r5

6e52: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

6e56: 2b00 cmp r3, #0

6e58: d0fa beq.n 6e50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc>

6e5a: 9a08 ldr r2, [sp, #32]

6e5c: f016 0f02 tst.w r6, #2

6e60: f833 3012 ldrh.w r3, [r3, r2, lsl #1]

6e64: bf14 ite ne

6e66: 2202 movne r2, #2

6e68: 2203 moveq r2, #3

6e6a: f3c3 010e ubfx r1, r3, #0, #15

6e6e: 4291 cmp r1, r2

6e70: dbee blt.n 6e50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc>

6e72: 041b lsls r3, r3, #16

6e74: d40f bmi.n 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6e76: 9b0c ldr r3, [sp, #48] ; 0x30

6e78: 990c ldr r1, [sp, #48] ; 0x30

6e7a: 681b ldr r3, [r3, #0]

6e7c: 1c5a adds r2, r3, #1

6e7e: 600a str r2, [r1, #0]

6e80: b94b cbnz r3, 6e96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x202>

6e82: 9b0b ldr r3, [sp, #44] ; 0x2c

6e84: 4620 mov r0, r4

6e86: 601d str r5, [r3, #0]

6e88: e7e3 b.n 6e52 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1be>

6e8a: f64f 74f1 movw r4, #65521 ; 0xfff1

6e8e: 2e06 cmp r6, #6

6e90: bf18 it ne

6e92: 45a4 cmpne ip, r4

6e94: d0a8 beq.n 6de8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x154>

6e96: 2000 movs r0, #0

6e98: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

6e9c: 58d0 ldr r0, [r2, r3]

6e9e: 6821 ldr r1, [r4, #0]

6ea0: f00b f956 bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

6ea4: 2800 cmp r0, #0

6ea6: d0d3 beq.n 6e50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc>

6ea8: e7cc b.n 6e44 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b0>

6eaa: 68e0 ldr r0, [r4, #12]

6eac: 2800 cmp r0, #0

6eae: d0cf beq.n 6e50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc>

6eb0: 4639 mov r1, r7

6eb2: f004 ff9b bl bdec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x40c>

6eb6: 2800 cmp r0, #0

6eb8: d0ca beq.n 6e50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc>

6eba: 4b04 ldr r3, [pc, #16] ; (6ecc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x238>)

6ebc: 2270 movs r2, #112 ; 0x70

6ebe: 4904 ldr r1, [pc, #16] ; (6ed0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x23c>)

6ec0: 4804 ldr r0, [pc, #16] ; (6ed4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x240>)

6ec2: 447b add r3, pc

6ec4: 4479 add r1, pc

6ec6: 4478 add r0, pc

6ec8: f009 fd60 bl 1098c <free@@GLIBC\_2.4+0x12c>

6ecc: f512 0000 adds.w r0, r2, #8388608 ; 0x800000

6ed0: f214 0000 ; <UNDEFINED> instruction: 0xf2140000

6ed4: f21e 0000 ; <UNDEFINED> instruction: 0xf21e0000

6ed8: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

6edc: b09b sub sp, #108 ; 0x6c

6ede: 9c25 ldr r4, [sp, #148] ; 0x94

6ee0: e9cd 320f strd r3, r2, [sp, #60] ; 0x3c

6ee4: f8df 2764 ldr.w r2, [pc, #1892] ; 764c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9b8>

6ee8: 6863 ldr r3, [r4, #4]

6eea: 447a add r2, pc

6eec: 900c str r0, [sp, #48] ; 0x30

6eee: 9212 str r2, [sp, #72] ; 0x48

6ef0: 4699 mov r9, r3

6ef2: 9d26 ldr r5, [sp, #152] ; 0x98

6ef4: f8dd b0a8 ldr.w fp, [sp, #168] ; 0xa8

6ef8: f8df 8754 ldr.w r8, [pc, #1876] ; 7650 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9bc>

6efc: 094b lsrs r3, r1, #5

6efe: 6827 ldr r7, [r4, #0]

6f00: 44f8 add r8, pc

6f02: f8df 2750 ldr.w r2, [pc, #1872] ; 7654 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9c0>

6f06: 930d str r3, [sp, #52] ; 0x34

6f08: eb07 0785 add.w r7, r7, r5, lsl #2

6f0c: f64a 23ab movw r3, #43691 ; 0xaaab

6f10: 447a add r2, pc

6f12: f6ca 23aa movt r3, #43690 ; 0xaaaa

6f16: 46c2 mov sl, r8

6f18: 9211 str r2, [sp, #68] ; 0x44

6f1a: 9313 str r3, [sp, #76] ; 0x4c

6f1c: 9109 str r1, [sp, #36] ; 0x24

6f1e: f8dd 80a4 ldr.w r8, [sp, #164] ; 0xa4

6f22: e03b b.n 6f9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x308>

6f24: f8d4 117c ldr.w r1, [r4, #380] ; 0x17c

6f28: 2900 cmp r1, #0

6f2a: d033 beq.n 6f94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x300>

6f2c: 6ba2 ldr r2, [r4, #56] ; 0x38

6f2e: 2000 movs r0, #0

6f30: 6b63 ldr r3, [r4, #52] ; 0x34

6f32: f8d4 6188 ldr.w r6, [r4, #392] ; 0x188

6f36: 6852 ldr r2, [r2, #4]

6f38: 685b ldr r3, [r3, #4]

6f3a: e9cd 0018 strd r0, r0, [sp, #96] ; 0x60

6f3e: 920e str r2, [sp, #56] ; 0x38

6f40: 930a str r3, [sp, #40] ; 0x28

6f42: 2e00 cmp r6, #0

6f44: d055 beq.n 6ff2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35e>

6f46: 9b09 ldr r3, [sp, #36] ; 0x24

6f48: f8d4 2180 ldr.w r2, [r4, #384] ; 0x180

6f4c: f003 001f and.w r0, r3, #31

6f50: f8d4 3184 ldr.w r3, [r4, #388] ; 0x184

6f54: 930b str r3, [sp, #44] ; 0x2c

6f56: 9b0d ldr r3, [sp, #52] ; 0x34

6f58: ea03 0c02 and.w ip, r3, r2

6f5c: 9a09 ldr r2, [sp, #36] ; 0x24

6f5e: 9b0b ldr r3, [sp, #44] ; 0x2c

6f60: 40da lsrs r2, r3

6f62: f856 302c ldr.w r3, [r6, ip, lsl #2]

6f66: f002 021f and.w r2, r2, #31

6f6a: fa23 f202 lsr.w r2, r3, r2

6f6e: 40c3 lsrs r3, r0

6f70: 4013 ands r3, r2

6f72: 07d8 lsls r0, r3, #31

6f74: f100 8093 bmi.w 709e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40a>

6f78: 2601 movs r6, #1

6f7a: 9b27 ldr r3, [sp, #156] ; 0x9c

6f7c: 2b00 cmp r3, #0

6f7e: bf08 it eq

6f80: 2600 moveq r6, #0

6f82: b13e cbz r6, 6f94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x300>

6f84: 68d8 ldr r0, [r3, #12]

6f86: b128 cbz r0, 6f94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x300>

6f88: 4621 mov r1, r4

6f8a: f004 ff2f bl bdec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x40c>

6f8e: 2800 cmp r0, #0

6f90: f040 8240 bne.w 7414 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x780>

6f94: 3501 adds r5, #1

6f96: 45a9 cmp r9, r5

6f98: f240 80cc bls.w 7134 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a0>

6f9c: f857 3b04 ldr.w r3, [r7], #4

6fa0: 695c ldr r4, [r3, #20]

6fa2: 4544 cmp r4, r8

6fa4: d0f6 beq.n 6f94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x300>

6fa6: f01b 0302 ands.w r3, fp, #2

6faa: 9308 str r3, [sp, #32]

6fac: d003 beq.n 6fb6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x322>

6fae: f894 3198 ldrb.w r3, [r4, #408] ; 0x198

6fb2: 079a lsls r2, r3, #30

6fb4: d0ee beq.n 6f94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x300>

6fb6: f894 3199 ldrb.w r3, [r4, #409] ; 0x199

6fba: 069b lsls r3, r3, #26

6fbc: d4ea bmi.n 6f94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x300>

6fbe: f8da 3000 ldr.w r3, [sl]

6fc2: 071e lsls r6, r3, #28

6fc4: d5ae bpl.n 6f24 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x290>

6fc6: 6862 ldr r2, [r4, #4]

6fc8: 7813 ldrb r3, [r2, #0]

6fca: b13b cbz r3, 6fdc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x348>

6fcc: f8df 0688 ldr.w r0, [pc, #1672] ; 7658 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9c4>

6fd0: 69a3 ldr r3, [r4, #24]

6fd2: 990c ldr r1, [sp, #48] ; 0x30

6fd4: 4478 add r0, pc

6fd6: f004 fecd bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

6fda: e7a3 b.n 6f24 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x290>

6fdc: f8df 367c ldr.w r3, [pc, #1660] ; 765c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9c8>

6fe0: 447b add r3, pc

6fe2: 681b ldr r3, [r3, #0]

6fe4: 681a ldr r2, [r3, #0]

6fe6: 2a00 cmp r2, #0

6fe8: d1f0 bne.n 6fcc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x338>

6fea: f8df 2674 ldr.w r2, [pc, #1652] ; 7660 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9cc>

6fee: 447a add r2, pc

6ff0: e7ec b.n 6fcc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x338>

6ff2: 9b10 ldr r3, [sp, #64] ; 0x40

6ff4: 6818 ldr r0, [r3, #0]

6ff6: 1c42 adds r2, r0, #1

6ff8: f000 820f beq.w 741a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x786>

6ffc: f00c fc62 bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

7000: 0089 lsls r1, r1, #2

7002: f8d4 3190 ldr.w r3, [r4, #400] ; 0x190

7006: 585e ldr r6, [r3, r1]

7008: 2e00 cmp r6, #0

700a: d0b5 beq.n 6f78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e4>

700c: ab18 add r3, sp, #96 ; 0x60

700e: aa19 add r2, sp, #100 ; 0x64

7010: 970b str r7, [sp, #44] ; 0x2c

7012: 9526 str r5, [sp, #152] ; 0x98

7014: 461d mov r5, r3

7016: f8cd 80a4 str.w r8, [sp, #164] ; 0xa4

701a: f8cd 9050 str.w r9, [sp, #80] ; 0x50

701e: f8cd a054 str.w sl, [sp, #84] ; 0x54

7022: 4692 mov sl, r2

7024: 9f0f ldr r7, [sp, #60] ; 0x3c

7026: f8dd 809c ldr.w r8, [sp, #156] ; 0x9c

702a: f8dd 9038 ldr.w r9, [sp, #56] ; 0x38

702e: e006 b.n 703e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3aa>

7030: f8d4 318c ldr.w r3, [r4, #396] ; 0x18c

7034: f853 6026 ldr.w r6, [r3, r6, lsl #2]

7038: 2e00 cmp r6, #0

703a: f000 8174 beq.w 7326 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x692>

703e: 990a ldr r1, [sp, #40] ; 0x28

7040: eb09 1c06 add.w ip, r9, r6, lsl #4

7044: 9506 str r5, [sp, #24]

7046: 4642 mov r2, r8

7048: f8cd a014 str.w sl, [sp, #20]

704c: 9103 str r1, [sp, #12]

704e: 4639 mov r1, r7

7050: 9b28 ldr r3, [sp, #160] ; 0xa0

7052: 9404 str r4, [sp, #16]

7054: 9602 str r6, [sp, #8]

7056: 980c ldr r0, [sp, #48] ; 0x30

7058: e9cd bc00 strd fp, ip, [sp]

705c: f7ff feb8 bl 6dd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13c>

7060: 2800 cmp r0, #0

7062: d0e5 beq.n 7030 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x39c>

7064: 9f0b ldr r7, [sp, #44] ; 0x2c

7066: 4603 mov r3, r0

7068: 9d26 ldr r5, [sp, #152] ; 0x98

706a: f8dd 80a4 ldr.w r8, [sp, #164] ; 0xa4

706e: e9dd 9a14 ldrd r9, sl, [sp, #80] ; 0x50

7072: 9a2b ldr r2, [sp, #172] ; 0xac

7074: 2a00 cmp r2, #0

7076: d06d beq.n 7154 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c0>

7078: 7b5a ldrb r2, [r3, #13]

707a: f002 0203 and.w r2, r2, #3

707e: 3a01 subs r2, #1

7080: 2a01 cmp r2, #1

7082: d908 bls.n 7096 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x402>

7084: 7b1a ldrb r2, [r3, #12]

7086: 0912 lsrs r2, r2, #4

7088: 2a02 cmp r2, #2

708a: d057 beq.n 713c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a8>

708c: 2a0a cmp r2, #10

708e: f000 80f8 beq.w 7282 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x5ee>

7092: 2a01 cmp r2, #1

7094: d057 beq.n 7146 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b2>

7096: fab6 f686 clz r6, r6

709a: 0976 lsrs r6, r6, #5

709c: e76d b.n 6f7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e6>

709e: 9809 ldr r0, [sp, #36] ; 0x24

70a0: f00c fc10 bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

70a4: f8d4 318c ldr.w r3, [r4, #396] ; 0x18c

70a8: f853 3021 ldr.w r3, [r3, r1, lsl #2]

70ac: 2b00 cmp r3, #0

70ae: f43f af63 beq.w 6f78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e4>

70b2: f8d4 2190 ldr.w r2, [r4, #400] ; 0x190

70b6: a918 add r1, sp, #96 ; 0x60

70b8: 9715 str r7, [sp, #84] ; 0x54

70ba: eb02 0383 add.w r3, r2, r3, lsl #2

70be: 910b str r1, [sp, #44] ; 0x2c

70c0: 9526 str r5, [sp, #152] ; 0x98

70c2: a919 add r1, sp, #100 ; 0x64

70c4: f8cd 80a4 str.w r8, [sp, #164] ; 0xa4

70c8: 461d mov r5, r3

70ca: f8cd 9058 str.w r9, [sp, #88] ; 0x58

70ce: f8cd a05c str.w sl, [sp, #92] ; 0x5c

70d2: 9114 str r1, [sp, #80] ; 0x50

70d4: 9f09 ldr r7, [sp, #36] ; 0x24

70d6: f8dd 8038 ldr.w r8, [sp, #56] ; 0x38

70da: f8dd 9030 ldr.w r9, [sp, #48] ; 0x30

70de: f8dd a03c ldr.w sl, [sp, #60] ; 0x3c

70e2: e002 b.n 70ea <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x456>

70e4: 07d9 lsls r1, r3, #31

70e6: f100 81f7 bmi.w 74d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x844>

70ea: 462e mov r6, r5

70ec: f855 3b04 ldr.w r3, [r5], #4

70f0: ea83 0207 eor.w r2, r3, r7

70f4: 0852 lsrs r2, r2, #1

70f6: d1f5 bne.n 70e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x450>

70f8: f8d4 1190 ldr.w r1, [r4, #400] ; 0x190

70fc: 9a0b ldr r2, [sp, #44] ; 0x2c

70fe: 1a76 subs r6, r6, r1

7100: 980a ldr r0, [sp, #40] ; 0x28

7102: 9914 ldr r1, [sp, #80] ; 0x50

7104: 10b6 asrs r6, r6, #2

7106: 9206 str r2, [sp, #24]

7108: e9cd 6002 strd r6, r0, [sp, #8]

710c: 4648 mov r0, r9

710e: e9cd 4104 strd r4, r1, [sp, #16]

7112: eb08 1c06 add.w ip, r8, r6, lsl #4

7116: 9b28 ldr r3, [sp, #160] ; 0xa0

7118: 4651 mov r1, sl

711a: 9a27 ldr r2, [sp, #156] ; 0x9c

711c: f8cd b000 str.w fp, [sp]

7120: f8cd c004 str.w ip, [sp, #4]

7124: f7ff fe54 bl 6dd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13c>

7128: 2800 cmp r0, #0

712a: f040 8256 bne.w 75da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x946>

712e: f855 3c04 ldr.w r3, [r5, #-4]

7132: e7d7 b.n 70e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x450>

7134: 2000 movs r0, #0

7136: b01b add sp, #108 ; 0x6c

7138: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

713c: 9a11 ldr r2, [sp, #68] ; 0x44

713e: 6b52 ldr r2, [r2, #52] ; 0x34

7140: 2a00 cmp r2, #0

7142: f040 81d4 bne.w 74ee <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x85a>

7146: 9a24 ldr r2, [sp, #144] ; 0x90

7148: 2001 movs r0, #1

714a: 6013 str r3, [r2, #0]

714c: 6054 str r4, [r2, #4]

714e: b01b add sp, #108 ; 0x6c

7150: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

7154: f894 2198 ldrb.w r2, [r4, #408] ; 0x198

7158: f3c2 0201 ubfx r2, r2, #0, #2

715c: f1bb 0f04 cmp.w fp, #4

7160: bf08 it eq

7162: 2a00 cmpeq r2, #0

7164: d188 bne.n 7078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e4>

7166: 6be2 ldr r2, [r4, #60] ; 0x3c

7168: 2a00 cmp r2, #0

716a: d047 beq.n 71fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x568>

716c: 6c21 ldr r1, [r4, #64] ; 0x40

716e: 2900 cmp r1, #0

7170: d044 beq.n 71fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x568>

7172: 6849 ldr r1, [r1, #4]

7174: 2900 cmp r1, #0

7176: d041 beq.n 71fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x568>

7178: 9813 ldr r0, [sp, #76] ; 0x4c

717a: 290b cmp r1, #11

717c: 6852 ldr r2, [r2, #4]

717e: fba0 c001 umull ip, r0, r0, r1

7182: 9214 str r2, [sp, #80] ; 0x50

7184: ea4f 00d0 mov.w r0, r0, lsr #3

7188: d938 bls.n 71fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x568>

718a: 9a2b ldr r2, [sp, #172] ; 0xac

718c: 940b str r4, [sp, #44] ; 0x2c

718e: 9526 str r5, [sp, #152] ; 0x98

7190: 4605 mov r5, r0

7192: 9c14 ldr r4, [sp, #80] ; 0x50

7194: 9315 str r3, [sp, #84] ; 0x54

7196: 9614 str r6, [sp, #80] ; 0x50

7198: 4616 mov r6, r2

719a: e00f b.n 71bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x528>

719c: f240 2371 movw r3, #625 ; 0x271

71a0: f1bc 0f14 cmp.w ip, #20

71a4: fa23 f101 lsr.w r1, r3, r1

71a8: ea6f 0101 mvn.w r1, r1

71ac: f001 0101 and.w r1, r1, #1

71b0: f000 8221 beq.w 75f6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x962>

71b4: 3601 adds r6, #1

71b6: 340c adds r4, #12

71b8: 42b5 cmp r5, r6

71ba: d91b bls.n 71f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x560>

71bc: 6860 ldr r0, [r4, #4]

71be: fa5f fc80 uxtb.w ip, r0

71c2: f1ac 010d sub.w r1, ip, #13

71c6: 2909 cmp r1, #9

71c8: d9e8 bls.n 719c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x508>

71ca: f1bc 0f14 cmp.w ip, #20

71ce: d1f1 bne.n 71b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x520>

71d0: 0a00 lsrs r0, r0, #8

71d2: 9b0e ldr r3, [sp, #56] ; 0x38

71d4: 990c ldr r1, [sp, #48] ; 0x30

71d6: 0100 lsls r0, r0, #4

71d8: 5818 ldr r0, [r3, r0]

71da: 9b0a ldr r3, [sp, #40] ; 0x28

71dc: 4418 add r0, r3

71de: f00a ffb7 bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

71e2: 2800 cmp r0, #0

71e4: d1e6 bne.n 71b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x520>

71e6: 9e14 ldr r6, [sp, #80] ; 0x50

71e8: 9c0b ldr r4, [sp, #44] ; 0x2c

71ea: fab6 f686 clz r6, r6

71ee: 9d26 ldr r5, [sp, #152] ; 0x98

71f0: 0976 lsrs r6, r6, #5

71f2: e6c2 b.n 6f7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e6>

71f4: 9b15 ldr r3, [sp, #84] ; 0x54

71f6: 9c0b ldr r4, [sp, #44] ; 0x2c

71f8: 9e14 ldr r6, [sp, #80] ; 0x50

71fa: 9d26 ldr r5, [sp, #152] ; 0x98

71fc: 6e62 ldr r2, [r4, #100] ; 0x64

71fe: 2a00 cmp r2, #0

7200: f43f af3a beq.w 7078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e4>

7204: 6ea1 ldr r1, [r4, #104] ; 0x68

7206: 2900 cmp r1, #0

7208: f43f af36 beq.w 7078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e4>

720c: 6849 ldr r1, [r1, #4]

720e: 2900 cmp r1, #0

7210: f43f af32 beq.w 7078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e4>

7214: 08c9 lsrs r1, r1, #3

7216: 6852 ldr r2, [r2, #4]

7218: f43f af2e beq.w 7078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e4>

721c: eb02 01c1 add.w r1, r2, r1, lsl #3

7220: 940b str r4, [sp, #44] ; 0x2c

7222: 9526 str r5, [sp, #152] ; 0x98

7224: 4614 mov r4, r2

7226: 9314 str r3, [sp, #80] ; 0x50

7228: 460d mov r5, r1

722a: e00f b.n 724c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x5b8>

722c: f240 2371 movw r3, #625 ; 0x271

7230: f1bc 0f14 cmp.w ip, #20

7234: fa23 f101 lsr.w r1, r3, r1

7238: ea6f 0101 mvn.w r1, r1

723c: f001 0101 and.w r1, r1, #1

7240: f000 81dd beq.w 75fe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x96a>

7244: 3408 adds r4, #8

7246: 42ac cmp r4, r5

7248: f000 81ed beq.w 7626 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x992>

724c: 6860 ldr r0, [r4, #4]

724e: fa5f fc80 uxtb.w ip, r0

7252: f1ac 010d sub.w r1, ip, #13

7256: 2909 cmp r1, #9

7258: d9e8 bls.n 722c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x598>

725a: f1bc 0f14 cmp.w ip, #20

725e: d1f1 bne.n 7244 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x5b0>

7260: 0a00 lsrs r0, r0, #8

7262: 9b0e ldr r3, [sp, #56] ; 0x38

7264: 990c ldr r1, [sp, #48] ; 0x30

7266: 0100 lsls r0, r0, #4

7268: 5818 ldr r0, [r3, r0]

726a: 9b0a ldr r3, [sp, #40] ; 0x28

726c: 4418 add r0, r3

726e: f00a ff6f bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

7272: 2800 cmp r0, #0

7274: d1e6 bne.n 7244 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x5b0>

7276: fab6 f686 clz r6, r6

727a: 9c0b ldr r4, [sp, #44] ; 0x2c

727c: 9d26 ldr r5, [sp, #152] ; 0x98

727e: 0976 lsrs r6, r6, #5

7280: e67b b.n 6f7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e6>

7282: 930b str r3, [sp, #44] ; 0x2c

7284: 254c movs r5, #76 ; 0x4c

7286: 69a3 ldr r3, [r4, #24]

7288: 49f6 ldr r1, [pc, #984] ; (7664 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9d0>)

728a: f8dd a024 ldr.w sl, [sp, #36] ; 0x24

728e: fb05 f503 mul.w r5, r5, r3

7292: 4479 add r1, pc

7294: 930d str r3, [sp, #52] ; 0x34

7296: f8d1 27f8 ldr.w r2, [r1, #2040] ; 0x7f8

729a: f105 0010 add.w r0, r5, #16

729e: 440d add r5, r1

72a0: 1843 adds r3, r0, r1

72a2: 930e str r3, [sp, #56] ; 0x38

72a4: 4618 mov r0, r3

72a6: 4790 blx r2

72a8: 6aab ldr r3, [r5, #40] ; 0x28

72aa: f8d5 802c ldr.w r8, [r5, #44] ; 0x2c

72ae: 9309 str r3, [sp, #36] ; 0x24

72b0: 2b00 cmp r3, #0

72b2: f000 80de beq.w 7472 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7de>

72b6: 4641 mov r1, r8

72b8: 4650 mov r0, sl

72ba: f00c fb03 bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

72be: 4650 mov r0, sl

72c0: 460e mov r6, r1

72c2: f1a8 0102 sub.w r1, r8, #2

72c6: f00c fafd bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

72ca: 4637 mov r7, r6

72cc: 9610 str r6, [sp, #64] ; 0x40

72ce: 9e0c ldr r6, [sp, #48] ; 0x30

72d0: f101 0901 add.w r9, r1, #1

72d4: ea4f 1b09 mov.w fp, r9, lsl #4

72d8: 465b mov r3, fp

72da: 46cb mov fp, r9

72dc: 4699 mov r9, r3

72de: 9b09 ldr r3, [sp, #36] ; 0x24

72e0: eb03 1507 add.w r5, r3, r7, lsl #4

72e4: e005 b.n 72f2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x65e>

72e6: b370 cbz r0, 7346 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x6b2>

72e8: 445f add r7, fp

72ea: 444d add r5, r9

72ec: 45b8 cmp r8, r7

72ee: f240 808b bls.w 7408 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x774>

72f2: e9d5 3000 ldrd r3, r0, [r5]

72f6: 459a cmp sl, r3

72f8: d1f5 bne.n 72e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x652>

72fa: 4631 mov r1, r6

72fc: f00a ff28 bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

7300: 2800 cmp r0, #0

7302: d1f1 bne.n 72e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x654>

7304: 9b08 ldr r3, [sp, #32]

7306: 2b00 cmp r3, #0

7308: f000 816f beq.w 75ea <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x956>

730c: 9b24 ldr r3, [sp, #144] ; 0x90

730e: 461a mov r2, r3

7310: 9b0b ldr r3, [sp, #44] ; 0x2c

7312: 6054 str r4, [r2, #4]

7314: 6013 str r3, [r2, #0]

7316: 4bd4 ldr r3, [pc, #848] ; (7668 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9d4>)

7318: 980e ldr r0, [sp, #56] ; 0x38

731a: 447b add r3, pc

731c: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

7320: 4798 blx r3

7322: 2001 movs r0, #1

7324: e707 b.n 7136 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a2>

7326: 9f0b ldr r7, [sp, #44] ; 0x2c

7328: 9d26 ldr r5, [sp, #152] ; 0x98

732a: f8dd 80a4 ldr.w r8, [sp, #164] ; 0xa4

732e: e9dd 9a14 ldrd r9, sl, [sp, #80] ; 0x50

7332: 9b18 ldr r3, [sp, #96] ; 0x60

7334: 2b01 cmp r3, #1

7336: f47f ae1f bne.w 6f78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e4>

733a: 9b19 ldr r3, [sp, #100] ; 0x64

733c: 2600 movs r6, #0

733e: 2b00 cmp r3, #0

7340: f47f ae97 bne.w 7072 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de>

7344: e618 b.n 6f78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e4>

7346: 464b mov r3, r9

7348: 49c8 ldr r1, [pc, #800] ; (766c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9d8>)

734a: 46d9 mov r9, fp

734c: 469b mov fp, r3

734e: 9b0d ldr r3, [sp, #52] ; 0x34

7350: 4479 add r1, pc

7352: 204c movs r0, #76 ; 0x4c

7354: eb08 0248 add.w r2, r8, r8, lsl #1

7358: 9e10 ldr r6, [sp, #64] ; 0x40

735a: fb00 1103 mla r1, r0, r3, r1

735e: 6b09 ldr r1, [r1, #48] ; 0x30

7360: ebb2 0f81 cmp.w r2, r1, lsl #2

7364: f240 80cf bls.w 7506 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x872>

7368: 9b0b ldr r3, [sp, #44] ; 0x2c

736a: 9909 ldr r1, [sp, #36] ; 0x24

736c: 6818 ldr r0, [r3, #0]

736e: 9b0a ldr r3, [sp, #40] ; 0x28

7370: 4403 add r3, r0

7372: 4618 mov r0, r3

7374: 9b08 ldr r3, [sp, #32]

7376: b353 cbz r3, 73ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x73a>

7378: 9a09 ldr r2, [sp, #36] ; 0x24

737a: eb02 1106 add.w r1, r2, r6, lsl #4

737e: e003 b.n 7388 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x6f4>

7380: 444e add r6, r9

7382: 4459 add r1, fp

7384: 45b0 cmp r8, r6

7386: d942 bls.n 740e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x77a>

7388: 684b ldr r3, [r1, #4]

738a: 2b00 cmp r3, #0

738c: d1f8 bne.n 7380 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x6ec>

738e: 9b0f ldr r3, [sp, #60] ; 0x3c

7390: e9c1 a000 strd sl, r0, [r1]

7394: 608b str r3, [r1, #8]

7396: 9b2b ldr r3, [sp, #172] ; 0xac

7398: 60cb str r3, [r1, #12]

739a: 4ab5 ldr r2, [pc, #724] ; (7670 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9dc>)

739c: 214c movs r1, #76 ; 0x4c

739e: 9b0d ldr r3, [sp, #52] ; 0x34

73a0: 447a add r2, pc

73a2: fb01 2203 mla r2, r1, r3, r2

73a6: 6b11 ldr r1, [r2, #48] ; 0x30

73a8: 3101 adds r1, #1

73aa: 6311 str r1, [r2, #48] ; 0x30

73ac: 4ab1 ldr r2, [pc, #708] ; (7674 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9e0>)

73ae: 980e ldr r0, [sp, #56] ; 0x38

73b0: 447a add r2, pc

73b2: f8d2 27fc ldr.w r2, [r2, #2044] ; 0x7fc

73b6: 4790 blx r2

73b8: 9b24 ldr r3, [sp, #144] ; 0x90

73ba: 2001 movs r0, #1

73bc: 461a mov r2, r3

73be: 9b0b ldr r3, [sp, #44] ; 0x2c

73c0: 6054 str r4, [r2, #4]

73c2: 6013 str r3, [r2, #0]

73c4: b01b add sp, #108 ; 0x6c

73c6: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

73ca: eba6 0608 sub.w r6, r6, r8

73ce: eb01 1206 add.w r2, r1, r6, lsl #4

73d2: e003 b.n 73dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x748>

73d4: 444e add r6, r9

73d6: 445a add r2, fp

73d8: 45b0 cmp r8, r6

73da: d9f6 bls.n 73ca <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x736>

73dc: 6853 ldr r3, [r2, #4]

73de: 2b00 cmp r3, #0

73e0: d1f8 bne.n 73d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x740>

73e2: f894 1198 ldrb.w r1, [r4, #408] ; 0x198

73e6: 9b0b ldr r3, [sp, #44] ; 0x2c

73e8: f001 0103 and.w r1, r1, #3

73ec: f8c2 a000 str.w sl, [r2]

73f0: 2902 cmp r1, #2

73f2: 6050 str r0, [r2, #4]

73f4: e9c2 3402 strd r3, r4, [r2, #8]

73f8: bf02 ittt eq

73fa: f8d4 220c ldreq.w r2, [r4, #524] ; 0x20c

73fe: f042 0208 orreq.w r2, r2, #8

7402: f8c4 220c streq.w r2, [r4, #524] ; 0x20c

7406: e7c8 b.n 739a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x706>

7408: eba7 0708 sub.w r7, r7, r8

740c: e767 b.n 72de <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x64a>

740e: eba6 0608 sub.w r6, r6, r8

7412: e7b2 b.n 737a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x6e6>

7414: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

7418: e68d b.n 7136 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a2>

741a: 9b0c ldr r3, [sp, #48] ; 0x30

741c: 781e ldrb r6, [r3, #0]

741e: 2e00 cmp r6, #0

7420: d061 beq.n 74e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x852>

7422: 785b ldrb r3, [r3, #1]

7424: b303 cbz r3, 7468 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7d4>

7426: 9a0c ldr r2, [sp, #48] ; 0x30

7428: eb03 1606 add.w r6, r3, r6, lsl #4

742c: 7890 ldrb r0, [r2, #2]

742e: b1d8 cbz r0, 7468 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7d4>

7430: 9b0c ldr r3, [sp, #48] ; 0x30

7432: eb00 1606 add.w r6, r0, r6, lsl #4

7436: 78da ldrb r2, [r3, #3]

7438: b1b2 cbz r2, 7468 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7d4>

743a: 9b0c ldr r3, [sp, #48] ; 0x30

743c: eb02 1606 add.w r6, r2, r6, lsl #4

7440: 791b ldrb r3, [r3, #4]

7442: b18b cbz r3, 7468 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7d4>

7444: 980c ldr r0, [sp, #48] ; 0x30

7446: eb03 1606 add.w r6, r3, r6, lsl #4

744a: 3005 adds r0, #5

744c: 7802 ldrb r2, [r0, #0]

744e: b14a cbz r2, 7464 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7d0>

7450: eb02 1606 add.w r6, r2, r6, lsl #4

7454: f810 2f01 ldrb.w r2, [r0, #1]!

7458: 0e33 lsrs r3, r6, #24

745a: f003 03f0 and.w r3, r3, #240 ; 0xf0

745e: 405e eors r6, r3

7460: 2a00 cmp r2, #0

7462: d1f5 bne.n 7450 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x7bc>

7464: f026 4670 bic.w r6, r6, #4026531840 ; 0xf0000000

7468: 4630 mov r0, r6

746a: f00c fa2b bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

746e: 0089 lsls r1, r1, #2

7470: e03a b.n 74e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x854>

7472: f1b8 0f00 cmp.w r8, #0

7476: f040 80c6 bne.w 7606 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x972>

747a: 211f movs r1, #31

747c: 2010 movs r0, #16

747e: f7f9 eab8 blx 9f0 <calloc@plt>

7482: 4603 mov r3, r0

7484: 9009 str r0, [sp, #36] ; 0x24

7486: 2800 cmp r0, #0

7488: f000 80d1 beq.w 762e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x99a>

748c: f64c 3209 movw r2, #51977 ; 0xcb09

7490: f6c8 523d movt r2, #36157 ; 0x8d3d

7494: 261f movs r6, #31

7496: e9c5 360a strd r3, r6, [r5, #40] ; 0x28

749a: fba2 020a umull r0, r2, r2, sl

749e: f241 0085 movw r0, #4229 ; 0x1085

74a2: f6c0 0042 movt r0, #2114 ; 0x842

74a6: 46b0 mov r8, r6

74a8: 261d movs r6, #29

74aa: 4973 ldr r1, [pc, #460] ; (7678 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9e4>)

74ac: fba0 300a umull r3, r0, r0, sl

74b0: 0912 lsrs r2, r2, #4

74b2: 9b12 ldr r3, [sp, #72] ; 0x48

74b4: fb06 a212 mls r2, r6, r2, sl

74b8: ebaa 0600 sub.w r6, sl, r0

74bc: 5859 ldr r1, [r3, r1]

74be: eb00 0056 add.w r0, r0, r6, lsr #1

74c2: f102 0901 add.w r9, r2, #1

74c6: 0906 lsrs r6, r0, #4

74c8: 6369 str r1, [r5, #52] ; 0x34

74ca: ea4f 1b09 mov.w fp, r9, lsl #4

74ce: ebc6 1646 rsb r6, r6, r6, lsl #5

74d2: ebaa 0606 sub.w r6, sl, r6

74d6: e747 b.n 7368 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x6d4>

74d8: 9f15 ldr r7, [sp, #84] ; 0x54

74da: 9d26 ldr r5, [sp, #152] ; 0x98

74dc: f8dd 80a4 ldr.w r8, [sp, #164] ; 0xa4

74e0: e9dd 9a16 ldrd r9, sl, [sp, #88] ; 0x58

74e4: e725 b.n 7332 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x69e>

74e6: 4631 mov r1, r6

74e8: 9b10 ldr r3, [sp, #64] ; 0x40

74ea: 601e str r6, [r3, #0]

74ec: e589 b.n 7002 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x36e>

74ee: 9a24 ldr r2, [sp, #144] ; 0x90

74f0: fab6 f686 clz r6, r6

74f4: 0976 lsrs r6, r6, #5

74f6: 6812 ldr r2, [r2, #0]

74f8: 2a00 cmp r2, #0

74fa: f47f ad3e bne.w 6f7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e6>

74fe: 9a24 ldr r2, [sp, #144] ; 0x90

7500: 6013 str r3, [r2, #0]

7502: 6054 str r4, [r2, #4]

7504: e539 b.n 6f7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e6>

7506: f108 0001 add.w r0, r8, #1

750a: f004 fc85 bl be18 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x438>

750e: 4601 mov r1, r0

7510: 4606 mov r6, r0

7512: 2010 movs r0, #16

7514: f7f9 ea6c blx 9f0 <calloc@plt>

7518: 4603 mov r3, r0

751a: 2800 cmp r0, #0

751c: f000 8087 beq.w 762e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x99a>

7520: 1eb2 subs r2, r6, #2

7522: 9210 str r2, [sp, #64] ; 0x40

7524: f1b8 0f00 cmp.w r8, #0

7528: d036 beq.n 7598 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x904>

752a: 9f09 ldr r7, [sp, #36] ; 0x24

752c: 4683 mov fp, r0

752e: 9411 str r4, [sp, #68] ; 0x44

7530: eb07 1208 add.w r2, r7, r8, lsl #4

7534: f8cd a04c str.w sl, [sp, #76] ; 0x4c

7538: 4691 mov r9, r2

753a: e002 b.n 7542 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8ae>

753c: 3710 adds r7, #16

753e: 45b9 cmp r9, r7

7540: d026 beq.n 7590 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8fc>

7542: 687d ldr r5, [r7, #4]

7544: 2d00 cmp r5, #0

7546: d0f9 beq.n 753c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8a8>

7548: f8d7 8000 ldr.w r8, [r7]

754c: 4631 mov r1, r6

754e: 68ba ldr r2, [r7, #8]

7550: f8d7 a00c ldr.w sl, [r7, #12]

7554: 4640 mov r0, r8

7556: 920c str r2, [sp, #48] ; 0x30

7558: f00c f9b4 bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

755c: 4640 mov r0, r8

755e: 460c mov r4, r1

7560: 9910 ldr r1, [sp, #64] ; 0x40

7562: f00c f9af bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

7566: 9a0c ldr r2, [sp, #48] ; 0x30

7568: 3101 adds r1, #1

756a: ea4f 1c01 mov.w ip, r1, lsl #4

756e: eb0b 1004 add.w r0, fp, r4, lsl #4

7572: e003 b.n 757c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8e8>

7574: 440c add r4, r1

7576: 4460 add r0, ip

7578: 42a6 cmp r6, r4

757a: d907 bls.n 758c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8f8>

757c: 6843 ldr r3, [r0, #4]

757e: 2b00 cmp r3, #0

7580: d1f8 bne.n 7574 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8e0>

7582: e9c0 8500 strd r8, r5, [r0]

7586: e9c0 2a02 strd r2, sl, [r0, #8]

758a: e7d7 b.n 753c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8a8>

758c: 1ba4 subs r4, r4, r6

758e: e7ee b.n 756e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x8da>

7590: 9c11 ldr r4, [sp, #68] ; 0x44

7592: 465b mov r3, fp

7594: f8dd a04c ldr.w sl, [sp, #76] ; 0x4c

7598: 4a38 ldr r2, [pc, #224] ; (767c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9e8>)

759a: 254c movs r5, #76 ; 0x4c

759c: 990d ldr r1, [sp, #52] ; 0x34

759e: 46b0 mov r8, r6

75a0: 447a add r2, pc

75a2: 9809 ldr r0, [sp, #36] ; 0x24

75a4: 930c str r3, [sp, #48] ; 0x30

75a6: fb05 2501 mla r5, r5, r1, r2

75aa: 9309 str r3, [sp, #36] ; 0x24

75ac: 6b6a ldr r2, [r5, #52] ; 0x34

75ae: 4790 blx r2

75b0: 9b0c ldr r3, [sp, #48] ; 0x30

75b2: 4a31 ldr r2, [pc, #196] ; (7678 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9e4>)

75b4: 4631 mov r1, r6

75b6: 62ee str r6, [r5, #44] ; 0x2c

75b8: 4650 mov r0, sl

75ba: 62ab str r3, [r5, #40] ; 0x28

75bc: 9b12 ldr r3, [sp, #72] ; 0x48

75be: 589a ldr r2, [r3, r2]

75c0: 636a str r2, [r5, #52] ; 0x34

75c2: f00c f97f bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

75c6: 4650 mov r0, sl

75c8: 460e mov r6, r1

75ca: 9910 ldr r1, [sp, #64] ; 0x40

75cc: f00c f97a bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

75d0: f101 0901 add.w r9, r1, #1

75d4: ea4f 1b09 mov.w fp, r9, lsl #4

75d8: e6c6 b.n 7368 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x6d4>

75da: 4603 mov r3, r0

75dc: 9f15 ldr r7, [sp, #84] ; 0x54

75de: 9d26 ldr r5, [sp, #152] ; 0x98

75e0: f8dd 80a4 ldr.w r8, [sp, #164] ; 0xa4

75e4: e9dd 9a16 ldrd r9, sl, [sp, #88] ; 0x58

75e8: e543 b.n 7072 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de>

75ea: e9d5 2302 ldrd r2, r3, [r5, #8]

75ee: 9924 ldr r1, [sp, #144] ; 0x90

75f0: 600a str r2, [r1, #0]

75f2: 604b str r3, [r1, #4]

75f4: e68f b.n 7316 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x682>

75f6: 2900 cmp r1, #0

75f8: f47f adea bne.w 71d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x53c>

75fc: e5da b.n 71b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x520>

75fe: 2900 cmp r1, #0

7600: f47f ae2e bne.w 7260 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x5cc>

7604: e61e b.n 7244 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x5b0>

7606: 4a1e ldr r2, [pc, #120] ; (7680 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9ec>)

7608: 447a add r2, pc

760a: 6812 ldr r2, [r2, #0]

760c: 0513 lsls r3, r2, #20

760e: f53f aecd bmi.w 73ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x718>

7612: 4b1c ldr r3, [pc, #112] ; (7684 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9f0>)

7614: f240 1221 movw r2, #289 ; 0x121

7618: 491b ldr r1, [pc, #108] ; (7688 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9f4>)

761a: 481c ldr r0, [pc, #112] ; (768c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9f8>)

761c: 447b add r3, pc

761e: 4479 add r1, pc

7620: 4478 add r0, pc

7622: f009 f9b3 bl 1098c <free@@GLIBC\_2.4+0x12c>

7626: 9b14 ldr r3, [sp, #80] ; 0x50

7628: 9c0b ldr r4, [sp, #44] ; 0x2c

762a: 9d26 ldr r5, [sp, #152] ; 0x98

762c: e524 b.n 7078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e4>

762e: 4b18 ldr r3, [pc, #96] ; (7690 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9fc>)

7630: 980e ldr r0, [sp, #56] ; 0x38

7632: 447b add r3, pc

7634: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

7638: 4798 blx r3

763a: 4916 ldr r1, [pc, #88] ; (7694 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa00>)

763c: 2002 movs r0, #2

763e: 4479 add r1, pc

7640: f004 fbc4 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7644: 207f movs r0, #127 ; 0x7f

7646: f00a fbff bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

764a: bf00 nop

764c: 2112 movs r1, #18

764e: 0002 movs r2, r0

7650: 1a6c subs r4, r5, r1

7652: 0002 movs r2, r0

7654: 1a5c subs r4, r3, r1

7656: 0002 movs r2, r0

7658: f15c 0000 adcs.w r0, ip, #0

765c: 28b8 cmp r0, #184 ; 0xb8

765e: 0002 movs r2, r0

7660: daea bge.n 7638 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x9a4>

7662: 0000 movs r0, r0

7664: 1dca adds r2, r1, #7

7666: 0002 movs r2, r0

7668: 1d42 adds r2, r0, #5

766a: 0002 movs r2, r0

766c: 1d0c adds r4, r1, #4

766e: 0002 movs r2, r0

7670: 1cbc adds r4, r7, #2

7672: 0002 movs r2, r0

7674: 1cac adds r4, r5, #2

7676: 0002 movs r2, r0

7678: 0058 lsls r0, r3, #1

767a: 0000 movs r0, r0

767c: 1abc subs r4, r7, r2

767e: 0002 movs r2, r0

7680: 1364 asrs r4, r4, #13

7682: 0002 movs r2, r0

7684: eda4 0000 stc 0, cr0, [r4]

7688: eaba 0000 ; <UNDEFINED> instruction: 0xeaba0000

768c: eb38 0000 ; <UNDEFINED> instruction: 0xeb380000

7690: 1a2a subs r2, r5, r0

7692: 0002 movs r2, r0

7694: dd1e ble.n 76d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa40>

7696: 0000 movs r0, r0

7698: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

769c: 468b mov fp, r1

769e: 7801 ldrb r1, [r0, #0]

76a0: b09b sub sp, #108 ; 0x6c

76a2: 4607 mov r7, r0

76a4: 4690 mov r8, r2

76a6: 930a str r3, [sp, #40] ; 0x28

76a8: 9d27 ldr r5, [sp, #156] ; 0x9c

76aa: 2900 cmp r1, #0

76ac: f000 80e0 beq.w 7870 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbdc>

76b0: 4603 mov r3, r0

76b2: f241 5405 movw r4, #5381 ; 0x1505

76b6: eb04 1444 add.w r4, r4, r4, lsl #5

76ba: 440c add r4, r1

76bc: f813 1f01 ldrb.w r1, [r3, #1]!

76c0: 2900 cmp r1, #0

76c2: d1f8 bne.n 76b6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa22>

76c4: f8df 2b40 ldr.w r2, [pc, #2880] ; 8208 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1574>

76c8: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

76cc: 9311 str r3, [sp, #68] ; 0x44

76ce: 2100 movs r1, #0

76d0: 447a add r2, pc

76d2: 9113 str r1, [sp, #76] ; 0x4c

76d4: 9114 str r1, [sp, #80] ; 0x50

76d6: f8d2 3508 ldr.w r3, [r2, #1288] ; 0x508

76da: 3301 adds r3, #1

76dc: f8c2 3508 str.w r3, [r2, #1288] ; 0x508

76e0: 9b24 ldr r3, [sp, #144] ; 0x90

76e2: b123 cbz r3, 76ee <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa5a>

76e4: 9b26 ldr r3, [sp, #152] ; 0x98

76e6: f033 0305 bics.w r3, r3, #5

76ea: f040 84a6 bne.w 803a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13a6>

76ee: 9b0a ldr r3, [sp, #40] ; 0x28

76f0: f8d3 c000 ldr.w ip, [r3]

76f4: f8d8 3000 ldr.w r3, [r8]

76f8: 2d00 cmp r5, #0

76fa: f040 80bc bne.w 7876 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbe2>

76fe: f1bc 0f00 cmp.w ip, #0

7702: f000 80c6 beq.w 7892 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbfe>

7706: 950b str r5, [sp, #44] ; 0x2c

7708: e9dd 600a ldrd r6, r0, [sp, #40] ; 0x28

770c: f10d 0944 add.w r9, sp, #68 ; 0x44

7710: f10d 0a4c add.w sl, sp, #76 ; 0x4c

7714: 9609 str r6, [sp, #36] ; 0x24

7716: e012 b.n 773e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xaaa>

7718: 1c03 adds r3, r0, #0

771a: bf18 it ne

771c: 2301 movne r3, #1

771e: 2d00 cmp r5, #0

7720: bf18 it ne

7722: 2300 movne r3, #0

7724: 2b00 cmp r3, #0

7726: f040 821c bne.w 7b62 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xece>

772a: 9a09 ldr r2, [sp, #36] ; 0x24

772c: 4618 mov r0, r3

772e: f8d8 3000 ldr.w r3, [r8]

7732: f852 cf04 ldr.w ip, [r2, #4]!

7736: 9209 str r2, [sp, #36] ; 0x24

7738: f1bc 0f00 cmp.w ip, #0

773c: d014 beq.n 7768 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xad4>

773e: 9e26 ldr r6, [sp, #152] ; 0x98

7740: 4621 mov r1, r4

7742: 9a25 ldr r2, [sp, #148] ; 0x94

7744: f8cd b01c str.w fp, [sp, #28]

7748: 9604 str r6, [sp, #16]

774a: 9e24 ldr r6, [sp, #144] ; 0x90

774c: 9206 str r2, [sp, #24]

774e: 464a mov r2, r9

7750: 9505 str r5, [sp, #20]

7752: e9cd 0602 strd r0, r6, [sp, #8]

7756: 4638 mov r0, r7

7758: e9cd ac00 strd sl, ip, [sp]

775c: f7ff fbbc bl 6ed8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x244>

7760: 2800 cmp r0, #0

7762: ddd9 ble.n 7718 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa84>

7764: f8d8 3000 ldr.w r3, [r8]

7768: 9a13 ldr r2, [sp, #76] ; 0x4c

776a: 2a00 cmp r2, #0

776c: f000 8091 beq.w 7892 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbfe>

7770: b123 cbz r3, 777c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xae8>

7772: 7b5a ldrb r2, [r3, #13]

7774: f002 0203 and.w r2, r2, #3

7778: 2a03 cmp r2, #3

777a: d01e beq.n 77ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb26>

777c: 9c14 ldr r4, [sp, #80] ; 0x50

777e: f04f 0a00 mov.w sl, #0

7782: f894 3198 ldrb.w r3, [r4, #408] ; 0x198

7786: f003 0303 and.w r3, r3, #3

778a: 2b02 cmp r3, #2

778c: f000 8154 beq.w 7a38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xda4>

7790: f8d4 3204 ldr.w r3, [r4, #516] ; 0x204

7794: 2b00 cmp r3, #0

7796: f000 814b beq.w 7a30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd9c>

779a: f8df 3a70 ldr.w r3, [pc, #2672] ; 820c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1578>

779e: f640 0204 movw r2, #2052 ; 0x804

77a2: 447b add r3, pc

77a4: 681b ldr r3, [r3, #0]

77a6: 4213 tst r3, r2

77a8: f040 80ac bne.w 7904 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc70>

77ac: 9b13 ldr r3, [sp, #76] ; 0x4c

77ae: f8c8 3000 str.w r3, [r8]

77b2: 4620 mov r0, r4

77b4: b01b add sp, #108 ; 0x6c

77b6: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

77ba: 9a25 ldr r2, [sp, #148] ; 0x94

77bc: 2a01 cmp r2, #1

77be: f000 81c5 beq.w 7b4c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xeb8>

77c2: 9a0a ldr r2, [sp, #40] ; 0x28

77c4: 6810 ldr r0, [r2, #0]

77c6: 2200 movs r2, #0

77c8: e9cd 2217 strd r2, r2, [sp, #92] ; 0x5c

77cc: 2800 cmp r0, #0

77ce: f000 81c6 beq.w 7b5e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xeca>

77d2: 9925 ldr r1, [sp, #148] ; 0x94

77d4: 4684 mov ip, r0

77d6: 9e0a ldr r6, [sp, #40] ; 0x28

77d8: f1a1 0a04 sub.w sl, r1, #4

77dc: a917 add r1, sp, #92 ; 0x5c

77de: faba fa8a clz sl, sl

77e2: 980b ldr r0, [sp, #44] ; 0x2c

77e4: 920e str r2, [sp, #56] ; 0x38

77e6: f8cd b02c str.w fp, [sp, #44] ; 0x2c

77ea: ea4f 1a5a mov.w sl, sl, lsr #5

77ee: 9609 str r6, [sp, #36] ; 0x24

77f0: 468b mov fp, r1

77f2: e008 b.n 7806 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb72>

77f4: 9b09 ldr r3, [sp, #36] ; 0x24

77f6: f853 cf04 ldr.w ip, [r3, #4]!

77fa: 9309 str r3, [sp, #36] ; 0x24

77fc: f1bc 0f00 cmp.w ip, #0

7800: d024 beq.n 784c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbb8>

7802: f8d8 3000 ldr.w r3, [r8]

7806: 7b19 ldrb r1, [r3, #12]

7808: 9a0e ldr r2, [sp, #56] ; 0x38

780a: f001 010f and.w r1, r1, #15

780e: 9e26 ldr r6, [sp, #152] ; 0x98

7810: 2901 cmp r1, #1

7812: bf14 ite ne

7814: 2100 movne r1, #0

7816: f00a 0101 andeq.w r1, sl, #1

781a: 9505 str r5, [sp, #20]

781c: 2900 cmp r1, #0

781e: 9924 ldr r1, [sp, #144] ; 0x90

7820: 9207 str r2, [sp, #28]

7822: 464a mov r2, r9

7824: bf14 ite ne

7826: f04f 0e04 movne.w lr, #4

782a: f04f 0e01 moveq.w lr, #1

782e: e9cd 0102 strd r0, r1, [sp, #8]

7832: 4621 mov r1, r4

7834: 9604 str r6, [sp, #16]

7836: 4638 mov r0, r7

7838: f8cd c004 str.w ip, [sp, #4]

783c: f8cd b000 str.w fp, [sp]

7840: f8cd e018 str.w lr, [sp, #24]

7844: f7ff fb48 bl 6ed8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x244>

7848: 2800 cmp r0, #0

784a: d0d3 beq.n 77f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb60>

784c: 9b17 ldr r3, [sp, #92] ; 0x5c

784e: f8dd b02c ldr.w fp, [sp, #44] ; 0x2c

7852: 2b00 cmp r3, #0

7854: f000 8183 beq.w 7b5e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xeca>

7858: 9b18 ldr r3, [sp, #96] ; 0x60

785a: 455b cmp r3, fp

785c: f000 817f beq.w 7b5e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xeca>

7860: f8d8 3000 ldr.w r3, [r8]

7864: 465c mov r4, fp

7866: e9cd 3b13 strd r3, fp, [sp, #76] ; 0x4c

786a: f04f 0a01 mov.w sl, #1

786e: e788 b.n 7782 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xaee>

7870: f241 5405 movw r4, #5381 ; 0x1505

7874: e726 b.n 76c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa30>

7876: f8dc 2000 ldr.w r2, [ip]

787a: 6811 ldr r1, [r2, #0]

787c: 428d cmp r5, r1

787e: f000 824e beq.w 7d1e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x108a>

7882: 2100 movs r1, #0

7884: f852 0f04 ldr.w r0, [r2, #4]!

7888: 3101 adds r1, #1

788a: 42a8 cmp r0, r5

788c: d1fa bne.n 7884 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbf0>

788e: 910b str r1, [sp, #44] ; 0x2c

7890: e73a b.n 7708 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa74>

7892: b11b cbz r3, 789c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc08>

7894: 7b1b ldrb r3, [r3, #12]

7896: 091b lsrs r3, r3, #4

7898: 2b02 cmp r3, #2

789a: d02f beq.n 78fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc68>

789c: f8df 3970 ldr.w r3, [pc, #2416] ; 8210 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x157c>

78a0: 447b add r3, pc

78a2: 681b ldr r3, [r3, #0]

78a4: 05de lsls r6, r3, #23

78a6: d429 bmi.n 78fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc68>

78a8: f1bb 0f00 cmp.w fp, #0

78ac: f000 8211 beq.w 7cd2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x103e>

78b0: f8db 1004 ldr.w r1, [fp, #4]

78b4: 9b24 ldr r3, [sp, #144] ; 0x90

78b6: 7808 ldrb r0, [r1, #0]

78b8: 2b00 cmp r3, #0

78ba: f000 8233 beq.w 7d24 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1090>

78be: 9b24 ldr r3, [sp, #144] ; 0x90

78c0: 681b ldr r3, [r3, #0]

78c2: 2b00 cmp r3, #0

78c4: f000 82ca beq.w 7e5c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11c8>

78c8: f8df 2948 ldr.w r2, [pc, #2376] ; 8214 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1580>

78cc: 447a add r2, pc

78ce: 2800 cmp r0, #0

78d0: f000 820b beq.w 7cea <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1056>

78d4: e9cd 2300 strd r2, r3, [sp]

78d8: ac17 add r4, sp, #92 ; 0x5c

78da: f8df 293c ldr.w r2, [pc, #2364] ; 8218 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1584>

78de: 463b mov r3, r7

78e0: 4620 mov r0, r4

78e2: 447a add r2, pc

78e4: f007 fc6a bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

78e8: f8df 2930 ldr.w r2, [pc, #2352] ; 821c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1588>

78ec: 2000 movs r0, #0

78ee: 4621 mov r1, r4

78f0: 447a add r2, pc

78f2: f009 f993 bl 10c1c <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x3c>

78f6: 4620 mov r0, r4

78f8: f007 fd12 bl f320 <\_dl\_exception\_free@@GLIBC\_PRIVATE>

78fc: 2400 movs r4, #0

78fe: f8c8 4000 str.w r4, [r8]

7902: e756 b.n 77b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb1e>

7904: 075a lsls r2, r3, #29

7906: d536 bpl.n 7976 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xce2>

7908: f8db 1004 ldr.w r1, [fp, #4]

790c: 780b ldrb r3, [r1, #0]

790e: b93b cbnz r3, 7920 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc8c>

7910: f8df 390c ldr.w r3, [pc, #2316] ; 8220 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x158c>

7914: 447b add r3, pc

7916: 681b ldr r3, [r3, #0]

7918: 6819 ldr r1, [r3, #0]

791a: 2900 cmp r1, #0

791c: f000 8230 beq.w 7d80 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x10ec>

7920: 6863 ldr r3, [r4, #4]

7922: f8db 2018 ldr.w r2, [fp, #24]

7926: 7818 ldrb r0, [r3, #0]

7928: b938 cbnz r0, 793a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xca6>

792a: f8df 38f8 ldr.w r3, [pc, #2296] ; 8224 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1590>

792e: 447b add r3, pc

7930: 681b ldr r3, [r3, #0]

7932: 681b ldr r3, [r3, #0]

7934: 2b00 cmp r3, #0

7936: f000 8227 beq.w 7d88 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x10f4>

793a: 69a0 ldr r0, [r4, #24]

793c: f1ba 0f00 cmp.w sl, #0

7940: f040 81c3 bne.w 7cca <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1036>

7944: f8df 48e0 ldr.w r4, [pc, #2272] ; 8228 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1594>

7948: 447c add r4, pc

794a: 9000 str r0, [sp, #0]

794c: f8df 08dc ldr.w r0, [pc, #2268] ; 822c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1598>

7950: e9cd 4701 strd r4, r7, [sp, #4]

7954: 4478 add r0, pc

7956: f004 fa0d bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

795a: 9b24 ldr r3, [sp, #144] ; 0x90

795c: 2b00 cmp r3, #0

795e: f000 81d4 beq.w 7d0a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1076>

7962: f8df 08cc ldr.w r0, [pc, #2252] ; 8230 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x159c>

7966: 6819 ldr r1, [r3, #0]

7968: 4478 add r0, pc

796a: f004 fa19 bl bda0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3c0>

796e: f8df 38c4 ldr.w r3, [pc, #2244] ; 8234 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15a0>

7972: 447b add r3, pc

7974: 681b ldr r3, [r3, #0]

7976: 051b lsls r3, r3, #20

7978: d558 bpl.n 7a2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd98>

797a: f8df 28bc ldr.w r2, [pc, #2236] ; 8238 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15a4>

797e: 2100 movs r1, #0

7980: f8df 38b8 ldr.w r3, [pc, #2232] ; 823c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15a8>

7984: 447a add r2, pc

7986: 9115 str r1, [sp, #84] ; 0x54

7988: 447b add r3, pc

798a: 9116 str r1, [sp, #88] ; 0x58

798c: f8d2 2174 ldr.w r2, [r2, #372] ; 0x174

7990: 681b ldr r3, [r3, #0]

7992: 2a00 cmp r2, #0

7994: f000 811e beq.w 7bd4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf40>

7998: 429a cmp r2, r3

799a: f000 811b beq.w 7bd4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf40>

799e: 9b13 ldr r3, [sp, #76] ; 0x4c

79a0: 2b00 cmp r3, #0

79a2: f000 81c4 beq.w 7d2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x109a>

79a6: 7b1a ldrb r2, [r3, #12]

79a8: 9925 ldr r1, [sp, #148] ; 0x94

79aa: f002 020f and.w r2, r2, #15

79ae: 2a06 cmp r2, #6

79b0: f001 0103 and.w r1, r1, #3

79b4: 9125 str r1, [sp, #148] ; 0x94

79b6: f000 8280 beq.w 7eba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1226>

79ba: 2a0a cmp r2, #10

79bc: f000 8273 beq.w 7ea6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1212>

79c0: f8df 287c ldr.w r2, [pc, #2172] ; 8240 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15ac>

79c4: 447a add r2, pc

79c6: f8d2 1174 ldr.w r1, [r2, #372] ; 0x174

79ca: ebab 0201 sub.w r2, fp, r1

79ce: fab2 f282 clz r2, r2

79d2: 0952 lsrs r2, r2, #5

79d4: 2900 cmp r1, #0

79d6: bf08 it eq

79d8: 2201 moveq r2, #1

79da: 9925 ldr r1, [sp, #148] ; 0x94

79dc: 2903 cmp r1, #3

79de: bfc8 it gt

79e0: f042 0201 orrgt.w r2, r2, #1

79e4: b312 cbz r2, 7a2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd98>

79e6: f8db 11b0 ldr.w r1, [fp, #432] ; 0x1b0

79ea: f8d8 2000 ldr.w r2, [r8]

79ee: 1a52 subs r2, r2, r1

79f0: 685b ldr r3, [r3, #4]

79f2: 2008 movs r0, #8

79f4: 9c14 ldr r4, [sp, #80] ; 0x50

79f6: e9cd 0305 strd r0, r3, [sp, #20]

79fa: 4603 mov r3, r0

79fc: f8d4 41b0 ldr.w r4, [r4, #432] ; 0x1b0

7a00: 9202 str r2, [sp, #8]

7a02: 9100 str r1, [sp, #0]

7a04: f8df 283c ldr.w r2, [pc, #2108] ; 8244 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15b0>

7a08: f8df 183c ldr.w r1, [pc, #2108] ; 8248 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15b4>

7a0c: 9003 str r0, [sp, #12]

7a0e: 447a add r2, pc

7a10: 9001 str r0, [sp, #4]

7a12: 4479 add r1, pc

7a14: 2001 movs r0, #1

7a16: 9404 str r4, [sp, #16]

7a18: f004 f9d8 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7a1c: f8df 182c ldr.w r1, [pc, #2092] ; 824c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15b8>

7a20: 463b mov r3, r7

7a22: 9a25 ldr r2, [sp, #148] ; 0x94

7a24: 2001 movs r0, #1

7a26: 4479 add r1, pc

7a28: f004 f9d0 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7a2c: 9c14 ldr r4, [sp, #80] ; 0x50

7a2e: e6bd b.n 77ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb18>

7a30: 2301 movs r3, #1

7a32: f8c4 3204 str.w r3, [r4, #516] ; 0x204

7a36: e6b0 b.n 779a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb06>

7a38: 9b26 ldr r3, [sp, #152] ; 0x98

7a3a: 43db mvns r3, r3

7a3c: f003 0301 and.w r3, r3, #1

7a40: 455c cmp r4, fp

7a42: bf08 it eq

7a44: f043 0301 orreq.w r3, r3, #1

7a48: 2b00 cmp r3, #0

7a4a: f47f aea1 bne.w 7790 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xafc>

7a4e: f8d4 320c ldr.w r3, [r4, #524] ; 0x20c

7a52: f013 0308 ands.w r3, r3, #8

7a56: 930f str r3, [sp, #60] ; 0x3c

7a58: f47f ae9a bne.w 7790 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xafc>

7a5c: f8db 61fc ldr.w r6, [fp, #508] ; 0x1fc

7a60: f3bf 8f5b dmb ish

7a64: f8db 31f8 ldr.w r3, [fp, #504] ; 0x1f8

7a68: 930e str r3, [sp, #56] ; 0x38

7a6a: b16b cbz r3, 7a88 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xdf4>

7a6c: 681b ldr r3, [r3, #0]

7a6e: b15b cbz r3, 7a88 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xdf4>

7a70: 429c cmp r4, r3

7a72: bf18 it ne

7a74: 9a0e ldrne r2, [sp, #56] ; 0x38

7a76: d103 bne.n 7a80 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xdec>

7a78: e1ee b.n 7e58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11c4>

7a7a: 429c cmp r4, r3

7a7c: f000 81ec beq.w 7e58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11c4>

7a80: f852 3f04 ldr.w r3, [r2, #4]!

7a84: 2b00 cmp r3, #0

7a86: d1f8 bne.n 7a7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xde6>

7a88: 2e00 cmp r6, #0

7a8a: f000 8232 beq.w 7ef2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x125e>

7a8e: f8d6 9000 ldr.w r9, [r6]

7a92: f1b9 0f00 cmp.w r9, #0

7a96: d00e beq.n 7ab6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe22>

7a98: 6873 ldr r3, [r6, #4]

7a9a: 429c cmp r4, r3

7a9c: bf1c itt ne

7a9e: 1d32 addne r2, r6, #4

7aa0: 2300 movne r3, #0

7aa2: d105 bne.n 7ab0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe1c>

7aa4: e1d8 b.n 7e58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11c4>

7aa6: f852 1f04 ldr.w r1, [r2, #4]!

7aaa: 428c cmp r4, r1

7aac: f000 81d4 beq.w 7e58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11c4>

7ab0: 3301 adds r3, #1

7ab2: 4599 cmp r9, r3

7ab4: d1f7 bne.n 7aa6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe12>

7ab6: e9d4 2396 ldrd r2, r3, [r4, #600] ; 0x258

7aba: e9cd 230c strd r2, r3, [sp, #48] ; 0x30

7abe: 9b26 ldr r3, [sp, #152] ; 0x98

7ac0: f013 0304 ands.w r3, r3, #4

7ac4: 9309 str r3, [sp, #36] ; 0x24

7ac6: f040 82ce bne.w 8066 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13d2>

7aca: f8df 3784 ldr.w r3, [pc, #1924] ; 8250 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15bc>

7ace: 447b add r3, pc

7ad0: f203 40c4 addw r0, r3, #1220 ; 0x4c4

7ad4: f8d3 37f8 ldr.w r3, [r3, #2040] ; 0x7f8

7ad8: 4798 blx r3

7ada: f8db 1018 ldr.w r1, [fp, #24]

7ade: 234c movs r3, #76 ; 0x4c

7ae0: f8df 2770 ldr.w r2, [pc, #1904] ; 8254 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15c0>

7ae4: fb03 f301 mul.w r3, r3, r1

7ae8: 447a add r2, pc

7aea: 58d3 ldr r3, [r2, r3]

7aec: 2b00 cmp r3, #0

7aee: bf18 it ne

7af0: 42a3 cmpne r3, r4

7af2: d004 beq.n 7afe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe6a>

7af4: 68db ldr r3, [r3, #12]

7af6: 2b00 cmp r3, #0

7af8: bf18 it ne

7afa: 42a3 cmpne r3, r4

7afc: d1fa bne.n 7af4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe60>

7afe: 2b00 cmp r3, #0

7b00: f000 8205 beq.w 7f0e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x127a>

7b04: e9d4 2396 ldrd r2, r3, [r4, #600] ; 0x258

7b08: e9dd 010c ldrd r0, r1, [sp, #48] ; 0x30

7b0c: 4299 cmp r1, r3

7b0e: bf08 it eq

7b10: 4290 cmpeq r0, r2

7b12: f000 8165 beq.w 7de0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x114c>

7b16: f8df 3740 ldr.w r3, [pc, #1856] ; 8258 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15c4>

7b1a: 447b add r3, pc

7b1c: f203 40c4 addw r0, r3, #1220 ; 0x4c4

7b20: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

7b24: 4798 blx r3

7b26: 9b09 ldr r3, [sp, #36] ; 0x24

7b28: 2b00 cmp r3, #0

7b2a: f040 8290 bne.w 804e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13ba>

7b2e: 9a26 ldr r2, [sp, #152] ; 0x98

7b30: 9925 ldr r1, [sp, #148] ; 0x94

7b32: 9824 ldr r0, [sp, #144] ; 0x90

7b34: 9202 str r2, [sp, #8]

7b36: 4642 mov r2, r8

7b38: 9101 str r1, [sp, #4]

7b3a: 4659 mov r1, fp

7b3c: 9000 str r0, [sp, #0]

7b3e: 4638 mov r0, r7

7b40: 9503 str r5, [sp, #12]

7b42: 9b0a ldr r3, [sp, #40] ; 0x28

7b44: f7ff fda8 bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

7b48: 4604 mov r4, r0

7b4a: e632 b.n 77b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb1e>

7b4c: 9a14 ldr r2, [sp, #80] ; 0x50

7b4e: 455a cmp r2, fp

7b50: d001 beq.n 7b56 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xec2>

7b52: e9cd 3b13 strd r3, fp, [sp, #76] ; 0x4c

7b56: 465c mov r4, fp

7b58: f04f 0a01 mov.w sl, #1

7b5c: e618 b.n 7790 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xafc>

7b5e: 9c14 ldr r4, [sp, #80] ; 0x50

7b60: e683 b.n 786a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xbd6>

7b62: 9b24 ldr r3, [sp, #144] ; 0x90

7b64: 2b00 cmp r3, #0

7b66: f000 82c2 beq.w 80ee <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x145a>

7b6a: f1bb 0f00 cmp.w fp, #0

7b6e: d026 beq.n 7bbe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf2a>

7b70: f8db 1004 ldr.w r1, [fp, #4]

7b74: 780b ldrb r3, [r1, #0]

7b76: b313 cbz r3, 7bbe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf2a>

7b78: 9b24 ldr r3, [sp, #144] ; 0x90

7b7a: 3002 adds r0, #2

7b7c: 681a ldr r2, [r3, #0]

7b7e: 68db ldr r3, [r3, #12]

7b80: f000 80bf beq.w 7d02 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x106e>

7b84: f8df 06d4 ldr.w r0, [pc, #1748] ; 825c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15c8>

7b88: 4478 add r0, pc

7b8a: ac17 add r4, sp, #92 ; 0x5c

7b8c: 9200 str r2, [sp, #0]

7b8e: f8df 26d0 ldr.w r2, [pc, #1744] ; 8260 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15cc>

7b92: e9cd 3001 strd r3, r0, [sp, #4]

7b96: 463b mov r3, r7

7b98: 4620 mov r0, r4

7b9a: 447a add r2, pc

7b9c: f007 fb0e bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

7ba0: f8df 26c0 ldr.w r2, [pc, #1728] ; 8264 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15d0>

7ba4: 4621 mov r1, r4

7ba6: 2000 movs r0, #0

7ba8: 447a add r2, pc

7baa: f009 f837 bl 10c1c <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x3c>

7bae: 4620 mov r0, r4

7bb0: f007 fbb6 bl f320 <\_dl\_exception\_free@@GLIBC\_PRIVATE>

7bb4: 2300 movs r3, #0

7bb6: 461c mov r4, r3

7bb8: f8c8 3000 str.w r3, [r8]

7bbc: e5f9 b.n 77b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xb1e>

7bbe: f8df 36a8 ldr.w r3, [pc, #1704] ; 8268 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15d4>

7bc2: 447b add r3, pc

7bc4: 681b ldr r3, [r3, #0]

7bc6: 6819 ldr r1, [r3, #0]

7bc8: 2900 cmp r1, #0

7bca: d1d5 bne.n 7b78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xee4>

7bcc: f8df 169c ldr.w r1, [pc, #1692] ; 826c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15d8>

7bd0: 4479 add r1, pc

7bd2: e7d1 b.n 7b78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xee4>

7bd4: 459b cmp fp, r3

7bd6: f43f aee2 beq.w 799e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd0a>

7bda: 783b ldrb r3, [r7, #0]

7bdc: 2b00 cmp r3, #0

7bde: f000 8193 beq.w 7f08 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1274>

7be2: 463a mov r2, r7

7be4: f241 5405 movw r4, #5381 ; 0x1505

7be8: eb04 1444 add.w r4, r4, r4, lsl #5

7bec: 441c add r4, r3

7bee: f812 3f01 ldrb.w r3, [r2, #1]!

7bf2: 2b00 cmp r3, #0

7bf4: d1f8 bne.n 7be8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf54>

7bf6: 9925 ldr r1, [sp, #148] ; 0x94

7bf8: 2500 movs r5, #0

7bfa: f8db 31d4 ldr.w r3, [fp, #468] ; 0x1d4

7bfe: aa12 add r2, sp, #72 ; 0x48

7c00: f8df 666c ldr.w r6, [pc, #1644] ; 8270 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15dc>

7c04: f04f 3cff mov.w ip, #4294967295 ; 0xffffffff

7c08: 9106 str r1, [sp, #24]

7c0a: a915 add r1, sp, #84 ; 0x54

7c0c: 9109 str r1, [sp, #36] ; 0x24

7c0e: 447e add r6, pc

7c10: e9cd 3501 strd r3, r5, [sp, #4]

7c14: 4621 mov r1, r4

7c16: 9824 ldr r0, [sp, #144] ; 0x90

7c18: 9b09 ldr r3, [sp, #36] ; 0x24

7c1a: f8cd b01c str.w fp, [sp, #28]

7c1e: 9003 str r0, [sp, #12]

7c20: 4638 mov r0, r7

7c22: 9505 str r5, [sp, #20]

7c24: 9504 str r5, [sp, #16]

7c26: 9300 str r3, [sp, #0]

7c28: f8d8 3000 ldr.w r3, [r8]

7c2c: f8d6 9028 ldr.w r9, [r6, #40] ; 0x28

7c30: 62b5 str r5, [r6, #40] ; 0x28

7c32: 920b str r2, [sp, #44] ; 0x2c

7c34: f8cd c048 str.w ip, [sp, #72] ; 0x48

7c38: f7ff f94e bl 6ed8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x244>

7c3c: 9a15 ldr r2, [sp, #84] ; 0x54

7c3e: 9b13 ldr r3, [sp, #76] ; 0x4c

7c40: 429a cmp r2, r3

7c42: f000 8112 beq.w 7e6a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11d6>

7c46: f8c6 9028 str.w r9, [r6, #40] ; 0x28

7c4a: 2b00 cmp r3, #0

7c4c: f000 80af beq.w 7dae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x111a>

7c50: 7b1a ldrb r2, [r3, #12]

7c52: 9925 ldr r1, [sp, #148] ; 0x94

7c54: f002 020f and.w r2, r2, #15

7c58: 2a06 cmp r2, #6

7c5a: f001 0103 and.w r1, r1, #3

7c5e: 9125 str r1, [sp, #148] ; 0x94

7c60: f000 81cf beq.w 8002 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x136e>

7c64: 2501 movs r5, #1

7c66: 2a0a cmp r2, #10

7c68: f000 81c6 beq.w 7ff8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1364>

7c6c: 2d00 cmp r5, #0

7c6e: f43f aea7 beq.w 79c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd2c>

7c72: 6858 ldr r0, [r3, #4]

7c74: 2208 movs r2, #8

7c76: 9914 ldr r1, [sp, #80] ; 0x50

7c78: 4613 mov r3, r2

7c7a: f8db 41b0 ldr.w r4, [fp, #432] ; 0x1b0

7c7e: 9006 str r0, [sp, #24]

7c80: 2001 movs r0, #1

7c82: 9205 str r2, [sp, #20]

7c84: f8d1 51b0 ldr.w r5, [r1, #432] ; 0x1b0

7c88: f8d8 1000 ldr.w r1, [r8]

7c8c: e9cd 4200 strd r4, r2, [sp]

7c90: 1b09 subs r1, r1, r4

7c92: e9cd 2503 strd r2, r5, [sp, #12]

7c96: 9102 str r1, [sp, #8]

7c98: f8df 25d8 ldr.w r2, [pc, #1496] ; 8274 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15e0>

7c9c: f8df 15d8 ldr.w r1, [pc, #1496] ; 8278 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15e4>

7ca0: 447a add r2, pc

7ca2: 4479 add r1, pc

7ca4: f004 f892 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7ca8: 9b15 ldr r3, [sp, #84] ; 0x54

7caa: 4619 mov r1, r3

7cac: b11b cbz r3, 7cb6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1022>

7cae: 9a16 ldr r2, [sp, #88] ; 0x58

7cb0: 6859 ldr r1, [r3, #4]

7cb2: f8d2 31b0 ldr.w r3, [r2, #432] ; 0x1b0

7cb6: 2208 movs r2, #8

7cb8: e9cd 2100 strd r2, r1, [sp]

7cbc: f8df 15bc ldr.w r1, [pc, #1468] ; 827c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15e8>

7cc0: 2001 movs r0, #1

7cc2: 4479 add r1, pc

7cc4: f004 f882 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7cc8: e6a8 b.n 7a1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd88>

7cca: f8df 45b4 ldr.w r4, [pc, #1460] ; 8280 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15ec>

7cce: 447c add r4, pc

7cd0: e63b b.n 794a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xcb6>

7cd2: 9b24 ldr r3, [sp, #144] ; 0x90

7cd4: 2b00 cmp r3, #0

7cd6: f000 8113 beq.w 7f00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x126c>

7cda: 9b24 ldr r3, [sp, #144] ; 0x90

7cdc: 681b ldr r3, [r3, #0]

7cde: 2b00 cmp r3, #0

7ce0: f000 8109 beq.w 7ef6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1262>

7ce4: f8df 259c ldr.w r2, [pc, #1436] ; 8284 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15f0>

7ce8: 447a add r2, pc

7cea: f8df 159c ldr.w r1, [pc, #1436] ; 8288 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15f4>

7cee: 4479 add r1, pc

7cf0: 6809 ldr r1, [r1, #0]

7cf2: 6809 ldr r1, [r1, #0]

7cf4: 2900 cmp r1, #0

7cf6: f47f aded bne.w 78d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc40>

7cfa: f8df 1590 ldr.w r1, [pc, #1424] ; 828c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15f8>

7cfe: 4479 add r1, pc

7d00: e5e8 b.n 78d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc40>

7d02: f8df 058c ldr.w r0, [pc, #1420] ; 8290 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15fc>

7d06: 4478 add r0, pc

7d08: e73f b.n 7b8a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xef6>

7d0a: f8df 0588 ldr.w r0, [pc, #1416] ; 8294 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1600>

7d0e: 4478 add r0, pc

7d10: f004 f846 bl bda0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3c0>

7d14: f8df 3580 ldr.w r3, [pc, #1408] ; 8298 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1604>

7d18: 447b add r3, pc

7d1a: 681b ldr r3, [r3, #0]

7d1c: e62b b.n 7976 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xce2>

7d1e: 2200 movs r2, #0

7d20: 920b str r2, [sp, #44] ; 0x2c

7d22: e4f1 b.n 7708 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa74>

7d24: f8df 2574 ldr.w r2, [pc, #1396] ; 829c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1608>

7d28: 447a add r2, pc

7d2a: 4613 mov r3, r2

7d2c: e5cf b.n 78ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc3a>

7d2e: ebab 0302 sub.w r3, fp, r2

7d32: fab3 f383 clz r3, r3

7d36: 095b lsrs r3, r3, #5

7d38: 2a00 cmp r2, #0

7d3a: bf08 it eq

7d3c: 2301 moveq r3, #1

7d3e: 9a25 ldr r2, [sp, #148] ; 0x94

7d40: 2a03 cmp r2, #3

7d42: bfc8 it gt

7d44: f043 0301 orrgt.w r3, r3, #1

7d48: 2b00 cmp r3, #0

7d4a: f43f ae6f beq.w 7a2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd98>

7d4e: f8db 11b0 ldr.w r1, [fp, #432] ; 0x1b0

7d52: 2408 movs r4, #8

7d54: f8d8 2000 ldr.w r2, [r8]

7d58: 4623 mov r3, r4

7d5a: 9405 str r4, [sp, #20]

7d5c: 2001 movs r0, #1

7d5e: 1a52 subs r2, r2, r1

7d60: 9100 str r1, [sp, #0]

7d62: 9202 str r2, [sp, #8]

7d64: 2200 movs r2, #0

7d66: f8df 1538 ldr.w r1, [pc, #1336] ; 82a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x160c>

7d6a: 9206 str r2, [sp, #24]

7d6c: 9204 str r2, [sp, #16]

7d6e: 4479 add r1, pc

7d70: f8df 2530 ldr.w r2, [pc, #1328] ; 82a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1610>

7d74: 9403 str r4, [sp, #12]

7d76: 9401 str r4, [sp, #4]

7d78: 447a add r2, pc

7d7a: f004 f827 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7d7e: e64d b.n 7a1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd88>

7d80: f8df 1524 ldr.w r1, [pc, #1316] ; 82a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1614>

7d84: 4479 add r1, pc

7d86: e5cb b.n 7920 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc8c>

7d88: f8df 3520 ldr.w r3, [pc, #1312] ; 82ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1618>

7d8c: 447b add r3, pc

7d8e: e5d4 b.n 793a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xca6>

7d90: 9b0a ldr r3, [sp, #40] ; 0x28

7d92: f8df 251c ldr.w r2, [pc, #1308] ; 82b0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x161c>

7d96: 9c09 ldr r4, [sp, #36] ; 0x24

7d98: e893 0003 ldmia.w r3, {r0, r1}

7d9c: 447a add r2, pc

7d9e: 9b13 ldr r3, [sp, #76] ; 0x4c

7da0: f8c2 9028 str.w r9, [r2, #40] ; 0x28

7da4: e884 0003 stmia.w r4, {r0, r1}

7da8: 2b00 cmp r3, #0

7daa: f47f af51 bne.w 7c50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xfbc>

7dae: f8db 01b0 ldr.w r0, [fp, #432] ; 0x1b0

7db2: 2208 movs r2, #8

7db4: f8d8 1000 ldr.w r1, [r8]

7db8: 4613 mov r3, r2

7dba: 9205 str r2, [sp, #20]

7dbc: 2400 movs r4, #0

7dbe: 1a09 subs r1, r1, r0

7dc0: 9203 str r2, [sp, #12]

7dc2: e9cd 2101 strd r2, r1, [sp, #4]

7dc6: f8df 24ec ldr.w r2, [pc, #1260] ; 82b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1620>

7dca: f8df 14ec ldr.w r1, [pc, #1260] ; 82b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1624>

7dce: 9000 str r0, [sp, #0]

7dd0: 447a add r2, pc

7dd2: 2001 movs r0, #1

7dd4: 9406 str r4, [sp, #24]

7dd6: 9404 str r4, [sp, #16]

7dd8: 4479 add r1, pc

7dda: f003 fff7 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7dde: e763 b.n 7ca8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1014>

7de0: f8d4 620c ldr.w r6, [r4, #524] ; 0x20c

7de4: 0735 lsls r5, r6, #28

7de6: d422 bmi.n 7e2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x119a>

7de8: f89b 3198 ldrb.w r3, [fp, #408] ; 0x198

7dec: f003 0303 and.w r3, r3, #3

7df0: 2b02 cmp r3, #2

7df2: f040 808f bne.w 7f14 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1280>

7df6: f8db 320c ldr.w r3, [fp, #524] ; 0x20c

7dfa: 0718 lsls r0, r3, #28

7dfc: f100 808a bmi.w 7f14 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1280>

7e00: f8db 3200 ldr.w r3, [fp, #512] ; 0x200

7e04: 454b cmp r3, r9

7e06: f240 80ac bls.w 7f62 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12ce>

7e0a: f8db 31fc ldr.w r3, [fp, #508] ; 0x1fc

7e0e: f109 0201 add.w r2, r9, #1

7e12: eb03 0389 add.w r3, r3, r9, lsl #2

7e16: 605c str r4, [r3, #4]

7e18: f3bf 8f5b dmb ish

7e1c: f8db 31fc ldr.w r3, [fp, #508] ; 0x1fc

7e20: 601a str r2, [r3, #0]

7e22: f8df 3498 ldr.w r3, [pc, #1176] ; 82bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1628>

7e26: 447b add r3, pc

7e28: 681b ldr r3, [r3, #0]

7e2a: 0659 lsls r1, r3, #25

7e2c: d479 bmi.n 7f22 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x128e>

7e2e: f8df 3490 ldr.w r3, [pc, #1168] ; 82c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x162c>

7e32: 447b add r3, pc

7e34: f203 40c4 addw r0, r3, #1220 ; 0x4c4

7e38: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

7e3c: 4798 blx r3

7e3e: 9b09 ldr r3, [sp, #36] ; 0x24

7e40: b153 cbz r3, 7e58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11c4>

7e42: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

7e46: 930b str r3, [sp, #44] ; 0x2c

7e48: 9b0b ldr r3, [sp, #44] ; 0x2c

7e4a: f5a3 6998 sub.w r9, r3, #1216 ; 0x4c0

7e4e: 2301 movs r3, #1

7e50: f8c9 3004 str.w r3, [r9, #4]

7e54: f3bf 8f5b dmb ish

7e58: 9c14 ldr r4, [sp, #80] ; 0x50

7e5a: e499 b.n 7790 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xafc>

7e5c: f8df 2464 ldr.w r2, [pc, #1124] ; 82c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1630>

7e60: f8df 3464 ldr.w r3, [pc, #1124] ; 82c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1634>

7e64: 447a add r2, pc

7e66: 447b add r3, pc

7e68: e531 b.n 78ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xc3a>

7e6a: 9816 ldr r0, [sp, #88] ; 0x58

7e6c: 9a14 ldr r2, [sp, #80] ; 0x50

7e6e: 4290 cmp r0, r2

7e70: f47f aee9 bne.w 7c46 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xfb2>

7e74: f89b c199 ldrb.w ip, [fp, #409] ; 0x199

7e78: 1c19 adds r1, r3, #0

7e7a: bf18 it ne

7e7c: 2101 movne r1, #1

7e7e: ea11 11dc ands.w r1, r1, ip, lsr #7

7e82: f040 80a5 bne.w 7fd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x133c>

7e86: f8c6 9028 str.w r9, [r6, #40] ; 0x28

7e8a: 2b00 cmp r3, #0

7e8c: f000 81a1 beq.w 81d2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x153e>

7e90: 7b1a ldrb r2, [r3, #12]

7e92: 9925 ldr r1, [sp, #148] ; 0x94

7e94: f002 020f and.w r2, r2, #15

7e98: 2a06 cmp r2, #6

7e9a: f001 0103 and.w r1, r1, #3

7e9e: 9125 str r1, [sp, #148] ; 0x94

7ea0: d00b beq.n 7eba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1226>

7ea2: 2500 movs r5, #0

7ea4: e6df b.n 7c66 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xfd2>

7ea6: 9a25 ldr r2, [sp, #148] ; 0x94

7ea8: f8db 11b0 ldr.w r1, [fp, #432] ; 0x1b0

7eac: f042 0208 orr.w r2, r2, #8

7eb0: 9225 str r2, [sp, #148] ; 0x94

7eb2: f8d8 2000 ldr.w r2, [r8]

7eb6: 1a52 subs r2, r2, r1

7eb8: e59a b.n 79f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd5c>

7eba: 9a14 ldr r2, [sp, #80] ; 0x50

7ebc: 6858 ldr r0, [r3, #4]

7ebe: 2108 movs r1, #8

7ec0: f8db 41b0 ldr.w r4, [fp, #432] ; 0x1b0

7ec4: 460b mov r3, r1

7ec6: e9cd 1005 strd r1, r0, [sp, #20]

7eca: 2001 movs r0, #1

7ecc: f8d2 51b0 ldr.w r5, [r2, #432] ; 0x1b0

7ed0: f8d8 2000 ldr.w r2, [r8]

7ed4: 9101 str r1, [sp, #4]

7ed6: 1b12 subs r2, r2, r4

7ed8: e9cd 1503 strd r1, r5, [sp, #12]

7edc: 9202 str r2, [sp, #8]

7ede: 2104 movs r1, #4

7ee0: 4afa ldr r2, [pc, #1000] ; (82cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1638>)

7ee2: 9125 str r1, [sp, #148] ; 0x94

7ee4: 49fa ldr r1, [pc, #1000] ; (82d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x163c>)

7ee6: 447a add r2, pc

7ee8: 9400 str r4, [sp, #0]

7eea: 4479 add r1, pc

7eec: f003 ff6e bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

7ef0: e594 b.n 7a1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd88>

7ef2: 46b1 mov r9, r6

7ef4: e5df b.n 7ab6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe22>

7ef6: 4af7 ldr r2, [pc, #988] ; (82d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1640>)

7ef8: 4bf7 ldr r3, [pc, #988] ; (82d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1644>)

7efa: 447a add r2, pc

7efc: 447b add r3, pc

7efe: e6f4 b.n 7cea <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1056>

7f00: 4af6 ldr r2, [pc, #984] ; (82dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1648>)

7f02: 447a add r2, pc

7f04: 4613 mov r3, r2

7f06: e6f0 b.n 7cea <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1056>

7f08: f241 5405 movw r4, #5381 ; 0x1505

7f0c: e673 b.n 7bf6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf62>

7f0e: 4bf4 ldr r3, [pc, #976] ; (82e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x164c>)

7f10: 447b add r3, pc

7f12: e603 b.n 7b1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe88>

7f14: 4bf3 ldr r3, [pc, #972] ; (82e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1650>)

7f16: f046 0608 orr.w r6, r6, #8

7f1a: f8c4 620c str.w r6, [r4, #524] ; 0x20c

7f1e: 447b add r3, pc

7f20: e788 b.n 7e34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11a0>

7f22: 6861 ldr r1, [r4, #4]

7f24: 780b ldrb r3, [r1, #0]

7f26: b92b cbnz r3, 7f34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12a0>

7f28: 4bef ldr r3, [pc, #956] ; (82e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1654>)

7f2a: 447b add r3, pc

7f2c: 681b ldr r3, [r3, #0]

7f2e: 6819 ldr r1, [r3, #0]

7f30: 2900 cmp r1, #0

7f32: d036 beq.n 7fa2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x130e>

7f34: f8db 3004 ldr.w r3, [fp, #4]

7f38: 69a2 ldr r2, [r4, #24]

7f3a: 7818 ldrb r0, [r3, #0]

7f3c: b920 cbnz r0, 7f48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12b4>

7f3e: 4beb ldr r3, [pc, #940] ; (82ec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1658>)

7f40: 447b add r3, pc

7f42: 681b ldr r3, [r3, #0]

7f44: 681b ldr r3, [r3, #0]

7f46: b14b cbz r3, 7f5c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12c8>

7f48: f8db 4018 ldr.w r4, [fp, #24]

7f4c: 48e8 ldr r0, [pc, #928] ; (82f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x165c>)

7f4e: 9400 str r4, [sp, #0]

7f50: 4478 add r0, pc

7f52: f003 ff0f bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

7f56: 4be7 ldr r3, [pc, #924] ; (82f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1660>)

7f58: 447b add r3, pc

7f5a: e76b b.n 7e34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11a0>

7f5c: 4be6 ldr r3, [pc, #920] ; (82f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1664>)

7f5e: 447b add r3, pc

7f60: e7f2 b.n 7f48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12b4>

7f62: b393 cbz r3, 7fca <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1336>

7f64: 005b lsls r3, r3, #1

7f66: 930a str r3, [sp, #40] ; 0x28

7f68: 9b0a ldr r3, [sp, #40] ; 0x28

7f6a: 1c58 adds r0, r3, #1

7f6c: 0080 lsls r0, r0, #2

7f6e: f7f8 ed2e blx 9cc <malloc@plt+0x4>

7f72: 4605 mov r5, r0

7f74: b310 cbz r0, 7fbc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1328>

7f76: f1b9 0f00 cmp.w r9, #0

7f7a: d115 bne.n 7fa8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1314>

7f7c: 464e mov r6, r9

7f7e: 442e add r6, r5

7f80: f109 0301 add.w r3, r9, #1

7f84: 6074 str r4, [r6, #4]

7f86: 602b str r3, [r5, #0]

7f88: f3bf 8f5b dmb ish

7f8c: 9b0a ldr r3, [sp, #40] ; 0x28

7f8e: f8db 01fc ldr.w r0, [fp, #508] ; 0x1fc

7f92: e9cb 537f strd r5, r3, [fp, #508] ; 0x1fc

7f96: 2800 cmp r0, #0

7f98: f43f af43 beq.w 7e22 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x118e>

7f9c: f005 fc04 bl d7a8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x268>

7fa0: e73f b.n 7e22 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x118e>

7fa2: 49d6 ldr r1, [pc, #856] ; (82fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1668>)

7fa4: 4479 add r1, pc

7fa6: e7c5 b.n 7f34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12a0>

7fa8: ea4f 0689 mov.w r6, r9, lsl #2

7fac: f8db 11fc ldr.w r1, [fp, #508] ; 0x1fc

7fb0: 3004 adds r0, #4

7fb2: 4632 mov r2, r6

7fb4: 3104 adds r1, #4

7fb6: f00b e8a4 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

7fba: e7e0 b.n 7f7e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12ea>

7fbc: 4bd0 ldr r3, [pc, #832] ; (8300 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x166c>)

7fbe: f046 0608 orr.w r6, r6, #8

7fc2: f8c4 620c str.w r6, [r4, #524] ; 0x20c

7fc6: 447b add r3, pc

7fc8: e734 b.n 7e34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11a0>

7fca: 230a movs r3, #10

7fcc: 930a str r3, [sp, #40] ; 0x28

7fce: e7cb b.n 7f68 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12d4>

7fd0: 7b19 ldrb r1, [r3, #12]

7fd2: 0909 lsrs r1, r1, #4

7fd4: 290a cmp r1, #10

7fd6: f000 80bb beq.w 8150 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14bc>

7fda: 9925 ldr r1, [sp, #148] ; 0x94

7fdc: f8c6 9028 str.w r9, [r6, #40] ; 0x28

7fe0: f001 0103 and.w r1, r1, #3

7fe4: 9125 str r1, [sp, #148] ; 0x94

7fe6: 7b19 ldrb r1, [r3, #12]

7fe8: f001 010f and.w r1, r1, #15

7fec: 2906 cmp r1, #6

7fee: f43f af65 beq.w 7ebc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1228>

7ff2: 290a cmp r1, #10

7ff4: f47f ace4 bne.w 79c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd2c>

7ff8: 9a25 ldr r2, [sp, #148] ; 0x94

7ffa: f042 0208 orr.w r2, r2, #8

7ffe: 9225 str r2, [sp, #148] ; 0x94

8000: e634 b.n 7c6c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xfd8>

8002: 6858 ldr r0, [r3, #4]

8004: 2208 movs r2, #8

8006: 9914 ldr r1, [sp, #80] ; 0x50

8008: 4613 mov r3, r2

800a: f8db 41b0 ldr.w r4, [fp, #432] ; 0x1b0

800e: 9006 str r0, [sp, #24]

8010: 2001 movs r0, #1

8012: 9205 str r2, [sp, #20]

8014: f8d1 51b0 ldr.w r5, [r1, #432] ; 0x1b0

8018: f8d8 1000 ldr.w r1, [r8]

801c: 9201 str r2, [sp, #4]

801e: 1b09 subs r1, r1, r4

8020: e9cd 2503 strd r2, r5, [sp, #12]

8024: 9102 str r1, [sp, #8]

8026: 2204 movs r2, #4

8028: 49b6 ldr r1, [pc, #728] ; (8304 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1670>)

802a: 9225 str r2, [sp, #148] ; 0x94

802c: 4ab6 ldr r2, [pc, #728] ; (8308 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1674>)

802e: 4479 add r1, pc

8030: 9400 str r4, [sp, #0]

8032: 447a add r2, pc

8034: f003 feca bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

8038: e636 b.n 7ca8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1014>

803a: 4bb4 ldr r3, [pc, #720] ; (830c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1678>)

803c: f240 3223 movw r2, #803 ; 0x323

8040: 49b3 ldr r1, [pc, #716] ; (8310 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x167c>)

8042: 48b4 ldr r0, [pc, #720] ; (8314 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1680>)

8044: 447b add r3, pc

8046: 4479 add r1, pc

8048: 4478 add r0, pc

804a: f008 fc9f bl 1098c <free@@GLIBC\_2.4+0x12c>

804e: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

8052: f5a3 6398 sub.w r3, r3, #1216 ; 0x4c0

8056: 2201 movs r2, #1

8058: 605a str r2, [r3, #4]

805a: f3bf 8f5b dmb ish

805e: f8db 31d0 ldr.w r3, [fp, #464] ; 0x1d0

8062: 930a str r3, [sp, #40] ; 0x28

8064: e563 b.n 7b2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe9a>

8066: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

806a: 930b str r3, [sp, #44] ; 0x2c

806c: 461a mov r2, r3

806e: f2a2 40bc subw r0, r2, #1212 ; 0x4bc

8072: 2300 movs r3, #0

8074: f3bf 8f5b dmb ish

8078: e850 2f00 ldrex r2, [r0]

807c: e840 3100 strex r1, r3, [r0]

8080: 2900 cmp r1, #0

8082: d1f9 bne.n 8078 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13e4>

8084: 2a02 cmp r2, #2

8086: f000 80b2 beq.w 81ee <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x155a>

808a: 4ba3 ldr r3, [pc, #652] ; (8318 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1684>)

808c: 447b add r3, pc

808e: f203 40c4 addw r0, r3, #1220 ; 0x4c4

8092: f8d3 37f8 ldr.w r3, [r3, #2040] ; 0x7f8

8096: 4798 blx r3

8098: f8db 31f8 ldr.w r3, [fp, #504] ; 0x1f8

809c: 9a0e ldr r2, [sp, #56] ; 0x38

809e: 429a cmp r2, r3

80a0: d00b beq.n 80ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1426>

80a2: b153 cbz r3, 80ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1426>

80a4: 681a ldr r2, [r3, #0]

80a6: b142 cbz r2, 80ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1426>

80a8: 42a2 cmp r2, r4

80aa: d102 bne.n 80b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x141e>

80ac: e038 b.n 8120 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x148c>

80ae: 4294 cmp r4, r2

80b0: d036 beq.n 8120 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x148c>

80b2: f853 2f04 ldr.w r2, [r3, #4]!

80b6: 2a00 cmp r2, #0

80b8: d1f9 bne.n 80ae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x141a>

80ba: f8db 31fc ldr.w r3, [fp, #508] ; 0x1fc

80be: 2b00 cmp r3, #0

80c0: f43f ad0b beq.w 7ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe46>

80c4: 429e cmp r6, r3

80c6: 681a ldr r2, [r3, #0]

80c8: d01b beq.n 8102 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x146e>

80ca: 2a00 cmp r2, #0

80cc: f000 809a beq.w 8204 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1570>

80d0: 6859 ldr r1, [r3, #4]

80d2: 428c cmp r4, r1

80d4: d024 beq.n 8120 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x148c>

80d6: 3304 adds r3, #4

80d8: 990f ldr r1, [sp, #60] ; 0x3c

80da: e003 b.n 80e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1450>

80dc: f853 0f04 ldr.w r0, [r3, #4]!

80e0: 4284 cmp r4, r0

80e2: d01d beq.n 8120 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x148c>

80e4: 3101 adds r1, #1

80e6: 4291 cmp r1, r2

80e8: d1f8 bne.n 80dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1448>

80ea: 4689 mov r9, r1

80ec: e4f5 b.n 7ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe46>

80ee: 4b8b ldr r3, [pc, #556] ; (831c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1688>)

80f0: f240 3239 movw r2, #825 ; 0x339

80f4: 498a ldr r1, [pc, #552] ; (8320 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x168c>)

80f6: 488b ldr r0, [pc, #556] ; (8324 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1690>)

80f8: 447b add r3, pc

80fa: 4479 add r1, pc

80fc: 4478 add r0, pc

80fe: f008 fc45 bl 1098c <free@@GLIBC\_2.4+0x12c>

8102: 4591 cmp r9, r2

8104: f4bf ace9 bcs.w 7ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe46>

8108: eb06 0689 add.w r6, r6, r9, lsl #2

810c: e004 b.n 8118 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1484>

810e: f109 0901 add.w r9, r9, #1

8112: 4591 cmp r9, r2

8114: f43f ace1 beq.w 7ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe46>

8118: f856 3f04 ldr.w r3, [r6, #4]!

811c: 429c cmp r4, r3

811e: d1f6 bne.n 810e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x147a>

8120: e9d4 1296 ldrd r1, r2, [r4, #600] ; 0x258

8124: e9dd 340c ldrd r3, r4, [sp, #48] ; 0x30

8128: 487f ldr r0, [pc, #508] ; (8328 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1694>)

812a: 4294 cmp r4, r2

812c: 4478 add r0, pc

812e: bf08 it eq

8130: 428b cmpeq r3, r1

8132: f8d0 67fc ldr.w r6, [r0, #2044] ; 0x7fc

8136: d061 beq.n 81fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1568>

8138: f200 40c4 addw r0, r0, #1220 ; 0x4c4

813c: 47b0 blx r6

813e: 9b0b ldr r3, [sp, #44] ; 0x2c

8140: f5a3 6998 sub.w r9, r3, #1216 ; 0x4c0

8144: 2301 movs r3, #1

8146: f8c9 3004 str.w r3, [r9, #4]

814a: f3bf 8f5b dmb ish

814e: e786 b.n 805e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13ca>

8150: f8db 61d4 ldr.w r6, [fp, #468] ; 0x1d4

8154: 462b mov r3, r5

8156: e9cd 5517 strd r5, r5, [sp, #92] ; 0x5c

815a: 6872 ldr r2, [r6, #4]

815c: e005 b.n 816a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14d6>

815e: 6831 ldr r1, [r6, #0]

8160: f851 1023 ldr.w r1, [r1, r3, lsl #2]

8164: 462b mov r3, r5

8166: 4288 cmp r0, r1

8168: d003 beq.n 8172 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14de>

816a: 429a cmp r2, r3

816c: f103 0501 add.w r5, r3, #1

8170: d1f5 bne.n 815e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14ca>

8172: ab17 add r3, sp, #92 ; 0x5c

8174: f04f 0a00 mov.w sl, #0

8178: 930a str r3, [sp, #40] ; 0x28

817a: e020 b.n 81be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x152a>

817c: 6833 ldr r3, [r6, #0]

817e: f853 3025 ldr.w r3, [r3, r5, lsl #2]

8182: 6e19 ldr r1, [r3, #96] ; 0x60

8184: b1d1 cbz r1, 81bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1528>

8186: f503 73b4 add.w r3, r3, #360 ; 0x168

818a: 9301 str r3, [sp, #4]

818c: 9b25 ldr r3, [sp, #148] ; 0x94

818e: 4621 mov r1, r4

8190: f8cd b01c str.w fp, [sp, #28]

8194: 4638 mov r0, r7

8196: f8cd a014 str.w sl, [sp, #20]

819a: 9306 str r3, [sp, #24]

819c: 9b24 ldr r3, [sp, #144] ; 0x90

819e: f8cd a010 str.w sl, [sp, #16]

81a2: 9a0b ldr r2, [sp, #44] ; 0x2c

81a4: e9cd a302 strd sl, r3, [sp, #8]

81a8: 9b0a ldr r3, [sp, #40] ; 0x28

81aa: 9300 str r3, [sp, #0]

81ac: f8d8 3000 ldr.w r3, [r8]

81b0: f7fe fe92 bl 6ed8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x244>

81b4: 2800 cmp r0, #0

81b6: f73f adeb bgt.w 7d90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x10fc>

81ba: 6872 ldr r2, [r6, #4]

81bc: 3501 adds r5, #1

81be: 4295 cmp r5, r2

81c0: d3dc bcc.n 817c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14e8>

81c2: 4a5a ldr r2, [pc, #360] ; (832c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1698>)

81c4: 9b13 ldr r3, [sp, #76] ; 0x4c

81c6: 447a add r2, pc

81c8: f8c2 9028 str.w r9, [r2, #40] ; 0x28

81cc: 2b00 cmp r3, #0

81ce: f47f ae5f bne.w 7e90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11fc>

81d2: 4b57 ldr r3, [pc, #348] ; (8330 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x169c>)

81d4: 447b add r3, pc

81d6: f8d3 3174 ldr.w r3, [r3, #372] ; 0x174

81da: 459b cmp fp, r3

81dc: bf18 it ne

81de: 2b00 cmpne r3, #0

81e0: f43f adb5 beq.w 7d4e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x10ba>

81e4: 9b25 ldr r3, [sp, #148] ; 0x94

81e6: 2b03 cmp r3, #3

81e8: f77f ac20 ble.w 7a2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xd98>

81ec: e5af b.n 7d4e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x10ba>

81ee: 2181 movs r1, #129 ; 0x81

81f0: 2201 movs r2, #1

81f2: f04f 0cf0 mov.w ip, #240 ; 0xf0

81f6: f008 fee3 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

81fa: e746 b.n 808a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x13f6>

81fc: f200 40c4 addw r0, r0, #1220 ; 0x4c4

8200: 47b0 blx r6

8202: e621 b.n 7e48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11b4>

8204: 4691 mov r9, r2

8206: e468 b.n 7ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xe46>

8208: 198c adds r4, r1, r6

820a: 0002 movs r2, r0

820c: 11ca asrs r2, r1, #7

820e: 0002 movs r2, r0

8210: 10cc asrs r4, r1, #3

8212: 0002 movs r2, r0

8214: e8cc 0000 ; <UNDEFINED> instruction: 0xe8cc0000

8218: e99a 0000 ; <UNDEFINED> instruction: 0xe99a0000

821c: e9a8 0000 ; <UNDEFINED> instruction: 0xe9a80000

8220: 1f84 subs r4, r0, #6

8222: 0002 movs r2, r0

8224: 1f6a subs r2, r5, #5

8226: 0002 movs r2, r0

8228: e868 0000 strd r0, r0, [r8], #-0

822c: e998 0000 ; <UNDEFINED> instruction: 0xe9980000

8230: e9b8 0000 ; <UNDEFINED> instruction: 0xe9b80000

8234: 0ffa lsrs r2, r7, #31

8236: 0002 movs r2, r0

8238: 0fe8 lsrs r0, r5, #31

823a: 0002 movs r2, r0

823c: 16d4 asrs r4, r2, #27

823e: 0002 movs r2, r0

8240: 0fa8 lsrs r0, r5, #30

8242: 0002 movs r2, r0

8244: e936 0000 ldmdb r6!, {}

8248: e93a 0000 ldmdb sl!, {}

824c: e916 0000 ldmdb r6, {}

8250: 158e asrs r6, r1, #22

8252: 0002 movs r2, r0

8254: 1574 asrs r4, r6, #21

8256: 0002 movs r2, r0

8258: 1542 asrs r2, r0, #21

825a: 0002 movs r2, r0

825c: d980 bls.n 8160 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x14cc>

825e: 0000 movs r0, r0

8260: e686 b.n 7f70 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12dc>

8262: 0000 movs r0, r0

8264: e6c0 b.n 7fe8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1354>

8266: 0000 movs r0, r0

8268: 1cd6 adds r6, r2, #3

826a: 0002 movs r2, r0

826c: cf08 ldmia r7!, {r3}

826e: 0000 movs r0, r0

8270: 144e asrs r6, r1, #17

8272: 0002 movs r2, r0

8274: e6d4 b.n 8020 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x138c>

8276: 0000 movs r0, r0

8278: e6aa b.n 7fd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x133c>

827a: 0000 movs r0, r0

827c: e666 b.n 7f4c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x12b8>

827e: 0000 movs r0, r0

8280: e4d6 b.n 7c30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf9c>

8282: 0000 movs r0, r0

8284: e4b0 b.n 7be8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf54>

8286: 0000 movs r0, r0

8288: 1baa subs r2, r5, r6

828a: 0002 movs r2, r0

828c: cdda ldmia r5!, {r1, r3, r4, r6, r7}

828e: 0000 movs r0, r0

8290: e47a b.n 7b88 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xef4>

8292: 0000 movs r0, r0

8294: e5da b.n 7e4c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11b8>

8296: 0000 movs r0, r0

8298: 0c54 lsrs r4, r2, #17

829a: 0002 movs r2, r0

829c: d7e0 bvc.n 8260 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x15cc>

829e: 0000 movs r0, r0

82a0: e5de b.n 7e60 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11cc>

82a2: 0000 movs r0, r0

82a4: e5cc b.n 7e40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x11ac>

82a6: 0000 movs r0, r0

82a8: cd54 ldmia r5!, {r2, r4, r6}

82aa: 0000 movs r0, r0

82ac: cd4c ldmia r5!, {r2, r3, r6}

82ae: 0000 movs r0, r0

82b0: 12c0 asrs r0, r0, #11

82b2: 0002 movs r2, r0

82b4: e5a4 b.n 7e00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x116c>

82b6: 0000 movs r0, r0

82b8: e574 b.n 7da4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1110>

82ba: 0000 movs r0, r0

82bc: 0b46 lsrs r6, r0, #13

82be: 0002 movs r2, r0

82c0: 122a asrs r2, r5, #8

82c2: 0002 movs r2, r0

82c4: e334 b.n 8930 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c9c>

82c6: 0000 movs r0, r0

82c8: d6a2 bvs.n 8210 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x157c>

82ca: 0000 movs r0, r0

82cc: e45e b.n 7b8c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xef8>

82ce: 0000 movs r0, r0

82d0: e462 b.n 7b98 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xf04>

82d2: 0000 movs r0, r0

82d4: e29e b.n 8814 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b80>

82d6: 0000 movs r0, r0

82d8: d60c bvs.n 82f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1660>

82da: 0000 movs r0, r0

82dc: d606 bvs.n 82ec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1658>

82de: 0000 movs r0, r0

82e0: 114c asrs r4, r1, #5

82e2: 0002 movs r2, r0

82e4: 113e asrs r6, r7, #4

82e6: 0002 movs r2, r0

82e8: 196e adds r6, r5, r5

82ea: 0002 movs r2, r0

82ec: 1958 adds r0, r3, r5

82ee: 0002 movs r2, r0

82f0: e35c b.n 89ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d18>

82f2: 0000 movs r0, r0

82f4: 1104 asrs r4, r0, #4

82f6: 0002 movs r2, r0

82f8: cb7a ldmia r3, {r1, r3, r4, r5, r6}

82fa: 0000 movs r0, r0

82fc: cb34 ldmia r3!, {r2, r4, r5}

82fe: 0000 movs r0, r0

8300: 1096 asrs r6, r2, #2

8302: 0002 movs r2, r0

8304: e31e b.n 8944 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cb0>

8306: 0000 movs r0, r0

8308: e342 b.n 8990 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cfc>

830a: 0000 movs r0, r0

830c: e39c b.n 8a48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1db4>

830e: 0000 movs r0, r0

8310: e092 b.n 8438 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x17a4>

8312: 0000 movs r0, r0

8314: e170 b.n 85f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1964>

8316: 0000 movs r0, r0

8318: 0fd0 lsrs r0, r2, #31

831a: 0002 movs r2, r0

831c: e2e8 b.n 88f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c5c>

831e: 0000 movs r0, r0

8320: dfde svc 222 ; 0xde

8322: 0000 movs r0, r0

8324: e114 b.n 8550 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x18bc>

8326: 0000 movs r0, r0

8328: 0f30 lsrs r0, r6, #28

832a: 0002 movs r2, r0

832c: 0e96 lsrs r6, r2, #26

832e: 0002 movs r2, r0

8330: 0798 lsls r0, r3, #30

8332: 0002 movs r2, r0

8334: b538 push {r3, r4, r5, lr}

8336: f8d0 3150 ldr.w r3, [r0, #336] ; 0x150

833a: b1cb cbz r3, 8370 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x16dc>

833c: 685b ldr r3, [r3, #4]

833e: 6819 ldr r1, [r3, #0]

8340: f8c0 117c str.w r1, [r0, #380] ; 0x17c

8344: e9d3 5201 ldrd r5, r2, [r3, #4]

8348: 1e54 subs r4, r2, #1

834a: 4214 tst r4, r2

834c: d11d bne.n 838a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x16f6>

834e: f8c0 4180 str.w r4, [r0, #384] ; 0x180

8352: 3310 adds r3, #16

8354: f853 4c04 ldr.w r4, [r3, #-4]

8358: 1b49 subs r1, r1, r5

835a: eb03 0282 add.w r2, r3, r2, lsl #2

835e: f8c0 3188 str.w r3, [r0, #392] ; 0x188

8362: eb02 0181 add.w r1, r2, r1, lsl #2

8366: e9c0 2163 strd r2, r1, [r0, #396] ; 0x18c

836a: f8c0 4184 str.w r4, [r0, #388] ; 0x184

836e: bd38 pop {r3, r4, r5, pc}

8370: 6b03 ldr r3, [r0, #48] ; 0x30

8372: 2b00 cmp r3, #0

8374: d0fb beq.n 836e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x16da>

8376: 685a ldr r2, [r3, #4]

8378: f852 3b08 ldr.w r3, [r2], #8

837c: f8c0 317c str.w r3, [r0, #380] ; 0x17c

8380: eb02 0383 add.w r3, r2, r3, lsl #2

8384: e9c0 3263 strd r3, r2, [r0, #396] ; 0x18c

8388: bd38 pop {r3, r4, r5, pc}

838a: 4b05 ldr r3, [pc, #20] ; (83a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x170c>)

838c: f44f 726d mov.w r2, #948 ; 0x3b4

8390: 4904 ldr r1, [pc, #16] ; (83a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1710>)

8392: 4805 ldr r0, [pc, #20] ; (83a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1714>)

8394: 447b add r3, pc

8396: 4479 add r1, pc

8398: 4478 add r0, pc

839a: f008 faf7 bl 1098c <free@@GLIBC\_2.4+0x12c>

839e: bf00 nop

83a0: e01c b.n 83dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1748>

83a2: 0000 movs r0, r0

83a4: dd42 ble.n 842c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1798>

83a6: 0000 movs r0, r0

83a8: dfe8 svc 232 ; 0xe8

83aa: 0000 movs r0, r0

83ac: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

83b0: 4605 mov r5, r0

83b2: 4e19 ldr r6, [pc, #100] ; (8418 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1784>)

83b4: 460c mov r4, r1

83b6: 447e add r6, pc

83b8: f206 40dc addw r0, r6, #1244 ; 0x4dc

83bc: f8d6 37f8 ldr.w r3, [r6, #2040] ; 0x7f8

83c0: 4798 blx r3

83c2: 234c movs r3, #76 ; 0x4c

83c4: fb03 f304 mul.w r3, r3, r4

83c8: 58f2 ldr r2, [r6, r3]

83ca: b90a cbnz r2, 83d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x173c>

83cc: e021 b.n 8412 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x177e>

83ce: 461a mov r2, r3

83d0: 68d3 ldr r3, [r2, #12]

83d2: 2b00 cmp r3, #0

83d4: d1fb bne.n 83ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x173a>

83d6: 612a str r2, [r5, #16]

83d8: 60d5 str r5, [r2, #12]

83da: 4b10 ldr r3, [pc, #64] ; (841c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1788>)

83dc: 214c movs r1, #76 ; 0x4c

83de: 447b add r3, pc

83e0: f503 629f add.w r2, r3, #1272 ; 0x4f8

83e4: f203 40dc addw r0, r3, #1244 ; 0x4dc

83e8: fb01 3404 mla r4, r1, r4, r3

83ec: f8d3 77fc ldr.w r7, [r3, #2044] ; 0x7fc

83f0: e9d2 1600 ldrd r1, r6, [r2]

83f4: f111 0e01 adds.w lr, r1, #1

83f8: f146 0c00 adc.w ip, r6, #0

83fc: 6863 ldr r3, [r4, #4]

83fe: 3301 adds r3, #1

8400: 6063 str r3, [r4, #4]

8402: e9c5 1696 strd r1, r6, [r5, #600] ; 0x258

8406: 463b mov r3, r7

8408: e9c2 ec00 strd lr, ip, [r2]

840c: e8bd 41f0 ldmia.w sp!, {r4, r5, r6, r7, r8, lr}

8410: 4718 bx r3

8412: 50f5 str r5, [r6, r3]

8414: e7e1 b.n 83da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1746>

8416: bf00 nop

8418: 0ca6 lsrs r6, r4, #18

841a: 0002 movs r2, r0

841c: 0c7e lsrs r6, r7, #17

841e: 0002 movs r2, r0

8420: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

8424: 4680 mov r8, r0

8426: b083 sub sp, #12

8428: 4608 mov r0, r1

842a: 4693 mov fp, r2

842c: 461d mov r5, r3

842e: 4689 mov r9, r1

8430: f00a f986 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

8434: 4b73 ldr r3, [pc, #460] ; (8604 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1970>)

8436: 447b add r3, pc

8438: f8d3 61b0 ldr.w r6, [r3, #432] ; 0x1b0

843c: 4607 mov r7, r0

843e: 1c42 adds r2, r0, #1

8440: b91e cbnz r6, 844a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x17b6>

8442: 9b0c ldr r3, [sp, #48] ; 0x30

8444: 165e asrs r6, r3, #25

8446: f006 0610 and.w r6, r6, #16

844a: ea4f 0ac6 mov.w sl, r6, lsl #3

844e: f207 2071 addw r0, r7, #625 ; 0x271

8452: 4450 add r0, sl

8454: 2101 movs r1, #1

8456: 9201 str r2, [sp, #4]

8458: f7f8 eaca blx 9f0 <calloc@plt>

845c: 4604 mov r4, r0

845e: 2800 cmp r0, #0

8460: f000 80ad beq.w 85be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x192a>

8464: f50a 7318 add.w r3, sl, #608 ; 0x260

8468: 6160 str r0, [r4, #20]

846a: eb00 0a03 add.w sl, r0, r3

846e: 9a01 ldr r2, [sp, #4]

8470: f8c0 a168 str.w sl, [r0, #360] ; 0x168

8474: 4649 mov r1, r9

8476: f10a 0004 add.w r0, sl, #4

847a: 61e0 str r0, [r4, #28]

847c: f10a 0010 add.w r0, sl, #16

8480: f00a ee3e blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

8484: f898 9000 ldrb.w r9, [r8]

8488: 2201 movs r2, #1

848a: f8ca 200c str.w r2, [sl, #12]

848e: f8ca 0004 str.w r0, [sl, #4]

8492: 4438 add r0, r7

8494: f1b9 0f00 cmp.w r9, #0

8498: f040 8095 bne.w 85c6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1932>

849c: 4b5a ldr r3, [pc, #360] ; (8608 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1974>)

849e: f894 2198 ldrb.w r2, [r4, #408] ; 0x198

84a2: 447b add r3, pc

84a4: 6060 str r0, [r4, #4]

84a6: f36b 0201 bfi r2, fp, #0, #2

84aa: f8c4 5170 str.w r5, [r4, #368] ; 0x170

84ae: 681b ldr r3, [r3, #0]

84b0: f884 2198 strb.w r2, [r4, #408] ; 0x198

84b4: 2200 movs r2, #0

84b6: 05d9 lsls r1, r3, #23

84b8: bf5c itt pl

84ba: 2301 movpl r3, #1

84bc: f8c4 3204 strpl.w r3, [r4, #516] ; 0x204

84c0: 9b0d ldr r3, [sp, #52] ; 0x34

84c2: 61a3 str r3, [r4, #24]

84c4: f504 7318 add.w r3, r4, #608 ; 0x260

84c8: b126 cbz r6, 84d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1840>

84ca: f843 4032 str.w r4, [r3, r2, lsl #3]

84ce: 3201 adds r2, #1

84d0: 4296 cmp r6, r2

84d2: d1fa bne.n 84ca <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1836>

84d4: 990d ldr r1, [sp, #52] ; 0x34

84d6: 234c movs r3, #76 ; 0x4c

84d8: 4a4c ldr r2, [pc, #304] ; (860c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1978>)

84da: fb03 fa01 mul.w sl, r3, r1

84de: 447a add r2, pc

84e0: f504 71de add.w r1, r4, #444 ; 0x1bc

84e4: 2304 movs r3, #4

84e6: f8c4 11d0 str.w r1, [r4, #464] ; 0x1d0

84ea: f8c4 31cc str.w r3, [r4, #460] ; 0x1cc

84ee: f852 200a ldr.w r2, [r2, sl]

84f2: 2a00 cmp r2, #0

84f4: d078 beq.n 85e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1954>

84f6: f502 73b0 add.w r3, r2, #352 ; 0x160

84fa: 2201 movs r2, #1

84fc: f8c4 31bc str.w r3, [r4, #444] ; 0x1bc

8500: b915 cbnz r5, 8508 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1874>

8502: 4625 mov r5, r4

8504: e063 b.n 85ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x193a>

8506: 461d mov r5, r3

8508: f8d5 3170 ldr.w r3, [r5, #368] ; 0x170

850c: 2b00 cmp r3, #0

850e: d1fa bne.n 8506 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1872>

8510: 2a00 cmp r2, #0

8512: d15a bne.n 85ca <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1936>

8514: f505 75b0 add.w r5, r5, #352 ; 0x160

8518: 600d str r5, [r1, #0]

851a: f504 73b0 add.w r3, r4, #352 ; 0x160

851e: f8c4 31d4 str.w r3, [r4, #468] ; 0x1d4

8522: f1b9 0f00 cmp.w r9, #0

8526: d04a beq.n 85be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x192a>

8528: 4640 mov r0, r8

852a: f00a f909 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

852e: f1b9 0f2f cmp.w r9, #47 ; 0x2f

8532: f100 0301 add.w r3, r0, #1

8536: 4682 mov sl, r0

8538: 9301 str r3, [sp, #4]

853a: d059 beq.n 85f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x195c>

853c: f8df 90d0 ldr.w r9, [pc, #208] ; 8610 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x197c>

8540: f04f 0b00 mov.w fp, #0

8544: 9d01 ldr r5, [sp, #4]

8546: 44f9 add r9, pc

8548: e007 b.n 855a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x18c6>

854a: f008 fda5 bl 11098 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x330>

854e: 46bb mov fp, r7

8550: b9b8 cbnz r0, 8582 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x18ee>

8552: f8d9 2000 ldr.w r2, [r9]

8556: 2a22 cmp r2, #34 ; 0x22

8558: d10d bne.n 8576 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x18e2>

855a: f105 0680 add.w r6, r5, #128 ; 0x80

855e: 4658 mov r0, fp

8560: 4631 mov r1, r6

8562: f7f8 ea4c blx 9fc <realloc@plt>

8566: f105 017f add.w r1, r5, #127 ; 0x7f

856a: eba1 010a sub.w r1, r1, sl

856e: 4635 mov r5, r6

8570: 4607 mov r7, r0

8572: 2800 cmp r0, #0

8574: d1e9 bne.n 854a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x18b6>

8576: 4658 mov r0, fp

8578: f04f 37ff mov.w r7, #4294967295 ; 0xffffffff

857c: f7f8 ea52 blx a24 <free@plt+0x4>

8580: e01b b.n 85ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1926>

8582: 4638 mov r0, r7

8584: f00a f8dc bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

8588: 4603 mov r3, r0

858a: 1838 adds r0, r7, r0

858c: f810 2c01 ldrb.w r2, [r0, #-1]

8590: 2a2f cmp r2, #47 ; 0x2f

8592: bf1e ittt ne

8594: 3001 addne r0, #1

8596: 222f movne r2, #47 ; 0x2f

8598: 54fa strbne r2, [r7, r3]

859a: 9a01 ldr r2, [sp, #4]

859c: 4641 mov r1, r8

859e: f00a fd95 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

85a2: e000 b.n 85a6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1912>

85a4: 4618 mov r0, r3

85a6: f810 2c01 ldrb.w r2, [r0, #-1]

85aa: 1e43 subs r3, r0, #1

85ac: 2a2f cmp r2, #47 ; 0x2f

85ae: d1f9 bne.n 85a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1910>

85b0: 429f cmp r7, r3

85b2: bf08 it eq

85b4: 4603 moveq r3, r0

85b6: 2200 movs r2, #0

85b8: 701a strb r2, [r3, #0]

85ba: f8c4 71ac str.w r7, [r4, #428] ; 0x1ac

85be: 4620 mov r0, r4

85c0: b003 add sp, #12

85c2: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

85c6: 4640 mov r0, r8

85c8: e768 b.n 849c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1808>

85ca: f8d4 31bc ldr.w r3, [r4, #444] ; 0x1bc

85ce: f505 75b0 add.w r5, r5, #352 ; 0x160

85d2: 429d cmp r5, r3

85d4: d0a1 beq.n 851a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1886>

85d6: 9a0c ldr r2, [sp, #48] ; 0x30

85d8: 0712 lsls r2, r2, #28

85da: bf58 it pl

85dc: f504 71e0 addpl.w r1, r4, #448 ; 0x1c0

85e0: d59a bpl.n 8518 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1884>

85e2: f8c4 31c0 str.w r3, [r4, #448] ; 0x1c0

85e6: e797 b.n 8518 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1884>

85e8: 2d00 cmp r5, #0

85ea: d18d bne.n 8508 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1874>

85ec: 4625 mov r5, r4

85ee: e791 b.n 8514 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1880>

85f0: 4618 mov r0, r3

85f2: f7f8 e9ec blx 9cc <malloc@plt+0x4>

85f6: 4607 mov r7, r0

85f8: 2800 cmp r0, #0

85fa: d1ce bne.n 859a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1906>

85fc: f04f 37ff mov.w r7, #4294967295 ; 0xffffffff

8600: e7db b.n 85ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1926>

8602: bf00 nop

8604: 0536 lsls r6, r6, #20

8606: 0002 movs r2, r0

8608: 04ca lsls r2, r1, #19

860a: 0002 movs r2, r0

860c: 0b7e lsrs r6, r7, #13

860e: 0002 movs r2, r0

8610: 140e asrs r6, r1, #16

8612: 0002 movs r2, r0

8614: e92d 43f0 stmdb sp!, {r4, r5, r6, r7, r8, r9, lr}

8618: 4614 mov r4, r2

861a: 1a8a subs r2, r1, r2

861c: 461d mov r5, r3

861e: 18d3 adds r3, r2, r3

8620: b083 sub sp, #12

8622: f003 427e and.w r2, r3, #4261412864 ; 0xfe000000

8626: f1b2 4f7e cmp.w r2, #4261412864 ; 0xfe000000

862a: bf18 it ne

862c: 2a00 cmpne r2, #0

862e: d028 beq.n 8682 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x19ee>

8630: f8df 80c4 ldr.w r8, [pc, #196] ; 86f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a64>

8634: 460e mov r6, r1

8636: 4607 mov r7, r0

8638: 44f8 add r8, pc

863a: f8d8 1000 ldr.w r1, [r8]

863e: b351 cbz r1, 8696 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a02>

8640: f8d8 2004 ldr.w r2, [r8, #4]

8644: 482d ldr r0, [pc, #180] ; (86fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a68>)

8646: 188b adds r3, r1, r2

8648: f24f 0c04 movw ip, #61444 ; 0xf004

864c: f2ce 5c1f movt ip, #58655 ; 0xe51f

8650: 4478 add r0, pc

8652: f841 c002 str.w ip, [r1, r2]

8656: 605e str r6, [r3, #4]

8658: 3208 adds r2, #8

865a: f8df c0a4 ldr.w ip, [pc, #164] ; 8700 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a6c>

865e: 1b2d subs r5, r5, r4

8660: 6901 ldr r1, [r0, #16]

8662: 442b add r3, r5

8664: 44fc add ip, pc

8666: 428a cmp r2, r1

8668: f8cc 2004 str.w r2, [ip, #4]

866c: bf24 itt cs

866e: 2200 movcs r2, #0

8670: e9cc 2200 strdcs r2, r2, [ip]

8674: f003 427e and.w r2, r3, #4261412864 ; 0xfe000000

8678: f1b2 4f7e cmp.w r2, #4261412864 ; 0xfe000000

867c: bf18 it ne

867e: 2a00 cmpne r2, #0

8680: d12b bne.n 86da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a46>

8682: 6822 ldr r2, [r4, #0]

8684: f3c3 0397 ubfx r3, r3, #2, #24

8688: f002 427f and.w r2, r2, #4278190080 ; 0xff000000

868c: 4313 orrs r3, r2

868e: 6023 str r3, [r4, #0]

8690: b003 add sp, #12

8692: e8bd 83f0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, pc}

8696: 481b ldr r0, [pc, #108] ; (8704 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a70>)

8698: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

869c: 9101 str r1, [sp, #4]

869e: 2322 movs r3, #34 ; 0x22

86a0: 4478 add r0, pc

86a2: 9200 str r2, [sp, #0]

86a4: 4689 mov r9, r1

86a6: 2207 movs r2, #7

86a8: f8d0 c010 ldr.w ip, [r0, #16]

86ac: 4608 mov r0, r1

86ae: 4661 mov r1, ip

86b0: f009 fb60 bl 11d74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x100c>

86b4: 1c43 adds r3, r0, #1

86b6: 4601 mov r1, r0

86b8: d016 beq.n 86e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a54>

86ba: f8d8 2004 ldr.w r2, [r8, #4]

86be: f8c8 0000 str.w r0, [r8]

86c2: 2a00 cmp r2, #0

86c4: d0be beq.n 8644 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x19b0>

86c6: 4b10 ldr r3, [pc, #64] ; (8708 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a74>)

86c8: f240 124f movw r2, #335 ; 0x14f

86cc: 490f ldr r1, [pc, #60] ; (870c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a78>)

86ce: 4810 ldr r0, [pc, #64] ; (8710 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a7c>)

86d0: 447b add r3, pc

86d2: 4479 add r1, pc

86d4: 4478 add r0, pc

86d6: f008 f959 bl 1098c <free@@GLIBC\_2.4+0x12c>

86da: 4b0e ldr r3, [pc, #56] ; (8714 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a80>)

86dc: 2200 movs r2, #0

86de: 6839 ldr r1, [r7, #0]

86e0: 4610 mov r0, r2

86e2: 447b add r3, pc

86e4: f7f8 e990 blx a08 <\_dl\_signal\_error@plt>

86e8: 4b0b ldr r3, [pc, #44] ; (8718 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a84>)

86ea: 464a mov r2, r9

86ec: 6839 ldr r1, [r7, #0]

86ee: 4648 mov r0, r9

86f0: 447b add r3, pc

86f2: f7f8 e98a blx a08 <\_dl\_signal\_error@plt>

86f6: bf00 nop

86f8: 129c asrs r4, r3, #10

86fa: 0002 movs r2, r0

86fc: 031c lsls r4, r3, #12

86fe: 0002 movs r2, r0

8700: 1270 asrs r0, r6, #9

8702: 0002 movs r2, r0

8704: 02cc lsls r4, r1, #11

8706: 0002 movs r2, r0

8708: d104 bne.n 8714 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a80>

870a: 0000 movs r0, r0

870c: c3a6 stmia r3!, {r1, r2, r5, r7}

870e: 0000 movs r0, r0

8710: c3c0 stmia r3!, {r6, r7}

8712: 0000 movs r0, r0

8714: c3c2 stmia r3!, {r1, r6, r7}

8716: 0000 movs r0, r0

8718: c368 stmia r3!, {r3, r5, r6}

871a: 0000 movs r0, r0

871c: f8d0 3244 ldr.w r3, [r0, #580] ; 0x244

8720: 1c5a adds r2, r3, #1

8722: d044 beq.n 87ae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b1a>

8724: 4a24 ldr r2, [pc, #144] ; (87b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b24>)

8726: f8d0 123c ldr.w r1, [r0, #572] ; 0x23c

872a: 447a add r2, pc

872c: f8d2 3820 ldr.w r3, [r2, #2080] ; 0x820

8730: 4299 cmp r1, r3

8732: d83a bhi.n 87aa <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b16>

8734: b570 push {r4, r5, r6, lr}

8736: 424d negs r5, r1

8738: f8d2 681c ldr.w r6, [r2, #2076] ; 0x81c

873c: f8d0 4240 ldr.w r4, [r0, #576] ; 0x240

8740: 1e73 subs r3, r6, #1

8742: 440b add r3, r1

8744: f8d0 1238 ldr.w r1, [r0, #568] ; 0x238

8748: 1b1b subs r3, r3, r4

874a: 402b ands r3, r5

874c: f8d2 5818 ldr.w r5, [r2, #2072] ; 0x818

8750: 4423 add r3, r4

8752: 4419 add r1, r3

8754: 428d cmp r5, r1

8756: d32c bcc.n 87b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b1e>

8758: 6944 ldr r4, [r0, #20]

875a: e9c0 6390 strd r6, r3, [r0, #576] ; 0x240

875e: f894 3198 ldrb.w r3, [r4, #408] ; 0x198

8762: f8c2 181c str.w r1, [r2, #2076] ; 0x81c

8766: f013 0304 ands.w r3, r3, #4

876a: d011 beq.n 8790 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1afc>

876c: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

8770: f8d2 2828 ldr.w r2, [r2, #2088] ; 0x828

8774: 681b ldr r3, [r3, #0]

8776: 4604 mov r4, r0

8778: 681b ldr r3, [r3, #0]

877a: 4293 cmp r3, r2

877c: d110 bne.n 87a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b0c>

877e: 4b0f ldr r3, [pc, #60] ; (87bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b28>)

8780: 4620 mov r0, r4

8782: 447b add r3, pc

8784: f8d3 382c ldr.w r3, [r3, #2092] ; 0x82c

8788: 4798 blx r3

878a: 2300 movs r3, #0

878c: 4618 mov r0, r3

878e: bd70 pop {r4, r5, r6, pc}

8790: f890 2199 ldrb.w r2, [r0, #409] ; 0x199

8794: f042 0204 orr.w r2, r2, #4

8798: f880 2199 strb.w r2, [r0, #409] ; 0x199

879c: 4618 mov r0, r3

879e: bd70 pop {r4, r5, r6, pc}

87a0: f8d0 0248 ldr.w r0, [r0, #584] ; 0x248

87a4: f004 fdfe bl d3a4 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x58>

87a8: e7e9 b.n 877e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1aea>

87aa: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

87ae: 4618 mov r0, r3

87b0: 4770 bx lr

87b2: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

87b6: e7f1 b.n 879c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b08>

87b8: 0932 lsrs r2, r6, #4

87ba: 0002 movs r2, r0

87bc: 08da lsrs r2, r3, #3

87be: 0002 movs r2, r0

87c0: f8d0 3244 ldr.w r3, [r0, #580] ; 0x244

87c4: b510 push {r4, lr}

87c6: 3301 adds r3, #1

87c8: 4604 mov r4, r0

87ca: d003 beq.n 87d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b40>

87cc: f7ff ffa6 bl 871c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a88>

87d0: b900 cbnz r0, 87d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b40>

87d2: bd10 pop {r4, pc}

87d4: 4b03 ldr r3, [pc, #12] ; (87e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b50>)

87d6: 2200 movs r2, #0

87d8: 6861 ldr r1, [r4, #4]

87da: 4610 mov r0, r2

87dc: 447b add r3, pc

87de: f7f8 e914 blx a08 <\_dl\_signal\_error@plt>

87e2: bf00 nop

87e4: dc18 bgt.n 8818 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b84>

87e6: 0000 movs r0, r0

87e8: b510 push {r4, lr}

87ea: 4604 mov r4, r0

87ec: f8d4 3244 ldr.w r3, [r4, #580] ; 0x244

87f0: ee1d 0f70 mrc 15, 0, r0, cr13, cr0, {3}

87f4: f8d4 2234 ldr.w r2, [r4, #564] ; 0x234

87f8: f8d4 1230 ldr.w r1, [r4, #560] ; 0x230

87fc: 4418 add r0, r3

87fe: f00a fc65 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

8802: e9d4 328d ldrd r3, r2, [r4, #564] ; 0x234

8806: 2100 movs r1, #0

8808: 1ad2 subs r2, r2, r3

880a: e8bd 4010 ldmia.w sp!, {r4, lr}

880e: f00b bab7 b.w 13d80 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x3018>

8812: bf00 nop

8814: 4a0f ldr r2, [pc, #60] ; (8854 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc0>)

8816: b510 push {r4, lr}

8818: 447a add r2, pc

881a: e9d0 4194 ldrd r4, r1, [r0, #592] ; 0x250

881e: 6803 ldr r3, [r0, #0]

8820: 6912 ldr r2, [r2, #16]

8822: 4423 add r3, r4

8824: 4419 add r1, r3

8826: 4252 negs r2, r2

8828: 4011 ands r1, r2

882a: 4013 ands r3, r2

882c: 428b cmp r3, r1

882e: d100 bne.n 8832 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b9e>

8830: bd10 pop {r4, pc}

8832: 4604 mov r4, r0

8834: 1ac9 subs r1, r1, r3

8836: 4618 mov r0, r3

8838: 2201 movs r2, #1

883a: f009 fad9 bl 11df0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1088>

883e: 2800 cmp r0, #0

8840: daf6 bge.n 8830 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b9c>

8842: 4805 ldr r0, [pc, #20] ; (8858 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc4>)

8844: 2200 movs r2, #0

8846: 4b05 ldr r3, [pc, #20] ; (885c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bc8>)

8848: 4478 add r0, pc

884a: 6861 ldr r1, [r4, #4]

884c: 447b add r3, pc

884e: 6800 ldr r0, [r0, #0]

8850: f7f8 e8da blx a08 <\_dl\_signal\_error@plt>

8854: 0154 lsls r4, r2, #5

8856: 0002 movs r2, r0

8858: 110c asrs r4, r1, #4

885a: 0002 movs r2, r0

885c: dcd4 bgt.n 8808 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b74>

885e: 0000 movs r0, r0

8860: b580 push {r7, lr}

8862: b088 sub sp, #32

8864: 4b0e ldr r3, [pc, #56] ; (88a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c0c>)

8866: 460c mov r4, r1

8868: 466d mov r5, sp

886a: 4607 mov r7, r0

886c: 447b add r3, pc

886e: 4628 mov r0, r5

8870: eb03 1142 add.w r1, r3, r2, lsl #5

8874: f009 fde4 bl 12440 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16d8>

8878: f3c4 1c03 ubfx ip, r4, #4, #4

887c: f004 040f and.w r4, r4, #15

8880: 462b mov r3, r5

8882: 2200 movs r2, #0

8884: 6879 ldr r1, [r7, #4]

8886: 4606 mov r6, r0

8888: 4806 ldr r0, [pc, #24] ; (88a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c10>)

888a: 70b2 strb r2, [r6, #2]

888c: 4478 add r0, pc

888e: f810 500c ldrb.w r5, [r0, ip]

8892: 5d04 ldrb r4, [r0, r4]

8894: 4610 mov r0, r2

8896: 7035 strb r5, [r6, #0]

8898: 7074 strb r4, [r6, #1]

889a: f7f8 e8b6 blx a08 <\_dl\_signal\_error@plt>

889e: bf00 nop

88a0: b9a0 cbnz r0, 88cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c38>

88a2: 0000 movs r0, r0

88a4: bccc pop {r2, r3, r6, r7}

88a6: 0000 movs r0, r0

88a8: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

88ac: b0a3 sub sp, #140 ; 0x8c

88ae: af04 add r7, sp, #16

88b0: 4615 mov r5, r2

88b2: f8df 6bf8 ldr.w r6, [pc, #3064] ; 94ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2818>

88b6: f002 0201 and.w r2, r2, #1

88ba: 63b9 str r1, [r7, #56] ; 0x38

88bc: 0129 lsls r1, r5, #4

88be: 447e add r6, pc

88c0: 4683 mov fp, r0

88c2: 62fb str r3, [r7, #44] ; 0x2c

88c4: 633a str r2, [r7, #48] ; 0x30

88c6: d40b bmi.n 88e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c4c>

88c8: 4619 mov r1, r3

88ca: f8df 3be4 ldr.w r3, [pc, #3044] ; 94b0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x281c>

88ce: 460a mov r2, r1

88d0: 447b add r3, pc

88d2: f8d3 31ac ldr.w r3, [r3, #428] ; 0x1ac

88d6: 2b00 cmp r3, #0

88d8: bf18 it ne

88da: f042 0201 orrne.w r2, r2, #1

88de: 62fa str r2, [r7, #44] ; 0x2c

88e0: f89b 3198 ldrb.w r3, [fp, #408] ; 0x198

88e4: 075a lsls r2, r3, #29

88e6: f100 81cd bmi.w 8c84 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1ff0>

88ea: 6afb ldr r3, [r7, #44] ; 0x2c

88ec: 2b00 cmp r3, #0

88ee: f000 81cd beq.w 8c8c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1ff8>

88f2: f8df 3bc0 ldr.w r3, [pc, #3008] ; 94b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2820>

88f6: 447b add r3, pc

88f8: 681b ldr r3, [r3, #0]

88fa: 069b lsls r3, r3, #26

88fc: f101 8120 bmi.w 9b40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2eac>

8900: f8db 3078 ldr.w r3, [fp, #120] ; 0x78

8904: 61bb str r3, [r7, #24]

8906: 2b00 cmp r3, #0

8908: f041 8131 bne.w 9b6e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2eda>

890c: f8db 2034 ldr.w r2, [fp, #52] ; 0x34

8910: 6b3b ldr r3, [r7, #48] ; 0x30

8912: f8db 107c ldr.w r1, [fp, #124] ; 0x7c

8916: f003 0301 and.w r3, r3, #1

891a: 6852 ldr r2, [r2, #4]

891c: 2900 cmp r1, #0

891e: bf08 it eq

8920: 2300 moveq r3, #0

8922: 637a str r2, [r7, #52] ; 0x34

8924: 2b00 cmp r3, #0

8926: f040 81b9 bne.w 8c9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2008>

892a: f107 0358 add.w r3, r7, #88 ; 0x58

892e: 2220 movs r2, #32

8930: 2100 movs r1, #0

8932: 623b str r3, [r7, #32]

8934: 4618 mov r0, r3

8936: f00a eb8c blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

893a: f8db 4064 ldr.w r4, [fp, #100] ; 0x64

893e: 2c00 cmp r4, #0

8940: f000 869a beq.w 9678 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29e4>

8944: f8db 3068 ldr.w r3, [fp, #104] ; 0x68

8948: 6866 ldr r6, [r4, #4]

894a: f8db 40c0 ldr.w r4, [fp, #192] ; 0xc0

894e: 685b ldr r3, [r3, #4]

8950: 65be str r6, [r7, #88] ; 0x58

8952: 18f0 adds r0, r6, r3

8954: 65fb str r3, [r7, #92] ; 0x5c

8956: b10c cbz r4, 895c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cc8>

8958: 6864 ldr r4, [r4, #4]

895a: 663c str r4, [r7, #96] ; 0x60

895c: f8db 2070 ldr.w r2, [fp, #112] ; 0x70

8960: b11a cbz r2, 896a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cd6>

8962: 6852 ldr r2, [r2, #4]

8964: 2a11 cmp r2, #17

8966: f001 803c beq.w 99e2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2d4e>

896a: f8df 2b4c ldr.w r2, [pc, #2892] ; 94b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2824>

896e: f005 7100 and.w r1, r5, #33554432 ; 0x2000000

8972: 61f9 str r1, [r7, #28]

8974: f107 0068 add.w r0, r7, #104 ; 0x68

8978: 447a add r2, pc

897a: f8df 1b40 ldr.w r1, [pc, #2880] ; 94bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2828>

897e: 60ba str r2, [r7, #8]

8980: f502 62a3 add.w r2, r2, #1304 ; 0x518

8984: 617a str r2, [r7, #20]

8986: 4479 add r1, pc

8988: 6a3a ldr r2, [r7, #32]

898a: 6079 str r1, [r7, #4]

898c: 62b8 str r0, [r7, #40] ; 0x28

898e: 627a str r2, [r7, #36] ; 0x24

8990: f501 62a3 add.w r2, r1, #1304 ; 0x518

8994: 60fa str r2, [r7, #12]

8996: 2200 movs r2, #0

8998: 18f5 adds r5, r6, r3

899a: f8db a000 ldr.w sl, [fp]

899e: 64bd str r5, [r7, #72] ; 0x48

89a0: 2a00 cmp r2, #0

89a2: f000 825c beq.w 8e5e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x21ca>

89a6: 42ae cmp r6, r5

89a8: d275 bcs.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

89aa: f8df 3b14 ldr.w r3, [pc, #2836] ; 94c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x282c>

89ae: f8df 8b14 ldr.w r8, [pc, #2836] ; 94c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2830>

89b2: 447b add r3, pc

89b4: 64fb str r3, [r7, #76] ; 0x4c

89b6: f8df 3b10 ldr.w r3, [pc, #2832] ; 94c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2834>

89ba: 44f8 add r8, pc

89bc: 447b add r3, pc

89be: 64bb str r3, [r7, #72] ; 0x48

89c0: e00c b.n 89dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d48>

89c2: f8db 3218 ldr.w r3, [fp, #536] ; 0x218

89c6: 2b00 cmp r3, #0

89c8: f040 8231 bne.w 8e2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x219a>

89cc: f85a 3004 ldr.w r3, [sl, r4]

89d0: 4453 add r3, sl

89d2: f84a 3004 str.w r3, [sl, r4]

89d6: 3608 adds r6, #8

89d8: 42b5 cmp r5, r6

89da: d95c bls.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

89dc: e9d6 4300 ldrd r4, r3, [r6]

89e0: eb0a 0904 add.w r9, sl, r4

89e4: b2d9 uxtb r1, r3

89e6: 2916 cmp r1, #22

89e8: d0eb beq.n 89c2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d2e>

89ea: 290d cmp r1, #13

89ec: f041 807b bne.w 9ae6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e52>

89f0: f8db 2038 ldr.w r2, [fp, #56] ; 0x38

89f4: ea4f 2e13 mov.w lr, r3, lsr #8

89f8: f8db 30e8 ldr.w r3, [fp, #232] ; 0xe8

89fc: ea4f 1c0e mov.w ip, lr, lsl #4

8a00: 6851 ldr r1, [r2, #4]

8a02: eb0c 0201 add.w r2, ip, r1

8a06: b143 cbz r3, 8a1a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d86>

8a08: 685b ldr r3, [r3, #4]

8a0a: f8db 0174 ldr.w r0, [fp, #372] ; 0x174

8a0e: f833 301e ldrh.w r3, [r3, lr, lsl #1]

8a12: f3c3 030e ubfx r3, r3, #0, #15

8a16: eb00 1303 add.w r3, r0, r3, lsl #4

8a1a: 657a str r2, [r7, #84] ; 0x54

8a1c: 7b10 ldrb r0, [r2, #12]

8a1e: ea5f 1e10 movs.w lr, r0, lsr #4

8a22: f000 84a0 beq.w 9366 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26d2>

8a26: f892 e00d ldrb.w lr, [r2, #13]

8a2a: f00e 0e03 and.w lr, lr, #3

8a2e: f10e 3eff add.w lr, lr, #4294967295 ; 0xffffffff

8a32: f1be 0f01 cmp.w lr, #1

8a36: f240 8496 bls.w 9366 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26d2>

8a3a: f8db 0220 ldr.w r0, [fp, #544] ; 0x220

8a3e: 4282 cmp r2, r0

8a40: f001 8177 beq.w 9d32 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x309e>

8a44: f8cb 2220 str.w r2, [fp, #544] ; 0x220

8a48: 2201 movs r2, #1

8a4a: f8cb 2224 str.w r2, [fp, #548] ; 0x224

8a4e: b11b cbz r3, 8a58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1dc4>

8a50: 685a ldr r2, [r3, #4]

8a52: 2a00 cmp r2, #0

8a54: bf08 it eq

8a56: 2300 moveq r3, #0

8a58: f85c 0001 ldr.w r0, [ip, r1]

8a5c: 2201 movs r2, #1

8a5e: 9300 str r3, [sp, #0]

8a60: 2100 movs r1, #0

8a62: 6b7b ldr r3, [r7, #52] ; 0x34

8a64: e9cd 2102 strd r2, r1, [sp, #8]

8a68: 4659 mov r1, fp

8a6a: 4418 add r0, r3

8a6c: 9201 str r2, [sp, #4]

8a6e: 6bbb ldr r3, [r7, #56] ; 0x38

8a70: f107 0254 add.w r2, r7, #84 ; 0x54

8a74: f7fe fe10 bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

8a78: 6d7a ldr r2, [r7, #84] ; 0x54

8a7a: 4603 mov r3, r0

8a7c: e9cb 028a strd r0, r2, [fp, #552] ; 0x228

8a80: 2a00 cmp r2, #0

8a82: f040 87fa bne.w 9a7a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2de6>

8a86: 3608 adds r6, #8

8a88: f8df 3a40 ldr.w r3, [pc, #2624] ; 94cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2838>

8a8c: 42b5 cmp r5, r6

8a8e: 447b add r3, pc

8a90: f8c9 3004 str.w r3, [r9, #4]

8a94: d8a2 bhi.n 89dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d48>

8a96: e9d7 2309 ldrd r2, r3, [r7, #36] ; 0x24

8a9a: 4293 cmp r3, r2

8a9c: f040 8449 bne.w 9332 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x269e>

8aa0: 2220 movs r2, #32

8aa2: 2100 movs r1, #0

8aa4: 6a38 ldr r0, [r7, #32]

8aa6: f00a ead4 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

8aaa: f8db 403c ldr.w r4, [fp, #60] ; 0x3c

8aae: 2c00 cmp r4, #0

8ab0: f000 876a beq.w 9988 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cf4>

8ab4: f8db 1040 ldr.w r1, [fp, #64] ; 0x40

8ab8: 6863 ldr r3, [r4, #4]

8aba: f8db 20c4 ldr.w r2, [fp, #196] ; 0xc4

8abe: 6849 ldr r1, [r1, #4]

8ac0: 65bb str r3, [r7, #88] ; 0x58

8ac2: 185c adds r4, r3, r1

8ac4: 65f9 str r1, [r7, #92] ; 0x5c

8ac6: b10a cbz r2, 8acc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e38>

8ac8: 6852 ldr r2, [r2, #4]

8aca: 663a str r2, [r7, #96] ; 0x60

8acc: f8db 2070 ldr.w r2, [fp, #112] ; 0x70

8ad0: b11a cbz r2, 8ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e46>

8ad2: 6852 ldr r2, [r2, #4]

8ad4: 2a07 cmp r2, #7

8ad6: f000 87a1 beq.w 9a1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2d88>

8ada: 6a38 ldr r0, [r7, #32]

8adc: f8df 29f0 ldr.w r2, [pc, #2544] ; 94d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x283c>

8ae0: 6338 str r0, [r7, #48] ; 0x30

8ae2: 447a add r2, pc

8ae4: f8df 09ec ldr.w r0, [pc, #2540] ; 94d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2840>

8ae8: f502 62a3 add.w r2, r2, #1304 ; 0x518

8aec: 623a str r2, [r7, #32]

8aee: 4478 add r0, pc

8af0: f8df 29e4 ldr.w r2, [pc, #2532] ; 94d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2844>

8af4: 6278 str r0, [r7, #36] ; 0x24

8af6: f8df 09e4 ldr.w r0, [pc, #2532] ; 94dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2848>

8afa: 447a add r2, pc

8afc: 613a str r2, [r7, #16]

8afe: 4478 add r0, pc

8b00: 6178 str r0, [r7, #20]

8b02: f8db 2038 ldr.w r2, [fp, #56] ; 0x38

8b06: 200c movs r0, #12

8b08: 6b3c ldr r4, [r7, #48] ; 0x30

8b0a: 1859 adds r1, r3, r1

8b0c: 6479 str r1, [r7, #68] ; 0x44

8b0e: 4619 mov r1, r3

8b10: 6852 ldr r2, [r2, #4]

8b12: 68a5 ldr r5, [r4, #8]

8b14: f8db 4000 ldr.w r4, [fp]

8b18: 64ba str r2, [r7, #72] ; 0x48

8b1a: 6a3a ldr r2, [r7, #32]

8b1c: 64fc str r4, [r7, #76] ; 0x4c

8b1e: 4593 cmp fp, r2

8b20: fb00 3505 mla r5, r0, r5, r3

8b24: d008 beq.n 8b38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1ea4>

8b26: 6cfa ldr r2, [r7, #76] ; 0x4c

8b28: 2a00 cmp r2, #0

8b2a: f040 834b bne.w 91c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2530>

8b2e: f8db 2120 ldr.w r2, [fp, #288] ; 0x120

8b32: 2a00 cmp r2, #0

8b34: f000 8346 beq.w 91c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2530>

8b38: f8db 30e8 ldr.w r3, [fp, #232] ; 0xe8

8b3c: 2b00 cmp r3, #0

8b3e: f000 8353 beq.w 91e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2554>

8b42: 685b ldr r3, [r3, #4]

8b44: 63fb str r3, [r7, #60] ; 0x3c

8b46: 6c7b ldr r3, [r7, #68] ; 0x44

8b48: 42ab cmp r3, r5

8b4a: d979 bls.n 8c40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fac>

8b4c: e9d5 8400 ldrd r8, r4, [r5]

8b50: 6cfb ldr r3, [r7, #76] ; 0x4c

8b52: f8db 0174 ldr.w r0, [fp, #372] ; 0x174

8b56: 4443 add r3, r8

8b58: 643b str r3, [r7, #64] ; 0x40

8b5a: 6bfb ldr r3, [r7, #60] ; 0x3c

8b5c: 0a22 lsrs r2, r4, #8

8b5e: b2e4 uxtb r4, r4

8b60: ea4f 1a02 mov.w sl, r2, lsl #4

8b64: 2c17 cmp r4, #23

8b66: f833 3012 ldrh.w r3, [r3, r2, lsl #1]

8b6a: 6cba ldr r2, [r7, #72] ; 0x48

8b6c: eb02 060a add.w r6, r2, sl

8b70: 657e str r6, [r7, #84] ; 0x54

8b72: f000 8154 beq.w 8e1e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x218a>

8b76: 2c00 cmp r4, #0

8b78: d05e beq.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8b7a: 7b32 ldrb r2, [r6, #12]

8b7c: 0911 lsrs r1, r2, #4

8b7e: f000 80bd beq.w 8cfc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2068>

8b82: 7b71 ldrb r1, [r6, #13]

8b84: f001 0103 and.w r1, r1, #3

8b88: 3901 subs r1, #1

8b8a: 2901 cmp r1, #1

8b8c: f240 80b6 bls.w 8cfc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2068>

8b90: f8db 2220 ldr.w r2, [fp, #544] ; 0x220

8b94: 4296 cmp r6, r2

8b96: f000 83a5 beq.w 92e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2650>

8b9a: 2c11 cmp r4, #17

8b9c: bf18 it ne

8b9e: 2c16 cmpne r4, #22

8ba0: bf08 it eq

8ba2: 2201 moveq r2, #1

8ba4: d007 beq.n 8bb6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f22>

8ba6: f1a4 0212 sub.w r2, r4, #18

8baa: 2c0d cmp r4, #13

8bac: bf18 it ne

8bae: 2a01 cmpne r2, #1

8bb0: bf94 ite ls

8bb2: 2201 movls r2, #1

8bb4: 2200 movhi r2, #0

8bb6: 2c14 cmp r4, #20

8bb8: bf08 it eq

8bba: f042 0202 orreq.w r2, r2, #2

8bbe: d003 beq.n 8bc8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f34>

8bc0: 2c15 cmp r4, #21

8bc2: bf08 it eq

8bc4: f042 0204 orreq.w r2, r2, #4

8bc8: f3c3 030e ubfx r3, r3, #0, #15

8bcc: e9cb 6288 strd r6, r2, [fp, #544] ; 0x220

8bd0: eb10 1303 adds.w r3, r0, r3, lsl #4

8bd4: d003 beq.n 8bde <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f4a>

8bd6: 6859 ldr r1, [r3, #4]

8bd8: 2900 cmp r1, #0

8bda: bf08 it eq

8bdc: 2300 moveq r3, #0

8bde: 6cb9 ldr r1, [r7, #72] ; 0x48

8be0: f04f 0c01 mov.w ip, #1

8be4: f851 000a ldr.w r0, [r1, sl]

8be8: 2100 movs r1, #0

8bea: 9201 str r2, [sp, #4]

8bec: 6b7a ldr r2, [r7, #52] ; 0x34

8bee: 9300 str r3, [sp, #0]

8bf0: 4410 add r0, r2

8bf2: 9103 str r1, [sp, #12]

8bf4: 6bbb ldr r3, [r7, #56] ; 0x38

8bf6: 4659 mov r1, fp

8bf8: f8cd c008 str.w ip, [sp, #8]

8bfc: f107 0254 add.w r2, r7, #84 ; 0x54

8c00: f7fe fd4a bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

8c04: 6d7b ldr r3, [r7, #84] ; 0x54

8c06: 4601 mov r1, r0

8c08: e9cb 038a strd r0, r3, [fp, #552] ; 0x228

8c0c: 2b00 cmp r3, #0

8c0e: f040 8104 bne.w 8e1a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2186>

8c12: 2c13 cmp r4, #19

8c14: d010 beq.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8c16: f200 80f4 bhi.w 8e02 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x216e>

8c1a: 2c02 cmp r4, #2

8c1c: f000 80f6 beq.w 8e0c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2178>

8c20: f240 80ea bls.w 8df8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2164>

8c24: 2c11 cmp r4, #17

8c26: d061 beq.n 8cec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2058>

8c28: 2c12 cmp r4, #18

8c2a: f041 8095 bne.w 9d58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30c4>

8c2e: 68aa ldr r2, [r5, #8]

8c30: 4413 add r3, r2

8c32: 6cfa ldr r2, [r7, #76] ; 0x4c

8c34: f842 3008 str.w r3, [r2, r8]

8c38: 6c7b ldr r3, [r7, #68] ; 0x44

8c3a: 350c adds r5, #12

8c3c: 42ab cmp r3, r5

8c3e: d885 bhi.n 8b4c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1eb8>

8c40: 6abb ldr r3, [r7, #40] ; 0x28

8c42: 6b3a ldr r2, [r7, #48] ; 0x30

8c44: 4293 cmp r3, r2

8c46: d005 beq.n 8c54 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fc0>

8c48: 6b3a ldr r2, [r7, #48] ; 0x30

8c4a: 3210 adds r2, #16

8c4c: 6813 ldr r3, [r2, #0]

8c4e: 6851 ldr r1, [r2, #4]

8c50: 633a str r2, [r7, #48] ; 0x30

8c52: e756 b.n 8b02 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e6e>

8c54: 6afb ldr r3, [r7, #44] ; 0x2c

8c56: 2b00 cmp r3, #0

8c58: f040 87e5 bne.w 9c26 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f92>

8c5c: f89b 3198 ldrb.w r3, [fp, #408] ; 0x198

8c60: 2402 movs r4, #2

8c62: f2c0 040f movt r4, #15

8c66: f043 0304 orr.w r3, r3, #4

8c6a: f88b 3198 strb.w r3, [fp, #408] ; 0x198

8c6e: 69bb ldr r3, [r7, #24]

8c70: 461d mov r5, r3

8c72: 2b00 cmp r3, #0

8c74: f040 8523 bne.w 96be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a2a>

8c78: f8db 3254 ldr.w r3, [fp, #596] ; 0x254

8c7c: b113 cbz r3, 8c84 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1ff0>

8c7e: 4658 mov r0, fp

8c80: f7ff fdc8 bl 8814 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b80>

8c84: 377c adds r7, #124 ; 0x7c

8c86: 46bd mov sp, r7

8c88: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

8c8c: f8db 3080 ldr.w r3, [fp, #128] ; 0x80

8c90: 2b00 cmp r3, #0

8c92: 6b3b ldr r3, [r7, #48] ; 0x30

8c94: bf18 it ne

8c96: 2300 movne r3, #0

8c98: 633b str r3, [r7, #48] ; 0x30

8c9a: e62a b.n 88f2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c5e>

8c9c: f8db 302c ldr.w r3, [fp, #44] ; 0x2c

8ca0: 685b ldr r3, [r3, #4]

8ca2: 685a ldr r2, [r3, #4]

8ca4: b122 cbz r2, 8cb0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x201c>

8ca6: f8db 1000 ldr.w r1, [fp]

8caa: 440a add r2, r1

8cac: f8cb 2218 str.w r2, [fp, #536] ; 0x218

8cb0: 6afa ldr r2, [r7, #44] ; 0x2c

8cb2: f8c3 b004 str.w fp, [r3, #4]

8cb6: 2a00 cmp r2, #0

8cb8: f000 84e7 beq.w 968a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29f6>

8cbc: f8df 2820 ldr.w r2, [pc, #2080] ; 94e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x284c>

8cc0: f8df 1820 ldr.w r1, [pc, #2080] ; 94e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2850>

8cc4: 447a add r2, pc

8cc6: 5871 ldr r1, [r6, r1]

8cc8: f8d2 0168 ldr.w r0, [r2, #360] ; 0x168

8ccc: 6099 str r1, [r3, #8]

8cce: 2800 cmp r0, #0

8cd0: f43f ae2b beq.w 892a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c96>

8cd4: 4659 mov r1, fp

8cd6: f003 f889 bl bdec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x40c>

8cda: 2800 cmp r0, #0

8cdc: f43f ae25 beq.w 892a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c96>

8ce0: f8df 3804 ldr.w r3, [pc, #2052] ; 94e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2854>

8ce4: 447b add r3, pc

8ce6: f8c3 b504 str.w fp, [r3, #1284] ; 0x504

8cea: e61e b.n 892a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c96>

8cec: 2900 cmp r1, #0

8cee: d0a3 beq.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8cf0: f8d1 3248 ldr.w r3, [r1, #584] ; 0x248

8cf4: 6cfa ldr r2, [r7, #76] ; 0x4c

8cf6: f842 3008 str.w r3, [r2, r8]

8cfa: e79d b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8cfc: 4659 mov r1, fp

8cfe: 4633 mov r3, r6

8d00: 89d8 ldrh r0, [r3, #14]

8d02: f64f 7cf1 movw ip, #65521 ; 0xfff1

8d06: f002 020f and.w r2, r2, #15

8d0a: 685b ldr r3, [r3, #4]

8d0c: 4560 cmp r0, ip

8d0e: bf14 ite ne

8d10: f8d1 9000 ldrne.w r9, [r1]

8d14: f04f 0900 moveq.w r9, #0

8d18: 2a0a cmp r2, #10

8d1a: 4499 add r9, r3

8d1c: d010 beq.n 8d40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x20ac>

8d1e: 2c13 cmp r4, #19

8d20: d026 beq.n 8d70 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x20dc>

8d22: d83f bhi.n 8da4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2110>

8d24: 2c02 cmp r4, #2

8d26: d072 beq.n 8e0e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x217a>

8d28: d931 bls.n 8d8e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x20fa>

8d2a: 2c11 cmp r4, #17

8d2c: d0de beq.n 8cec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2058>

8d2e: 2c12 cmp r4, #18

8d30: f041 8012 bne.w 9d58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30c4>

8d34: 6d7b ldr r3, [r7, #84] ; 0x54

8d36: 2b00 cmp r3, #0

8d38: f43f af79 beq.w 8c2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f9a>

8d3c: 685b ldr r3, [r3, #4]

8d3e: e776 b.n 8c2e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f9a>

8d40: 69fb ldr r3, [r7, #28]

8d42: fab3 f383 clz r3, r3

8d46: 095b lsrs r3, r3, #5

8d48: 2800 cmp r0, #0

8d4a: bf14 ite ne

8d4c: 4618 movne r0, r3

8d4e: 2000 moveq r0, #0

8d50: 2800 cmp r0, #0

8d52: d0e4 beq.n 8d1e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x208a>

8d54: f8df 3794 ldr.w r3, [pc, #1940] ; 94ec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2858>

8d58: 60f9 str r1, [r7, #12]

8d5a: 447b add r3, pc

8d5c: 6c18 ldr r0, [r3, #64] ; 0x40

8d5e: 47c8 blx r9

8d60: 2c13 cmp r4, #19

8d62: 68f9 ldr r1, [r7, #12]

8d64: 4681 mov r9, r0

8d66: d1dc bne.n 8d22 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x208e>

8d68: 6d7b ldr r3, [r7, #84] ; 0x54

8d6a: 2b00 cmp r3, #0

8d6c: f43f af64 beq.w 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8d70: f8d1 2244 ldr.w r2, [r1, #580] ; 0x244

8d74: 1c53 adds r3, r2, #1

8d76: 2b01 cmp r3, #1

8d78: f240 8681 bls.w 9a7e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2dea>

8d7c: 6d7b ldr r3, [r7, #84] ; 0x54

8d7e: 68a9 ldr r1, [r5, #8]

8d80: 685b ldr r3, [r3, #4]

8d82: 440b add r3, r1

8d84: 4413 add r3, r2

8d86: 6cfa ldr r2, [r7, #76] ; 0x4c

8d88: f842 3008 str.w r3, [r2, r8]

8d8c: e754 b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8d8e: 2c01 cmp r4, #1

8d90: f040 87e2 bne.w 9d58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30c4>

8d94: 6c3a ldr r2, [r7, #64] ; 0x40

8d96: 4649 mov r1, r9

8d98: 68ab ldr r3, [r5, #8]

8d9a: f10b 0004 add.w r0, fp, #4

8d9e: f7ff fc39 bl 8614 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1980>

8da2: e749 b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8da4: 2c16 cmp r4, #22

8da6: d819 bhi.n 8ddc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2148>

8da8: 2c15 cmp r4, #21

8daa: d230 bcs.n 8e0e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x217a>

8dac: 6d7b ldr r3, [r7, #84] ; 0x54

8dae: 2b00 cmp r3, #0

8db0: f43f af42 beq.w 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8db4: 689b ldr r3, [r3, #8]

8db6: 68b2 ldr r2, [r6, #8]

8db8: 4293 cmp r3, r2

8dba: d83b bhi.n 8e34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x21a0>

8dbc: f8df 1730 ldr.w r1, [pc, #1840] ; 94f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x285c>

8dc0: 4479 add r1, pc

8dc2: 6a49 ldr r1, [r1, #36] ; 0x24

8dc4: 2900 cmp r1, #0

8dc6: bf18 it ne

8dc8: 4293 cmpne r3, r2

8dca: d333 bcc.n 8e34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x21a0>

8dcc: 429a cmp r2, r3

8dce: 4649 mov r1, r9

8dd0: 6c38 ldr r0, [r7, #64] ; 0x40

8dd2: bf28 it cs

8dd4: 461a movcs r2, r3

8dd6: f00a e994 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

8dda: e72d b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8ddc: 2ca0 cmp r4, #160 ; 0xa0

8dde: f040 87bb bne.w 9d58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30c4>

8de2: 6a79 ldr r1, [r7, #36] ; 0x24

8de4: f8db 3000 ldr.w r3, [fp]

8de8: 68aa ldr r2, [r5, #8]

8dea: 6c08 ldr r0, [r1, #64] ; 0x40

8dec: 4413 add r3, r2

8dee: 4798 blx r3

8df0: 6cfb ldr r3, [r7, #76] ; 0x4c

8df2: f843 0008 str.w r0, [r3, r8]

8df6: e71f b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8df8: 2c01 cmp r4, #1

8dfa: f040 87ad bne.w 9d58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30c4>

8dfe: 4699 mov r9, r3

8e00: e7c8 b.n 8d94 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2100>

8e02: 2c16 cmp r4, #22

8e04: d8ea bhi.n 8ddc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2148>

8e06: 2c15 cmp r4, #21

8e08: f4ff af16 bcc.w 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8e0c: 4699 mov r9, r3

8e0e: 6cfa ldr r2, [r7, #76] ; 0x4c

8e10: 68ab ldr r3, [r5, #8]

8e12: 444b add r3, r9

8e14: f842 3008 str.w r3, [r2, r8]

8e18: e70e b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8e1a: 7b1a ldrb r2, [r3, #12]

8e1c: e770 b.n 8d00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x206c>

8e1e: 68aa ldr r2, [r5, #8]

8e20: f8db 3000 ldr.w r3, [fp]

8e24: 4413 add r3, r2

8e26: 6cfa ldr r2, [r7, #76] ; 0x4c

8e28: f842 3008 str.w r3, [r2, r8]

8e2c: e704 b.n 8c38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fa4>

8e2e: f84a 3004 str.w r3, [sl, r4]

8e32: e5d0 b.n 89d6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d42>

8e34: 697b ldr r3, [r7, #20]

8e36: 681a ldr r2, [r3, #0]

8e38: f8db 3034 ldr.w r3, [fp, #52] ; 0x34

8e3c: 6812 ldr r2, [r2, #0]

8e3e: 685c ldr r4, [r3, #4]

8e40: 2a00 cmp r2, #0

8e42: f000 8407 beq.w 9654 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29c0>

8e46: 6cbb ldr r3, [r7, #72] ; 0x48

8e48: 2002 movs r0, #2

8e4a: 6939 ldr r1, [r7, #16]

8e4c: f853 300a ldr.w r3, [r3, sl]

8e50: 4423 add r3, r4

8e52: f002 ffbb bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

8e56: 6d7b ldr r3, [r7, #84] ; 0x54

8e58: 68b2 ldr r2, [r6, #8]

8e5a: 689b ldr r3, [r3, #8]

8e5c: e7b6 b.n 8dcc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2138>

8e5e: f8db 2038 ldr.w r2, [fp, #56] ; 0x38

8e62: eb06 04c4 add.w r4, r6, r4, lsl #3

8e66: f8df 368c ldr.w r3, [pc, #1676] ; 94f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2860>

8e6a: 447b add r3, pc

8e6c: 6852 ldr r2, [r2, #4]

8e6e: f503 63a3 add.w r3, r3, #1304 ; 0x518

8e72: 459b cmp fp, r3

8e74: bf18 it ne

8e76: f1ba 0f00 cmpne.w sl, #0

8e7a: 64fa str r2, [r7, #76] ; 0x4c

8e7c: f040 83ef bne.w 965e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29ca>

8e80: f8db 30e8 ldr.w r3, [fp, #232] ; 0xe8

8e84: 2b00 cmp r3, #0

8e86: f000 843d beq.w 9704 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a70>

8e8a: 685b ldr r3, [r3, #4]

8e8c: 63fb str r3, [r7, #60] ; 0x3c

8e8e: 6cbb ldr r3, [r7, #72] ; 0x48

8e90: 42a3 cmp r3, r4

8e92: f67f ae00 bls.w 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

8e96: f8df 3660 ldr.w r3, [pc, #1632] ; 94f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2864>

8e9a: 447b add r3, pc

8e9c: 613b str r3, [r7, #16]

8e9e: e9d4 9500 ldrd r9, r5, [r4]

8ea2: 6bf9 ldr r1, [r7, #60] ; 0x3c

8ea4: eb0a 0209 add.w r2, sl, r9

8ea8: 643a str r2, [r7, #64] ; 0x40

8eaa: 0a2b lsrs r3, r5, #8

8eac: b2ed uxtb r5, r5

8eae: 2d17 cmp r5, #23

8eb0: f8db 0174 ldr.w r0, [fp, #372] ; 0x174

8eb4: ea4f 1203 mov.w r2, r3, lsl #4

8eb8: f831 3013 ldrh.w r3, [r1, r3, lsl #1]

8ebc: 6cf9 ldr r1, [r7, #76] ; 0x4c

8ebe: 647a str r2, [r7, #68] ; 0x44

8ec0: eb01 0802 add.w r8, r1, r2

8ec4: f8c7 8054 str.w r8, [r7, #84] ; 0x54

8ec8: f000 8158 beq.w 917c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24e8>

8ecc: 2d00 cmp r5, #0

8ece: d05a beq.n 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

8ed0: f898 200c ldrb.w r2, [r8, #12]

8ed4: 0911 lsrs r1, r2, #4

8ed6: f000 809a beq.w 900e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x237a>

8eda: f898 100d ldrb.w r1, [r8, #13]

8ede: f001 0103 and.w r1, r1, #3

8ee2: 3901 subs r1, #1

8ee4: 2901 cmp r1, #1

8ee6: f240 8092 bls.w 900e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x237a>

8eea: f8db 2220 ldr.w r2, [fp, #544] ; 0x220

8eee: 4590 cmp r8, r2

8ef0: f000 836a beq.w 95c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2934>

8ef4: 2d11 cmp r5, #17

8ef6: bf18 it ne

8ef8: 2d16 cmpne r5, #22

8efa: bf08 it eq

8efc: 2201 moveq r2, #1

8efe: d007 beq.n 8f10 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x227c>

8f00: f1a5 0212 sub.w r2, r5, #18

8f04: 2d0d cmp r5, #13

8f06: bf18 it ne

8f08: 2a01 cmpne r2, #1

8f0a: bf94 ite ls

8f0c: 2201 movls r2, #1

8f0e: 2200 movhi r2, #0

8f10: 2d14 cmp r5, #20

8f12: bf08 it eq

8f14: f042 0202 orreq.w r2, r2, #2

8f18: d003 beq.n 8f22 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x228e>

8f1a: 2d15 cmp r5, #21

8f1c: bf08 it eq

8f1e: f042 0204 orreq.w r2, r2, #4

8f22: f3c3 030e ubfx r3, r3, #0, #15

8f26: e9cb 8288 strd r8, r2, [fp, #544] ; 0x220

8f2a: eb10 1303 adds.w r3, r0, r3, lsl #4

8f2e: d003 beq.n 8f38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22a4>

8f30: 6859 ldr r1, [r3, #4]

8f32: 2900 cmp r1, #0

8f34: bf08 it eq

8f36: 2300 moveq r3, #0

8f38: 6cf9 ldr r1, [r7, #76] ; 0x4c

8f3a: 6c78 ldr r0, [r7, #68] ; 0x44

8f3c: 6b7e ldr r6, [r7, #52] ; 0x34

8f3e: 5808 ldr r0, [r1, r0]

8f40: 2100 movs r1, #0

8f42: 9201 str r2, [sp, #4]

8f44: 2201 movs r2, #1

8f46: 4430 add r0, r6

8f48: 9300 str r3, [sp, #0]

8f4a: 9103 str r1, [sp, #12]

8f4c: 4659 mov r1, fp

8f4e: 6bbb ldr r3, [r7, #56] ; 0x38

8f50: 9202 str r2, [sp, #8]

8f52: f107 0254 add.w r2, r7, #84 ; 0x54

8f56: f7fe fb9f bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

8f5a: 6d7e ldr r6, [r7, #84] ; 0x54

8f5c: 4603 mov r3, r0

8f5e: e9cb 068a strd r0, r6, [fp, #552] ; 0x228

8f62: 2e00 cmp r6, #0

8f64: f040 8108 bne.w 9178 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24e4>

8f68: 2d12 cmp r5, #18

8f6a: d00c beq.n 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

8f6c: f200 80f4 bhi.w 9158 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24c4>

8f70: 2d02 cmp r5, #2

8f72: f000 8110 beq.w 9196 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2502>

8f76: d936 bls.n 8fe6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2352>

8f78: 2d0d cmp r5, #13

8f7a: d125 bne.n 8fc8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2334>

8f7c: 6c3a ldr r2, [r7, #64] ; 0x40

8f7e: f8df 357c ldr.w r3, [pc, #1404] ; 94fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2868>

8f82: 447b add r3, pc

8f84: 6053 str r3, [r2, #4]

8f86: 6cbb ldr r3, [r7, #72] ; 0x48

8f88: 3408 adds r4, #8

8f8a: 42a3 cmp r3, r4

8f8c: d887 bhi.n 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

8f8e: e582 b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

8f90: 3100 adds r1, #0

8f92: 69fa ldr r2, [r7, #28]

8f94: bf18 it ne

8f96: 2101 movne r1, #1

8f98: 2a00 cmp r2, #0

8f9a: bf18 it ne

8f9c: 2100 movne r1, #0

8f9e: 2900 cmp r1, #0

8fa0: d044 beq.n 902c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2398>

8fa2: f8df 255c ldr.w r2, [pc, #1372] ; 9500 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x286c>

8fa6: 603b str r3, [r7, #0]

8fa8: 447a add r2, pc

8faa: 6c10 ldr r0, [r2, #64] ; 0x40

8fac: 47b0 blx r6

8fae: 2d12 cmp r5, #18

8fb0: 4606 mov r6, r0

8fb2: f000 81c7 beq.w 9344 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26b0>

8fb6: 683b ldr r3, [r7, #0]

8fb8: d85f bhi.n 907a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x23e6>

8fba: 2d02 cmp r5, #2

8fbc: f000 80eb beq.w 9196 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2502>

8fc0: d911 bls.n 8fe6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2352>

8fc2: 2d0d cmp r5, #13

8fc4: f000 8670 beq.w 9ca8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3014>

8fc8: 2d11 cmp r5, #17

8fca: f040 80cf bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

8fce: 2b00 cmp r3, #0

8fd0: d0d9 beq.n 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

8fd2: f8d3 3248 ldr.w r3, [r3, #584] ; 0x248

8fd6: 3408 adds r4, #8

8fd8: f84a 3009 str.w r3, [sl, r9]

8fdc: 6cbb ldr r3, [r7, #72] ; 0x48

8fde: 42a3 cmp r3, r4

8fe0: f63f af5d bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

8fe4: e557 b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

8fe6: 2d01 cmp r5, #1

8fe8: f040 80c0 bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

8fec: f85a 3009 ldr.w r3, [sl, r9]

8ff0: 4631 mov r1, r6

8ff2: 6c3a ldr r2, [r7, #64] ; 0x40

8ff4: f10b 0004 add.w r0, fp, #4

8ff8: f343 0317 sbfx r3, r3, #0, #24

8ffc: 3408 adds r4, #8

8ffe: 009b lsls r3, r3, #2

9000: f7ff fb08 bl 8614 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1980>

9004: 6cbb ldr r3, [r7, #72] ; 0x48

9006: 42a3 cmp r3, r4

9008: f63f af49 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

900c: e543 b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

900e: 465b mov r3, fp

9010: 4646 mov r6, r8

9012: 89f1 ldrh r1, [r6, #14]

9014: f64f 70f1 movw r0, #65521 ; 0xfff1

9018: f002 020f and.w r2, r2, #15

901c: 6876 ldr r6, [r6, #4]

901e: 4281 cmp r1, r0

9020: bf14 ite ne

9022: 6818 ldrne r0, [r3, #0]

9024: 2000 moveq r0, #0

9026: 2a0a cmp r2, #10

9028: 4406 add r6, r0

902a: d0b1 beq.n 8f90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22fc>

902c: 2d12 cmp r5, #18

902e: f000 8189 beq.w 9344 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26b0>

9032: d822 bhi.n 907a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x23e6>

9034: 2d02 cmp r5, #2

9036: f000 80ae beq.w 9196 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2502>

903a: d9d4 bls.n 8fe6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2352>

903c: 2d0d cmp r5, #13

903e: d1c3 bne.n 8fc8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2334>

9040: 6d79 ldr r1, [r7, #84] ; 0x54

9042: 6862 ldr r2, [r4, #4]

9044: 0a12 lsrs r2, r2, #8

9046: f8d3 2244 ldr.w r2, [r3, #580] ; 0x244

904a: bf0c ite eq

904c: f85a 5009 ldreq.w r5, [sl, r9]

9050: 684d ldrne r5, [r1, #4]

9052: 1c50 adds r0, r2, #1

9054: f000 85d8 beq.w 9c08 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f74>

9058: 2a00 cmp r2, #0

905a: f000 8653 beq.w 9d04 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3070>

905e: 442a add r2, r5

9060: f84a 2009 str.w r2, [sl, r9]

9064: 6c3a ldr r2, [r7, #64] ; 0x40

9066: 3408 adds r4, #8

9068: f8df 3498 ldr.w r3, [pc, #1176] ; 9504 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2870>

906c: 447b add r3, pc

906e: 6053 str r3, [r2, #4]

9070: 6cbb ldr r3, [r7, #72] ; 0x48

9072: 42a3 cmp r3, r4

9074: f63f af13 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

9078: e50d b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

907a: 2d16 cmp r5, #22

907c: d81b bhi.n 90b6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2422>

907e: 2d15 cmp r5, #21

9080: f080 8098 bcs.w 91b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2520>

9084: 2d13 cmp r5, #19

9086: d128 bne.n 90da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2446>

9088: 6d7e ldr r6, [r7, #84] ; 0x54

908a: 2e00 cmp r6, #0

908c: f43f af7b beq.w 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

9090: f8d3 2244 ldr.w r2, [r3, #580] ; 0x244

9094: 1c51 adds r1, r2, #1

9096: 2901 cmp r1, #1

9098: f240 862a bls.w 9cf0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x305c>

909c: f85a 1009 ldr.w r1, [sl, r9]

90a0: 3408 adds r4, #8

90a2: 6873 ldr r3, [r6, #4]

90a4: 440a add r2, r1

90a6: 441a add r2, r3

90a8: 6cbb ldr r3, [r7, #72] ; 0x48

90aa: f84a 2009 str.w r2, [sl, r9]

90ae: 42a3 cmp r3, r4

90b0: f63f aef5 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

90b4: e4ef b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

90b6: 2da0 cmp r5, #160 ; 0xa0

90b8: d158 bne.n 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

90ba: 6939 ldr r1, [r7, #16]

90bc: 3408 adds r4, #8

90be: f8db 3000 ldr.w r3, [fp]

90c2: f85a 2009 ldr.w r2, [sl, r9]

90c6: 6c08 ldr r0, [r1, #64] ; 0x40

90c8: 4413 add r3, r2

90ca: 4798 blx r3

90cc: 6cbb ldr r3, [r7, #72] ; 0x48

90ce: 42a3 cmp r3, r4

90d0: f84a 0009 str.w r0, [sl, r9]

90d4: f63f aee3 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

90d8: e4dd b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

90da: 2d14 cmp r5, #20

90dc: d146 bne.n 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

90de: 6d7b ldr r3, [r7, #84] ; 0x54

90e0: 2b00 cmp r3, #0

90e2: f43f af50 beq.w 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

90e6: 689b ldr r3, [r3, #8]

90e8: f8d8 2008 ldr.w r2, [r8, #8]

90ec: 4293 cmp r3, r2

90ee: d814 bhi.n 911a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2486>

90f0: f8df 1414 ldr.w r1, [pc, #1044] ; 9508 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2874>

90f4: 4479 add r1, pc

90f6: 6a49 ldr r1, [r1, #36] ; 0x24

90f8: 2900 cmp r1, #0

90fa: bf18 it ne

90fc: 4293 cmpne r3, r2

90fe: d30c bcc.n 911a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2486>

9100: 429a cmp r2, r3

9102: 4631 mov r1, r6

9104: 6c38 ldr r0, [r7, #64] ; 0x40

9106: bf28 it cs

9108: 461a movcs r2, r3

910a: f009 effa blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

910e: 6cbb ldr r3, [r7, #72] ; 0x48

9110: 3408 adds r4, #8

9112: 42a3 cmp r3, r4

9114: f63f aec3 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

9118: e4bd b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

911a: 4bfc ldr r3, [pc, #1008] ; (950c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2878>)

911c: f8db 2034 ldr.w r2, [fp, #52] ; 0x34

9120: 447b add r3, pc

9122: 681b ldr r3, [r3, #0]

9124: 6855 ldr r5, [r2, #4]

9126: 681a ldr r2, [r3, #0]

9128: 2a00 cmp r2, #0

912a: f000 82bc beq.w 96a6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a12>

912e: 6c79 ldr r1, [r7, #68] ; 0x44

9130: 2002 movs r0, #2

9132: 6cfb ldr r3, [r7, #76] ; 0x4c

9134: 585b ldr r3, [r3, r1]

9136: 49f6 ldr r1, [pc, #984] ; (9510 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x287c>)

9138: 442b add r3, r5

913a: 4479 add r1, pc

913c: f002 fe46 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

9140: 6d7b ldr r3, [r7, #84] ; 0x54

9142: f8d8 2008 ldr.w r2, [r8, #8]

9146: 4631 mov r1, r6

9148: 6c38 ldr r0, [r7, #64] ; 0x40

914a: 689b ldr r3, [r3, #8]

914c: 429a cmp r2, r3

914e: bf28 it cs

9150: 461a movcs r2, r3

9152: f009 efd6 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

9156: e7da b.n 910e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x247a>

9158: 2d16 cmp r5, #22

915a: d8ac bhi.n 90b6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2422>

915c: 2d15 cmp r5, #21

915e: d229 bcs.n 91b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2520>

9160: 2d13 cmp r5, #19

9162: f43f af10 beq.w 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

9166: 2d14 cmp r5, #20

9168: f43f af0d beq.w 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

916c: 46aa mov sl, r5

916e: 4651 mov r1, sl

9170: 4658 mov r0, fp

9172: 2200 movs r2, #0

9174: f7ff fb74 bl 8860 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bcc>

9178: 7b32 ldrb r2, [r6, #12]

917a: e74a b.n 9012 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x237e>

917c: f85a 3009 ldr.w r3, [sl, r9]

9180: 3408 adds r4, #8

9182: f8db 2000 ldr.w r2, [fp]

9186: 4413 add r3, r2

9188: f84a 3009 str.w r3, [sl, r9]

918c: 6cbb ldr r3, [r7, #72] ; 0x48

918e: 42a3 cmp r3, r4

9190: f63f ae85 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

9194: e47f b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

9196: 697b ldr r3, [r7, #20]

9198: 459b cmp fp, r3

919a: f000 82a4 beq.w 96e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a52>

919e: 6c3b ldr r3, [r7, #64] ; 0x40

91a0: 3408 adds r4, #8

91a2: 681b ldr r3, [r3, #0]

91a4: 441e add r6, r3

91a6: 6cbb ldr r3, [r7, #72] ; 0x48

91a8: f84a 6009 str.w r6, [sl, r9]

91ac: 42a3 cmp r3, r4

91ae: f63f ae76 bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

91b2: e470 b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

91b4: 6cbb ldr r3, [r7, #72] ; 0x48

91b6: 3408 adds r4, #8

91b8: f84a 6009 str.w r6, [sl, r9]

91bc: 42a3 cmp r3, r4

91be: f63f ae6e bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

91c2: e468 b.n 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

91c4: 42ab cmp r3, r5

91c6: bf38 it cc

91c8: 6cfa ldrcc r2, [r7, #76] ; 0x4c

91ca: f4bf acb5 bcs.w 8b38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1ea4>

91ce: 688b ldr r3, [r1, #8]

91d0: 310c adds r1, #12

91d2: f851 0c0c ldr.w r0, [r1, #-12]

91d6: 428d cmp r5, r1

91d8: 4413 add r3, r2

91da: 5083 str r3, [r0, r2]

91dc: d8f7 bhi.n 91ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x253a>

91de: f8db 30e8 ldr.w r3, [fp, #232] ; 0xe8

91e2: 2b00 cmp r3, #0

91e4: f47f acad bne.w 8b42 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1eae>

91e8: 6c7b ldr r3, [r7, #68] ; 0x44

91ea: 42ab cmp r3, r5

91ec: f67f ad28 bls.w 8c40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fac>

91f0: 4bc8 ldr r3, [pc, #800] ; (9514 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2880>)

91f2: 447b add r3, pc

91f4: 63fb str r3, [r7, #60] ; 0x3c

91f6: 4bc8 ldr r3, [pc, #800] ; (9518 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2884>)

91f8: 447b add r3, pc

91fa: 60fb str r3, [r7, #12]

91fc: 4bc7 ldr r3, [pc, #796] ; (951c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2888>)

91fe: 447b add r3, pc

9200: 60bb str r3, [r7, #8]

9202: e9d5 9400 ldrd r9, r4, [r5]

9206: 6cfb ldr r3, [r7, #76] ; 0x4c

9208: ea4f 2814 mov.w r8, r4, lsr #8

920c: b2e4 uxtb r4, r4

920e: 444b add r3, r9

9210: 2c17 cmp r4, #23

9212: 643b str r3, [r7, #64] ; 0x40

9214: ea4f 1808 mov.w r8, r8, lsl #4

9218: 6cbb ldr r3, [r7, #72] ; 0x48

921a: eb03 0608 add.w r6, r3, r8

921e: 657e str r6, [r7, #84] ; 0x54

9220: f000 81b0 beq.w 9584 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28f0>

9224: 2c00 cmp r4, #0

9226: d058 beq.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

9228: 7b32 ldrb r2, [r6, #12]

922a: 0913 lsrs r3, r2, #4

922c: f000 80e4 beq.w 93f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2764>

9230: 7b73 ldrb r3, [r6, #13]

9232: f003 0303 and.w r3, r3, #3

9236: 3b01 subs r3, #1

9238: 2b01 cmp r3, #1

923a: f240 80dd bls.w 93f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2764>

923e: f8db 3220 ldr.w r3, [fp, #544] ; 0x220

9242: 429e cmp r6, r3

9244: f000 81e3 beq.w 960e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x297a>

9248: 2c11 cmp r4, #17

924a: bf18 it ne

924c: 2c16 cmpne r4, #22

924e: bf08 it eq

9250: 2301 moveq r3, #1

9252: d007 beq.n 9264 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x25d0>

9254: f1a4 0312 sub.w r3, r4, #18

9258: 2c0d cmp r4, #13

925a: bf18 it ne

925c: 2b01 cmpne r3, #1

925e: bf94 ite ls

9260: 2301 movls r3, #1

9262: 2300 movhi r3, #0

9264: 2c14 cmp r4, #20

9266: bf08 it eq

9268: f043 0302 orreq.w r3, r3, #2

926c: d003 beq.n 9276 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x25e2>

926e: 2c15 cmp r4, #21

9270: bf08 it eq

9272: f043 0304 orreq.w r3, r3, #4

9276: 6cba ldr r2, [r7, #72] ; 0x48

9278: 2100 movs r1, #0

927a: f04f 0c01 mov.w ip, #1

927e: f852 0008 ldr.w r0, [r2, r8]

9282: 6b7a ldr r2, [r7, #52] ; 0x34

9284: f8cb 3224 str.w r3, [fp, #548] ; 0x224

9288: f8cb 6220 str.w r6, [fp, #544] ; 0x220

928c: 4410 add r0, r2

928e: 9301 str r3, [sp, #4]

9290: f107 0254 add.w r2, r7, #84 ; 0x54

9294: 6bbb ldr r3, [r7, #56] ; 0x38

9296: 9103 str r1, [sp, #12]

9298: 9100 str r1, [sp, #0]

929a: 4659 mov r1, fp

929c: f8cd c008 str.w ip, [sp, #8]

92a0: f7fe f9fa bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

92a4: 6d7b ldr r3, [r7, #84] ; 0x54

92a6: 4601 mov r1, r0

92a8: e9cb 038a strd r0, r3, [fp, #552] ; 0x228

92ac: 2b00 cmp r3, #0

92ae: f040 8167 bne.w 9580 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28ec>

92b2: 2c13 cmp r4, #19

92b4: d011 beq.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

92b6: f200 8152 bhi.w 955e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28ca>

92ba: 2c02 cmp r4, #2

92bc: f000 8154 beq.w 9568 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28d4>

92c0: f240 8159 bls.w 9576 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28e2>

92c4: 2c11 cmp r4, #17

92c6: f000 808e beq.w 93e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2752>

92ca: 2c12 cmp r4, #18

92cc: f040 8566 bne.w 9d9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3108>

92d0: 68aa ldr r2, [r5, #8]

92d2: 4413 add r3, r2

92d4: 6cfa ldr r2, [r7, #76] ; 0x4c

92d6: f842 3009 str.w r3, [r2, r9]

92da: 6c7b ldr r3, [r7, #68] ; 0x44

92dc: 350c adds r5, #12

92de: 42ab cmp r3, r5

92e0: d88f bhi.n 9202 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x256e>

92e2: e4ad b.n 8c40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fac>

92e4: f1a4 010d sub.w r1, r4, #13

92e8: 2909 cmp r1, #9

92ea: bf96 itet ls

92ec: f240 2271 movwls r2, #625 ; 0x271

92f0: 2200 movhi r2, #0

92f2: 40ca lsrls r2, r1

92f4: f8db 1224 ldr.w r1, [fp, #548] ; 0x224

92f8: bf98 it ls

92fa: f002 0201 andls.w r2, r2, #1

92fe: 2c14 cmp r4, #20

9300: f000 8367 beq.w 99d2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2d3e>

9304: 2c15 cmp r4, #21

9306: f040 81bc bne.w 9682 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29ee>

930a: 2a00 cmp r2, #0

930c: bf0c ite eq

930e: 2204 moveq r2, #4

9310: 2205 movne r2, #5

9312: 428a cmp r2, r1

9314: f47f ac47 bne.w 8ba6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f12>

9318: 4981 ldr r1, [pc, #516] ; (9520 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x288c>)

931a: f8db 322c ldr.w r3, [fp, #556] ; 0x22c

931e: 4479 add r1, pc

9320: f8d1 250c ldr.w r2, [r1, #1292] ; 0x50c

9324: 657b str r3, [r7, #84] ; 0x54

9326: 3201 adds r2, #1

9328: f8c1 250c str.w r2, [r1, #1292] ; 0x50c

932c: f8db 1228 ldr.w r1, [fp, #552] ; 0x228

9330: e46c b.n 8c0c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f78>

9332: 6a79 ldr r1, [r7, #36] ; 0x24

9334: e9d1 6304 ldrd r6, r3, [r1, #16]

9338: e9d1 4206 ldrd r4, r2, [r1, #24]

933c: 3110 adds r1, #16

933e: 6279 str r1, [r7, #36] ; 0x24

9340: f7ff bb2a b.w 8998 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d04>

9344: 6d7e ldr r6, [r7, #84] ; 0x54

9346: 2e00 cmp r6, #0

9348: f43f ae1d beq.w 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

934c: 6872 ldr r2, [r6, #4]

934e: 3408 adds r4, #8

9350: f85a 3009 ldr.w r3, [sl, r9]

9354: 4413 add r3, r2

9356: f84a 3009 str.w r3, [sl, r9]

935a: 6cbb ldr r3, [r7, #72] ; 0x48

935c: 42a3 cmp r3, r4

935e: f63f ad9e bhi.w 8e9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x220a>

9362: f7ff bb98 b.w 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

9366: 465b mov r3, fp

9368: f8b2 c00e ldrh.w ip, [r2, #14]

936c: f64f 71f1 movw r1, #65521 ; 0xfff1

9370: f000 000f and.w r0, r0, #15

9374: 458c cmp ip, r1

9376: bf14 ite ne

9378: 6819 ldrne r1, [r3, #0]

937a: 2100 moveq r1, #0

937c: 280a cmp r0, #10

937e: f000 847b beq.w 9c78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2fe4>

9382: 6d79 ldr r1, [r7, #84] ; 0x54

9384: 6872 ldr r2, [r6, #4]

9386: 0a12 lsrs r2, r2, #8

9388: f8d3 2244 ldr.w r2, [r3, #580] ; 0x244

938c: bf0c ite eq

938e: f85a 1004 ldreq.w r1, [sl, r4]

9392: 6849 ldrne r1, [r1, #4]

9394: 1c50 adds r0, r2, #1

9396: f000 83c1 beq.w 9b1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e88>

939a: 2a00 cmp r2, #0

939c: f000 8491 beq.w 9cc2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x302e>

93a0: 440a add r2, r1

93a2: f84a 2004 str.w r2, [sl, r4]

93a6: f8c9 8004 str.w r8, [r9, #4]

93aa: f7ff bb14 b.w 89d6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d42>

93ae: 69fb ldr r3, [r7, #28]

93b0: fab3 f383 clz r3, r3

93b4: 095b lsrs r3, r3, #5

93b6: 2800 cmp r0, #0

93b8: bf14 ite ne

93ba: 4618 movne r0, r3

93bc: 2000 moveq r0, #0

93be: b360 cbz r0, 941a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2786>

93c0: 4b58 ldr r3, [pc, #352] ; (9524 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2890>)

93c2: 6079 str r1, [r7, #4]

93c4: 447b add r3, pc

93c6: 6c18 ldr r0, [r3, #64] ; 0x40

93c8: 47d0 blx sl

93ca: 2c13 cmp r4, #19

93cc: 6879 ldr r1, [r7, #4]

93ce: 4682 mov sl, r0

93d0: f000 80e0 beq.w 9594 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2900>

93d4: d832 bhi.n 943c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27a8>

93d6: 2c02 cmp r4, #2

93d8: f000 80c7 beq.w 956a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28d6>

93dc: f240 80b4 bls.w 9548 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28b4>

93e0: 2c11 cmp r4, #17

93e2: f040 80e9 bne.w 95b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2924>

93e6: 2900 cmp r1, #0

93e8: f43f af77 beq.w 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

93ec: f8d1 3248 ldr.w r3, [r1, #584] ; 0x248

93f0: 6cfa ldr r2, [r7, #76] ; 0x4c

93f2: f842 3009 str.w r3, [r2, r9]

93f6: e770 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

93f8: 4659 mov r1, fp

93fa: 4633 mov r3, r6

93fc: 89d8 ldrh r0, [r3, #14]

93fe: f64f 7cf1 movw ip, #65521 ; 0xfff1

9402: f002 020f and.w r2, r2, #15

9406: 685b ldr r3, [r3, #4]

9408: 4560 cmp r0, ip

940a: bf14 ite ne

940c: f8d1 a000 ldrne.w sl, [r1]

9410: f04f 0a00 moveq.w sl, #0

9414: 2a0a cmp r2, #10

9416: 449a add sl, r3

9418: d0c9 beq.n 93ae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x271a>

941a: 2c13 cmp r4, #19

941c: f000 80ba beq.w 9594 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2900>

9420: d80c bhi.n 943c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27a8>

9422: 2c02 cmp r4, #2

9424: f000 80a1 beq.w 956a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28d6>

9428: f240 808e bls.w 9548 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28b4>

942c: 2c11 cmp r4, #17

942e: d0da beq.n 93e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2752>

9430: 2c12 cmp r4, #18

9432: f040 84b3 bne.w 9d9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3108>

9436: 6d7b ldr r3, [r7, #84] ; 0x54

9438: 685b ldr r3, [r3, #4]

943a: e749 b.n 92d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x263c>

943c: 2c16 cmp r4, #22

943e: d875 bhi.n 952c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2898>

9440: 2c15 cmp r4, #21

9442: f080 8092 bcs.w 956a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28d6>

9446: 6d7b ldr r3, [r7, #84] ; 0x54

9448: 2b00 cmp r3, #0

944a: f43f af46 beq.w 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

944e: 689b ldr r3, [r3, #8]

9450: 68b2 ldr r2, [r6, #8]

9452: 4293 cmp r3, r2

9454: d80e bhi.n 9474 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27e0>

9456: 4934 ldr r1, [pc, #208] ; (9528 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2894>)

9458: 4479 add r1, pc

945a: 6a49 ldr r1, [r1, #36] ; 0x24

945c: 2900 cmp r1, #0

945e: bf18 it ne

9460: 4293 cmpne r3, r2

9462: d307 bcc.n 9474 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27e0>

9464: 429a cmp r2, r3

9466: 4651 mov r1, sl

9468: 6c38 ldr r0, [r7, #64] ; 0x40

946a: bf28 it cs

946c: 461a movcs r2, r3

946e: f009 ee48 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

9472: e732 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

9474: 68fb ldr r3, [r7, #12]

9476: 681a ldr r2, [r3, #0]

9478: f8db 3034 ldr.w r3, [fp, #52] ; 0x34

947c: 6812 ldr r2, [r2, #0]

947e: 685c ldr r4, [r3, #4]

9480: 2a00 cmp r2, #0

9482: f000 832d beq.w 9ae0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e4c>

9486: 6cbb ldr r3, [r7, #72] ; 0x48

9488: 2002 movs r0, #2

948a: 68b9 ldr r1, [r7, #8]

948c: f853 3008 ldr.w r3, [r3, r8]

9490: 4423 add r3, r4

9492: f002 fc9b bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

9496: 6d7b ldr r3, [r7, #84] ; 0x54

9498: 68b2 ldr r2, [r6, #8]

949a: 4651 mov r1, sl

949c: 6c38 ldr r0, [r7, #64] ; 0x40

949e: 689b ldr r3, [r3, #8]

94a0: 429a cmp r2, r3

94a2: bf28 it cs

94a4: 461a movcs r2, r3

94a6: f009 ee2c blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

94aa: e716 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

94ac: 073e lsls r6, r7, #28

94ae: 0002 movs r2, r0

94b0: 009c lsls r4, r3, #2

94b2: 0002 movs r2, r0

94b4: 0076 lsls r6, r6, #1

94b6: 0002 movs r2, r0

94b8: 06e4 lsls r4, r4, #27

94ba: 0002 movs r2, r0

94bc: 06d6 lsls r6, r2, #27

94be: 0002 movs r2, r0

94c0: 77af strb r7, [r5, #30]

94c2: 0000 movs r0, r0

94c4: 7797 strb r7, [r2, #30]

94c6: 0000 movs r0, r0

94c8: ffb0 0001 vrev64.8 d0, d1

94cc: 76c7 strb r7, [r0, #27]

94ce: 0000 movs r0, r0

94d0: 057a lsls r2, r7, #21

94d2: 0002 movs r2, r0

94d4: fe7e 0001 cdp2 0, 7, cr0, cr14, cr1, {0}

94d8: d9a2 bls.n 9420 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x278c>

94da: 0000 movs r0, r0

94dc: 0d9a lsrs r2, r3, #22

94de: 0002 movs r2, r0

94e0: fca8 0001 stc2 0, cr0, [r8], #4

94e4: 0044 lsls r4, r0, #1

94e6: 0000 movs r0, r0

94e8: 0378 lsls r0, r7, #13

94ea: 0002 movs r2, r0

94ec: fc12 0001 ldc2 0, cr0, [r2], {1}

94f0: fbac 0001 umull r0, r0, ip, r1

94f4: 01f2 lsls r2, r6, #7

94f6: 0002 movs r2, r0

94f8: fad2 0001 ; <UNDEFINED> instruction: 0xfad20001

94fc: 71d3 strb r3, [r2, #7]

94fe: 0000 movs r0, r0

9500: f9c4 0001 vst1.8 {d16[0]}, [r4], r1

9504: 70e5 strb r5, [r4, #3]

9506: 0000 movs r0, r0

9508: f878 0001 ldr??.w r0, [r8, r1]

950c: 0778 lsls r0, r7, #29

950e: 0002 movs r2, r0

9510: d362 bcc.n 95d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2944>

9512: 0000 movs r0, r0

9514: f77a 0001 ; <UNDEFINED> instruction: 0xf77a0001

9518: 06a0 lsls r0, r4, #26

951a: 0002 movs r2, r0

951c: d29e bcs.n 945c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27c8>

951e: 0000 movs r0, r0

9520: fd3e 0001 ldc2 0, cr0, [lr, #-4]!

9524: f5a8 0001 sub.w r0, r8, #8454144 ; 0x810000

9528: f514 0001 adds.w r0, r4, #8454144 ; 0x810000

952c: 2ca0 cmp r4, #160 ; 0xa0

952e: f040 8435 bne.w 9d9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3108>

9532: 6bf9 ldr r1, [r7, #60] ; 0x3c

9534: f8db 3000 ldr.w r3, [fp]

9538: 68aa ldr r2, [r5, #8]

953a: 6c08 ldr r0, [r1, #64] ; 0x40

953c: 4413 add r3, r2

953e: 4798 blx r3

9540: 6cfb ldr r3, [r7, #76] ; 0x4c

9542: f843 0009 str.w r0, [r3, r9]

9546: e6c8 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

9548: 2c01 cmp r4, #1

954a: f040 8427 bne.w 9d9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3108>

954e: 6c3a ldr r2, [r7, #64] ; 0x40

9550: 4651 mov r1, sl

9552: 68ab ldr r3, [r5, #8]

9554: f10b 0004 add.w r0, fp, #4

9558: f7ff f85c bl 8614 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1980>

955c: e6bd b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

955e: 2c16 cmp r4, #22

9560: d8e4 bhi.n 952c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2898>

9562: 2c15 cmp r4, #21

9564: f4ff aeb9 bcc.w 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

9568: 469a mov sl, r3

956a: 6cfa ldr r2, [r7, #76] ; 0x4c

956c: 68ab ldr r3, [r5, #8]

956e: 4453 add r3, sl

9570: f842 3009 str.w r3, [r2, r9]

9574: e6b1 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

9576: 2c01 cmp r4, #1

9578: f040 8410 bne.w 9d9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3108>

957c: 469a mov sl, r3

957e: e7e6 b.n 954e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x28ba>

9580: 7b1a ldrb r2, [r3, #12]

9582: e73b b.n 93fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2768>

9584: 68aa ldr r2, [r5, #8]

9586: f8db 3000 ldr.w r3, [fp]

958a: 4413 add r3, r2

958c: 6cfa ldr r2, [r7, #76] ; 0x4c

958e: f842 3009 str.w r3, [r2, r9]

9592: e6a2 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

9594: 6d7a ldr r2, [r7, #84] ; 0x54

9596: 2a00 cmp r2, #0

9598: f43f ae9f beq.w 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

959c: f8d1 3244 ldr.w r3, [r1, #580] ; 0x244

95a0: 1c58 adds r0, r3, #1

95a2: 2801 cmp r0, #1

95a4: f240 839b bls.w 9cde <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x304a>

95a8: 68a9 ldr r1, [r5, #8]

95aa: 6852 ldr r2, [r2, #4]

95ac: 440b add r3, r1

95ae: 4413 add r3, r2

95b0: 6cfa ldr r2, [r7, #76] ; 0x4c

95b2: f842 3009 str.w r3, [r2, r9]

95b6: e690 b.n 92da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2646>

95b8: 2c12 cmp r4, #18

95ba: f040 83ef bne.w 9d9c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3108>

95be: 6d7b ldr r3, [r7, #84] ; 0x54

95c0: 2b00 cmp r3, #0

95c2: f47f af39 bne.w 9438 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27a4>

95c6: e683 b.n 92d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x263c>

95c8: f1a5 010d sub.w r1, r5, #13

95cc: 2909 cmp r1, #9

95ce: bf96 itet ls

95d0: f240 2271 movwls r2, #625 ; 0x271

95d4: 2200 movhi r2, #0

95d6: 40ca lsrls r2, r1

95d8: f8db 1224 ldr.w r1, [fp, #548] ; 0x224

95dc: bf98 it ls

95de: f002 0201 andls.w r2, r2, #1

95e2: 2d14 cmp r5, #20

95e4: d057 beq.n 9696 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a02>

95e6: 2d15 cmp r5, #21

95e8: f000 8281 beq.w 9aee <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e5a>

95ec: 428a cmp r2, r1

95ee: f47f ac81 bne.w 8ef4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2260>

95f2: f8df 27dc ldr.w r2, [pc, #2012] ; 9dd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x313c>

95f6: f8db 622c ldr.w r6, [fp, #556] ; 0x22c

95fa: 447a add r2, pc

95fc: f8d2 350c ldr.w r3, [r2, #1292] ; 0x50c

9600: 657e str r6, [r7, #84] ; 0x54

9602: 3301 adds r3, #1

9604: f8c2 350c str.w r3, [r2, #1292] ; 0x50c

9608: f8db 3228 ldr.w r3, [fp, #552] ; 0x228

960c: e4a9 b.n 8f62 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22ce>

960e: f1a4 020d sub.w r2, r4, #13

9612: 2a09 cmp r2, #9

9614: bf96 itet ls

9616: f240 2371 movwls r3, #625 ; 0x271

961a: 2300 movhi r3, #0

961c: 40d3 lsrls r3, r2

961e: f8db 2224 ldr.w r2, [fp, #548] ; 0x224

9622: bf98 it ls

9624: f003 0301 andls.w r3, r3, #1

9628: 2c14 cmp r4, #20

962a: d040 beq.n 96ae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a1a>

962c: 2c15 cmp r4, #21

962e: f000 827f beq.w 9b30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e9c>

9632: 4293 cmp r3, r2

9634: f47f ae08 bne.w 9248 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x25b4>

9638: f8df 1798 ldr.w r1, [pc, #1944] ; 9dd4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3140>

963c: f8db 322c ldr.w r3, [fp, #556] ; 0x22c

9640: 4479 add r1, pc

9642: f8d1 250c ldr.w r2, [r1, #1292] ; 0x50c

9646: 657b str r3, [r7, #84] ; 0x54

9648: 3201 adds r2, #1

964a: f8c1 250c str.w r2, [r1, #1292] ; 0x50c

964e: f8db 1228 ldr.w r1, [fp, #552] ; 0x228

9652: e62b b.n 92ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2618>

9654: f8df 2780 ldr.w r2, [pc, #1920] ; 9dd8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3144>

9658: 447a add r2, pc

965a: f7ff bbf4 b.w 8e46 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x21b2>

965e: 42a6 cmp r6, r4

9660: f4bf ac0e bcs.w 8e80 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x21ec>

9664: f856 2b08 ldr.w r2, [r6], #8

9668: 42b4 cmp r4, r6

966a: f85a 3002 ldr.w r3, [sl, r2]

966e: 4453 add r3, sl

9670: f84a 3002 str.w r3, [sl, r2]

9674: d8f6 bhi.n 9664 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29d0>

9676: e403 b.n 8e80 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x21ec>

9678: 4620 mov r0, r4

967a: 4623 mov r3, r4

967c: 4626 mov r6, r4

967e: f7ff b96d b.w 895c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cc8>

9682: 428a cmp r2, r1

9684: f47f aa89 bne.w 8b9a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f06>

9688: e646 b.n 9318 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2684>

968a: f8df 2750 ldr.w r2, [pc, #1872] ; 9ddc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3148>

968e: 58b2 ldr r2, [r6, r2]

9690: 609a str r2, [r3, #8]

9692: f7ff b94a b.w 892a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c96>

9696: 2a00 cmp r2, #0

9698: bf0c ite eq

969a: 2202 moveq r2, #2

969c: 2203 movne r2, #3

969e: 428a cmp r2, r1

96a0: f47f ac2e bne.w 8f00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x226c>

96a4: e7a5 b.n 95f2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x295e>

96a6: f8df 2738 ldr.w r2, [pc, #1848] ; 9de0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x314c>

96aa: 447a add r2, pc

96ac: e53f b.n 912e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x249a>

96ae: 2b00 cmp r3, #0

96b0: bf0c ite eq

96b2: 2302 moveq r3, #2

96b4: 2303 movne r3, #3

96b6: 4293 cmp r3, r2

96b8: f47f adcc bne.w 9254 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x25c0>

96bc: e7bc b.n 9638 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29a4>

96be: e9d5 1201 ldrd r1, r2, [r5, #4]

96c2: 6828 ldr r0, [r5, #0]

96c4: f008 fb94 bl 11df0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1088>

96c8: 2800 cmp r0, #0

96ca: f2c0 8374 blt.w 9db6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3122>

96ce: 6828 ldr r0, [r5, #0]

96d0: 2200 movs r2, #0

96d2: 6869 ldr r1, [r5, #4]

96d4: 46a4 mov ip, r4

96d6: 4401 add r1, r0

96d8: f007 fc72 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

96dc: 68ed ldr r5, [r5, #12]

96de: 2d00 cmp r5, #0

96e0: f43f aaca beq.w 8c78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fe4>

96e4: e7eb b.n 96be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a2a>

96e6: f8b8 200e ldrh.w r2, [r8, #14]

96ea: f64f 73f1 movw r3, #65521 ; 0xfff1

96ee: 429a cmp r2, r3

96f0: bf16 itet ne

96f2: 68bb ldrne r3, [r7, #8]

96f4: 2200 moveq r2, #0

96f6: f8d3 2518 ldrne.w r2, [r3, #1304] ; 0x518

96fa: f8d8 3004 ldr.w r3, [r8, #4]

96fe: 1af6 subs r6, r6, r3

9700: 1ab6 subs r6, r6, r2

9702: e54c b.n 919e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x250a>

9704: 6cbb ldr r3, [r7, #72] ; 0x48

9706: 42a3 cmp r3, r4

9708: f67f a9c5 bls.w 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

970c: f8df 36d4 ldr.w r3, [pc, #1748] ; 9de4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3150>

9710: f8c7 a044 str.w sl, [r7, #68] ; 0x44

9714: 447b add r3, pc

9716: 63fb str r3, [r7, #60] ; 0x3c

9718: e9d4 9500 ldrd r9, r5, [r4]

971c: 6c7b ldr r3, [r7, #68] ; 0x44

971e: ea4f 2815 mov.w r8, r5, lsr #8

9722: b2ed uxtb r5, r5

9724: 444b add r3, r9

9726: 2d17 cmp r5, #23

9728: 643b str r3, [r7, #64] ; 0x40

972a: ea4f 1808 mov.w r8, r8, lsl #4

972e: 6cfb ldr r3, [r7, #76] ; 0x4c

9730: eb03 0608 add.w r6, r3, r8

9734: 657e str r6, [r7, #84] ; 0x54

9736: f000 8101 beq.w 993c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ca8>

973a: 2d00 cmp r5, #0

973c: d052 beq.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

973e: 7b31 ldrb r1, [r6, #12]

9740: 090b lsrs r3, r1, #4

9742: d060 beq.n 9806 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b72>

9744: 7b73 ldrb r3, [r6, #13]

9746: f003 0303 and.w r3, r3, #3

974a: 3b01 subs r3, #1

974c: 2b01 cmp r3, #1

974e: d95a bls.n 9806 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b72>

9750: f8db 3220 ldr.w r3, [fp, #544] ; 0x220

9754: 429e cmp r6, r3

9756: f000 819b beq.w 9a90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2dfc>

975a: 2d11 cmp r5, #17

975c: bf18 it ne

975e: 2d16 cmpne r5, #22

9760: bf08 it eq

9762: 2301 moveq r3, #1

9764: d007 beq.n 9776 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ae2>

9766: f1a5 0312 sub.w r3, r5, #18

976a: 2d0d cmp r5, #13

976c: bf18 it ne

976e: 2b01 cmpne r3, #1

9770: bf94 ite ls

9772: 2301 movls r3, #1

9774: 2300 movhi r3, #0

9776: 2d14 cmp r5, #20

9778: bf08 it eq

977a: f043 0302 orreq.w r3, r3, #2

977e: d003 beq.n 9788 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2af4>

9780: 2d15 cmp r5, #21

9782: bf08 it eq

9784: f043 0304 orreq.w r3, r3, #4

9788: 6cfa ldr r2, [r7, #76] ; 0x4c

978a: 2100 movs r1, #0

978c: f04f 0c01 mov.w ip, #1

9790: f852 0008 ldr.w r0, [r2, r8]

9794: 6b7a ldr r2, [r7, #52] ; 0x34

9796: f8cb 3224 str.w r3, [fp, #548] ; 0x224

979a: f8cb 6220 str.w r6, [fp, #544] ; 0x220

979e: 4410 add r0, r2

97a0: 9301 str r3, [sp, #4]

97a2: f107 0254 add.w r2, r7, #84 ; 0x54

97a6: 6bbb ldr r3, [r7, #56] ; 0x38

97a8: 9103 str r1, [sp, #12]

97aa: 9100 str r1, [sp, #0]

97ac: 4659 mov r1, fp

97ae: f8cd c008 str.w ip, [sp, #8]

97b2: f7fd ff71 bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

97b6: 6d7b ldr r3, [r7, #84] ; 0x54

97b8: 4602 mov r2, r0

97ba: e9cb 038a strd r0, r3, [fp, #552] ; 0x228

97be: 2b00 cmp r3, #0

97c0: f040 80ba bne.w 9938 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ca4>

97c4: 2d12 cmp r5, #18

97c6: d00d beq.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

97c8: f200 80ab bhi.w 9922 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c8e>

97cc: 2d02 cmp r5, #2

97ce: f000 80be beq.w 994e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cba>

97d2: f240 80a1 bls.w 9918 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c84>

97d6: 2d0d cmp r5, #13

97d8: d10a bne.n 97f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b5c>

97da: 6c3a ldr r2, [r7, #64] ; 0x40

97dc: f8df 3608 ldr.w r3, [pc, #1544] ; 9de8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3154>

97e0: 447b add r3, pc

97e2: 6053 str r3, [r2, #4]

97e4: 6cbb ldr r3, [r7, #72] ; 0x48

97e6: 3408 adds r4, #8

97e8: 42a3 cmp r3, r4

97ea: d895 bhi.n 9718 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2a84>

97ec: f7ff b953 b.w 8a96 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e02>

97f0: 2d11 cmp r5, #17

97f2: f47f acbb bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

97f6: 2a00 cmp r2, #0

97f8: d0f4 beq.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

97fa: f8d2 3248 ldr.w r3, [r2, #584] ; 0x248

97fe: 6c7a ldr r2, [r7, #68] ; 0x44

9800: f842 3009 str.w r3, [r2, r9]

9804: e7ee b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9806: 465a mov r2, fp

9808: 4633 mov r3, r6

980a: 89d8 ldrh r0, [r3, #14]

980c: f64f 7cf1 movw ip, #65521 ; 0xfff1

9810: f001 010f and.w r1, r1, #15

9814: 685b ldr r3, [r3, #4]

9816: 4560 cmp r0, ip

9818: bf14 ite ne

981a: f8d2 a000 ldrne.w sl, [r2]

981e: f04f 0a00 moveq.w sl, #0

9822: 290a cmp r1, #10

9824: 449a add sl, r3

9826: f000 80b3 beq.w 9990 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cfc>

982a: 2d12 cmp r5, #18

982c: f000 80a0 beq.w 9970 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cdc>

9830: d81f bhi.n 9872 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2bde>

9832: 2d02 cmp r5, #2

9834: f000 808c beq.w 9950 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cbc>

9838: d95e bls.n 98f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c64>

983a: 2d0d cmp r5, #13

983c: d1d8 bne.n 97f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b5c>

983e: 6d79 ldr r1, [r7, #84] ; 0x54

9840: 6863 ldr r3, [r4, #4]

9842: 0a1b lsrs r3, r3, #8

9844: bf0a itet eq

9846: 6c7b ldreq r3, [r7, #68] ; 0x44

9848: 684d ldrne r5, [r1, #4]

984a: f853 5009 ldreq.w r5, [r3, r9]

984e: f8d2 3244 ldr.w r3, [r2, #580] ; 0x244

9852: 1c59 adds r1, r3, #1

9854: f000 8283 beq.w 9d5e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30ca>

9858: 2b00 cmp r3, #0

985a: f000 8295 beq.w 9d88 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30f4>

985e: 6c79 ldr r1, [r7, #68] ; 0x44

9860: 442b add r3, r5

9862: f8df 2588 ldr.w r2, [pc, #1416] ; 9dec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3158>

9866: f841 3009 str.w r3, [r1, r9]

986a: 447a add r2, pc

986c: 6c3b ldr r3, [r7, #64] ; 0x40

986e: 605a str r2, [r3, #4]

9870: e7b8 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9872: 2d16 cmp r5, #22

9874: d815 bhi.n 98a2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c0e>

9876: 2d15 cmp r5, #21

9878: d276 bcs.n 9968 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cd4>

987a: 2d13 cmp r5, #19

987c: d120 bne.n 98c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c2c>

987e: 6d79 ldr r1, [r7, #84] ; 0x54

9880: 2900 cmp r1, #0

9882: d0af beq.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9884: f8d2 3244 ldr.w r3, [r2, #580] ; 0x244

9888: 1c58 adds r0, r3, #1

988a: 2801 cmp r0, #1

988c: f240 8273 bls.w 9d76 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30e2>

9890: 6c7d ldr r5, [r7, #68] ; 0x44

9892: 684a ldr r2, [r1, #4]

9894: f855 0009 ldr.w r0, [r5, r9]

9898: 4403 add r3, r0

989a: 4413 add r3, r2

989c: f845 3009 str.w r3, [r5, r9]

98a0: e7a0 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

98a2: 2da0 cmp r5, #160 ; 0xa0

98a4: f47f ac62 bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

98a8: 6c7d ldr r5, [r7, #68] ; 0x44

98aa: 6bf9 ldr r1, [r7, #60] ; 0x3c

98ac: f8db 3000 ldr.w r3, [fp]

98b0: f855 2009 ldr.w r2, [r5, r9]

98b4: 6c08 ldr r0, [r1, #64] ; 0x40

98b6: 4413 add r3, r2

98b8: 4798 blx r3

98ba: f845 0009 str.w r0, [r5, r9]

98be: e791 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

98c0: 2d14 cmp r5, #20

98c2: f47f ac53 bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

98c6: 6d7b ldr r3, [r7, #84] ; 0x54

98c8: 2b00 cmp r3, #0

98ca: d08b beq.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

98cc: 689b ldr r3, [r3, #8]

98ce: 68b2 ldr r2, [r6, #8]

98d0: 4293 cmp r3, r2

98d2: f200 80bb bhi.w 9a4c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2db8>

98d6: f8df 1518 ldr.w r1, [pc, #1304] ; 9df0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x315c>

98da: 4479 add r1, pc

98dc: 6a49 ldr r1, [r1, #36] ; 0x24

98de: 2900 cmp r1, #0

98e0: bf18 it ne

98e2: 4293 cmpne r3, r2

98e4: f0c0 80b2 bcc.w 9a4c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2db8>

98e8: 429a cmp r2, r3

98ea: 4651 mov r1, sl

98ec: 6c38 ldr r0, [r7, #64] ; 0x40

98ee: bf28 it cs

98f0: 461a movcs r2, r3

98f2: f009 ec06 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

98f6: e775 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

98f8: 2d01 cmp r5, #1

98fa: f47f ac37 bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

98fe: 6c7b ldr r3, [r7, #68] ; 0x44

9900: 4651 mov r1, sl

9902: 6c3a ldr r2, [r7, #64] ; 0x40

9904: f10b 0004 add.w r0, fp, #4

9908: f853 3009 ldr.w r3, [r3, r9]

990c: f343 0317 sbfx r3, r3, #0, #24

9910: 009b lsls r3, r3, #2

9912: f7fe fe7f bl 8614 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1980>

9916: e765 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9918: 2d01 cmp r5, #1

991a: f47f ac27 bne.w 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

991e: 469a mov sl, r3

9920: e7ed b.n 98fe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c6a>

9922: 2d16 cmp r5, #22

9924: d8bd bhi.n 98a2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c0e>

9926: 2d15 cmp r5, #21

9928: d21d bcs.n 9966 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cd2>

992a: 2d13 cmp r5, #19

992c: f43f af5a beq.w 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9930: 2d14 cmp r5, #20

9932: f43f af57 beq.w 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9936: e419 b.n 916c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24d8>

9938: 7b19 ldrb r1, [r3, #12]

993a: e766 b.n 980a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b76>

993c: 6c79 ldr r1, [r7, #68] ; 0x44

993e: f8db 2000 ldr.w r2, [fp]

9942: f851 3009 ldr.w r3, [r1, r9]

9946: 4413 add r3, r2

9948: f841 3009 str.w r3, [r1, r9]

994c: e74a b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

994e: 469a mov sl, r3

9950: 68fb ldr r3, [r7, #12]

9952: 459b cmp fp, r3

9954: f000 80d3 beq.w 9afe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e6a>

9958: 6c3b ldr r3, [r7, #64] ; 0x40

995a: 681b ldr r3, [r3, #0]

995c: 449a add sl, r3

995e: 6c7b ldr r3, [r7, #68] ; 0x44

9960: f843 a009 str.w sl, [r3, r9]

9964: e73e b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9966: 469a mov sl, r3

9968: 6c7b ldr r3, [r7, #68] ; 0x44

996a: f843 a009 str.w sl, [r3, r9]

996e: e739 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9970: 6d7b ldr r3, [r7, #84] ; 0x54

9972: 2b00 cmp r3, #0

9974: f43f af36 beq.w 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9978: 6c79 ldr r1, [r7, #68] ; 0x44

997a: 685a ldr r2, [r3, #4]

997c: f851 3009 ldr.w r3, [r1, r9]

9980: 4413 add r3, r2

9982: f841 3009 str.w r3, [r1, r9]

9986: e72d b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9988: 4621 mov r1, r4

998a: 4623 mov r3, r4

998c: f7ff b89e b.w 8acc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e38>

9990: 1c03 adds r3, r0, #0

9992: 69f9 ldr r1, [r7, #28]

9994: bf18 it ne

9996: 2301 movne r3, #1

9998: 2900 cmp r1, #0

999a: bf18 it ne

999c: 2300 movne r3, #0

999e: 2b00 cmp r3, #0

99a0: f43f af43 beq.w 982a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b96>

99a4: f8df 344c ldr.w r3, [pc, #1100] ; 9df4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3160>

99a8: 613a str r2, [r7, #16]

99aa: 447b add r3, pc

99ac: 6c18 ldr r0, [r3, #64] ; 0x40

99ae: 47d0 blx sl

99b0: 2d12 cmp r5, #18

99b2: 4682 mov sl, r0

99b4: d0dc beq.n 9970 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cdc>

99b6: 693a ldr r2, [r7, #16]

99b8: f63f af5b bhi.w 9872 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2bde>

99bc: 2d02 cmp r5, #2

99be: d0c7 beq.n 9950 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cbc>

99c0: d99a bls.n 98f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c64>

99c2: 2d0d cmp r5, #13

99c4: f47f af14 bne.w 97f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b5c>

99c8: 6d79 ldr r1, [r7, #84] ; 0x54

99ca: 2900 cmp r1, #0

99cc: f47f af38 bne.w 9840 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2bac>

99d0: e703 b.n 97da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b46>

99d2: 2a00 cmp r2, #0

99d4: bf0c ite eq

99d6: 2202 moveq r2, #2

99d8: 2203 movne r2, #3

99da: 428a cmp r2, r1

99dc: f47f a8e3 bne.w 8ba6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1f12>

99e0: e49a b.n 9318 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2684>

99e2: f8db 2028 ldr.w r2, [fp, #40] ; 0x28

99e6: f8db 107c ldr.w r1, [fp, #124] ; 0x7c

99ea: f8d2 e004 ldr.w lr, [r2, #4]

99ee: 6849 ldr r1, [r1, #4]

99f0: 6b3a ldr r2, [r7, #48] ; 0x30

99f2: eb01 0c0e add.w ip, r1, lr

99f6: 4584 cmp ip, r0

99f8: bf02 ittt eq

99fa: eba3 030e subeq.w r3, r3, lr

99fe: 65fb streq r3, [r7, #92] ; 0x5c

9a00: 1998 addeq r0, r3, r6

9a02: b92a cbnz r2, 9a10 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2d7c>

9a04: 4281 cmp r1, r0

9a06: bf04 itt eq

9a08: 4473 addeq r3, lr

9a0a: 65fb streq r3, [r7, #92] ; 0x5c

9a0c: f43e afad beq.w 896a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cd6>

9a10: 6b3a ldr r2, [r7, #48] ; 0x30

9a12: e9c7 1e1a strd r1, lr, [r7, #104] ; 0x68

9a16: 677a str r2, [r7, #116] ; 0x74

9a18: f7fe bfa7 b.w 896a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1cd6>

9a1c: f8db 007c ldr.w r0, [fp, #124] ; 0x7c

9a20: f8db 2028 ldr.w r2, [fp, #40] ; 0x28

9a24: 6840 ldr r0, [r0, #4]

9a26: 6852 ldr r2, [r2, #4]

9a28: 1885 adds r5, r0, r2

9a2a: 42a5 cmp r5, r4

9a2c: 6b3d ldr r5, [r7, #48] ; 0x30

9a2e: bf02 ittt eq

9a30: 1a89 subeq r1, r1, r2

9a32: 65f9 streq r1, [r7, #92] ; 0x5c

9a34: 18cc addeq r4, r1, r3

9a36: b92d cbnz r5, 9a44 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2db0>

9a38: 42a0 cmp r0, r4

9a3a: bf04 itt eq

9a3c: 1889 addeq r1, r1, r2

9a3e: 65f9 streq r1, [r7, #92] ; 0x5c

9a40: f43f a84b beq.w 8ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e46>

9a44: e9c7 021a strd r0, r2, [r7, #104] ; 0x68

9a48: f7ff b847 b.w 8ada <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1e46>

9a4c: 4bea ldr r3, [pc, #936] ; (9df8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3164>)

9a4e: f8db 2034 ldr.w r2, [fp, #52] ; 0x34

9a52: 447b add r3, pc

9a54: 681b ldr r3, [r3, #0]

9a56: 6855 ldr r5, [r2, #4]

9a58: 681a ldr r2, [r3, #0]

9a5a: 2a00 cmp r2, #0

9a5c: f000 8166 beq.w 9d2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3098>

9a60: 6cfb ldr r3, [r7, #76] ; 0x4c

9a62: 2002 movs r0, #2

9a64: 49e5 ldr r1, [pc, #916] ; (9dfc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3168>)

9a66: f853 3008 ldr.w r3, [r3, r8]

9a6a: 4479 add r1, pc

9a6c: 442b add r3, r5

9a6e: f002 f9ad bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

9a72: 6d7b ldr r3, [r7, #84] ; 0x54

9a74: 68b2 ldr r2, [r6, #8]

9a76: 689b ldr r3, [r3, #8]

9a78: e736 b.n 98e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2c54>

9a7a: 7b10 ldrb r0, [r2, #12]

9a7c: e474 b.n 9368 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26d4>

9a7e: 4608 mov r0, r1

9a80: 6439 str r1, [r7, #64] ; 0x40

9a82: f7fe fe9d bl 87c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b2c>

9a86: 6c39 ldr r1, [r7, #64] ; 0x40

9a88: f8d1 2244 ldr.w r2, [r1, #580] ; 0x244

9a8c: f7ff b976 b.w 8d7c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x20e8>

9a90: f1a5 030d sub.w r3, r5, #13

9a94: 2b09 cmp r3, #9

9a96: bf96 itet ls

9a98: f240 2271 movwls r2, #625 ; 0x271

9a9c: 2300 movhi r3, #0

9a9e: fa22 f303 lsrls.w r3, r2, r3

9aa2: f8db 2224 ldr.w r2, [fp, #548] ; 0x224

9aa6: bf98 it ls

9aa8: f003 0301 andls.w r3, r3, #1

9aac: 2d14 cmp r5, #20

9aae: f000 8135 beq.w 9d1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3088>

9ab2: 2d15 cmp r5, #21

9ab4: f040 80fe bne.w 9cb4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3020>

9ab8: 2b00 cmp r3, #0

9aba: bf0c ite eq

9abc: 2304 moveq r3, #4

9abe: 2305 movne r3, #5

9ac0: 4293 cmp r3, r2

9ac2: f47f ae50 bne.w 9766 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ad2>

9ac6: 49ce ldr r1, [pc, #824] ; (9e00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x316c>)

9ac8: f8db 322c ldr.w r3, [fp, #556] ; 0x22c

9acc: 4479 add r1, pc

9ace: f8d1 250c ldr.w r2, [r1, #1292] ; 0x50c

9ad2: 657b str r3, [r7, #84] ; 0x54

9ad4: 3201 adds r2, #1

9ad6: f8c1 250c str.w r2, [r1, #1292] ; 0x50c

9ada: f8db 2228 ldr.w r2, [fp, #552] ; 0x228

9ade: e66e b.n 97be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b2a>

9ae0: 4ac8 ldr r2, [pc, #800] ; (9e04 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3170>)

9ae2: 447a add r2, pc

9ae4: e4cf b.n 9486 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x27f2>

9ae6: 4658 mov r0, fp

9ae8: 2201 movs r2, #1

9aea: f7fe feb9 bl 8860 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bcc>

9aee: 2a00 cmp r2, #0

9af0: bf0c ite eq

9af2: 2204 moveq r2, #4

9af4: 2205 movne r2, #5

9af6: 428a cmp r2, r1

9af8: f47f aa02 bne.w 8f00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x226c>

9afc: e579 b.n 95f2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x295e>

9afe: 89f2 ldrh r2, [r6, #14]

9b00: f64f 73f1 movw r3, #65521 ; 0xfff1

9b04: 429a cmp r2, r3

9b06: bf16 itet ne

9b08: 687b ldrne r3, [r7, #4]

9b0a: 2200 moveq r2, #0

9b0c: f8d3 2518 ldrne.w r2, [r3, #1304] ; 0x518

9b10: 6873 ldr r3, [r6, #4]

9b12: ebaa 0a03 sub.w sl, sl, r3

9b16: ebaa 0a02 sub.w sl, sl, r2

9b1a: e71d b.n 9958 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2cc4>

9b1c: 4618 mov r0, r3

9b1e: f006 f9dd bl fedc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x9c>

9b22: 6cfb ldr r3, [r7, #76] ; 0x4c

9b24: f84a 0004 str.w r0, [sl, r4]

9b28: f8c9 3004 str.w r3, [r9, #4]

9b2c: f7fe bf53 b.w 89d6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1d42>

9b30: 2b00 cmp r3, #0

9b32: bf0c ite eq

9b34: 2304 moveq r3, #4

9b36: 2305 movne r3, #5

9b38: 4293 cmp r3, r2

9b3a: f47f ab8b bne.w 9254 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x25c0>

9b3e: e57b b.n 9638 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x29a4>

9b40: f8db 1004 ldr.w r1, [fp, #4]

9b44: 780b ldrb r3, [r1, #0]

9b46: b933 cbnz r3, 9b56 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ec2>

9b48: 4baf ldr r3, [pc, #700] ; (9e08 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3174>)

9b4a: 447b add r3, pc

9b4c: 681b ldr r3, [r3, #0]

9b4e: 6819 ldr r1, [r3, #0]

9b50: b909 cbnz r1, 9b56 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ec2>

9b52: 49ae ldr r1, [pc, #696] ; (9e0c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3178>)

9b54: 4479 add r1, pc

9b56: 6b3b ldr r3, [r7, #48] ; 0x30

9b58: 2b00 cmp r3, #0

9b5a: f000 80af beq.w 9cbc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3028>

9b5e: 4aac ldr r2, [pc, #688] ; (9e10 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x317c>)

9b60: 447a add r2, pc

9b62: 48ac ldr r0, [pc, #688] ; (9e14 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3180>)

9b64: 4478 add r0, pc

9b66: f002 f905 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

9b6a: f7fe bec9 b.w 8900 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c6c>

9b6e: f8bb 115c ldrh.w r1, [fp, #348] ; 0x15c

9b72: f8db 2154 ldr.w r2, [fp, #340] ; 0x154

9b76: eb02 1341 add.w r3, r2, r1, lsl #5

9b7a: 429a cmp r2, r3

9b7c: 4614 mov r4, r2

9b7e: f080 8113 bcs.w 9da8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3114>

9b82: f8df a294 ldr.w sl, [pc, #660] ; 9e18 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3184>

9b86: f246 2940 movw r9, #25152 ; 0x6240

9b8a: 2300 movs r3, #0

9b8c: f2c7 3951 movt r9, #29521 ; 0x7351

9b90: 44fa add sl, pc

9b92: 61bb str r3, [r7, #24]

9b94: 46a8 mov r8, r5

9b96: e004 b.n 9ba2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f0e>

9b98: 3420 adds r4, #32

9b9a: eb02 1341 add.w r3, r2, r1, lsl #5

9b9e: 429c cmp r4, r3

9ba0: d23e bcs.n 9c20 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f8c>

9ba2: 6823 ldr r3, [r4, #0]

9ba4: 2b01 cmp r3, #1

9ba6: d1f7 bne.n 9b98 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f04>

9ba8: 69a3 ldr r3, [r4, #24]

9baa: 079d lsls r5, r3, #30

9bac: d4f4 bmi.n 9b98 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f04>

9bae: f8da 3010 ldr.w r3, [sl, #16]

9bb2: b084 sub sp, #16

9bb4: 68a0 ldr r0, [r4, #8]

9bb6: ad04 add r5, sp, #16

9bb8: 6962 ldr r2, [r4, #20]

9bba: 1e59 subs r1, r3, #1

9bbc: 4401 add r1, r0

9bbe: f1c3 0c00 rsb ip, r3, #0

9bc2: 4411 add r1, r2

9bc4: f8db 2000 ldr.w r2, [fp]

9bc8: ea00 000c and.w r0, r0, ip

9bcc: ea01 010c and.w r1, r1, ip

9bd0: 1883 adds r3, r0, r2

9bd2: 1a09 subs r1, r1, r0

9bd4: 2203 movs r2, #3

9bd6: 6069 str r1, [r5, #4]

9bd8: 4618 mov r0, r3

9bda: 602b str r3, [r5, #0]

9bdc: f008 f908 bl 11df0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1088>

9be0: 2800 cmp r0, #0

9be2: f2c0 80f2 blt.w 9dca <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3136>

9be6: 69a0 ldr r0, [r4, #24]

9be8: 69bb ldr r3, [r7, #24]

9bea: f000 0007 and.w r0, r0, #7

9bee: 61bd str r5, [r7, #24]

9bf0: f8db 2154 ldr.w r2, [fp, #340] ; 0x154

9bf4: 0080 lsls r0, r0, #2

9bf6: 60eb str r3, [r5, #12]

9bf8: fa49 f000 asr.w r0, r9, r0

9bfc: f8bb 115c ldrh.w r1, [fp, #348] ; 0x15c

9c00: f000 000f and.w r0, r0, #15

9c04: 60a8 str r0, [r5, #8]

9c06: e7c7 b.n 9b98 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f04>

9c08: 4618 mov r0, r3

9c0a: 4629 mov r1, r5

9c0c: f006 f966 bl fedc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x9c>

9c10: 6c3a ldr r2, [r7, #64] ; 0x40

9c12: 4b82 ldr r3, [pc, #520] ; (9e1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3188>)

9c14: 447b add r3, pc

9c16: f84a 0009 str.w r0, [sl, r9]

9c1a: 6053 str r3, [r2, #4]

9c1c: f7ff b9b3 b.w 8f86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22f2>

9c20: 4645 mov r5, r8

9c22: f7fe be73 b.w 890c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c78>

9c26: f8db 3028 ldr.w r3, [fp, #40] ; 0x28

9c2a: 2b00 cmp r3, #0

9c2c: f43f a816 beq.w 8c5c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fc8>

9c30: f8db 2070 ldr.w r2, [fp, #112] ; 0x70

9c34: 6858 ldr r0, [r3, #4]

9c36: 6853 ldr r3, [r2, #4]

9c38: 2b07 cmp r3, #7

9c3a: bf0c ite eq

9c3c: 210c moveq r1, #12

9c3e: 2108 movne r1, #8

9c40: f009 fd12 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

9c44: 4601 mov r1, r0

9c46: 2018 movs r0, #24

9c48: f7f6 eed2 blx 9f0 <calloc@plt>

9c4c: f8cb 01a4 str.w r0, [fp, #420] ; 0x1a4

9c50: 2800 cmp r0, #0

9c52: f47f a803 bne.w 8c5c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1fc8>

9c56: 4b72 ldr r3, [pc, #456] ; (9e20 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x318c>)

9c58: 447b add r3, pc

9c5a: 681b ldr r3, [r3, #0]

9c5c: 681a ldr r2, [r3, #0]

9c5e: 2a00 cmp r2, #0

9c60: f000 80a6 beq.w 9db0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x311c>

9c64: 496f ldr r1, [pc, #444] ; (9e24 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3190>)

9c66: 2002 movs r0, #2

9c68: f8db 3004 ldr.w r3, [fp, #4]

9c6c: 4479 add r1, pc

9c6e: f002 f8ad bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

9c72: 207f movs r0, #127 ; 0x7f

9c74: f008 f8e8 bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

9c78: 69f8 ldr r0, [r7, #28]

9c7a: fab0 f080 clz r0, r0

9c7e: 0940 lsrs r0, r0, #5

9c80: f1bc 0f00 cmp.w ip, #0

9c84: bf08 it eq

9c86: 2000 moveq r0, #0

9c88: 2800 cmp r0, #0

9c8a: f43f ab7a beq.w 9382 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26ee>

9c8e: 647b str r3, [r7, #68] ; 0x44

9c90: 6cbb ldr r3, [r7, #72] ; 0x48

9c92: 6852 ldr r2, [r2, #4]

9c94: 6c18 ldr r0, [r3, #64] ; 0x40

9c96: 4411 add r1, r2

9c98: 4788 blx r1

9c9a: 6d79 ldr r1, [r7, #84] ; 0x54

9c9c: 6c7b ldr r3, [r7, #68] ; 0x44

9c9e: 2900 cmp r1, #0

9ca0: f47f ab70 bne.w 9384 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x26f0>

9ca4: f7fe beef b.w 8a86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1df2>

9ca8: 6d79 ldr r1, [r7, #84] ; 0x54

9caa: 2900 cmp r1, #0

9cac: f47f a9c9 bne.w 9042 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x23ae>

9cb0: f7ff b964 b.w 8f7c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x22e8>

9cb4: 4293 cmp r3, r2

9cb6: f47f ad50 bne.w 975a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ac6>

9cba: e704 b.n 9ac6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e32>

9cbc: 4a5a ldr r2, [pc, #360] ; (9e28 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3194>)

9cbe: 447a add r2, pc

9cc0: e74f b.n 9b62 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ece>

9cc2: 4618 mov r0, r3

9cc4: 6439 str r1, [r7, #64] ; 0x40

9cc6: 647b str r3, [r7, #68] ; 0x44

9cc8: f7fe fd28 bl 871c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a88>

9ccc: e9d7 1310 ldrd r1, r3, [r7, #64] ; 0x40

9cd0: 2800 cmp r0, #0

9cd2: f47f af23 bne.w 9b1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e88>

9cd6: f8d3 2244 ldr.w r2, [r3, #580] ; 0x244

9cda: f7ff bb61 b.w 93a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x270c>

9cde: 4608 mov r0, r1

9ce0: 6439 str r1, [r7, #64] ; 0x40

9ce2: f7fe fd6d bl 87c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b2c>

9ce6: 6c39 ldr r1, [r7, #64] ; 0x40

9ce8: 6d7a ldr r2, [r7, #84] ; 0x54

9cea: f8d1 3244 ldr.w r3, [r1, #580] ; 0x244

9cee: e45b b.n 95a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2914>

9cf0: 4618 mov r0, r3

9cf2: 647b str r3, [r7, #68] ; 0x44

9cf4: f7fe fd64 bl 87c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b2c>

9cf8: 6c7b ldr r3, [r7, #68] ; 0x44

9cfa: 6d7e ldr r6, [r7, #84] ; 0x54

9cfc: f8d3 2244 ldr.w r2, [r3, #580] ; 0x244

9d00: f7ff b9cc b.w 909c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2408>

9d04: 4618 mov r0, r3

9d06: 647b str r3, [r7, #68] ; 0x44

9d08: f7fe fd08 bl 871c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a88>

9d0c: 6c7b ldr r3, [r7, #68] ; 0x44

9d0e: 2800 cmp r0, #0

9d10: f47f af7a bne.w 9c08 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2f74>

9d14: f8d3 2244 ldr.w r2, [r3, #580] ; 0x244

9d18: f7ff b9a1 b.w 905e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x23ca>

9d1c: 2b00 cmp r3, #0

9d1e: bf0c ite eq

9d20: 2302 moveq r3, #2

9d22: 2303 movne r3, #3

9d24: 4293 cmp r3, r2

9d26: f47f ad1e bne.w 9766 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2ad2>

9d2a: e6cc b.n 9ac6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2e32>

9d2c: 4a3f ldr r2, [pc, #252] ; (9e2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3198>)

9d2e: 447a add r2, pc

9d30: e696 b.n 9a60 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2dcc>

9d32: f8db 0224 ldr.w r0, [fp, #548] ; 0x224

9d36: 2801 cmp r0, #1

9d38: f47e ae84 bne.w 8a44 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1db0>

9d3c: 493c ldr r1, [pc, #240] ; (9e30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x319c>)

9d3e: f8db 222c ldr.w r2, [fp, #556] ; 0x22c

9d42: 4479 add r1, pc

9d44: f8d1 350c ldr.w r3, [r1, #1292] ; 0x50c

9d48: 657a str r2, [r7, #84] ; 0x54

9d4a: 3301 adds r3, #1

9d4c: f8c1 350c str.w r3, [r1, #1292] ; 0x50c

9d50: f8db 3228 ldr.w r3, [fp, #552] ; 0x228

9d54: f7fe be94 b.w 8a80 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1dec>

9d58: 46a2 mov sl, r4

9d5a: f7ff ba08 b.w 916e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x24da>

9d5e: 4610 mov r0, r2

9d60: 4629 mov r1, r5

9d62: f006 f8bb bl fedc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x9c>

9d66: 6c7a ldr r2, [r7, #68] ; 0x44

9d68: 4b32 ldr r3, [pc, #200] ; (9e34 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31a0>)

9d6a: 447b add r3, pc

9d6c: f842 0009 str.w r0, [r2, r9]

9d70: 6c3a ldr r2, [r7, #64] ; 0x40

9d72: 6053 str r3, [r2, #4]

9d74: e536 b.n 97e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2b50>

9d76: 4610 mov r0, r2

9d78: 643a str r2, [r7, #64] ; 0x40

9d7a: f7fe fd21 bl 87c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1b2c>

9d7e: 6c3a ldr r2, [r7, #64] ; 0x40

9d80: 6d79 ldr r1, [r7, #84] ; 0x54

9d82: f8d2 3244 ldr.w r3, [r2, #580] ; 0x244

9d86: e583 b.n 9890 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2bfc>

9d88: 4610 mov r0, r2

9d8a: 613a str r2, [r7, #16]

9d8c: f7fe fcc6 bl 871c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1a88>

9d90: 693a ldr r2, [r7, #16]

9d92: 2800 cmp r0, #0

9d94: d1e3 bne.n 9d5e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x30ca>

9d96: f8d2 3244 ldr.w r3, [r2, #580] ; 0x244

9d9a: e560 b.n 985e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2bca>

9d9c: 46a1 mov r9, r4

9d9e: 4658 mov r0, fp

9da0: 4649 mov r1, r9

9da2: 2200 movs r2, #0

9da4: f7fe fd5c bl 8860 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bcc>

9da8: 2300 movs r3, #0

9daa: 61bb str r3, [r7, #24]

9dac: f7fe bdae b.w 890c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c78>

9db0: 4a21 ldr r2, [pc, #132] ; (9e38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31a4>)

9db2: 447a add r2, pc

9db4: e756 b.n 9c64 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x2fd0>

9db6: 4b21 ldr r3, [pc, #132] ; (9e3c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31a8>)

9db8: 447b add r3, pc

9dba: 4821 ldr r0, [pc, #132] ; (9e40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31ac>)

9dbc: 2200 movs r2, #0

9dbe: f8db 1004 ldr.w r1, [fp, #4]

9dc2: 4478 add r0, pc

9dc4: 6800 ldr r0, [r0, #0]

9dc6: f7f6 ee20 blx a08 <\_dl\_signal\_error@plt>

9dca: 4b1e ldr r3, [pc, #120] ; (9e44 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31b0>)

9dcc: 447b add r3, pc

9dce: e7f4 b.n 9dba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3126>

9dd0: fa62 0001 ; <UNDEFINED> instruction: 0xfa620001

9dd4: fa1c 0001 ; <UNDEFINED> instruction: 0xfa1c0001

9dd8: af90 add r7, sp, #576 ; 0x240

9dda: 0000 movs r0, r0

9ddc: 0040 lsls r0, r0, #1

9dde: 0000 movs r0, r0

9de0: af3e add r7, sp, #248 ; 0xf8

9de2: 0000 movs r0, r0

9de4: f258 0001 ; <UNDEFINED> instruction: 0xf2580001

9de8: 6975 ldr r5, [r6, #20]

9dea: 0000 movs r0, r0

9dec: 68e7 ldr r7, [r4, #12]

9dee: 0000 movs r0, r0

9df0: f092 0001 eors.w r0, r2, #1

9df4: efc2 0001 vaddl.s8 q8, d2, d1

9df8: fe46 0001 cdp2 0, 4, cr0, cr6, cr1, {0}

9dfc: ca32 ldmia r2!, {r1, r4, r5}

9dfe: 0000 movs r0, r0

9e00: f590 0001 ; <UNDEFINED> instruction: 0xf5900001

9e04: ab06 add r3, sp, #24

9e06: 0000 movs r0, r0

9e08: fd4e 0001 stc2l 0, cr0, [lr, #-4]

9e0c: af84 add r7, sp, #528 ; 0x210

9e0e: 0000 movs r0, r0

9e10: c8c0 ldmia r0!, {r6, r7}

9e12: 0000 movs r0, r0

9e14: c918 ldmia r1!, {r3, r4}

9e16: 0000 movs r0, r0

9e18: eddc 0001 ldcl 0, cr0, [ip, #4]

9e1c: 654d str r5, [r1, #84] ; 0x54

9e1e: 0000 movs r0, r0

9e20: fc40 0001 mcrr2 0, 0, r0, r0, cr1

9e24: c87c ldmia r0!, {r2, r3, r4, r5, r6}

9e26: 0000 movs r0, r0

9e28: b84a ; <UNDEFINED> instruction: 0xb84a

9e2a: 0000 movs r0, r0

9e2c: a8ba add r0, sp, #744 ; 0x2e8

9e2e: 0000 movs r0, r0

9e30: f31a 0001 ; <UNDEFINED> instruction: 0xf31a0001

9e34: 63f7 str r7, [r6, #60] ; 0x3c

9e36: 0000 movs r0, r0

9e38: a836 add r0, sp, #216 ; 0xd8

9e3a: 0000 movs r0, r0

9e3c: c69c stmia r6!, {r2, r3, r4, r7}

9e3e: 0000 movs r0, r0

9e40: fb92 0001 ; <UNDEFINED> instruction: 0xfb920001

9e44: c65c stmia r6!, {r2, r3, r4, r6}

9e46: 0000 movs r0, r0

9e48: b570 push {r4, r5, r6, lr}

9e4a: 4605 mov r5, r0

9e4c: f8d1 41f8 ldr.w r4, [r1, #504] ; 0x1f8

9e50: 2301 movs r3, #1

9e52: f845 1b04 str.w r1, [r5], #4

9e56: f891 2198 ldrb.w r2, [r1, #408] ; 0x198

9e5a: f363 1246 bfi r2, r3, #5, #2

9e5e: f881 2198 strb.w r2, [r1, #408] ; 0x198

9e62: b1ac cbz r4, 9e90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31fc>

9e64: 6861 ldr r1, [r4, #4]

9e66: 4606 mov r6, r0

9e68: 3404 adds r4, #4

9e6a: b189 cbz r1, 9e90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31fc>

9e6c: f891 3198 ldrb.w r3, [r1, #408] ; 0x198

9e70: f013 0f60 tst.w r3, #96 ; 0x60

9e74: d006 beq.n 9e84 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31f0>

9e76: f854 1f04 ldr.w r1, [r4, #4]!

9e7a: 2900 cmp r1, #0

9e7c: d1f6 bne.n 9e6c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31d8>

9e7e: 1bad subs r5, r5, r6

9e80: 10a8 asrs r0, r5, #2

9e82: bd70 pop {r4, r5, r6, pc}

9e84: 4628 mov r0, r5

9e86: f7ff ffdf bl 9e48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31b4>

9e8a: eb05 0580 add.w r5, r5, r0, lsl #2

9e8e: e7f2 b.n 9e76 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31e2>

9e90: 4618 mov r0, r3

9e92: bd70 pop {r4, r5, r6, pc}

9e94: b530 push {r4, r5, lr}

9e96: 4604 mov r4, r0

9e98: 6800 ldr r0, [r0, #0]

9e9a: b083 sub sp, #12

9e9c: 6921 ldr r1, [r4, #16]

9e9e: f890 2198 ldrb.w r2, [r0, #408] ; 0x198

9ea2: 6985 ldr r5, [r0, #24]

9ea4: 0793 lsls r3, r2, #30

9ea6: 68a3 ldr r3, [r4, #8]

9ea8: bf14 ite ne

9eaa: f002 0203 andne.w r2, r2, #3

9eae: 2201 moveq r2, #1

9eb0: e9cd 3500 strd r3, r5, [sp]

9eb4: 6863 ldr r3, [r4, #4]

9eb6: f7fc fab9 bl 642c <free@plt+0x5a0c>

9eba: 6160 str r0, [r4, #20]

9ebc: b003 add sp, #12

9ebe: bd30 pop {r4, r5, pc}

9ec0: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

9ec4: 240c movs r4, #12

9ec6: fb04 f402 mul.w r4, r4, r2

9eca: f2ad 4d7c subw sp, sp, #1148 ; 0x47c

9ece: af00 add r7, sp, #0

9ed0: 4605 mov r5, r0

9ed2: f890 0198 ldrb.w r0, [r0, #408] ; 0x198

9ed6: 2601 movs r6, #1

9ed8: 341f adds r4, #31

9eda: 633d str r5, [r7, #48] ; 0x30

9edc: f024 0407 bic.w r4, r4, #7

9ee0: f366 1046 bfi r0, r6, #5, #2

9ee4: ebad 0d04 sub.w sp, sp, r4

9ee8: f885 0198 strb.w r0, [r5, #408] ; 0x198

9eec: 6b38 ldr r0, [r7, #48] ; 0x30

9eee: 2500 movs r5, #0

9ef0: 46ea mov sl, sp

9ef2: 623b str r3, [r7, #32]

9ef4: f8ca 5000 str.w r5, [sl]

9ef8: f10a 030c add.w r3, sl, #12

9efc: e9ca 0301 strd r0, r3, [sl, #4]

9f00: 2a00 cmp r2, #0

9f02: f000 8378 beq.w a5f6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3962>

9f06: f10a 0318 add.w r3, sl, #24

9f0a: eb01 0c82 add.w ip, r1, r2, lsl #2

9f0e: f851 0b04 ldr.w r0, [r1], #4

9f12: f843 5c0c str.w r5, [r3, #-12]

9f16: 458c cmp ip, r1

9f18: e943 0302 strd r0, r3, [r3, #-8]

9f1c: f103 030c add.w r3, r3, #12

9f20: f890 4198 ldrb.w r4, [r0, #408] ; 0x198

9f24: f366 1446 bfi r4, r6, #5, #2

9f28: f880 4198 strb.w r4, [r0, #408] ; 0x198

9f2c: d1ef bne.n 9f0e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x327a>

9f2e: 1c53 adds r3, r2, #1

9f30: 63bb str r3, [r7, #56] ; 0x38

9f32: f8da 8004 ldr.w r8, [sl, #4]

9f36: 461a mov r2, r3

9f38: 230c movs r3, #12

9f3a: fb03 a302 mla r3, r3, r2, sl

9f3e: 3b0c subs r3, #12

9f40: 637b str r3, [r7, #52] ; 0x34

9f42: f8df 2950 ldr.w r2, [pc, #2384] ; a894 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c00>

9f46: 2000 movs r0, #0

9f48: f8df 394c ldr.w r3, [pc, #2380] ; a898 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c04>

9f4c: f107 0470 add.w r4, r7, #112 ; 0x70

9f50: 447a add r2, pc

9f52: f8df 1948 ldr.w r1, [pc, #2376] ; a89c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c08>

9f56: f502 62a3 add.w r2, r2, #1304 ; 0x518

9f5a: e9c7 2a09 strd r2, sl, [r7, #36] ; 0x24

9f5e: 6b7a ldr r2, [r7, #52] ; 0x34

9f60: 447b add r3, pc

9f62: 4479 add r1, pc

9f64: 4646 mov r6, r8

9f66: f501 61a3 add.w r1, r1, #1304 ; 0x518

9f6a: 613c str r4, [r7, #16]

9f6c: 6090 str r0, [r2, #8]

9f6e: 681a ldr r2, [r3, #0]

9f70: 6039 str r1, [r7, #0]

9f72: 6018 str r0, [r3, #0]

9f74: f44f 6380 mov.w r3, #1024 ; 0x400

9f78: 617a str r2, [r7, #20]

9f7a: 6063 str r3, [r4, #4]

9f7c: f107 0378 add.w r3, r7, #120 ; 0x78

9f80: f8c7 a00c str.w sl, [r7, #12]

9f84: 60bb str r3, [r7, #8]

9f86: 6023 str r3, [r4, #0]

9f88: f8d6 2160 ldr.w r2, [r6, #352] ; 0x160

9f8c: 2301 movs r3, #1

9f8e: 6ab9 ldr r1, [r7, #40] ; 0x28

9f90: 63fa str r2, [r7, #60] ; 0x3c

9f92: 600b str r3, [r1, #0]

9f94: 2a00 cmp r2, #0

9f96: f000 8269 beq.w a46c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x37d8>

9f9a: 2300 movs r3, #0

9f9c: 63fb str r3, [r7, #60] ; 0x3c

9f9e: 6a73 ldr r3, [r6, #36] ; 0x24

9fa0: 2b00 cmp r3, #0

9fa2: d138 bne.n a016 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3382>

9fa4: f8d6 30f4 ldr.w r3, [r6, #244] ; 0xf4

9fa8: bbab cbnz r3, a016 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3382>

9faa: f8d6 30ec ldr.w r3, [r6, #236] ; 0xec

9fae: bb93 cbnz r3, a016 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3382>

9fb0: 469a mov sl, r3

9fb2: 6bfa ldr r2, [r7, #60] ; 0x3c

9fb4: b33a cbz r2, a006 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3372>

9fb6: ea4f 00ca mov.w r0, sl, lsl #3

9fba: 2300 movs r3, #0

9fbc: 300c adds r0, #12

9fbe: f842 302a str.w r3, [r2, sl, lsl #2]

9fc2: f7f6 ed04 blx 9cc <malloc@plt+0x4>

9fc6: ea4f 0a8a mov.w sl, sl, lsl #2

9fca: 4604 mov r4, r0

9fcc: 2800 cmp r0, #0

9fce: f000 8418 beq.w a802 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b6e>

9fd2: f10a 0504 add.w r5, sl, #4

9fd6: 6bf9 ldr r1, [r7, #60] ; 0x3c

9fd8: f840 6b04 str.w r6, [r0], #4

9fdc: f10a 0a08 add.w sl, sl, #8

9fe0: 462a mov r2, r5

9fe2: f009 e88e blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

9fe6: 462a mov r2, r5

9fe8: eb04 000a add.w r0, r4, sl

9fec: 4621 mov r1, r4

9fee: f009 e888 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

9ff2: f3bf 8f5b dmb ish

9ff6: f896 319a ldrb.w r3, [r6, #410] ; 0x19a

9ffa: f8c6 41f8 str.w r4, [r6, #504] ; 0x1f8

9ffe: f043 0301 orr.w r3, r3, #1

a002: f886 319a strb.w r3, [r6, #410] ; 0x19a

a006: 6abb ldr r3, [r7, #40] ; 0x28

a008: 681b ldr r3, [r3, #0]

a00a: 2b00 cmp r3, #0

a00c: f040 82e6 bne.w a5dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3948>

a010: 6abb ldr r3, [r7, #40] ; 0x28

a012: 685e ldr r6, [r3, #4]

a014: e7b8 b.n 9f88 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x32f4>

a016: 6b73 ldr r3, [r6, #52] ; 0x34

a018: f107 0558 add.w r5, r7, #88 ; 0x58

a01c: 68b4 ldr r4, [r6, #8]

a01e: 6a39 ldr r1, [r7, #32]

a020: 685b ldr r3, [r3, #4]

a022: 602e str r6, [r5, #0]

a024: 461a mov r2, r3

a026: 643b str r3, [r7, #64] ; 0x40

a028: 6823 ldr r3, [r4, #0]

a02a: 60ea str r2, [r5, #12]

a02c: f8d7 24a0 ldr.w r2, [r7, #1184] ; 0x4a0

a030: 6069 str r1, [r5, #4]

a032: 60aa str r2, [r5, #8]

a034: 2b00 cmp r3, #0

a036: d0bb beq.n 9fb0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x331c>

a038: f8df 2864 ldr.w r2, [pc, #2148] ; a8a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c0c>

a03c: f04f 0a00 mov.w sl, #0

a040: 647e str r6, [r7, #68] ; 0x44

a042: 447a add r2, pc

a044: 607a str r2, [r7, #4]

a046: f64f 72fd movw r2, #65533 ; 0xfffd

a04a: f6c7 72ff movt r2, #32767 ; 0x7fff

a04e: 61fa str r2, [r7, #28]

a050: 6aba ldr r2, [r7, #40] ; 0x28

a052: 61ba str r2, [r7, #24]

a054: e063 b.n a11e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x348a>

a056: f8df 384c ldr.w r3, [pc, #2124] ; a8a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c10>

a05a: 447b add r3, pc

a05c: 681b ldr r3, [r3, #0]

a05e: 2b00 cmp r3, #0

a060: f040 83a4 bne.w a7ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b18>

a064: 4648 mov r0, r9

a066: f008 fb6b bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

a06a: 6c7b ldr r3, [r7, #68] ; 0x44

a06c: 4602 mov r2, r0

a06e: f8d3 01ac ldr.w r0, [r3, #428] ; 0x1ac

a072: 2800 cmp r0, #0

a074: d061 beq.n a13a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x34a6>

a076: 1c46 adds r6, r0, #1

a078: d072 beq.n a160 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x34cc>

a07a: 62fa str r2, [r7, #44] ; 0x2c

a07c: f008 fb60 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

a080: 6afa ldr r2, [r7, #44] ; 0x2c

a082: 4684 mov ip, r0

a084: f8df b820 ldr.w fp, [pc, #2080] ; a8a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c14>

a088: 4649 mov r1, r9

a08a: 6c78 ldr r0, [r7, #68] ; 0x44

a08c: 44fb add fp, pc

a08e: f8db 300c ldr.w r3, [fp, #12]

a092: 2b17 cmp r3, #23

a094: bf38 it cc

a096: 2317 movcc r3, #23

a098: 4563 cmp r3, ip

a09a: bf38 it cc

a09c: 4663 movcc r3, ip

a09e: 3b04 subs r3, #4

a0a0: fb08 2803 mla r8, r8, r3, r2

a0a4: f108 0807 add.w r8, r8, #7

a0a8: f028 0807 bic.w r8, r8, #7

a0ac: ebad 0d08 sub.w sp, sp, r8

a0b0: 466a mov r2, sp

a0b2: f7fb fde7 bl 5c84 <free@plt+0x5264>

a0b6: 7802 ldrb r2, [r0, #0]

a0b8: 4603 mov r3, r0

a0ba: 2a00 cmp r2, #0

a0bc: d053 beq.n a166 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x34d2>

a0be: f8df 17ec ldr.w r1, [pc, #2028] ; a8ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c18>

a0c2: f107 084c add.w r8, r7, #76 ; 0x4c

a0c6: 462a mov r2, r5

a0c8: 612b str r3, [r5, #16]

a0ca: 4479 add r1, pc

a0cc: 4640 mov r0, r8

a0ce: f7f6 ec76 blx 9bc <\_dl\_catch\_exception@plt>

a0d2: 6d3b ldr r3, [r7, #80] ; 0x50

a0d4: 2b00 cmp r3, #0

a0d6: f040 80aa bne.w a22e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x359a>

a0da: 696a ldr r2, [r5, #20]

a0dc: f892 3198 ldrb.w r3, [r2, #408] ; 0x198

a0e0: f013 0360 ands.w r3, r3, #96 ; 0x60

a0e4: d111 bne.n a10a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3476>

a0e6: 6bb9 ldr r1, [r7, #56] ; 0x38

a0e8: b084 sub sp, #16

a0ea: 2001 movs r0, #1

a0ec: 3101 adds r1, #1

a0ee: 63b9 str r1, [r7, #56] ; 0x38

a0f0: 4669 mov r1, sp

a0f2: e9c1 3200 strd r3, r2, [r1]

a0f6: 608b str r3, [r1, #8]

a0f8: 6b7b ldr r3, [r7, #52] ; 0x34

a0fa: 6379 str r1, [r7, #52] ; 0x34

a0fc: 6099 str r1, [r3, #8]

a0fe: f892 3198 ldrb.w r3, [r2, #408] ; 0x198

a102: f360 1346 bfi r3, r0, #5, #2

a106: f882 3198 strb.w r3, [r2, #408] ; 0x198

a10a: 6bfb ldr r3, [r7, #60] ; 0x3c

a10c: b11b cbz r3, a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a10e: f843 202a str.w r2, [r3, sl, lsl #2]

a112: f10a 0a01 add.w sl, sl, #1

a116: f854 3f08 ldr.w r3, [r4, #8]!

a11a: 2b00 cmp r3, #0

a11c: d031 beq.n a182 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x34ee>

a11e: 2b01 cmp r3, #1

a120: d131 bne.n a186 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x34f2>

a122: 6863 ldr r3, [r4, #4]

a124: 6c3a ldr r2, [r7, #64] ; 0x40

a126: eb02 0903 add.w r9, r2, r3

a12a: 4648 mov r0, r9

a12c: f7fb fd78 bl 5c20 <free@plt+0x5200>

a130: 4680 mov r8, r0

a132: 2800 cmp r0, #0

a134: d18f bne.n a056 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x33c2>

a136: 464b mov r3, r9

a138: e7c1 b.n a0be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x342a>

a13a: 4618 mov r0, r3

a13c: 685b ldr r3, [r3, #4]

a13e: 6a79 ldr r1, [r7, #36] ; 0x24

a140: 781b ldrb r3, [r3, #0]

a142: 4288 cmp r0, r1

a144: bf18 it ne

a146: 2b00 cmpne r3, #0

a148: f040 839a bne.w a880 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3bec>

a14c: 62fa str r2, [r7, #44] ; 0x2c

a14e: f003 faa5 bl d69c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x15c>

a152: 6c7a ldr r2, [r7, #68] ; 0x44

a154: 1e41 subs r1, r0, #1

a156: f8c2 01ac str.w r0, [r2, #428] ; 0x1ac

a15a: 3103 adds r1, #3

a15c: 6afa ldr r2, [r7, #44] ; 0x2c

a15e: d98c bls.n a07a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x33e6>

a160: f04f 0c00 mov.w ip, #0

a164: e78e b.n a084 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x33f0>

a166: f8db 3000 ldr.w r3, [fp]

a16a: 07d9 lsls r1, r3, #31

a16c: d5d3 bpl.n a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a16e: f8df 0740 ldr.w r0, [pc, #1856] ; a8b0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c1c>

a172: 4649 mov r1, r9

a174: 4478 add r0, pc

a176: f001 fdfd bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

a17a: f854 3f08 ldr.w r3, [r4, #8]!

a17e: 2b00 cmp r3, #0

a180: d1cd bne.n a11e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x348a>

a182: 6c7e ldr r6, [r7, #68] ; 0x44

a184: e715 b.n 9fb2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x331e>

a186: 69fa ldr r2, [r7, #28]

a188: f023 0302 bic.w r3, r3, #2

a18c: 4293 cmp r3, r2

a18e: d1c2 bne.n a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a190: 6863 ldr r3, [r4, #4]

a192: 6c3a ldr r2, [r7, #64] ; 0x40

a194: eb02 0b03 add.w fp, r2, r3

a198: 4658 mov r0, fp

a19a: f7fb fd41 bl 5c20 <free@plt+0x5200>

a19e: 4681 mov r9, r0

a1a0: 2800 cmp r0, #0

a1a2: f000 8180 beq.w a4a6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3812>

a1a6: f8df 370c ldr.w r3, [pc, #1804] ; a8b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c20>

a1aa: 447b add r3, pc

a1ac: 681b ldr r3, [r3, #0]

a1ae: 2b00 cmp r3, #0

a1b0: f040 8357 bne.w a862 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3bce>

a1b4: 4658 mov r0, fp

a1b6: f008 fac3 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

a1ba: 6c7b ldr r3, [r7, #68] ; 0x44

a1bc: 4606 mov r6, r0

a1be: f8d3 01ac ldr.w r0, [r3, #428] ; 0x1ac

a1c2: 2800 cmp r0, #0

a1c4: f000 81f4 beq.w a5b0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x391c>

a1c8: 1c42 adds r2, r0, #1

a1ca: f000 8203 beq.w a5d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3940>

a1ce: f008 fab7 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

a1d2: 4602 mov r2, r0

a1d4: f8df 86e0 ldr.w r8, [pc, #1760] ; a8b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c24>

a1d8: 4659 mov r1, fp

a1da: 6c78 ldr r0, [r7, #68] ; 0x44

a1dc: 44f8 add r8, pc

a1de: f8d8 300c ldr.w r3, [r8, #12]

a1e2: 2b17 cmp r3, #23

a1e4: bf38 it cc

a1e6: 2317 movcc r3, #23

a1e8: 4293 cmp r3, r2

a1ea: bf38 it cc

a1ec: 4613 movcc r3, r2

a1ee: 3b04 subs r3, #4

a1f0: fb09 6303 mla r3, r9, r3, r6

a1f4: 3307 adds r3, #7

a1f6: f023 0307 bic.w r3, r3, #7

a1fa: ebad 0d03 sub.w sp, sp, r3

a1fe: 466a mov r2, sp

a200: f7fb fd40 bl 5c84 <free@plt+0x5264>

a204: 7802 ldrb r2, [r0, #0]

a206: 2a00 cmp r2, #0

a208: f040 814c bne.w a4a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3810>

a20c: 6823 ldr r3, [r4, #0]

a20e: 69f9 ldr r1, [r7, #28]

a210: 428b cmp r3, r1

a212: f000 82dc beq.w a7ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b3a>

a216: f8d8 3000 ldr.w r3, [r8]

a21a: 07db lsls r3, r3, #31

a21c: f57f af7b bpl.w a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a220: f8df 0698 ldr.w r0, [pc, #1688] ; a8bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c28>

a224: 4659 mov r1, fp

a226: 4478 add r0, pc

a228: f001 fda4 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

a22c: e773 b.n a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a22e: 63f8 str r0, [r7, #60] ; 0x3c

a230: f8d7 a00c ldr.w sl, [r7, #12]

a234: 2800 cmp r0, #0

a236: f000 8115 beq.w a464 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x37d0>

a23a: 693b ldr r3, [r7, #16]

a23c: 6818 ldr r0, [r3, #0]

a23e: 68bb ldr r3, [r7, #8]

a240: 4298 cmp r0, r3

a242: d001 beq.n a248 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35b4>

a244: f7f6 ebee blx a24 <free@plt+0x4>

a248: f8df 2674 ldr.w r2, [pc, #1652] ; a8c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c2c>

a24c: 697b ldr r3, [r7, #20]

a24e: 447a add r2, pc

a250: 3300 adds r3, #0

a252: 6811 ldr r1, [r2, #0]

a254: bf18 it ne

a256: 2301 movne r3, #1

a258: 2900 cmp r1, #0

a25a: bf18 it ne

a25c: 2300 movne r3, #0

a25e: 2b00 cmp r3, #0

a260: f040 81a3 bne.w a5aa <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3916>

a264: 6b3a ldr r2, [r7, #48] ; 0x30

a266: f8d2 31f8 ldr.w r3, [r2, #504] ; 0x1f8

a26a: 643b str r3, [r7, #64] ; 0x40

a26c: b14b cbz r3, a282 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35ee>

a26e: f892 3198 ldrb.w r3, [r2, #408] ; 0x198

a272: f003 0303 and.w r3, r3, #3

a276: 2b02 cmp r3, #2

a278: bf1c itt ne

a27a: 2300 movne r3, #0

a27c: 643b strne r3, [r7, #64] ; 0x40

a27e: f000 8185 beq.w a58c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x38f8>

a282: 6bbb ldr r3, [r7, #56] ; 0x38

a284: 00d8 lsls r0, r3, #3

a286: 3004 adds r0, #4

a288: f7f6 eba0 blx 9cc <malloc@plt+0x4>

a28c: 6478 str r0, [r7, #68] ; 0x44

a28e: 2800 cmp r0, #0

a290: f000 82ee beq.w a870 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3bdc>

a294: 6bbb ldr r3, [r7, #56] ; 0x38

a296: 2400 movs r4, #0

a298: 6b3a ldr r2, [r7, #48] ; 0x30

a29a: 1c5e adds r6, r3, #1

a29c: 6a38 ldr r0, [r7, #32]

a29e: f8c2 3164 str.w r3, [r2, #356] ; 0x164

a2a2: 6c7b ldr r3, [r7, #68] ; 0x44

a2a4: eb03 0686 add.w r6, r3, r6, lsl #2

a2a8: f8c2 6160 str.w r6, [r2, #352] ; 0x160

a2ac: 4652 mov r2, sl

a2ae: 6853 ldr r3, [r2, #4]

a2b0: 2800 cmp r0, #0

a2b2: f040 80cb bne.w a44c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x37b8>

a2b6: f846 3024 str.w r3, [r6, r4, lsl #2]

a2ba: 3401 adds r4, #1

a2bc: 6853 ldr r3, [r2, #4]

a2be: f893 1198 ldrb.w r1, [r3, #408] ; 0x198

a2c2: f36f 1146 bfc r1, #5, #2

a2c6: f883 1198 strb.w r1, [r3, #408] ; 0x198

a2ca: 6892 ldr r2, [r2, #8]

a2cc: 2a00 cmp r2, #0

a2ce: d1ee bne.n a2ae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x361a>

a2d0: f8df 35f0 ldr.w r3, [pc, #1520] ; a8c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c30>

a2d4: 4692 mov sl, r2

a2d6: 447b add r3, pc

a2d8: 681b ldr r3, [r3, #0]

a2da: 0519 lsls r1, r3, #20

a2dc: f100 8191 bmi.w a602 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x396e>

a2e0: 6833 ldr r3, [r6, #0]

a2e2: 6b3a ldr r2, [r7, #48] ; 0x30

a2e4: 4293 cmp r3, r2

a2e6: f040 8282 bne.w a7ee <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b5a>

a2ea: 6b3b ldr r3, [r7, #48] ; 0x30

a2ec: 00a2 lsls r2, r4, #2

a2ee: 637a str r2, [r7, #52] ; 0x34

a2f0: f8d3 91fc ldr.w r9, [r3, #508] ; 0x1fc

a2f4: 6c7b ldr r3, [r7, #68] ; 0x44

a2f6: 3304 adds r3, #4

a2f8: 62fb str r3, [r7, #44] ; 0x2c

a2fa: 1e63 subs r3, r4, #1

a2fc: 63bb str r3, [r7, #56] ; 0x38

a2fe: f1b9 0f00 cmp.w r9, #0

a302: f000 8127 beq.w a554 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x38c0>

a306: 2c01 cmp r4, #1

a308: 46cb mov fp, r9

a30a: f85b 5b04 ldr.w r5, [fp], #4

a30e: f240 81ef bls.w a6f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a5c>

a312: 1d33 adds r3, r6, #4

a314: eb06 0c02 add.w ip, r6, r2

a318: 2001 movs r0, #1

a31a: f853 2b04 ldr.w r2, [r3], #4

a31e: 459c cmp ip, r3

a320: f892 1198 ldrb.w r1, [r2, #408] ; 0x198

a324: f360 1146 bfi r1, r0, #5, #2

a328: f882 1198 strb.w r1, [r2, #408] ; 0x198

a32c: d1f5 bne.n a31a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3686>

a32e: 2d00 cmp r5, #0

a330: d04a beq.n a3c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3734>

a332: e9c7 6409 strd r6, r4, [r7, #36] ; 0x24

a336: 2500 movs r5, #0

a338: 6b3c ldr r4, [r7, #48] ; 0x30

a33a: e004 b.n a346 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x36b2>

a33c: f8d9 3000 ldr.w r3, [r9]

a340: 4645 mov r5, r8

a342: 4598 cmp r8, r3

a344: d23c bcs.n a3c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x372c>

a346: f85b 2025 ldr.w r2, [fp, r5, lsl #2]

a34a: 00ae lsls r6, r5, #2

a34c: f105 0801 add.w r8, r5, #1

a350: f892 2198 ldrb.w r2, [r2, #408] ; 0x198

a354: f012 0f60 tst.w r2, #96 ; 0x60

a358: d0f0 beq.n a33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x36a8>

a35a: f8d4 0200 ldr.w r0, [r4, #512] ; 0x200

a35e: 3001 adds r0, #1

a360: 0080 lsls r0, r0, #2

a362: f7f6 eb34 blx 9cc <malloc@plt+0x4>

a366: 4682 mov sl, r0

a368: 2800 cmp r0, #0

a36a: d0e7 beq.n a33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x36a8>

a36c: 4632 mov r2, r6

a36e: 4659 mov r1, fp

a370: 3004 adds r0, #4

a372: f008 eec6 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

a376: f8d9 c000 ldr.w ip, [r9]

a37a: 45e0 cmp r8, ip

a37c: f080 812c bcs.w a5d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3944>

a380: 1d33 adds r3, r6, #4

a382: eb09 0e8c add.w lr, r9, ip, lsl #2

a386: 444b add r3, r9

a388: 4628 mov r0, r5

a38a: f853 2f04 ldr.w r2, [r3, #4]!

a38e: f892 1198 ldrb.w r1, [r2, #408] ; 0x198

a392: f011 0f60 tst.w r1, #96 ; 0x60

a396: bf02 ittt eq

a398: eb0a 0180 addeq.w r1, sl, r0, lsl #2

a39c: 3001 addeq r0, #1

a39e: 604a streq r2, [r1, #4]

a3a0: 459e cmp lr, r3

a3a2: d1f2 bne.n a38a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x36f6>

a3a4: f10c 3cff add.w ip, ip, #4294967295 ; 0xffffffff

a3a8: 44e0 add r8, ip

a3aa: eba8 0805 sub.w r8, r8, r5

a3ae: f8ca 0000 str.w r0, [sl]

a3b2: f108 0801 add.w r8, r8, #1

a3b6: f8d9 3000 ldr.w r3, [r9]

a3ba: 4645 mov r5, r8

a3bc: 4598 cmp r8, r3

a3be: d3c2 bcc.n a346 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x36b2>

a3c0: e9d7 6409 ldrd r6, r4, [r7, #36] ; 0x24

a3c4: 2c01 cmp r4, #1

a3c6: d90c bls.n a3e2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x374e>

a3c8: 4630 mov r0, r6

a3ca: 2301 movs r3, #1

a3cc: f850 2f04 ldr.w r2, [r0, #4]!

a3d0: 3301 adds r3, #1

a3d2: 42a3 cmp r3, r4

a3d4: f892 1198 ldrb.w r1, [r2, #408] ; 0x198

a3d8: f36f 1146 bfc r1, #5, #2

a3dc: f882 1198 strb.w r1, [r2, #408] ; 0x198

a3e0: d3f4 bcc.n a3cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3738>

a3e2: 6c7c ldr r4, [r7, #68] ; 0x44

a3e4: 4631 mov r1, r6

a3e6: 6b7d ldr r5, [r7, #52] ; 0x34

a3e8: 4620 mov r0, r4

a3ea: 462a mov r2, r5

a3ec: f008 ee88 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

a3f0: 2300 movs r3, #0

a3f2: 461a mov r2, r3

a3f4: 6bb9 ldr r1, [r7, #56] ; 0x38

a3f6: 6af8 ldr r0, [r7, #44] ; 0x2c

a3f8: f004 ff9c bl f334 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x14>

a3fc: 2300 movs r3, #0

a3fe: 5163 str r3, [r4, r5]

a400: f3bf 8f5b dmb ish

a404: 6b3a ldr r2, [r7, #48] ; 0x30

a406: 4620 mov r0, r4

a408: f892 319a ldrb.w r3, [r2, #410] ; 0x19a

a40c: 4611 mov r1, r2

a40e: f8c1 41f8 str.w r4, [r1, #504] ; 0x1f8

a412: f043 0301 orr.w r3, r3, #1

a416: f882 319a strb.w r3, [r2, #410] ; 0x19a

a41a: f1ba 0f00 cmp.w sl, #0

a41e: d007 beq.n a430 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x379c>

a420: f3bf 8f5b dmb ish

a424: f8d2 01fc ldr.w r0, [r2, #508] ; 0x1fc

a428: f8c2 a1fc str.w sl, [r2, #508] ; 0x1fc

a42c: f003 f9bc bl d7a8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x268>

a430: 6c3b ldr r3, [r7, #64] ; 0x40

a432: b113 cbz r3, a43a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x37a6>

a434: 4618 mov r0, r3

a436: f003 f9b7 bl d7a8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x268>

a43a: 6bfb ldr r3, [r7, #60] ; 0x3c

a43c: 2b00 cmp r3, #0

a43e: f040 81cc bne.w a7da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b46>

a442: f207 477c addw r7, r7, #1148 ; 0x47c

a446: 46bd mov sp, r7

a448: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

a44c: f893 1199 ldrb.w r1, [r3, #409] ; 0x199

a450: 078d lsls r5, r1, #30

a452: f57f af30 bpl.w a2b6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3622>

a456: 6b3d ldr r5, [r7, #48] ; 0x30

a458: f8d5 1164 ldr.w r1, [r5, #356] ; 0x164

a45c: 3901 subs r1, #1

a45e: f8c5 1164 str.w r1, [r5, #356] ; 0x164

a462: e72c b.n a2be <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x362a>

a464: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

a468: 63fb str r3, [r7, #60] ; 0x3c

a46a: e6e6 b.n a23a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35a6>

a46c: 6b3b ldr r3, [r7, #48] ; 0x30

a46e: f8d6 21f8 ldr.w r2, [r6, #504] ; 0x1f8

a472: 1b9b subs r3, r3, r6

a474: bf18 it ne

a476: 2301 movne r3, #1

a478: 2a00 cmp r2, #0

a47a: bf18 it ne

a47c: 2300 movne r3, #0

a47e: 2b00 cmp r3, #0

a480: f43f ad8d beq.w 9f9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x330a>

a484: f8b6 115e ldrh.w r1, [r6, #350] ; 0x15e

a488: 2900 cmp r1, #0

a48a: f43f ad88 beq.w 9f9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x330a>

a48e: 2204 movs r2, #4

a490: 6938 ldr r0, [r7, #16]

a492: f007 fc2f bl 11cf4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf8c>

a496: 2800 cmp r0, #0

a498: f000 81ca beq.w a830 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b9c>

a49c: 693b ldr r3, [r7, #16]

a49e: 681b ldr r3, [r3, #0]

a4a0: 63fb str r3, [r7, #60] ; 0x3c

a4a2: e57c b.n 9f9e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x330a>

a4a4: 4683 mov fp, r0

a4a6: 687b ldr r3, [r7, #4]

a4a8: f8c5 b010 str.w fp, [r5, #16]

a4ac: 681b ldr r3, [r3, #0]

a4ae: 07de lsls r6, r3, #31

a4b0: f100 8167 bmi.w a782 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3aee>

a4b4: f107 084c add.w r8, r7, #76 ; 0x4c

a4b8: f8df 140c ldr.w r1, [pc, #1036] ; a8c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c34>

a4bc: 462a mov r2, r5

a4be: 4479 add r1, pc

a4c0: 4640 mov r0, r8

a4c2: f7f6 ea7c blx 9bc <\_dl\_catch\_exception@plt>

a4c6: f8d7 c050 ldr.w ip, [r7, #80] ; 0x50

a4ca: f1bc 0f00 cmp.w ip, #0

a4ce: f040 814f bne.w a770 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3adc>

a4d2: 69be ldr r6, [r7, #24]

a4d4: b084 sub sp, #16

a4d6: 696b ldr r3, [r5, #20]

a4d8: 46e8 mov r8, sp

a4da: 6830 ldr r0, [r6, #0]

a4dc: 46c6 mov lr, r8

a4de: 6871 ldr r1, [r6, #4]

a4e0: 68b2 ldr r2, [r6, #8]

a4e2: e8ae 0007 stmia.w lr!, {r0, r1, r2}

a4e6: 6bfa ldr r2, [r7, #60] ; 0x3c

a4e8: f8c6 c000 str.w ip, [r6]

a4ec: 6073 str r3, [r6, #4]

a4ee: b122 cbz r2, a4fa <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3866>

a4f0: f842 302a str.w r3, [r2, sl, lsl #2]

a4f4: f10a 0a01 add.w sl, sl, #1

a4f8: 696b ldr r3, [r5, #20]

a4fa: f893 2198 ldrb.w r2, [r3, #408] ; 0x198

a4fe: f012 0f60 tst.w r2, #96 ; 0x60

a502: f040 80e3 bne.w a6cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a38>

a506: 6bba ldr r2, [r7, #56] ; 0x38

a508: 2001 movs r0, #1

a50a: 6919 ldr r1, [r3, #16]

a50c: 3201 adds r2, #1

a50e: 63ba str r2, [r7, #56] ; 0x38

a510: 69ba ldr r2, [r7, #24]

a512: f8c2 8008 str.w r8, [r2, #8]

a516: f893 2198 ldrb.w r2, [r3, #408] ; 0x198

a51a: f360 1246 bfi r2, r0, #5, #2

a51e: f883 2198 strb.w r2, [r3, #408] ; 0x198

a522: 68da ldr r2, [r3, #12]

a524: b109 cbz r1, a52a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3896>

a526: 60ca str r2, [r1, #12]

a528: 68da ldr r2, [r3, #12]

a52a: b102 cbz r2, a52e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x389a>

a52c: 6111 str r1, [r2, #16]

a52e: f8d8 2004 ldr.w r2, [r8, #4]

a532: 6911 ldr r1, [r2, #16]

a534: 6119 str r1, [r3, #16]

a536: 6113 str r3, [r2, #16]

a538: 6919 ldr r1, [r3, #16]

a53a: b101 cbz r1, a53e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x38aa>

a53c: 60cb str r3, [r1, #12]

a53e: 60da str r2, [r3, #12]

a540: 6b7b ldr r3, [r7, #52] ; 0x34

a542: 69ba ldr r2, [r7, #24]

a544: 4293 cmp r3, r2

a546: bf11 iteee ne

a548: f8c7 8018 strne.w r8, [r7, #24]

a54c: 4643 moveq r3, r8

a54e: 61bb streq r3, [r7, #24]

a550: 637b streq r3, [r7, #52] ; 0x34

a552: e5e0 b.n a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a554: 6c7d ldr r5, [r7, #68] ; 0x44

a556: 4631 mov r1, r6

a558: 6b7a ldr r2, [r7, #52] ; 0x34

a55a: 4628 mov r0, r5

a55c: f008 edd0 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

a560: 464b mov r3, r9

a562: 464a mov r2, r9

a564: 6bb9 ldr r1, [r7, #56] ; 0x38

a566: 6af8 ldr r0, [r7, #44] ; 0x2c

a568: f004 fee4 bl f334 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x14>

a56c: f845 9024 str.w r9, [r5, r4, lsl #2]

a570: f3bf 8f5b dmb ish

a574: 4628 mov r0, r5

a576: 6b3a ldr r2, [r7, #48] ; 0x30

a578: f892 319a ldrb.w r3, [r2, #410] ; 0x19a

a57c: 4611 mov r1, r2

a57e: f8c1 51f8 str.w r5, [r1, #504] ; 0x1f8

a582: f043 0301 orr.w r3, r3, #1

a586: f882 319a strb.w r3, [r2, #410] ; 0x19a

a58a: e751 b.n a430 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x379c>

a58c: f8d2 3160 ldr.w r3, [r2, #352] ; 0x160

a590: 2b00 cmp r3, #0

a592: f43f ae76 beq.w a282 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35ee>

a596: 4bcd ldr r3, [pc, #820] ; (a8cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c38>)

a598: f44f 72ec mov.w r2, #472 ; 0x1d8

a59c: 49cc ldr r1, [pc, #816] ; (a8d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c3c>)

a59e: 48cd ldr r0, [pc, #820] ; (a8d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c40>)

a5a0: 447b add r3, pc

a5a2: 4479 add r1, pc

a5a4: 4478 add r0, pc

a5a6: f006 f9f1 bl 1098c <free@@GLIBC\_2.4+0x12c>

a5aa: 697b ldr r3, [r7, #20]

a5ac: 6013 str r3, [r2, #0]

a5ae: e659 b.n a264 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35d0>

a5b0: 4619 mov r1, r3

a5b2: 685b ldr r3, [r3, #4]

a5b4: 683a ldr r2, [r7, #0]

a5b6: 781b ldrb r3, [r3, #0]

a5b8: 4291 cmp r1, r2

a5ba: bf18 it ne

a5bc: 2b00 cmpne r3, #0

a5be: f040 80fc bne.w a7ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b26>

a5c2: f003 f86b bl d69c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x15c>

a5c6: 6c79 ldr r1, [r7, #68] ; 0x44

a5c8: 1e42 subs r2, r0, #1

a5ca: f8c1 01ac str.w r0, [r1, #428] ; 0x1ac

a5ce: 3203 adds r2, #3

a5d0: f67f adfd bls.w a1ce <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x353a>

a5d4: 2200 movs r2, #0

a5d6: e5fd b.n a1d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3540>

a5d8: 4628 mov r0, r5

a5da: e6e8 b.n a3ae <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x371a>

a5dc: 6abb ldr r3, [r7, #40] ; 0x28

a5de: e003 b.n a5e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3954>

a5e0: 681a ldr r2, [r3, #0]

a5e2: 2a00 cmp r2, #0

a5e4: f000 80bb beq.w a75e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3aca>

a5e8: 689b ldr r3, [r3, #8]

a5ea: 2b00 cmp r3, #0

a5ec: d1f8 bne.n a5e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x394c>

a5ee: f8d7 a00c ldr.w sl, [r7, #12]

a5f2: 63fb str r3, [r7, #60] ; 0x3c

a5f4: e621 b.n a23a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x35a6>

a5f6: 63be str r6, [r7, #56] ; 0x38

a5f8: f8d7 8030 ldr.w r8, [r7, #48] ; 0x30

a5fc: f8c7 a034 str.w sl, [r7, #52] ; 0x34

a600: e49f b.n 9f42 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x32ae>

a602: 4bb5 ldr r3, [pc, #724] ; (a8d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c44>)

a604: 6b3a ldr r2, [r7, #48] ; 0x30

a606: 447b add r3, pc

a608: 681b ldr r3, [r3, #0]

a60a: 4293 cmp r3, r2

a60c: f47f ae68 bne.w a2e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x364c>

a610: 2c00 cmp r4, #0

a612: f43f ae65 beq.w a2e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x364c>

a616: 46d0 mov r8, sl

a618: f8c7 a038 str.w sl, [r7, #56] ; 0x38

a61c: f8d7 9044 ldr.w r9, [r7, #68] ; 0x44

a620: 4693 mov fp, r2

a622: e004 b.n a62e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x399a>

a624: f108 0801 add.w r8, r8, #1

a628: 45a0 cmp r8, r4

a62a: f000 80b9 beq.w a7a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b0c>

a62e: f856 5028 ldr.w r5, [r6, r8, lsl #2]

a632: 45ab cmp fp, r5

a634: d0f6 beq.n a624 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3990>

a636: f8d5 31d4 ldr.w r3, [r5, #468] ; 0x1d4

a63a: b113 cbz r3, a642 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x39ae>

a63c: 685b ldr r3, [r3, #4]

a63e: 2b00 cmp r3, #0

a640: d1f0 bne.n a624 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3990>

a642: f8d5 30f4 ldr.w r3, [r5, #244] ; 0xf4

a646: 2b00 cmp r3, #0

a648: f040 80fa bne.w a840 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3bac>

a64c: f8d5 a0ec ldr.w sl, [r5, #236] ; 0xec

a650: f1ba 0f00 cmp.w sl, #0

a654: f040 80f4 bne.w a840 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3bac>

a658: 4629 mov r1, r5

a65a: 4648 mov r0, r9

a65c: f7ff fbf4 bl 9e48 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x31b4>

a660: 42a0 cmp r0, r4

a662: 4606 mov r6, r0

a664: f200 80f3 bhi.w a84e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3bba>

a668: b1c8 cbz r0, a69e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a0a>

a66a: 464b mov r3, r9

a66c: eb09 0080 add.w r0, r9, r0, lsl #2

a670: f853 1b04 ldr.w r1, [r3], #4

a674: f891 c198 ldrb.w ip, [r1, #408] ; 0x198

a678: f36a 1c46 bfi ip, sl, #5, #2

a67c: f881 c198 strb.w ip, [r1, #408] ; 0x198

a680: e00b b.n a69a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a06>

a682: 681a ldr r2, [r3, #0]

a684: f892 1198 ldrb.w r1, [r2, #408] ; 0x198

a688: f36f 1146 bfc r1, #5, #2

a68c: f882 1198 strb.w r1, [r2, #408] ; 0x198

a690: f853 2b04 ldr.w r2, [r3], #4

a694: 6e12 ldr r2, [r2, #96] ; 0x60

a696: 2a00 cmp r2, #0

a698: d163 bne.n a762 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ace>

a69a: 4298 cmp r0, r3

a69c: d1f1 bne.n a682 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x39ee>

a69e: 1cb2 adds r2, r6, #2

a6a0: ea4f 0a82 mov.w sl, r2, lsl #2

a6a4: 4650 mov r0, sl

a6a6: f7f6 e992 blx 9cc <malloc@plt+0x4>

a6aa: 4603 mov r3, r0

a6ac: f8c5 01d4 str.w r0, [r5, #468] ; 0x1d4

a6b0: 2800 cmp r0, #0

a6b2: f000 80b5 beq.w a820 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b8c>

a6b6: 3008 adds r0, #8

a6b8: 605e str r6, [r3, #4]

a6ba: f1aa 0208 sub.w r2, sl, #8

a6be: 4649 mov r1, r9

a6c0: 6018 str r0, [r3, #0]

a6c2: f008 ed1e blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

a6c6: f8db 6160 ldr.w r6, [fp, #352] ; 0x160

a6ca: e7ab b.n a624 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3990>

a6cc: 4641 mov r1, r8

a6ce: 6c7e ldr r6, [r7, #68] ; 0x44

a6d0: e003 b.n a6da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a46>

a6d2: 6850 ldr r0, [r2, #4]

a6d4: 4298 cmp r0, r3

a6d6: d029 beq.n a72c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a98>

a6d8: 4611 mov r1, r2

a6da: 688a ldr r2, [r1, #8]

a6dc: 2a00 cmp r2, #0

a6de: d1f8 bne.n a6d2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a3e>

a6e0: e8b8 0007 ldmia.w r8!, {r0, r1, r2}

a6e4: 69bb ldr r3, [r7, #24]

a6e6: 647e str r6, [r7, #68] ; 0x44

a6e8: 6018 str r0, [r3, #0]

a6ea: 6059 str r1, [r3, #4]

a6ec: 609a str r2, [r3, #8]

a6ee: e512 b.n a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a6f0: 2d00 cmp r5, #0

a6f2: f47f ae1e bne.w a332 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x369e>

a6f6: 6c7c ldr r4, [r7, #68] ; 0x44

a6f8: 4631 mov r1, r6

a6fa: 6b7e ldr r6, [r7, #52] ; 0x34

a6fc: 4620 mov r0, r4

a6fe: 4632 mov r2, r6

a700: f008 ecfe blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

a704: 462b mov r3, r5

a706: 462a mov r2, r5

a708: 6af8 ldr r0, [r7, #44] ; 0x2c

a70a: 6bb9 ldr r1, [r7, #56] ; 0x38

a70c: f004 fe12 bl f334 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x14>

a710: 51a5 str r5, [r4, r6]

a712: f3bf 8f5b dmb ish

a716: 4620 mov r0, r4

a718: 6b3a ldr r2, [r7, #48] ; 0x30

a71a: f892 319a ldrb.w r3, [r2, #410] ; 0x19a

a71e: f8c2 41f8 str.w r4, [r2, #504] ; 0x1f8

a722: f043 0301 orr.w r3, r3, #1

a726: f882 319a strb.w r3, [r2, #410] ; 0x19a

a72a: e681 b.n a430 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x379c>

a72c: 69ba ldr r2, [r7, #24]

a72e: 647e str r6, [r7, #68] ; 0x44

a730: 6918 ldr r0, [r3, #16]

a732: f8c2 8008 str.w r8, [r2, #8]

a736: 6b7a ldr r2, [r7, #52] ; 0x34

a738: f8d1 c008 ldr.w ip, [r1, #8]

a73c: 4616 mov r6, r2

a73e: 4594 cmp ip, r2

a740: bf08 it eq

a742: 460e moveq r6, r1

a744: f8dc 2008 ldr.w r2, [ip, #8]

a748: 637e str r6, [r7, #52] ; 0x34

a74a: 608a str r2, [r1, #8]

a74c: 68da ldr r2, [r3, #12]

a74e: b108 cbz r0, a754 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ac0>

a750: 60c2 str r2, [r0, #12]

a752: 68da ldr r2, [r3, #12]

a754: 2a00 cmp r2, #0

a756: f43f aeea beq.w a52e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x389a>

a75a: 6110 str r0, [r2, #16]

a75c: e6e7 b.n a52e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x389a>

a75e: 62bb str r3, [r7, #40] ; 0x28

a760: e456 b.n a010 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x337c>

a762: f895 2199 ldrb.w r2, [r5, #409] ; 0x199

a766: f042 0280 orr.w r2, r2, #128 ; 0x80

a76a: f885 2199 strb.w r2, [r5, #409] ; 0x199

a76e: e794 b.n a69a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3a06>

a770: 6823 ldr r3, [r4, #0]

a772: 69fa ldr r2, [r7, #28]

a774: 4293 cmp r3, r2

a776: f47f ad5a bne.w a22e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x359a>

a77a: 4640 mov r0, r8

a77c: f004 fdd0 bl f320 <\_dl\_exception\_free@@GLIBC\_PRIVATE>

a780: e4c9 b.n a116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3482>

a782: 6c7b ldr r3, [r7, #68] ; 0x44

a784: 685a ldr r2, [r3, #4]

a786: 7813 ldrb r3, [r2, #0]

a788: b923 cbnz r3, a794 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b00>

a78a: 4b54 ldr r3, [pc, #336] ; (a8dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c48>)

a78c: 447b add r3, pc

a78e: 681b ldr r3, [r3, #0]

a790: 681a ldr r2, [r3, #0]

a792: b142 cbz r2, a7a6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b12>

a794: 4852 ldr r0, [pc, #328] ; (a8e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c4c>)

a796: 4659 mov r1, fp

a798: 4478 add r0, pc

a79a: f001 faeb bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

a79e: e689 b.n a4b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3820>

a7a0: f8d7 a038 ldr.w sl, [r7, #56] ; 0x38

a7a4: e59c b.n a2e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x364c>

a7a6: 4a4f ldr r2, [pc, #316] ; (a8e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c50>)

a7a8: 447a add r2, pc

a7aa: e7f3 b.n a794 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b00>

a7ac: 4b4e ldr r3, [pc, #312] ; (a8e8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c54>)

a7ae: 2200 movs r2, #0

a7b0: 4649 mov r1, r9

a7b2: 4610 mov r0, r2

a7b4: 447b add r3, pc

a7b6: f7f6 e928 blx a08 <\_dl\_signal\_error@plt>

a7ba: 4b4c ldr r3, [pc, #304] ; (a8ec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c58>)

a7bc: f44f 7290 mov.w r2, #288 ; 0x120

a7c0: 494b ldr r1, [pc, #300] ; (a8f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c5c>)

a7c2: 484c ldr r0, [pc, #304] ; (a8f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c60>)

a7c4: 447b add r3, pc

a7c6: 4479 add r1, pc

a7c8: 4478 add r0, pc

a7ca: f006 f8df bl 1098c <free@@GLIBC\_2.4+0x12c>

a7ce: 4b4a ldr r3, [pc, #296] ; (a8f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c64>)

a7d0: 4659 mov r1, fp

a7d2: 4610 mov r0, r2

a7d4: 447b add r3, pc

a7d6: f7f6 e918 blx a08 <\_dl\_signal\_error@plt>

a7da: 1c5a adds r2, r3, #1

a7dc: f04f 0200 mov.w r2, #0

a7e0: f107 014c add.w r1, r7, #76 ; 0x4c

a7e4: bf14 ite ne

a7e6: 4618 movne r0, r3

a7e8: 4610 moveq r0, r2

a7ea: f7f6 e8f6 blx 9d8 <\_dl\_signal\_exception@plt>

a7ee: 4b43 ldr r3, [pc, #268] ; (a8fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c68>)

a7f0: f240 2229 movw r2, #553 ; 0x229

a7f4: 4942 ldr r1, [pc, #264] ; (a900 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c6c>)

a7f6: 4843 ldr r0, [pc, #268] ; (a904 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c70>)

a7f8: 447b add r3, pc

a7fa: 4479 add r1, pc

a7fc: 4478 add r0, pc

a7fe: f006 f8c5 bl 1098c <free@@GLIBC\_2.4+0x12c>

a802: 693b ldr r3, [r7, #16]

a804: 6818 ldr r0, [r3, #0]

a806: 68bb ldr r3, [r7, #8]

a808: 4298 cmp r0, r3

a80a: d001 beq.n a810 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b7c>

a80c: f7f6 e90a blx a24 <free@plt+0x4>

a810: 6b3b ldr r3, [r7, #48] ; 0x30

a812: 2200 movs r2, #0

a814: 200c movs r0, #12

a816: 6859 ldr r1, [r3, #4]

a818: 4b3b ldr r3, [pc, #236] ; (a908 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c74>)

a81a: 447b add r3, pc

a81c: f7f6 e8f4 blx a08 <\_dl\_signal\_error@plt>

a820: 6b3b ldr r3, [r7, #48] ; 0x30

a822: 4602 mov r2, r0

a824: 200c movs r0, #12

a826: 6859 ldr r1, [r3, #4]

a828: 4b38 ldr r3, [pc, #224] ; (a90c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c78>)

a82a: 447b add r3, pc

a82c: f7f6 e8ec blx a08 <\_dl\_signal\_error@plt>

a830: 6b3b ldr r3, [r7, #48] ; 0x30

a832: 4602 mov r2, r0

a834: 200c movs r0, #12

a836: 6859 ldr r1, [r3, #4]

a838: 4b35 ldr r3, [pc, #212] ; (a910 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c7c>)

a83a: 447b add r3, pc

a83c: f7f6 e8e4 blx a08 <\_dl\_signal\_error@plt>

a840: 4b34 ldr r3, [pc, #208] ; (a914 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c80>)

a842: 2200 movs r2, #0

a844: 6869 ldr r1, [r5, #4]

a846: 2016 movs r0, #22

a848: 447b add r3, pc

a84a: f7f6 e8de blx a08 <\_dl\_signal\_error@plt>

a84e: 4b32 ldr r3, [pc, #200] ; (a918 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c84>)

a850: f44f 7204 mov.w r2, #528 ; 0x210

a854: 4931 ldr r1, [pc, #196] ; (a91c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c88>)

a856: 4832 ldr r0, [pc, #200] ; (a920 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c8c>)

a858: 447b add r3, pc

a85a: 4479 add r1, pc

a85c: 4478 add r0, pc

a85e: f006 f895 bl 1098c <free@@GLIBC\_2.4+0x12c>

a862: 4b30 ldr r3, [pc, #192] ; (a924 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c90>)

a864: 2200 movs r2, #0

a866: 4659 mov r1, fp

a868: 4610 mov r0, r2

a86a: 447b add r3, pc

a86c: f7f6 e8cc blx a08 <\_dl\_signal\_error@plt>

a870: 6b3b ldr r3, [r7, #48] ; 0x30

a872: 4602 mov r2, r0

a874: 200c movs r0, #12

a876: 6859 ldr r1, [r3, #4]

a878: 4b2b ldr r3, [pc, #172] ; (a928 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c94>)

a87a: 447b add r3, pc

a87c: f7f6 e8c4 blx a08 <\_dl\_signal\_error@plt>

a880: 4b2a ldr r3, [pc, #168] ; (a92c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c98>)

a882: 22f4 movs r2, #244 ; 0xf4

a884: 492a ldr r1, [pc, #168] ; (a930 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3c9c>)

a886: 482b ldr r0, [pc, #172] ; (a934 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ca0>)

a888: 447b add r3, pc

a88a: 4479 add r1, pc

a88c: 4478 add r0, pc

a88e: f006 f87d bl 1098c <free@@GLIBC\_2.4+0x12c>

a892: bf00 nop

a894: f10c 0001 add.w r0, ip, #1

a898: f9f4 0001 ldr??.w r0, [r4, #1]

a89c: f0fa 0001 ; <UNDEFINED> instruction: 0xf0fa0001

a8a0: e92a 0001 stmdb sl!, {r0}

a8a4: eee2 0001 cdp 0, 14, cr0, cr2, cr1, {0}

a8a8: e8e0 0001 strd r0, r0, [r0], #4

a8ac: fdc7 ffff stc2l 15, cr15, [r7, #1020] ; 0x3fc

a8b0: c440 stmia r4!, {r6}

a8b2: 0000 movs r0, r0

a8b4: ed92 0001 ldc 0, cr0, [r2, #4]

a8b8: e790 b.n a7dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3b48>

a8ba: 0001 movs r1, r0

a8bc: c38e stmia r3!, {r1, r2, r3, r7}

a8be: 0000 movs r0, r0

a8c0: f706 0001 ; <UNDEFINED> instruction: 0xf7060001

a8c4: e696 b.n a5f4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3960>

a8c6: 0001 movs r1, r0

a8c8: f9d3 ffff ldr??.w pc, [r3, #4095] ; 0xfff

a8cc: c188 stmia r1!, {r3, r7}

a8ce: 0000 movs r0, r0

a8d0: c006 stmia r0!, {r1, r2}

a8d2: 0000 movs r0, r0

a8d4: c0d8 stmia r0!, {r3, r4, r6, r7}

a8d6: 0000 movs r0, r0

a8d8: ea56 0001 orrs.w r0, r6, r1

a8dc: f10c 0001 add.w r0, ip, #1

a8e0: be94 bkpt 0x0094

a8e2: 0000 movs r0, r0

a8e4: a330 add r3, pc, #192 ; (adr r3, a9a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d14>)

a8e6: 0000 movs r0, r0

a8e8: bdcc pop {r2, r3, r6, r7, pc}

a8ea: 0000 movs r0, r0

a8ec: bf64 itt vs

a8ee: 0000 movvs r0, r0

a8f0: bde2 popvs {r1, r5, r6, r7, pc}

a8f2: 0000 movs r0, r0

a8f4: b688 ; <UNDEFINED> instruction: 0xb688

a8f6: 0000 movs r0, r0

a8f8: be30 bkpt 0x0030

a8fa: 0000 movs r0, r0

a8fc: bf30 wfi

a8fe: 0000 movs r0, r0

a900: bdae pop {r1, r2, r3, r5, r7, pc}

a902: 0000 movs r0, r0

a904: bf08 it eq

a906: 0000 moveq r0, r0

a908: be42 bkpt 0x0042

a90a: 0000 movs r0, r0

a90c: be76 bkpt 0x0076

a90e: 0000 movs r0, r0

a910: bd22 pop {r1, r5, pc}

a912: 0000 movs r0, r0

a914: be7c bkpt 0x007c

a916: 0000 movs r0, r0

a918: bed0 bkpt 0x00d0

a91a: 0000 movs r0, r0

a91c: bd4e pop {r1, r2, r3, r6, pc}

a91e: 0000 movs r0, r0

a920: be98 bkpt 0x0098

a922: 0000 movs r0, r0

a924: bd16 pop {r1, r2, r4, pc}

a926: 0000 movs r0, r0

a928: be26 bkpt 0x0026

a92a: 0000 movs r0, r0

a92c: bea0 bkpt 0x00a0

a92e: 0000 movs r0, r0

a930: bd1e pop {r1, r2, r3, r4, pc}

a932: 0000 movs r0, r0

a934: b5c4 push {r2, r6, r7, lr}

a936: 0000 movs r0, r0

a938: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

a93c: b097 sub sp, #92 ; 0x5c

a93e: af00 add r7, sp, #0

a940: 4606 mov r6, r0

a942: f107 0450 add.w r4, r7, #80 ; 0x50

a946: 61b8 str r0, [r7, #24]

a948: 200b movs r0, #11

a94a: 60f9 str r1, [r7, #12]

a94c: 4621 mov r1, r4

a94e: 61fa str r2, [r7, #28]

a950: 2200 movs r2, #0

a952: 617b str r3, [r7, #20]

a954: 603c str r4, [r7, #0]

a956: f005 fa73 bl fe40 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE>

a95a: f8df 359c ldr.w r3, [pc, #1436] ; aef8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4264>

a95e: f116 0c00 adds.w ip, r6, #0

a962: 6d3a ldr r2, [r7, #80] ; 0x50

a964: 447b add r3, pc

a966: 6d79 ldr r1, [r7, #84] ; 0x54

a968: bf18 it ne

a96a: f04f 0c01 movne.w ip, #1

a96e: 6c18 ldr r0, [r3, #64] ; 0x40

a970: 6c5b ldr r3, [r3, #68] ; 0x44

a972: 60ba str r2, [r7, #8]

a974: 4002 ands r2, r0

a976: 623a str r2, [r7, #32]

a978: 613b str r3, [r7, #16]

a97a: 400b ands r3, r1

a97c: 627b str r3, [r7, #36] ; 0x24

a97e: 6079 str r1, [r7, #4]

a980: 6338 str r0, [r7, #48] ; 0x30

a982: e9d7 0108 ldrd r0, r1, [r7, #32]

a986: ea50 0301 orrs.w r3, r0, r1

a98a: f000 82a9 beq.w aee0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x424c>

a98e: 2300 movs r3, #0

a990: 2520 movs r5, #32

a992: f8c7 c028 str.w ip, [r7, #40] ; 0x28

a996: 469e mov lr, r3

a998: f06f 021f mvn.w r2, #31

a99c: 2601 movs r6, #1

a99e: 46ac mov ip, r5

a9a0: fa01 fc0c lsl.w ip, r1, ip

a9a4: fa20 f403 lsr.w r4, r0, r3

a9a8: fa21 f202 lsr.w r2, r1, r2

a9ac: ea44 040c orr.w r4, r4, ip

a9b0: 4314 orrs r4, r2

a9b2: 6aba ldr r2, [r7, #40] ; 0x28

a9b4: f004 0401 and.w r4, r4, #1

a9b8: e9c7 4e10 strd r4, lr, [r7, #64] ; 0x40

a9bc: 1c55 adds r5, r2, #1

a9be: 637d str r5, [r7, #52] ; 0x34

a9c0: e9d7 4510 ldrd r4, r5, [r7, #64] ; 0x40

a9c4: 3301 adds r3, #1

a9c6: f1c3 0c20 rsb ip, r3, #32

a9ca: fa06 f803 lsl.w r8, r6, r3

a9ce: 432c orrs r4, r5

a9d0: fa26 f40c lsr.w r4, r6, ip

a9d4: bf1e ittt ne

a9d6: 3202 addne r2, #2

a9d8: 6b7d ldrne r5, [r7, #52] ; 0x34

a9da: 637a strne r2, [r7, #52] ; 0x34

a9dc: f1a3 0220 sub.w r2, r3, #32

a9e0: fa06 f902 lsl.w r9, r6, r2

a9e4: bf18 it ne

a9e6: 62bd strne r5, [r7, #40] ; 0x28

a9e8: ea49 0904 orr.w r9, r9, r4

a9ec: f1d8 0400 rsbs r4, r8, #0

a9f0: ea04 0a00 and.w sl, r4, r0

a9f4: eb69 0549 sbc.w r5, r9, r9, lsl #1

a9f8: ea05 0b01 and.w fp, r5, r1

a9fc: ea5a 040b orrs.w r4, sl, fp

aa00: d1ce bne.n a9a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d0c>

aa02: 6b7c ldr r4, [r7, #52] ; 0x34

aa04: f8df 34f4 ldr.w r3, [pc, #1268] ; aefc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4268>

aa08: 447b add r3, pc

aa0a: f8d3 3180 ldr.w r3, [r3, #384] ; 0x180

aa0e: 643b str r3, [r7, #64] ; 0x40

aa10: 2b00 cmp r3, #0

aa12: f000 822f beq.w ae74 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41e0>

aa16: 6c3b ldr r3, [r7, #64] ; 0x40

aa18: f8b3 115c ldrh.w r1, [r3, #348] ; 0x15c

aa1c: f8d3 6154 ldr.w r6, [r3, #340] ; 0x154

aa20: 2900 cmp r1, #0

aa22: f000 810d beq.w ac40 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3fac>

aa26: f04f 0a00 mov.w sl, #0

aa2a: 637c str r4, [r7, #52] ; 0x34

aa2c: 4689 mov r9, r1

aa2e: e005 b.n aa3c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3da8>

aa30: f10a 0a01 add.w sl, sl, #1

aa34: 3620 adds r6, #32

aa36: 45d1 cmp r9, sl

aa38: f000 8101 beq.w ac3e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3faa>

aa3c: 6833 ldr r3, [r6, #0]

aa3e: 2b04 cmp r3, #4

aa40: d1f6 bne.n aa30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d9c>

aa42: f8d6 b01c ldr.w fp, [r6, #28]

aa46: f1bb 0f03 cmp.w fp, #3

aa4a: f240 80f6 bls.w ac3a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3fa6>

aa4e: f1ab 0304 sub.w r3, fp, #4

aa52: f033 0304 bics.w r3, r3, #4

aa56: d1eb bne.n aa30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d9c>

aa58: 6c3b ldr r3, [r7, #64] ; 0x40

aa5a: 6971 ldr r1, [r6, #20]

aa5c: 68b2 ldr r2, [r6, #8]

aa5e: 681b ldr r3, [r3, #0]

aa60: 290c cmp r1, #12

aa62: 441a add r2, r3

aa64: f1c2 0c0c rsb ip, r2, #12

aa68: d9e2 bls.n aa30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d9c>

aa6a: f1cb 0000 rsb r0, fp, #0

aa6e: f10b 080b add.w r8, fp, #11

aa72: f10b 3eff add.w lr, fp, #4294967295 ; 0xffffffff

aa76: e009 b.n aa8c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3df8>

aa78: 4443 add r3, r8

aa7a: 4475 add r5, lr

aa7c: 4003 ands r3, r0

aa7e: 442b add r3, r5

aa80: 4003 ands r3, r0

aa82: 441a add r2, r3

aa84: eb02 030c add.w r3, r2, ip

aa88: 428b cmp r3, r1

aa8a: d2d1 bcs.n aa30 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d9c>

aa8c: 6894 ldr r4, [r2, #8]

aa8e: e9d2 3500 ldrd r3, r5, [r2]

aa92: 2c02 cmp r4, #2

aa94: d1f0 bne.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aa96: 2b04 cmp r3, #4

aa98: d1ee bne.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aa9a: 7b14 ldrb r4, [r2, #12]

aa9c: 2c47 cmp r4, #71 ; 0x47

aa9e: d1eb bne.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aaa0: 7b54 ldrb r4, [r2, #13]

aaa2: 2c4e cmp r4, #78 ; 0x4e

aaa4: d1e8 bne.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aaa6: 7b94 ldrb r4, [r2, #14]

aaa8: 2c55 cmp r4, #85 ; 0x55

aaaa: d1e5 bne.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aaac: 7bd4 ldrb r4, [r2, #15]

aaae: 2c00 cmp r4, #0

aab0: d1e2 bne.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aab2: 2d0a cmp r5, #10

aab4: d9e0 bls.n aa78 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3de4>

aab6: f10b 0b0f add.w fp, fp, #15

aaba: 46a0 mov r8, r4

aabc: ea0b 0000 and.w r0, fp, r0

aac0: 6b7c ldr r4, [r7, #52] ; 0x34

aac2: 1813 adds r3, r2, r0

aac4: f8df c438 ldr.w ip, [pc, #1080] ; af00 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x426c>

aac8: 5812 ldr r2, [r2, r0]

aaca: f103 0608 add.w r6, r3, #8

aace: 685b ldr r3, [r3, #4]

aad0: 44fc add ip, pc

aad2: 4414 add r4, r2

aad4: 466a mov r2, sp

aad6: 46a9 mov r9, r5

aad8: 687d ldr r5, [r7, #4]

aada: eba2 02c4 sub.w r2, r2, r4, lsl #3

aade: 643b str r3, [r7, #64] ; 0x40

aae0: 095b lsrs r3, r3, #5

aae2: 6839 ldr r1, [r7, #0]

aae4: 4695 mov sp, r2

aae6: 693a ldr r2, [r7, #16]

aae8: 200b movs r0, #11

aaea: 46c2 mov sl, r8

aaec: 431a orrs r2, r3

aaee: f8cc 2044 str.w r2, [ip, #68] ; 0x44

aaf2: 6c3a ldr r2, [r7, #64] ; 0x40

aaf4: 432b orrs r3, r5

aaf6: 68bd ldr r5, [r7, #8]

aaf8: 657b str r3, [r7, #84] ; 0x54

aafa: 6b3b ldr r3, [r7, #48] ; 0x30

aafc: 06d2 lsls r2, r2, #27

aafe: 4313 orrs r3, r2

ab00: 432a orrs r2, r5

ab02: f1a9 0508 sub.w r5, r9, #8

ab06: f8cc 3040 str.w r3, [ip, #64] ; 0x40

ab0a: 653a str r2, [r7, #80] ; 0x50

ab0c: f004 ffb2 bl fa74 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x754>

ab10: 1973 adds r3, r6, r5

ab12: 46e9 mov r9, sp

ab14: 429e cmp r6, r3

ab16: 461a mov r2, r3

ab18: bf24 itt cs

ab1a: 464b movcs r3, r9

ab1c: 2201 movcs r2, #1

ab1e: d229 bcs.n ab74 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ee0>

ab20: 637c str r4, [r7, #52] ; 0x34

ab22: 46b3 mov fp, r6

ab24: 464c mov r4, r9

ab26: 4690 mov r8, r2

ab28: f10b 0901 add.w r9, fp, #1

ab2c: ea4f 05ca mov.w r5, sl, lsl #3

ab30: f10a 0601 add.w r6, sl, #1

ab34: 4648 mov r0, r9

ab36: f007 fe03 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

ab3a: f89b 3000 ldrb.w r3, [fp]

ab3e: 2201 movs r2, #1

ab40: eb04 0c05 add.w ip, r4, r5

ab44: fa02 f303 lsl.w r3, r2, r3

ab48: 6c3a ldr r2, [r7, #64] ; 0x40

ab4a: 4213 tst r3, r2

ab4c: f000 819b beq.w ae86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41f2>

ab50: 3508 adds r5, #8

ab52: f844 903a str.w r9, [r4, sl, lsl #3]

ab56: f8cc 0004 str.w r0, [ip, #4]

ab5a: eb04 0c05 add.w ip, r4, r5

ab5e: f10a 0202 add.w r2, sl, #2

ab62: 46b2 mov sl, r6

ab64: 3001 adds r0, #1

ab66: eb09 0b00 add.w fp, r9, r0

ab6a: 45c3 cmp fp, r8

ab6c: d3dc bcc.n ab28 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3e94>

ab6e: 46a1 mov r9, r4

ab70: 6b7c ldr r4, [r7, #52] ; 0x34

ab72: 4663 mov r3, ip

ab74: e9d7 0108 ldrd r0, r1, [r7, #32]

ab78: ea50 0501 orrs.w r5, r0, r1

ab7c: d06d beq.n ac5a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3fc6>

ab7e: 4ae1 ldr r2, [pc, #900] ; (af04 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4270>)

ab80: ea4f 03ca mov.w r3, sl, lsl #3

ab84: f10a 0601 add.w r6, sl, #1

ab88: 2500 movs r5, #0

ab8a: 447a add r2, pc

ab8c: 623e str r6, [r7, #32]

ab8e: 324c adds r2, #76 ; 0x4c

ab90: f04f 0801 mov.w r8, #1

ab94: 633a str r2, [r7, #48] ; 0x30

ab96: 461e mov r6, r3

ab98: 460a mov r2, r1

ab9a: 4601 mov r1, r0

ab9c: 613c str r4, [r7, #16]

ab9e: f8c7 a034 str.w sl, [r7, #52] ; 0x34

aba2: f1c5 0320 rsb r3, r5, #32

aba6: fa21 f005 lsr.w r0, r1, r5

abaa: fa02 f403 lsl.w r4, r2, r3

abae: f1a5 0c20 sub.w ip, r5, #32

abb2: 4320 orrs r0, r4

abb4: fa22 f40c lsr.w r4, r2, ip

abb8: 4320 orrs r0, r4

abba: 643b str r3, [r7, #64] ; 0x40

abbc: f000 0301 and.w r3, r0, #1

abc0: 63bb str r3, [r7, #56] ; 0x38

abc2: 2300 movs r3, #0

abc4: 63fb str r3, [r7, #60] ; 0x3c

abc6: e9d7 ab0e ldrd sl, fp, [r7, #56] ; 0x38

abca: eb09 0406 add.w r4, r9, r6

abce: ea5a 030b orrs.w r3, sl, fp

abd2: d03e beq.n ac52 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3fbe>

abd4: 6b3b ldr r3, [r7, #48] ; 0x30

abd6: e9c7 120a strd r1, r2, [r7, #40] ; 0x28

abda: 220a movs r2, #10

abdc: fb02 3005 mla r0, r2, r5, r3

abe0: fa08 f30c lsl.w r3, r8, ip

abe4: 64fb str r3, [r7, #76] ; 0x4c

abe6: f849 0006 str.w r0, [r9, r6]

abea: 3608 adds r6, #8

abec: f007 fda8 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

abf0: 6c3b ldr r3, [r7, #64] ; 0x40

abf2: 6cfa ldr r2, [r7, #76] ; 0x4c

abf4: fa28 f303 lsr.w r3, r8, r3

abf8: 431a orrs r2, r3

abfa: 64fa str r2, [r7, #76] ; 0x4c

abfc: 6b7a ldr r2, [r7, #52] ; 0x34

abfe: fa08 f305 lsl.w r3, r8, r5

ac02: 64bb str r3, [r7, #72] ; 0x48

ac04: 3501 adds r5, #1

ac06: e9d7 ab12 ldrd sl, fp, [r7, #72] ; 0x48

ac0a: f102 0e02 add.w lr, r2, #2

ac0e: e9d7 120a ldrd r1, r2, [r7, #40] ; 0x28

ac12: eb09 0306 add.w r3, r9, r6

ac16: 4593 cmp fp, r2

ac18: 465e mov r6, fp

ac1a: bf08 it eq

ac1c: 458a cmpeq sl, r1

ac1e: ea8a 0c01 eor.w ip, sl, r1

ac22: ea86 0602 eor.w r6, r6, r2

ac26: 6060 str r0, [r4, #4]

ac28: d015 beq.n ac56 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3fc2>

ac2a: 6a3b ldr r3, [r7, #32]

ac2c: 4632 mov r2, r6

ac2e: 4661 mov r1, ip

ac30: 637b str r3, [r7, #52] ; 0x34

ac32: 00de lsls r6, r3, #3

ac34: 3301 adds r3, #1

ac36: 623b str r3, [r7, #32]

ac38: e7b3 b.n aba2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3f0e>

ac3a: 469b mov fp, r3

ac3c: e70c b.n aa58 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3dc4>

ac3e: 6b7c ldr r4, [r7, #52] ; 0x34

ac40: 466b mov r3, sp

ac42: eba3 03c4 sub.w r3, r3, r4, lsl #3

ac46: 2201 movs r2, #1

ac48: f04f 0a00 mov.w sl, #0

ac4c: 469d mov sp, r3

ac4e: 4699 mov r9, r3

ac50: e790 b.n ab74 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ee0>

ac52: 3501 adds r5, #1

ac54: e7a5 b.n aba2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3f0e>

ac56: 693c ldr r4, [r7, #16]

ac58: 4672 mov r2, lr

ac5a: 69b9 ldr r1, [r7, #24]

ac5c: b129 cbz r1, ac6a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3fd6>

ac5e: 6019 str r1, [r3, #0]

ac60: 68f9 ldr r1, [r7, #12]

ac62: 6059 str r1, [r3, #4]

ac64: eb09 03c2 add.w r3, r9, r2, lsl #3

ac68: 3201 adds r2, #1

ac6a: 49a7 ldr r1, [pc, #668] ; (af08 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4274>)

ac6c: 4294 cmp r4, r2

ac6e: f04f 0003 mov.w r0, #3

ac72: 4479 add r1, pc

ac74: e9c3 1000 strd r1, r0, [r3]

ac78: f040 8135 bne.w aee6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4252>

ac7c: f8d9 3004 ldr.w r3, [r9, #4]

ac80: 2c01 cmp r4, #1

ac82: 64bb str r3, [r7, #72] ; 0x48

ac84: f000 80e6 beq.w ae54 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41c0>

ac88: f104 5300 add.w r3, r4, #536870912 ; 0x20000000

ac8c: 6cba ldr r2, [r7, #72] ; 0x48

ac8e: 3b01 subs r3, #1

ac90: 2c02 cmp r4, #2

ac92: f102 0002 add.w r0, r2, #2

ac96: ea4f 03c3 mov.w r3, r3, lsl #3

ac9a: eb09 0803 add.w r8, r9, r3

ac9e: f8d8 2004 ldr.w r2, [r8, #4]

aca2: 4410 add r0, r2

aca4: 643a str r2, [r7, #64] ; 0x40

aca6: f200 80bd bhi.w ae24 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4190>

acaa: 69fa ldr r2, [r7, #28]

acac: f04f 0a08 mov.w sl, #8

acb0: 2601 movs r6, #1

acb2: fa0a fa04 lsl.w sl, sl, r4

acb6: fa06 f304 lsl.w r3, r6, r4

acba: 4450 add r0, sl

acbc: 6013 str r3, [r2, #0]

acbe: 623b str r3, [r7, #32]

acc0: f7f5 ee84 blx 9cc <malloc@plt+0x4>

acc4: 62b8 str r0, [r7, #40] ; 0x28

acc6: 2800 cmp r0, #0

acc8: f000 80cd beq.w ae66 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41d2>

accc: 6abb ldr r3, [r7, #40] ; 0x28

acce: 2c02 cmp r4, #2

acd0: f104 35ff add.w r5, r4, #4294967295 ; 0xffffffff

acd4: 633d str r5, [r7, #48] ; 0x30

acd6: eb03 000a add.w r0, r3, sl

acda: fa06 fb05 lsl.w fp, r6, r5

acde: 6018 str r0, [r3, #0]

ace0: 6098 str r0, [r3, #8]

ace2: f8d9 3000 ldr.w r3, [r9]

ace6: 63bb str r3, [r7, #56] ; 0x38

ace8: f000 80d2 beq.w ae90 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41fc>

acec: f8d8 3000 ldr.w r3, [r8]

acf0: 46da mov sl, fp

acf2: f04f 082f mov.w r8, #47 ; 0x2f

acf6: 61fd str r5, [r7, #28]

acf8: 637b str r3, [r7, #52] ; 0x34

acfa: 1ea3 subs r3, r4, #2

acfc: 469b mov fp, r3

acfe: 6c3a ldr r2, [r7, #64] ; 0x40

ad00: f1aa 0a02 sub.w sl, sl, #2

ad04: 6b79 ldr r1, [r7, #52] ; 0x34

ad06: f109 0504 add.w r5, r9, #4

ad0a: f008 f9df bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

ad0e: 465c mov r4, fp

ad10: f800 8b01 strb.w r8, [r0], #1

ad14: e001 b.n ad1a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4086>

ad16: 3c01 subs r4, #1

ad18: d00e beq.n ad38 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40a4>

ad1a: fa06 f304 lsl.w r3, r6, r4

ad1e: ea13 0f0a tst.w r3, sl

ad22: d0f8 beq.n ad16 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4082>

ad24: f855 2034 ldr.w r2, [r5, r4, lsl #3]

ad28: f859 1034 ldr.w r1, [r9, r4, lsl #3]

ad2c: f008 f9ce bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

ad30: 3c01 subs r4, #1

ad32: f800 8b01 strb.w r8, [r0], #1

ad36: d1f0 bne.n ad1a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4086>

ad38: 6cba ldr r2, [r7, #72] ; 0x48

ad3a: 6bb9 ldr r1, [r7, #56] ; 0x38

ad3c: f008 f9c6 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

ad40: f800 8b01 strb.w r8, [r0], #1

ad44: f1ba 0f00 cmp.w sl, #0

ad48: d1d9 bne.n acfe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x406a>

ad4a: e9d7 5307 ldrd r5, r3, [r7, #28]

ad4e: b14b cbz r3, ad64 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40d0>

ad50: 6aba ldr r2, [r7, #40] ; 0x28

ad52: 2300 movs r3, #0

ad54: 6a38 ldr r0, [r7, #32]

ad56: 1d11 adds r1, r2, #4

ad58: 461a mov r2, r3

ad5a: f841 2033 str.w r2, [r1, r3, lsl #3]

ad5e: 3301 adds r3, #1

ad60: 4283 cmp r3, r0

ad62: d3fa bcc.n ad5a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40c6>

ad64: f109 0c04 add.w ip, r9, #4

ad68: 6a3c ldr r4, [r7, #32]

ad6a: f8d7 9028 ldr.w r9, [r7, #40] ; 0x28

ad6e: f04f 0e01 mov.w lr, #1

ad72: 46a8 mov r8, r5

ad74: fa0e f605 lsl.w r6, lr, r5

ad78: b1ac cbz r4, ada6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4112>

ad7a: 464a mov r2, r9

ad7c: 4623 mov r3, r4

ad7e: 3b01 subs r3, #1

ad80: 3208 adds r2, #8

ad82: 421e tst r6, r3

ad84: d00d beq.n ada2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x410e>

ad86: f852 0c04 ldr.w r0, [r2, #-4]

ad8a: 3b01 subs r3, #1

ad8c: f85c 1035 ldr.w r1, [ip, r5, lsl #3]

ad90: 421e tst r6, r3

ad92: f100 0001 add.w r0, r0, #1

ad96: f102 0208 add.w r2, r2, #8

ad9a: 4401 add r1, r0

ad9c: f842 1c0c str.w r1, [r2, #-12]

ada0: d1f1 bne.n ad86 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40f2>

ada2: 2b00 cmp r3, #0

ada4: d1eb bne.n ad7e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40ea>

ada6: b10d cbz r5, adac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4118>

ada8: 3d01 subs r5, #1

adaa: e7e2 b.n ad72 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40de>

adac: 6a3b ldr r3, [r7, #32]

adae: 2001 movs r0, #1

adb0: 4644 mov r4, r8

adb2: 1e9e subs r6, r3, #2

adb4: 6b3b ldr r3, [r7, #48] ; 0x30

adb6: 4098 lsls r0, r3

adb8: 6abb ldr r3, [r7, #40] ; 0x28

adba: 4286 cmp r6, r0

adbc: f103 0510 add.w r5, r3, #16

adc0: d018 beq.n adf4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4160>

adc2: 3318 adds r3, #24

adc4: 3e01 subs r6, #1

adc6: 07f2 lsls r2, r6, #31

adc8: bf48 it mi

adca: e953 2106 ldrdmi r2, r1, [r3, #-24]

adce: f103 0308 add.w r3, r3, #8

add2: bf54 ite pl

add4: f853 2c18 ldrpl.w r2, [r3, #-24]

add8: 1852 addmi r2, r2, r1

adda: 4286 cmp r6, r0

addc: f843 2c10 str.w r2, [r3, #-16]

ade0: d1f0 bne.n adc4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4130>

ade2: 6a3b ldr r3, [r7, #32]

ade4: f103 5900 add.w r9, r3, #536870912 ; 0x20000000

ade8: f1a9 0902 sub.w r9, r9, #2

adec: eba9 0900 sub.w r9, r9, r0

adf0: eb05 05c9 add.w r5, r5, r9, lsl #3

adf4: 6b3b ldr r3, [r7, #48] ; 0x30

adf6: f06f 0207 mvn.w r2, #7

adfa: 6c39 ldr r1, [r7, #64] ; 0x40

adfc: 409a lsls r2, r3

adfe: 3101 adds r1, #1

ae00: 442a add r2, r5

ae02: f852 3034 ldr.w r3, [r2, r4, lsl #3]

ae06: 440b add r3, r1

ae08: f845 3034 str.w r3, [r5, r4, lsl #3]

ae0c: 3401 adds r4, #1

ae0e: 42a0 cmp r0, r4

ae10: d1f7 bne.n ae02 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x416e>

ae12: 6abb ldr r3, [r7, #40] ; 0x28

ae14: 697a ldr r2, [r7, #20]

ae16: 6ab8 ldr r0, [r7, #40] ; 0x28

ae18: 375c adds r7, #92 ; 0x5c

ae1a: 685b ldr r3, [r3, #4]

ae1c: 6013 str r3, [r2, #0]

ae1e: 46bd mov sp, r7

ae20: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

ae24: 0040 lsls r0, r0, #1

ae26: f1a8 0108 sub.w r1, r8, #8

ae2a: 464b mov r3, r9

ae2c: 68da ldr r2, [r3, #12]

ae2e: 3308 adds r3, #8

ae30: 4299 cmp r1, r3

ae32: f102 0201 add.w r2, r2, #1

ae36: 4410 add r0, r2

ae38: d1f8 bne.n ae2c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4198>

ae3a: 2c03 cmp r4, #3

ae3c: d007 beq.n ae4e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41ba>

ae3e: 2c1f cmp r4, #31

ae40: d811 bhi.n ae66 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41d2>

ae42: f100 0340 add.w r3, r0, #64 ; 0x40

ae46: f1c4 0223 rsb r2, r4, #35 ; 0x23

ae4a: 40d3 lsrs r3, r2

ae4c: d10b bne.n ae66 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x41d2>

ae4e: 1ee3 subs r3, r4, #3

ae50: 4098 lsls r0, r3

ae52: e72a b.n acaa <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4016>

ae54: f103 0011 add.w r0, r3, #17

ae58: 69fb ldr r3, [r7, #28]

ae5a: 2402 movs r4, #2

ae5c: 601c str r4, [r3, #0]

ae5e: f7f5 edb6 blx 9cc <malloc@plt+0x4>

ae62: 62b8 str r0, [r7, #40] ; 0x28

ae64: bb08 cbnz r0, aeaa <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4216>

ae66: 4b29 ldr r3, [pc, #164] ; (af0c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4278>)

ae68: 2200 movs r2, #0

ae6a: 4611 mov r1, r2

ae6c: 200c movs r0, #12

ae6e: 447b add r3, pc

ae70: f7f5 edca blx a08 <\_dl\_signal\_error@plt>

ae74: 466b mov r3, sp

ae76: eba3 03c4 sub.w r3, r3, r4, lsl #3

ae7a: f8d7 a040 ldr.w sl, [r7, #64] ; 0x40

ae7e: 2201 movs r2, #1

ae80: 469d mov sp, r3

ae82: 4699 mov r9, r3

ae84: e676 b.n ab74 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ee0>

ae86: 6b7b ldr r3, [r7, #52] ; 0x34

ae88: 4632 mov r2, r6

ae8a: 3b01 subs r3, #1

ae8c: 637b str r3, [r7, #52] ; 0x34

ae8e: e669 b.n ab64 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3ed0>

ae90: e9d9 1202 ldrd r1, r2, [r9, #8]

ae94: 242f movs r4, #47 ; 0x2f

ae96: f008 f919 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

ae9a: 6cba ldr r2, [r7, #72] ; 0x48

ae9c: 6bb9 ldr r1, [r7, #56] ; 0x38

ae9e: f800 4b01 strb.w r4, [r0], #1

aea2: f008 f913 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

aea6: 7004 strb r4, [r0, #0]

aea8: e752 b.n ad50 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x40bc>

aeaa: 6abb ldr r3, [r7, #40] ; 0x28

aeac: 6cba ldr r2, [r7, #72] ; 0x48

aeae: f103 0010 add.w r0, r3, #16

aeb2: f8d9 1000 ldr.w r1, [r9]

aeb6: 461d mov r5, r3

aeb8: 6018 str r0, [r3, #0]

aeba: 1c53 adds r3, r2, #1

aebc: e9c5 3001 strd r3, r0, [r5, #4]

aec0: 2300 movs r3, #0

aec2: 60eb str r3, [r5, #12]

aec4: f008 f902 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

aec8: 69fa ldr r2, [r7, #28]

aeca: 232f movs r3, #47 ; 0x2f

aecc: 6014 str r4, [r2, #0]

aece: 697a ldr r2, [r7, #20]

aed0: 7003 strb r3, [r0, #0]

aed2: 686b ldr r3, [r5, #4]

aed4: 6ab8 ldr r0, [r7, #40] ; 0x28

aed6: 375c adds r7, #92 ; 0x5c

aed8: 6013 str r3, [r2, #0]

aeda: 46bd mov sp, r7

aedc: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

aee0: f10c 0401 add.w r4, ip, #1

aee4: e58e b.n aa04 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x3d70>

aee6: 4b0a ldr r3, [pc, #40] ; (af10 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x427c>)

aee8: 22cc movs r2, #204 ; 0xcc

aeea: 490a ldr r1, [pc, #40] ; (af14 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4280>)

aeec: 480a ldr r0, [pc, #40] ; (af18 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4284>)

aeee: 447b add r3, pc

aef0: 4479 add r1, pc

aef2: 4478 add r0, pc

aef4: f005 fd4a bl 1098c <free@@GLIBC\_2.4+0x12c>

aef8: e008 b.n af0c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4278>

aefa: 0001 movs r1, r0

aefc: df64 svc 100 ; 0x64

aefe: 0001 movs r1, r0

af00: de9c udf #156 ; 0x9c

af02: 0001 movs r1, r0

af04: dde2 ble.n aecc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4238>

af06: 0001 movs r1, r0

af08: bace revsh r6, r1

af0a: 0000 movs r0, r0

af0c: b8ee ; <UNDEFINED> instruction: 0xb8ee

af0e: 0000 movs r0, r0

af10: b88e ; <UNDEFINED> instruction: 0xb88e

af12: 0000 movs r0, r0

af14: b854 ; <UNDEFINED> instruction: 0xb854

af16: 0000 movs r0, r0

af18: b85e ; <UNDEFINED> instruction: 0xb85e

af1a: 0000 movs r0, r0

af1c: 6fc2 ldr r2, [r0, #124] ; 0x7c

af1e: e92d 43f0 stmdb sp!, {r4, r5, r6, r7, r8, r9, lr}

af22: 4604 mov r4, r0

af24: e9d0 630d ldrd r6, r3, [r0, #52] ; 0x34

af28: b087 sub sp, #28

af2a: 6850 ldr r0, [r2, #4]

af2c: 6822 ldr r2, [r4, #0]

af2e: 180f adds r7, r1, r0

af30: 685b ldr r3, [r3, #4]

af32: 580d ldr r5, [r1, r0]

af34: 6879 ldr r1, [r7, #4]

af36: 6870 ldr r0, [r6, #4]

af38: 4415 add r5, r2

af3a: 0a0e lsrs r6, r1, #8

af3c: b2c9 uxtb r1, r1

af3e: 2916 cmp r1, #22

af40: eb03 1306 add.w r3, r3, r6, lsl #4

af44: 9305 str r3, [sp, #20]

af46: d176 bne.n b036 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x43a2>

af48: 7b59 ldrb r1, [r3, #13]

af4a: 0789 lsls r1, r1, #30

af4c: d149 bne.n afe2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x434e>

af4e: f8d4 20e8 ldr.w r2, [r4, #232] ; 0xe8

af52: b162 cbz r2, af6e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x42da>

af54: 6852 ldr r2, [r2, #4]

af56: f8d4 1174 ldr.w r1, [r4, #372] ; 0x174

af5a: f832 2016 ldrh.w r2, [r2, r6, lsl #1]

af5e: f3c2 020e ubfx r2, r2, #0, #15

af62: eb01 1202 add.w r2, r1, r2, lsl #4

af66: 6851 ldr r1, [r2, #4]

af68: 2900 cmp r1, #0

af6a: bf08 it eq

af6c: 2200 moveq r2, #0

af6e: ee1d 7f70 mrc 15, 0, r7, cr13, cr0, {3}

af72: f5a7 6698 sub.w r6, r7, #1216 ; 0x4c0

af76: 6831 ldr r1, [r6, #0]

af78: 2900 cmp r1, #0

af7a: d13b bne.n aff4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4360>

af7c: 2101 movs r1, #1

af7e: 681b ldr r3, [r3, #0]

af80: f04f 0800 mov.w r8, #0

af84: 9102 str r1, [sp, #8]

af86: f04f 0901 mov.w r9, #1

af8a: 9200 str r2, [sp, #0]

af8c: 4418 add r0, r3

af8e: f8cd 800c str.w r8, [sp, #12]

af92: 4621 mov r1, r4

af94: f8cd 9004 str.w r9, [sp, #4]

af98: aa05 add r2, sp, #20

af9a: f8d4 31d0 ldr.w r3, [r4, #464] ; 0x1d0

af9e: f7fc fb7b bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

afa2: 6833 ldr r3, [r6, #0]

afa4: 4604 mov r4, r0

afa6: bb63 cbnz r3, b002 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x436e>

afa8: 9b05 ldr r3, [sp, #20]

afaa: 461a mov r2, r3

afac: b183 cbz r3, afd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x433c>

afae: 89da ldrh r2, [r3, #14]

afb0: f64f 71f1 movw r1, #65521 ; 0xfff1

afb4: 428a cmp r2, r1

afb6: bf18 it ne

afb8: 2c00 cmpne r4, #0

afba: 6859 ldr r1, [r3, #4]

afbc: bf1a itte ne

afbe: 2201 movne r2, #1

afc0: 6822 ldrne r2, [r4, #0]

afc2: 2200 moveq r2, #0

afc4: 440a add r2, r1

afc6: 7b1b ldrb r3, [r3, #12]

afc8: f003 030f and.w r3, r3, #15

afcc: 2b0a cmp r3, #10

afce: d02c beq.n b02a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4396>

afd0: 4b1d ldr r3, [pc, #116] ; (b048 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x43b4>)

afd2: 447b add r3, pc

afd4: 6b1b ldr r3, [r3, #48] ; 0x30

afd6: b903 cbnz r3, afda <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4346>

afd8: 602a str r2, [r5, #0]

afda: 4610 mov r0, r2

afdc: b007 add sp, #28

afde: e8bd 83f0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, pc}

afe2: 89dc ldrh r4, [r3, #14]

afe4: f64f 70f1 movw r0, #65521 ; 0xfff1

afe8: 6859 ldr r1, [r3, #4]

afea: 4284 cmp r4, r0

afec: bf08 it eq

afee: 2200 moveq r2, #0

aff0: 440a add r2, r1

aff2: e7e8 b.n afc6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4332>

aff4: 2301 movs r3, #1

aff6: 6073 str r3, [r6, #4]

aff8: f3bf 8f5b dmb ish

affc: 2105 movs r1, #5

affe: 9b05 ldr r3, [sp, #20]

b000: e7bd b.n af7e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x42ea>

b002: f2a7 40bc subw r0, r7, #1212 ; 0x4bc

b006: f3bf 8f5b dmb ish

b00a: e850 3f00 ldrex r3, [r0]

b00e: e840 8200 strex r2, r8, [r0]

b012: 2a00 cmp r2, #0

b014: d1f9 bne.n b00a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4376>

b016: 2b02 cmp r3, #2

b018: d1c6 bne.n afa8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4314>

b01a: 464a mov r2, r9

b01c: 4643 mov r3, r8

b01e: 2181 movs r1, #129 ; 0x81

b020: f04f 0cf0 mov.w ip, #240 ; 0xf0

b024: f005 ffcc bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

b028: e7be b.n afa8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4314>

b02a: 4b08 ldr r3, [pc, #32] ; (b04c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x43b8>)

b02c: 447b add r3, pc

b02e: 6c18 ldr r0, [r3, #64] ; 0x40

b030: 4790 blx r2

b032: 4602 mov r2, r0

b034: e7cc b.n afd0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x433c>

b036: 4b06 ldr r3, [pc, #24] ; (b050 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x43bc>)

b038: 2250 movs r2, #80 ; 0x50

b03a: 4906 ldr r1, [pc, #24] ; (b054 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x43c0>)

b03c: 4806 ldr r0, [pc, #24] ; (b058 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x43c4>)

b03e: 447b add r3, pc

b040: 4479 add r1, pc

b042: 4478 add r0, pc

b044: f005 fca2 bl 1098c <free@@GLIBC\_2.4+0x12c>

b048: d99a bls.n af80 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x42ec>

b04a: 0001 movs r1, r0

b04c: d940 bls.n b0d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x443c>

b04e: 0001 movs r1, r0

b050: b7d6 ; <UNDEFINED> instruction: 0xb7d6

b052: 0000 movs r0, r0

b054: b754 ; <UNDEFINED> instruction: 0xb754

b056: 0000 movs r0, r0

b058: b762 ; <UNDEFINED> instruction: 0xb762

b05a: 0000 movs r0, r0

b05c: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

b060: b093 sub sp, #76 ; 0x4c

b062: f8d0 91a4 ldr.w r9, [r0, #420] ; 0x1a4

b066: 9204 str r2, [sp, #16]

b068: 9305 str r3, [sp, #20]

b06a: f1b9 0f00 cmp.w r9, #0

b06e: f000 818d beq.w b38c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46f8>

b072: 08ca lsrs r2, r1, #3

b074: 460b mov r3, r1

b076: 2118 movs r1, #24

b078: 4607 mov r7, r0

b07a: fb01 fb02 mul.w fp, r1, r2

b07e: eb09 050b add.w r5, r9, fp

b082: 696a ldr r2, [r5, #20]

b084: f3bf 8f5b dmb ish

b088: 4690 mov r8, r2

b08a: f1b8 0f00 cmp.w r8, #0

b08e: f040 80da bne.w b246 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x45b2>

b092: 6fc0 ldr r0, [r0, #124] ; 0x7c

b094: e9d7 120d ldrd r1, r2, [r7, #52] ; 0x34

b098: 6840 ldr r0, [r0, #4]

b09a: 6852 ldr r2, [r2, #4]

b09c: 4403 add r3, r0

b09e: 6848 ldr r0, [r1, #4]

b0a0: 685e ldr r6, [r3, #4]

b0a2: 0a34 lsrs r4, r6, #8

b0a4: b2f6 uxtb r6, r6

b0a6: 2e16 cmp r6, #22

b0a8: ea4f 1104 mov.w r1, r4, lsl #4

b0ac: eb01 0302 add.w r3, r1, r2

b0b0: 930c str r3, [sp, #48] ; 0x30

b0b2: f040 81b4 bne.w b41e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x478a>

b0b6: 7b5e ldrb r6, [r3, #13]

b0b8: 07b6 lsls r6, r6, #30

b0ba: f040 8172 bne.w b3a2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x470e>

b0be: f8d7 30e8 ldr.w r3, [r7, #232] ; 0xe8

b0c2: b163 cbz r3, b0de <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x444a>

b0c4: 685e ldr r6, [r3, #4]

b0c6: f8d7 3174 ldr.w r3, [r7, #372] ; 0x174

b0ca: f836 4014 ldrh.w r4, [r6, r4, lsl #1]

b0ce: f3c4 040e ubfx r4, r4, #0, #15

b0d2: eb03 1304 add.w r3, r3, r4, lsl #4

b0d6: 685c ldr r4, [r3, #4]

b0d8: 2c00 cmp r4, #0

b0da: bf08 it eq

b0dc: 2300 moveq r3, #0

b0de: ee1d af70 mrc 15, 0, sl, cr13, cr0, {3}

b0e2: f5aa 6698 sub.w r6, sl, #1216 ; 0x4c0

b0e6: 6834 ldr r4, [r6, #0]

b0e8: 2c00 cmp r4, #0

b0ea: f040 8176 bne.w b3da <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4746>

b0ee: 2401 movs r4, #1

b0f0: 588a ldr r2, [r1, r2]

b0f2: 4639 mov r1, r7

b0f4: 9402 str r4, [sp, #8]

b0f6: 9300 str r3, [sp, #0]

b0f8: 2300 movs r3, #0

b0fa: 9303 str r3, [sp, #12]

b0fc: 2301 movs r3, #1

b0fe: 9301 str r3, [sp, #4]

b100: 4410 add r0, r2

b102: f8d7 31d0 ldr.w r3, [r7, #464] ; 0x1d0

b106: aa0c add r2, sp, #48 ; 0x30

b108: f7fc fac6 bl 7698 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0xa04>

b10c: 6833 ldr r3, [r6, #0]

b10e: 4604 mov r4, r0

b110: 2b00 cmp r3, #0

b112: f040 8168 bne.w b3e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4752>

b116: 9b0c ldr r3, [sp, #48] ; 0x30

b118: 2b00 cmp r3, #0

b11a: f000 8127 beq.w b36c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46d8>

b11e: 89d9 ldrh r1, [r3, #14]

b120: f64f 72f1 movw r2, #65521 ; 0xfff1

b124: 4291 cmp r1, r2

b126: bf18 it ne

b128: 2c00 cmpne r4, #0

b12a: 7b1a ldrb r2, [r3, #12]

b12c: 6859 ldr r1, [r3, #4]

b12e: f002 020f and.w r2, r2, #15

b132: bf18 it ne

b134: f8d4 8000 ldrne.w r8, [r4]

b138: 2a0a cmp r2, #10

b13a: 4488 add r8, r1

b13c: f000 8169 beq.w b412 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x477e>

b140: f8df e300 ldr.w lr, [pc, #768] ; b444 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47b0>

b144: 44fe add lr, pc

b146: f8de 21b0 ldr.w r2, [lr, #432] ; 0x1b0

b14a: 2a00 cmp r2, #0

b14c: f000 810e beq.w b36c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46d8>

b150: 6ba2 ldr r2, [r4, #56] ; 0x38

b152: f897 1199 ldrb.w r1, [r7, #409] ; 0x199

b156: f894 0199 ldrb.w r0, [r4, #409] ; 0x199

b15a: 6852 ldr r2, [r2, #4]

b15c: 4301 orrs r1, r0

b15e: 606c str r4, [r5, #4]

b160: 1a9a subs r2, r3, r2

b162: 06ce lsls r6, r1, #27

b164: ea4f 1222 mov.w r2, r2, asr #4

b168: bf58 it pl

b16a: f64f 73ff movwpl r3, #65535 ; 0xffff

b16e: 60aa str r2, [r5, #8]

b170: bf58 it pl

b172: 60eb strpl r3, [r5, #12]

b174: f140 80fa bpl.w b36c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46d8>

b178: ae0e add r6, sp, #56 ; 0x38

b17a: e9cd 6606 strd r6, r6, [sp, #24]

b17e: 6b66 ldr r6, [r4, #52] ; 0x34

b180: 2200 movs r2, #0

b182: 920d str r2, [sp, #52] ; 0x34

b184: f04f 0c03 mov.w ip, #3

b188: cb0f ldmia r3, {r0, r1, r2, r3}

b18a: 46a2 mov sl, r4

b18c: 9608 str r6, [sp, #32]

b18e: 9e07 ldr r6, [sp, #28]

b190: f8de 41ac ldr.w r4, [lr, #428] ; 0x1ac

b194: 970b str r7, [sp, #44] ; 0x2c

b196: e886 000f stmia.w r6, {r0, r1, r2, r3}

b19a: 2601 movs r6, #1

b19c: 9b08 ldr r3, [sp, #32]

b19e: f8cd 803c str.w r8, [sp, #60] ; 0x3c

b1a2: 46b8 mov r8, r7

b1a4: f8cd e01c str.w lr, [sp, #28]

b1a8: 685b ldr r3, [r3, #4]

b1aa: f8c5 c00c str.w ip, [r5, #12]

b1ae: 9308 str r3, [sp, #32]

b1b0: ab0d add r3, sp, #52 ; 0x34

b1b2: e9cd 3909 strd r3, r9, [sp, #36] ; 0x24

b1b6: e03a b.n b22e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x459a>

b1b8: f8da 3264 ldr.w r3, [sl, #612] ; 0x264

b1bc: 07d9 lsls r1, r3, #31

b1be: d53a bpl.n b236 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x45a2>

b1c0: f8d4 9010 ldr.w r9, [r4, #16]

b1c4: 9b0d ldr r3, [sp, #52] ; 0x34

b1c6: f1b9 0f00 cmp.w r9, #0

b1ca: d018 beq.n b1fe <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x456a>

b1cc: 68af ldr r7, [r5, #8]

b1ce: f50a 7318 add.w r3, sl, #608 ; 0x260

b1d2: 990c ldr r1, [sp, #48] ; 0x30

b1d4: f508 7218 add.w r2, r8, #608 ; 0x260

b1d8: 9806 ldr r0, [sp, #24]

b1da: f8d1 c000 ldr.w ip, [r1]

b1de: 4639 mov r1, r7

b1e0: 9f09 ldr r7, [sp, #36] ; 0x24

b1e2: 9700 str r7, [sp, #0]

b1e4: 9f08 ldr r7, [sp, #32]

b1e6: 44bc add ip, r7

b1e8: f8cd c004 str.w ip, [sp, #4]

b1ec: 47c8 blx r9

b1ee: 9b0f ldr r3, [sp, #60] ; 0x3c

b1f0: 4283 cmp r3, r0

b1f2: 9b0d ldr r3, [sp, #52] ; 0x34

b1f4: bf1e ittt ne

b1f6: 900f strne r0, [sp, #60] ; 0x3c

b1f8: f043 0310 orrne.w r3, r3, #16

b1fc: 930d strne r3, [sp, #52] ; 0x34

b1fe: 68e8 ldr r0, [r5, #12]

b200: f003 0103 and.w r1, r3, #3

b204: 0072 lsls r2, r6, #1

b206: 4003 ands r3, r0

b208: fa01 f202 lsl.w r2, r1, r2

b20c: f003 0303 and.w r3, r3, #3

b210: 4313 orrs r3, r2

b212: 60eb str r3, [r5, #12]

b214: 9b07 ldr r3, [sp, #28]

b216: f108 0808 add.w r8, r8, #8

b21a: 6a24 ldr r4, [r4, #32]

b21c: f10a 0a08 add.w sl, sl, #8

b220: 1c72 adds r2, r6, #1

b222: f8d3 31b0 ldr.w r3, [r3, #432] ; 0x1b0

b226: 42b3 cmp r3, r6

b228: f240 809a bls.w b360 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46cc>

b22c: 4616 mov r6, r2

b22e: f8d8 3264 ldr.w r3, [r8, #612] ; 0x264

b232: 0798 lsls r0, r3, #30

b234: d4c0 bmi.n b1b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4524>

b236: 68eb ldr r3, [r5, #12]

b238: 0072 lsls r2, r6, #1

b23a: 2103 movs r1, #3

b23c: fa01 f202 lsl.w r2, r1, r2

b240: 4313 orrs r3, r2

b242: 60eb str r3, [r5, #12]

b244: e7e6 b.n b214 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4580>

b246: f859 800b ldr.w r8, [r9, fp]

b24a: 4e7f ldr r6, [pc, #508] ; (b448 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47b4>)

b24c: 447e add r6, pc

b24e: f8d6 31b0 ldr.w r3, [r6, #432] ; 0x1b0

b252: 2b00 cmp r3, #0

b254: f000 8097 beq.w b386 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46f2>

b258: 68ec ldr r4, [r5, #12]

b25a: f014 0e01 ands.w lr, r4, #1

b25e: f040 8092 bne.w b386 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46f2>

b262: f1b8 0f00 cmp.w r8, #0

b266: f000 80e3 beq.w b430 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x479c>

b26a: 686a ldr r2, [r5, #4]

b26c: ab0e add r3, sp, #56 ; 0x38

b26e: 9306 str r3, [sp, #24]

b270: 46b3 mov fp, r6

b272: 461c mov r4, r3

b274: 68a9 ldr r1, [r5, #8]

b276: e9d2 230d ldrd r2, r3, [r2, #52] ; 0x34

b27a: f04f 3aff mov.w sl, #4294967295 ; 0xffffffff

b27e: f8d6 61ac ldr.w r6, [r6, #428] ; 0x1ac

b282: 46a9 mov r9, r5

b284: 685b ldr r3, [r3, #4]

b286: f8d2 c004 ldr.w ip, [r2, #4]

b28a: eb03 1301 add.w r3, r3, r1, lsl #4

b28e: cb0f ldmia r3, {r0, r1, r2, r3}

b290: e884 000f stmia.w r4, {r0, r1, r2, r3}

b294: 4674 mov r4, lr

b296: 692b ldr r3, [r5, #16]

b298: eb0c 0200 add.w r2, ip, r0

b29c: f8cd 803c str.w r8, [sp, #60] ; 0x3c

b2a0: 9207 str r2, [sp, #28]

b2a2: 930c str r3, [sp, #48] ; 0x30

b2a4: 6975 ldr r5, [r6, #20]

b2a6: 3401 adds r4, #1

b2a8: 2d00 cmp r5, #0

b2aa: d047 beq.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b2ac: ea4f 0844 mov.w r8, r4, lsl #1

b2b0: f8d9 300c ldr.w r3, [r9, #12]

b2b4: 2201 movs r2, #1

b2b6: fa02 f208 lsl.w r2, r2, r8

b2ba: 421a tst r2, r3

b2bc: d13e bne.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b2be: f8d9 0004 ldr.w r0, [r9, #4]

b2c2: 00e3 lsls r3, r4, #3

b2c4: f503 7316 add.w r3, r3, #600 ; 0x258

b2c8: f8d9 1008 ldr.w r1, [r9, #8]

b2cc: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

b2d0: 920d str r2, [sp, #52] ; 0x34

b2d2: 18fa adds r2, r7, r3

b2d4: 4403 add r3, r0

b2d6: 9807 ldr r0, [sp, #28]

b2d8: f10d 0c30 add.w ip, sp, #48 ; 0x30

b2dc: f8cd c004 str.w ip, [sp, #4]

b2e0: 9002 str r0, [sp, #8]

b2e2: 9805 ldr r0, [sp, #20]

b2e4: 9000 str r0, [sp, #0]

b2e6: a80d add r0, sp, #52 ; 0x34

b2e8: 9003 str r0, [sp, #12]

b2ea: 9806 ldr r0, [sp, #24]

b2ec: 47a8 blx r5

b2ee: 9b0f ldr r3, [sp, #60] ; 0x3c

b2f0: 2102 movs r1, #2

b2f2: 4283 cmp r3, r0

b2f4: 9b0c ldr r3, [sp, #48] ; 0x30

b2f6: bf1e ittt ne

b2f8: 900f strne r0, [sp, #60] ; 0x3c

b2fa: f043 0310 orrne.w r3, r3, #16

b2fe: 930c strne r3, [sp, #48] ; 0x30

b300: f003 0303 and.w r3, r3, #3

b304: f8d9 200c ldr.w r2, [r9, #12]

b308: fa03 f308 lsl.w r3, r3, r8

b30c: fa01 f808 lsl.w r8, r1, r8

b310: 4313 orrs r3, r2

b312: f8c9 300c str.w r3, [r9, #12]

b316: ea18 0f03 tst.w r8, r3

b31a: d10f bne.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b31c: 9b0d ldr r3, [sp, #52] ; 0x34

b31e: 1c5a adds r2, r3, #1

b320: d00c beq.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b322: f11a 0f02 cmn.w sl, #2

b326: d009 beq.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b328: f1ba 3fff cmp.w sl, #4294967295 ; 0xffffffff

b32c: bf08 it eq

b32e: 469a moveq sl, r3

b330: d004 beq.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b332: 459a cmp sl, r3

b334: d002 beq.n b33c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46a8>

b336: 459a cmp sl, r3

b338: bfb8 it lt

b33a: 469a movlt sl, r3

b33c: f8db 31b0 ldr.w r3, [fp, #432] ; 0x1b0

b340: 6a36 ldr r6, [r6, #32]

b342: 42a3 cmp r3, r4

b344: d8ae bhi.n b2a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4610>

b346: f8dd 803c ldr.w r8, [sp, #60] ; 0x3c

b34a: 9b1c ldr r3, [sp, #112] ; 0x70

b34c: 4641 mov r1, r8

b34e: 9804 ldr r0, [sp, #16]

b350: f8c3 a000 str.w sl, [r3]

b354: f001 fb74 bl ca40 <\_dl\_mcount@@GLIBC\_2.4>

b358: 4640 mov r0, r8

b35a: b013 add sp, #76 ; 0x4c

b35c: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

b360: 9b0d ldr r3, [sp, #52] ; 0x34

b362: e9dd 970a ldrd r9, r7, [sp, #40] ; 0x28

b366: f8dd 803c ldr.w r8, [sp, #60] ; 0x3c

b36a: 612b str r3, [r5, #16]

b36c: 4b37 ldr r3, [pc, #220] ; (b44c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47b8>)

b36e: 447b add r3, pc

b370: 6b1b ldr r3, [r3, #48] ; 0x30

b372: 2b00 cmp r3, #0

b374: f47f af69 bne.w b24a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x45b6>

b378: f849 800b str.w r8, [r9, fp]

b37c: 2301 movs r3, #1

b37e: f3bf 8f5b dmb ish

b382: 616b str r3, [r5, #20]

b384: e761 b.n b24a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x45b6>

b386: f04f 3aff mov.w sl, #4294967295 ; 0xffffffff

b38a: e7de b.n b34a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46b6>

b38c: 9a1c ldr r2, [sp, #112] ; 0x70

b38e: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

b392: 6013 str r3, [r2, #0]

b394: f7ff fdc2 bl af1c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4288>

b398: 4680 mov r8, r0

b39a: 4640 mov r0, r8

b39c: b013 add sp, #76 ; 0x4c

b39e: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

b3a2: 89d9 ldrh r1, [r3, #14]

b3a4: f64f 72f1 movw r2, #65521 ; 0xfff1

b3a8: 4291 cmp r1, r2

b3aa: 7b1a ldrb r2, [r3, #12]

b3ac: 6859 ldr r1, [r3, #4]

b3ae: f002 020f and.w r2, r2, #15

b3b2: bf18 it ne

b3b4: f8d7 8000 ldrne.w r8, [r7]

b3b8: 2a0a cmp r2, #10

b3ba: 4488 add r8, r1

b3bc: bf18 it ne

b3be: 463c movne r4, r7

b3c0: f47f aebe bne.w b140 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x44ac>

b3c4: 4b22 ldr r3, [pc, #136] ; (b450 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47bc>)

b3c6: 463c mov r4, r7

b3c8: 447b add r3, pc

b3ca: 6c18 ldr r0, [r3, #64] ; 0x40

b3cc: 47c0 blx r8

b3ce: 4680 mov r8, r0

b3d0: 9b0c ldr r3, [sp, #48] ; 0x30

b3d2: 2b00 cmp r3, #0

b3d4: f47f aeb4 bne.w b140 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x44ac>

b3d8: e7c8 b.n b36c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x46d8>

b3da: 2401 movs r4, #1

b3dc: 6074 str r4, [r6, #4]

b3de: 2405 movs r4, #5

b3e0: f3bf 8f5b dmb ish

b3e4: e684 b.n b0f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x445c>

b3e6: f2aa 40bc subw r0, sl, #1212 ; 0x4bc

b3ea: 2200 movs r2, #0

b3ec: f3bf 8f5b dmb ish

b3f0: e850 3f00 ldrex r3, [r0]

b3f4: e840 2100 strex r1, r2, [r0]

b3f8: 2900 cmp r1, #0

b3fa: d1f9 bne.n b3f0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x475c>

b3fc: 2b02 cmp r3, #2

b3fe: f47f ae8a bne.w b116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4482>

b402: 2201 movs r2, #1

b404: 2300 movs r3, #0

b406: 2181 movs r1, #129 ; 0x81

b408: f04f 0cf0 mov.w ip, #240 ; 0xf0

b40c: f005 fdd8 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

b410: e681 b.n b116 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4482>

b412: 4b10 ldr r3, [pc, #64] ; (b454 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47c0>)

b414: 447b add r3, pc

b416: 6c18 ldr r0, [r3, #64] ; 0x40

b418: 47c0 blx r8

b41a: 4680 mov r8, r0

b41c: e7d8 b.n b3d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x473c>

b41e: 4b0e ldr r3, [pc, #56] ; (b458 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47c4>)

b420: 22e5 movs r2, #229 ; 0xe5

b422: 490e ldr r1, [pc, #56] ; (b45c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47c8>)

b424: 480e ldr r0, [pc, #56] ; (b460 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47cc>)

b426: 447b add r3, pc

b428: 4479 add r1, pc

b42a: 4478 add r0, pc

b42c: f005 faae bl 1098c <free@@GLIBC\_2.4+0x12c>

b430: 4b0c ldr r3, [pc, #48] ; (b464 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47d0>)

b432: f240 128f movw r2, #399 ; 0x18f

b436: 490c ldr r1, [pc, #48] ; (b468 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47d4>)

b438: 480c ldr r0, [pc, #48] ; (b46c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47d8>)

b43a: 447b add r3, pc

b43c: 4479 add r1, pc

b43e: 4478 add r0, pc

b440: f005 faa4 bl 1098c <free@@GLIBC\_2.4+0x12c>

b444: d828 bhi.n b498 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4804>

b446: 0001 movs r1, r0

b448: d720 bvc.n b48c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47f8>

b44a: 0001 movs r1, r0

b44c: d5fe bpl.n b44c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x47b8>

b44e: 0001 movs r1, r0

b450: d5a4 bpl.n b39c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4708>

b452: 0001 movs r1, r0

b454: d558 bpl.n b508 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4874>

b456: 0001 movs r1, r0

b458: b3da cbz r2, b4d2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x483e>

b45a: 0000 movs r0, r0

b45c: b36c cbz r4, b4ba <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4826>

b45e: 0000 movs r0, r0

b460: b37a cbz r2, b4c2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x482e>

b462: 0000 movs r0, r0

b464: b3c6 cbz r6, b4d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4844>

b466: 0000 movs r0, r0

b468: b358 cbz r0, b4c2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x482e>

b46a: 0000 movs r0, r0

b46c: b39a cbz r2, b4d6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4842>

b46e: 0000 movs r0, r0

b470: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

b474: 08cc lsrs r4, r1, #3

b476: 2118 movs r1, #24

b478: 4681 mov r9, r0

b47a: fb01 f104 mul.w r1, r1, r4

b47e: f8d0 41a4 ldr.w r4, [r0, #420] ; 0x1a4

b482: 4694 mov ip, r2

b484: 469e mov lr, r3

b486: f8df 808c ldr.w r8, [pc, #140] ; b514 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4880>

b48a: b08d sub sp, #52 ; 0x34

b48c: 1867 adds r7, r4, r1

b48e: f10d 0a20 add.w sl, sp, #32

b492: 44f8 add r8, pc

b494: e9d7 2001 ldrd r2, r0, [r7, #4]

b498: 5864 ldr r4, [r4, r1]

b49a: f8d8 51ac ldr.w r5, [r8, #428] ; 0x1ac

b49e: e9d2 230d ldrd r2, r3, [r2, #52] ; 0x34

b4a2: 685b ldr r3, [r3, #4]

b4a4: 6856 ldr r6, [r2, #4]

b4a6: eb03 1300 add.w r3, r3, r0, lsl #4

b4aa: cb0f ldmia r3, {r0, r1, r2, r3}

b4ac: e88a 000f stmia.w sl, {r0, r1, r2, r3}

b4b0: 1833 adds r3, r6, r0

b4b2: 9305 str r3, [sp, #20]

b4b4: f8d8 31b0 ldr.w r3, [r8, #432] ; 0x1b0

b4b8: 9409 str r4, [sp, #36] ; 0x24

b4ba: b33b cbz r3, b50c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4878>

b4bc: f509 7918 add.w r9, r9, #608 ; 0x260

b4c0: 2400 movs r4, #0

b4c2: f04f 0b02 mov.w fp, #2

b4c6: e9cd ce06 strd ip, lr, [sp, #24]

b4ca: 9504 str r5, [sp, #16]

b4cc: 9b04 ldr r3, [sp, #16]

b4ce: 0061 lsls r1, r4, #1

b4d0: fa4b f101 asr.w r1, fp, r1

b4d4: 699e ldr r6, [r3, #24]

b4d6: b18e cbz r6, b4fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4868>

b4d8: 68fd ldr r5, [r7, #12]

b4da: 00e2 lsls r2, r4, #3

b4dc: f502 7318 add.w r3, r2, #608 ; 0x260

b4e0: 4650 mov r0, sl

b4e2: 4229 tst r1, r5

b4e4: 444a add r2, r9

b4e6: d109 bne.n b4fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4868>

b4e8: 9d05 ldr r5, [sp, #20]

b4ea: e9d7 c101 ldrd ip, r1, [r7, #4]

b4ee: 9502 str r5, [sp, #8]

b4f0: 4463 add r3, ip

b4f2: 9d07 ldr r5, [sp, #28]

b4f4: 9501 str r5, [sp, #4]

b4f6: 9d06 ldr r5, [sp, #24]

b4f8: 9500 str r5, [sp, #0]

b4fa: 47b0 blx r6

b4fc: 9a04 ldr r2, [sp, #16]

b4fe: 3401 adds r4, #1

b500: f8d8 31b0 ldr.w r3, [r8, #432] ; 0x1b0

b504: 6a12 ldr r2, [r2, #32]

b506: 42a3 cmp r3, r4

b508: 9204 str r2, [sp, #16]

b50a: d8df bhi.n b4cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4838>

b50c: b00d add sp, #52 ; 0x34

b50e: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

b512: bf00 nop

b514: d4da bmi.n b4cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4838>

b516: 0001 movs r1, r0

b518: f8d0 c004 ldr.w ip, [r0, #4]

b51c: e92d 43f8 stmdb sp!, {r3, r4, r5, r6, r7, r8, r9, lr}

b520: f890 4198 ldrb.w r4, [r0, #408] ; 0x198

b524: f044 0408 orr.w r4, r4, #8

b528: f880 4198 strb.w r4, [r0, #408] ; 0x198

b52c: f89c 4000 ldrb.w r4, [ip]

b530: 2c00 cmp r4, #0

b532: d047 beq.n b5c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4930>

b534: 4698 mov r8, r3

b536: 6d03 ldr r3, [r0, #80] ; 0x50

b538: 4617 mov r7, r2

b53a: 460e mov r6, r1

b53c: 4605 mov r5, r0

b53e: b31b cbz r3, b588 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48f4>

b540: 4a23 ldr r2, [pc, #140] ; (b5d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x493c>)

b542: 447a add r2, pc

b544: 6812 ldr r2, [r2, #0]

b546: 0791 lsls r1, r2, #30

b548: d427 bmi.n b59a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4906>

b54a: 685c ldr r4, [r3, #4]

b54c: 4642 mov r2, r8

b54e: 682b ldr r3, [r5, #0]

b550: 4639 mov r1, r7

b552: 4630 mov r0, r6

b554: 4423 add r3, r4

b556: 4798 blx r3

b558: f8d5 3084 ldr.w r3, [r5, #132] ; 0x84

b55c: b193 cbz r3, b584 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48f0>

b55e: f8d5 108c ldr.w r1, [r5, #140] ; 0x8c

b562: 685c ldr r4, [r3, #4]

b564: 682a ldr r2, [r5, #0]

b566: 684b ldr r3, [r1, #4]

b568: 4414 add r4, r2

b56a: ea5f 0993 movs.w r9, r3, lsr #2

b56e: d009 beq.n b584 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48f0>

b570: eb04 0989 add.w r9, r4, r9, lsl #2

b574: f854 5b04 ldr.w r5, [r4], #4

b578: 4642 mov r2, r8

b57a: 4639 mov r1, r7

b57c: 4630 mov r0, r6

b57e: 47a8 blx r5

b580: 45a1 cmp r9, r4

b582: d1f7 bne.n b574 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48e0>

b584: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

b588: f8d0 3084 ldr.w r3, [r0, #132] ; 0x84

b58c: 2b00 cmp r3, #0

b58e: d0f9 beq.n b584 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48f0>

b590: 4a10 ldr r2, [pc, #64] ; (b5d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4940>)

b592: 447a add r2, pc

b594: 6812 ldr r2, [r2, #0]

b596: 0792 lsls r2, r2, #30

b598: d5e1 bpl.n b55e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48ca>

b59a: b954 cbnz r4, b5b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x491e>

b59c: 4b0e ldr r3, [pc, #56] ; (b5d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4944>)

b59e: 447b add r3, pc

b5a0: 681b ldr r3, [r3, #0]

b5a2: f8d3 c000 ldr.w ip, [r3]

b5a6: f1bc 0f00 cmp.w ip, #0

b5aa: d102 bne.n b5b2 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x491e>

b5ac: f8df c02c ldr.w ip, [pc, #44] ; b5dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4948>

b5b0: 44fc add ip, pc

b5b2: 480b ldr r0, [pc, #44] ; (b5e0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x494c>)

b5b4: 4661 mov r1, ip

b5b6: 4478 add r0, pc

b5b8: f000 fbdc bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

b5bc: 6d2b ldr r3, [r5, #80] ; 0x50

b5be: 2b00 cmp r3, #0

b5c0: d0ca beq.n b558 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48c4>

b5c2: e7c2 b.n b54a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48b6>

b5c4: f890 5198 ldrb.w r5, [r0, #408] ; 0x198

b5c8: 07ad lsls r5, r5, #30

b5ca: d0db beq.n b584 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48f0>

b5cc: e7b2 b.n b534 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48a0>

b5ce: bf00 nop

b5d0: d42a bmi.n b628 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4994>

b5d2: 0001 movs r1, r0

b5d4: d3da bcc.n b58c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x48f8>

b5d6: 0001 movs r1, r0

b5d8: e2fa b.n bbd0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1f0>

b5da: 0001 movs r1, r0

b5dc: 9528 str r5, [sp, #160] ; 0xa0

b5de: 0000 movs r0, r0

b5e0: b26a sxtb r2, r5

b5e2: 0000 movs r0, r0

b5e4: e92d 47f0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, lr}

b5e8: 4606 mov r6, r0

b5ea: 4c30 ldr r4, [pc, #192] ; (b6ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a18>)

b5ec: 460f mov r7, r1

b5ee: f8d0 a0a0 ldr.w sl, [r0, #160] ; 0xa0

b5f2: 4690 mov r8, r2

b5f4: 447c add r4, pc

b5f6: f8d0 50a4 ldr.w r5, [r0, #164] ; 0xa4

b5fa: 4699 mov r9, r3

b5fc: f8d4 0500 ldr.w r0, [r4, #1280] ; 0x500

b600: b9f8 cbnz r0, b642 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49ae>

b602: f1ba 0f00 cmp.w sl, #0

b606: bf18 it ne

b608: 2d00 cmpne r5, #0

b60a: d126 bne.n b65a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49c6>

b60c: f8d6 4164 ldr.w r4, [r6, #356] ; 0x164

b610: f114 34ff adds.w r4, r4, #4294967295 ; 0xffffffff

b614: 464b mov r3, r9

b616: 4642 mov r2, r8

b618: 4639 mov r1, r7

b61a: d310 bcc.n b63e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49aa>

b61c: f8d6 01f8 ldr.w r0, [r6, #504] ; 0x1f8

b620: f850 5024 ldr.w r5, [r0, r4, lsl #2]

b624: 4628 mov r0, r5

b626: f895 5198 ldrb.w r5, [r5, #408] ; 0x198

b62a: 072d lsls r5, r5, #28

b62c: d4f0 bmi.n b610 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x497c>

b62e: f7ff ff73 bl b518 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4884>

b632: f114 34ff adds.w r4, r4, #4294967295 ; 0xffffffff

b636: 464b mov r3, r9

b638: 4642 mov r2, r8

b63a: 4639 mov r1, r7

b63c: d2ee bcs.n b61c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4988>

b63e: e8bd 87f0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, pc}

b642: f890 4198 ldrb.w r4, [r0, #408] ; 0x198

b646: 0724 lsls r4, r4, #28

b648: d401 bmi.n b64e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49ba>

b64a: f7ff ff65 bl b518 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4884>

b64e: 4b18 ldr r3, [pc, #96] ; (b6b0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a1c>)

b650: 2200 movs r2, #0

b652: 447b add r3, pc

b654: f8c3 2500 str.w r2, [r3, #1280] ; 0x500

b658: e7d3 b.n b602 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x496e>

b65a: 686d ldr r5, [r5, #4]

b65c: 2d03 cmp r5, #3

b65e: d9d5 bls.n b60c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4978>

b660: 4b14 ldr r3, [pc, #80] ; (b6b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a20>)

b662: 447b add r3, pc

b664: 681b ldr r3, [r3, #0]

b666: 079b lsls r3, r3, #30

b668: d40f bmi.n b68a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49f6>

b66a: f8da 4004 ldr.w r4, [sl, #4]

b66e: f025 0503 bic.w r5, r5, #3

b672: 6833 ldr r3, [r6, #0]

b674: 441c add r4, r3

b676: 4425 add r5, r4

b678: f854 3b04 ldr.w r3, [r4], #4

b67c: 464a mov r2, r9

b67e: 4641 mov r1, r8

b680: 4638 mov r0, r7

b682: 4798 blx r3

b684: 42a5 cmp r5, r4

b686: d1f7 bne.n b678 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49e4>

b688: e7c0 b.n b60c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4978>

b68a: 6871 ldr r1, [r6, #4]

b68c: 780b ldrb r3, [r1, #0]

b68e: b923 cbnz r3, b69a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a06>

b690: 4b09 ldr r3, [pc, #36] ; (b6b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a24>)

b692: 447b add r3, pc

b694: 681b ldr r3, [r3, #0]

b696: 6819 ldr r1, [r3, #0]

b698: b121 cbz r1, b6a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a10>

b69a: 4808 ldr r0, [pc, #32] ; (b6bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a28>)

b69c: 4478 add r0, pc

b69e: f000 fb69 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

b6a2: e7e2 b.n b66a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x49d6>

b6a4: 4906 ldr r1, [pc, #24] ; (b6c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a2c>)

b6a6: 4479 add r1, pc

b6a8: e7f7 b.n b69a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a06>

b6aa: bf00 nop

b6ac: da68 bge.n b780 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4aec>

b6ae: 0001 movs r1, r0

b6b0: da0a bge.n b6c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a34>

b6b2: 0001 movs r1, r0

b6b4: d30a bcc.n b6cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a38>

b6b6: 0001 movs r1, r0

b6b8: e206 b.n bac8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe8>

b6ba: 0001 movs r1, r0

b6bc: b198 cbz r0, b6e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a52>

b6be: 0000 movs r0, r0

b6c0: 9432 str r4, [sp, #200] ; 0xc8

b6c2: 0000 movs r0, r0

b6c4: 4bae ldr r3, [pc, #696] ; (b980 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cec>)

b6c6: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

b6ca: 447b add r3, pc

b6cc: b08f sub sp, #60 ; 0x3c

b6ce: f8d3 24c0 ldr.w r2, [r3, #1216] ; 0x4c0

b6d2: af00 add r7, sp, #0

b6d4: f1b2 0a01 subs.w sl, r2, #1

b6d8: f100 80dc bmi.w b894 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c00>

b6dc: 4aa9 ldr r2, [pc, #676] ; (b984 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cf0>)

b6de: 46d3 mov fp, sl

b6e0: f203 41c4 addw r1, r3, #1220 ; 0x4c4

b6e4: 607b str r3, [r7, #4]

b6e6: 447a add r2, pc

b6e8: e9c7 310c strd r3, r1, [r7, #48] ; 0x30

b6ec: 613a str r2, [r7, #16]

b6ee: f202 43c4 addw r3, r2, #1220 ; 0x4c4

b6f2: 60fb str r3, [r7, #12]

b6f4: 2300 movs r3, #0

b6f6: 62fb str r3, [r7, #44] ; 0x2c

b6f8: 687a ldr r2, [r7, #4]

b6fa: 234c movs r3, #76 ; 0x4c

b6fc: f8df 8288 ldr.w r8, [pc, #648] ; b988 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cf4>

b700: 465d mov r5, fp

b702: fb03 290b mla r9, r3, fp, r2

b706: 4ba1 ldr r3, [pc, #644] ; (b98c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cf8>)

b708: 44f8 add r8, pc

b70a: 447b add r3, pc

b70c: 62bb str r3, [r7, #40] ; 0x28

b70e: e00a b.n b726 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a92>

b710: f8d8 37fc ldr.w r3, [r8, #2044] ; 0x7fc

b714: f208 40c4 addw r0, r8, #1220 ; 0x4c4

b718: 3d01 subs r5, #1

b71a: 4798 blx r3

b71c: 1c6c adds r4, r5, #1

b71e: f1a9 094c sub.w r9, r9, #76 ; 0x4c

b722: f000 80af beq.w b884 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bf0>

b726: 6b3b ldr r3, [r7, #48] ; 0x30

b728: 6b78 ldr r0, [r7, #52] ; 0x34

b72a: f8d3 37f8 ldr.w r3, [r3, #2040] ; 0x7f8

b72e: 4798 blx r3

b730: f8d9 0004 ldr.w r0, [r9, #4]

b734: 2800 cmp r0, #0

b736: d0eb beq.n b710 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a7c>

b738: f8d9 3000 ldr.w r3, [r9]

b73c: 6af9 ldr r1, [r7, #44] ; 0x2c

b73e: f893 2199 ldrb.w r2, [r3, #409] ; 0x199

b742: f3c2 02c0 ubfx r2, r2, #3, #1

b746: 428a cmp r2, r1

b748: d1e2 bne.n b710 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a7c>

b74a: 0082 lsls r2, r0, #2

b74c: f8c7 d01c str.w sp, [r7, #28]

b750: 3207 adds r2, #7

b752: 2600 movs r6, #0

b754: f022 0207 bic.w r2, r2, #7

b758: ebad 0d02 sub.w sp, sp, r2

b75c: 466c mov r4, sp

b75e: e001 b.n b764 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4ad0>

b760: 68db ldr r3, [r3, #12]

b762: b193 cbz r3, b78a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4af6>

b764: 695a ldr r2, [r3, #20]

b766: 429a cmp r2, r3

b768: d1fa bne.n b760 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4acc>

b76a: 42b0 cmp r0, r6

b76c: f240 80ed bls.w b94a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cb6>

b770: f8d3 2194 ldr.w r2, [r3, #404] ; 0x194

b774: f844 3026 str.w r3, [r4, r6, lsl #2]

b778: 3201 adds r2, #1

b77a: f8c3 6214 str.w r6, [r3, #532] ; 0x214

b77e: f8c3 2194 str.w r2, [r3, #404] ; 0x194

b782: 3601 adds r6, #1

b784: 68db ldr r3, [r3, #12]

b786: 2b00 cmp r3, #0

b788: d1ec bne.n b764 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4ad0>

b78a: 1a33 subs r3, r6, r0

b78c: fab3 f383 clz r3, r3

b790: 095b lsrs r3, r3, #5

b792: 2d00 cmp r5, #0

b794: bf0c ite eq

b796: 461a moveq r2, r3

b798: 2201 movne r2, #1

b79a: 2a00 cmp r2, #0

b79c: f000 80e7 beq.w b96e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cda>

b7a0: fab5 f185 clz r1, r5

b7a4: 0949 lsrs r1, r1, #5

b7a6: 430b orrs r3, r1

b7a8: f040 8098 bne.w b8dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c48>

b7ac: 3801 subs r0, #1

b7ae: 4286 cmp r6, r0

b7b0: f040 80d4 bne.w b95c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cc8>

b7b4: 4620 mov r0, r4

b7b6: 2301 movs r3, #1

b7b8: 1a71 subs r1, r6, r1

b7ba: 2200 movs r2, #0

b7bc: f003 fdba bl f334 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x14>

b7c0: 693b ldr r3, [r7, #16]

b7c2: 68f8 ldr r0, [r7, #12]

b7c4: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

b7c8: 4798 blx r3

b7ca: 2e00 cmp r6, #0

b7cc: d052 beq.n b874 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4be0>

b7ce: 4b70 ldr r3, [pc, #448] ; (b990 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cfc>)

b7d0: 4a70 ldr r2, [pc, #448] ; (b994 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d00>)

b7d2: 447b add r3, pc

b7d4: 627b str r3, [r7, #36] ; 0x24

b7d6: 1f23 subs r3, r4, #4

b7d8: 447a add r2, pc

b7da: 60ba str r2, [r7, #8]

b7dc: 2200 movs r2, #0

b7de: f8c7 9018 str.w r9, [r7, #24]

b7e2: 469b mov fp, r3

b7e4: 4691 mov r9, r2

b7e6: 623d str r5, [r7, #32]

b7e8: f8c7 8014 str.w r8, [r7, #20]

b7ec: f85b 4f04 ldr.w r4, [fp, #4]!

b7f0: f894 3198 ldrb.w r3, [r4, #408] ; 0x198

b7f4: 071d lsls r5, r3, #28

b7f6: d530 bpl.n b85a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bc6>

b7f8: f8d4 2088 ldr.w r2, [r4, #136] ; 0x88

b7fc: f36f 03c3 bfc r3, #3, #1

b800: f884 3198 strb.w r3, [r4, #408] ; 0x198

b804: 2a00 cmp r2, #0

b806: d04e beq.n b8a6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c12>

b808: 6abb ldr r3, [r7, #40] ; 0x28

b80a: 681b ldr r3, [r3, #0]

b80c: 0799 lsls r1, r3, #30

b80e: d452 bmi.n b8b6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c22>

b810: f8d4 3090 ldr.w r3, [r4, #144] ; 0x90

b814: 6851 ldr r1, [r2, #4]

b816: 6822 ldr r2, [r4, #0]

b818: 685b ldr r3, [r3, #4]

b81a: eb02 0a01 add.w sl, r2, r1

b81e: 089b lsrs r3, r3, #2

b820: bf1e ittt ne

b822: f103 4380 addne.w r3, r3, #1073741824 ; 0x40000000

b826: f103 33ff addne.w r3, r3, #4294967295 ; 0xffffffff

b82a: eb0a 0883 addne.w r8, sl, r3, lsl #2

b82e: d101 bne.n b834 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4ba0>

b830: e006 b.n b840 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bac>

b832: 46a8 mov r8, r5

b834: 4645 mov r5, r8

b836: f855 3904 ldr.w r3, [r5], #-4

b83a: 4798 blx r3

b83c: 45c2 cmp sl, r8

b83e: d1f8 bne.n b832 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b9e>

b840: 6d63 ldr r3, [r4, #84] ; 0x54

b842: b11b cbz r3, b84c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bb8>

b844: 685a ldr r2, [r3, #4]

b846: 6823 ldr r3, [r4, #0]

b848: 4413 add r3, r2

b84a: 4798 blx r3

b84c: 6afb ldr r3, [r7, #44] ; 0x2c

b84e: b923 cbnz r3, b85a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bc6>

b850: 6a7b ldr r3, [r7, #36] ; 0x24

b852: f8d3 31b0 ldr.w r3, [r3, #432] ; 0x1b0

b856: 2b00 cmp r3, #0

b858: d145 bne.n b8e6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c52>

b85a: f109 0901 add.w r9, r9, #1

b85e: f8d4 3194 ldr.w r3, [r4, #404] ; 0x194

b862: 454e cmp r6, r9

b864: f103 33ff add.w r3, r3, #4294967295 ; 0xffffffff

b868: f8c4 3194 str.w r3, [r4, #404] ; 0x194

b86c: d1be bne.n b7ec <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b58>

b86e: 6a3d ldr r5, [r7, #32]

b870: e9d7 8905 ldrd r8, r9, [r7, #20]

b874: 3d01 subs r5, #1

b876: f1a9 094c sub.w r9, r9, #76 ; 0x4c

b87a: 1c6c adds r4, r5, #1

b87c: f8d7 d01c ldr.w sp, [r7, #28]

b880: f47f af51 bne.w b726 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a92>

b884: 6afb ldr r3, [r7, #44] ; 0x2c

b886: b92b cbnz r3, b894 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c00>

b888: 4b43 ldr r3, [pc, #268] ; (b998 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d04>)

b88a: 447b add r3, pc

b88c: f8d3 31b0 ldr.w r3, [r3, #432] ; 0x1b0

b890: 2b00 cmp r3, #0

b892: d141 bne.n b918 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c84>

b894: 4b41 ldr r3, [pc, #260] ; (b99c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d08>)

b896: 447b add r3, pc

b898: 681b ldr r3, [r3, #0]

b89a: 0618 lsls r0, r3, #24

b89c: d44a bmi.n b934 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4ca0>

b89e: 373c adds r7, #60 ; 0x3c

b8a0: 46bd mov sp, r7

b8a2: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

b8a6: 6d63 ldr r3, [r4, #84] ; 0x54

b8a8: 2b00 cmp r3, #0

b8aa: d0cf beq.n b84c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bb8>

b8ac: 4a3c ldr r2, [pc, #240] ; (b9a0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d0c>)

b8ae: 447a add r2, pc

b8b0: 6812 ldr r2, [r2, #0]

b8b2: 0792 lsls r2, r2, #30

b8b4: d5c6 bpl.n b844 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bb0>

b8b6: 6861 ldr r1, [r4, #4]

b8b8: 780b ldrb r3, [r1, #0]

b8ba: b92b cbnz r3, b8c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c34>

b8bc: 4b39 ldr r3, [pc, #228] ; (b9a4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d10>)

b8be: 447b add r3, pc

b8c0: 681b ldr r3, [r3, #0]

b8c2: 6819 ldr r1, [r3, #0]

b8c4: 2900 cmp r1, #0

b8c6: d032 beq.n b92e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c9a>

b8c8: 4837 ldr r0, [pc, #220] ; (b9a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d14>)

b8ca: 6a3a ldr r2, [r7, #32]

b8cc: 4478 add r0, pc

b8ce: f000 fa51 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

b8d2: f8d4 2088 ldr.w r2, [r4, #136] ; 0x88

b8d6: 2a00 cmp r2, #0

b8d8: d0b2 beq.n b840 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bac>

b8da: e799 b.n b810 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b7c>

b8dc: 1d20 adds r0, r4, #4

b8de: 2d00 cmp r5, #0

b8e0: f43f af69 beq.w b7b6 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b22>

b8e4: e766 b.n b7b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4b20>

b8e6: 6a7b ldr r3, [r7, #36] ; 0x24

b8e8: f504 7518 add.w r5, r4, #608 ; 0x260

b8ec: 603c str r4, [r7, #0]

b8ee: f8d7 a02c ldr.w sl, [r7, #44] ; 0x2c

b8f2: 46a8 mov r8, r5

b8f4: f8d3 21ac ldr.w r2, [r3, #428] ; 0x1ac

b8f8: 68bc ldr r4, [r7, #8]

b8fa: 4615 mov r5, r2

b8fc: 69eb ldr r3, [r5, #28]

b8fe: b113 cbz r3, b906 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c72>

b900: eb08 00ca add.w r0, r8, sl, lsl #3

b904: 4798 blx r3

b906: f8d4 31b0 ldr.w r3, [r4, #432] ; 0x1b0

b90a: f10a 0a01 add.w sl, sl, #1

b90e: 6a2d ldr r5, [r5, #32]

b910: 4553 cmp r3, sl

b912: d8f3 bhi.n b8fc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c68>

b914: 683c ldr r4, [r7, #0]

b916: e7a0 b.n b85a <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4bc6>

b918: 4b24 ldr r3, [pc, #144] ; (b9ac <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d18>)

b91a: 2201 movs r2, #1

b91c: 62fa str r2, [r7, #44] ; 0x2c

b91e: 447b add r3, pc

b920: f8d3 34c0 ldr.w r3, [r3, #1216] ; 0x4c0

b924: f1b3 0b01 subs.w fp, r3, #1

b928: f57f aee6 bpl.w b6f8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4a64>

b92c: e7b2 b.n b894 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c00>

b92e: 4920 ldr r1, [pc, #128] ; (b9b0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d1c>)

b930: 4479 add r1, pc

b932: e7c9 b.n b8c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c34>

b934: 4b1f ldr r3, [pc, #124] ; (b9b4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d20>)

b936: 4820 ldr r0, [pc, #128] ; (b9b8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d24>)

b938: 447b add r3, pc

b93a: 4478 add r0, pc

b93c: f8d3 250c ldr.w r2, [r3, #1292] ; 0x50c

b940: f8d3 1508 ldr.w r1, [r3, #1288] ; 0x508

b944: f000 fa16 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

b948: e7a9 b.n b89e <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c0a>

b94a: 4b1c ldr r3, [pc, #112] ; (b9bc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d28>)

b94c: 224d movs r2, #77 ; 0x4d

b94e: 491c ldr r1, [pc, #112] ; (b9c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d2c>)

b950: 481c ldr r0, [pc, #112] ; (b9c4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d30>)

b952: 447b add r3, pc

b954: 4479 add r1, pc

b956: 4478 add r0, pc

b958: f005 f818 bl 1098c <free@@GLIBC\_2.4+0x12c>

b95c: 4b1a ldr r3, [pc, #104] ; (b9c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d34>)

b95e: 2258 movs r2, #88 ; 0x58

b960: 491a ldr r1, [pc, #104] ; (b9cc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d38>)

b962: 481b ldr r0, [pc, #108] ; (b9d0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d3c>)

b964: 447b add r3, pc

b966: 4479 add r1, pc

b968: 4478 add r0, pc

b96a: f005 f80f bl 1098c <free@@GLIBC\_2.4+0x12c>

b96e: 4b19 ldr r3, [pc, #100] ; (b9d4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d40>)

b970: 2257 movs r2, #87 ; 0x57

b972: 4919 ldr r1, [pc, #100] ; (b9d8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d44>)

b974: 4819 ldr r0, [pc, #100] ; (b9dc <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4d48>)

b976: 447b add r3, pc

b978: 4479 add r1, pc

b97a: 4478 add r0, pc

b97c: f005 f806 bl 1098c <free@@GLIBC\_2.4+0x12c>

b980: d992 bls.n b8a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c14>

b982: 0001 movs r1, r0

b984: d976 bls.n ba74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x94>

b986: 0001 movs r1, r0

b988: d954 bls.n ba34 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x54>

b98a: 0001 movs r1, r0

b98c: d262 bcs.n ba54 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x74>

b98e: 0001 movs r1, r0

b990: d19a bne.n b8c8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c34>

b992: 0001 movs r1, r0

b994: d194 bne.n b8c0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c2c>

b996: 0001 movs r1, r0

b998: d0e2 beq.n b960 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4ccc>

b99a: 0001 movs r1, r0

b99c: d0d6 beq.n b94c <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4cb8>

b99e: 0001 movs r1, r0

b9a0: d0be beq.n b920 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4c8c>

b9a2: 0001 movs r1, r0

b9a4: dfda svc 218 ; 0xda

b9a6: 0001 movs r1, r0

b9a8: aff4 add r7, sp, #976 ; 0x3d0

b9aa: 0000 movs r0, r0

b9ac: d73e bvc.n ba2c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4c>

b9ae: 0001 movs r1, r0

b9b0: 91a8 str r1, [sp, #672] ; 0x2a0

b9b2: 0000 movs r0, r0

b9b4: d724 bvc.n ba00 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x20>

b9b6: 0001 movs r1, r0

b9b8: afa2 add r7, sp, #648 ; 0x288

b9ba: 0000 movs r0, r0

b9bc: b002 add sp, #8

b9be: 0000 movs r0, r0

b9c0: aef8 add r6, sp, #992 ; 0x3e0

b9c2: 0000 movs r0, r0

b9c4: af02 add r7, sp, #8

b9c6: 0000 movs r0, r0

b9c8: aff0 add r7, sp, #960 ; 0x3c0

b9ca: 0000 movs r0, r0

b9cc: aee6 add r6, sp, #920 ; 0x398

b9ce: 0000 movs r0, r0

b9d0: af20 add r7, sp, #128 ; 0x80

b9d2: 0000 movs r0, r0

b9d4: afde add r7, sp, #888 ; 0x378

b9d6: 0000 movs r0, r0

b9d8: aed4 add r6, sp, #848 ; 0x350

b9da: 0000 movs r0, r0

b9dc: aeea add r6, sp, #936 ; 0x3a8

...

0000b9e0 <\_dl\_debug\_state@@GLIBC\_PRIVATE>:

b9e0: 4770 bx lr

b9e2: bf00 nop

b9e4: b410 push {r4}

b9e6: 4c15 ldr r4, [pc, #84] ; (ba3c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5c>)

b9e8: 447c add r4, pc

b9ea: b1d1 cbz r1, ba22 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x42>

b9ec: 224c movs r2, #76 ; 0x4c

b9ee: 4b14 ldr r3, [pc, #80] ; (ba40 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x60>)

b9f0: fb02 f201 mul.w r2, r2, r1

b9f4: 447b add r3, pc

b9f6: 3238 adds r2, #56 ; 0x38

b9f8: 4413 add r3, r2

b9fa: 685a ldr r2, [r3, #4]

b9fc: b1b2 cbz r2, ba2c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4c>

b9fe: b160 cbz r0, ba1a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3a>

ba00: 2201 movs r2, #1

ba02: 601a str r2, [r3, #0]

ba04: 224c movs r2, #76 ; 0x4c

ba06: 4c0f ldr r4, [pc, #60] ; (ba44 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x64>)

ba08: fb02 f101 mul.w r1, r2, r1

ba0c: 447c add r4, pc

ba0e: 4a0e ldr r2, [pc, #56] ; (ba48 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x68>)

ba10: 6118 str r0, [r3, #16]

ba12: 447a add r2, pc

ba14: 5861 ldr r1, [r4, r1]

ba16: e9c3 1201 strd r1, r2, [r3, #4]

ba1a: 4618 mov r0, r3

ba1c: f85d 4b04 ldr.w r4, [sp], #4

ba20: 4770 bx lr

ba22: 4b0a ldr r3, [pc, #40] ; (ba4c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x6c>)

ba24: 58e3 ldr r3, [r4, r3]

ba26: 685a ldr r2, [r3, #4]

ba28: 2a00 cmp r2, #0

ba2a: d1e8 bne.n b9fe <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1e>

ba2c: 2201 movs r2, #1

ba2e: 601a str r2, [r3, #0]

ba30: 2800 cmp r0, #0

ba32: d1e7 bne.n ba04 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x24>

ba34: 4a05 ldr r2, [pc, #20] ; (ba4c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x6c>)

ba36: 58a2 ldr r2, [r4, r2]

ba38: 6910 ldr r0, [r2, #16]

ba3a: e7e3 b.n ba04 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x24>

ba3c: d614 bvs.n ba68 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x88>

ba3e: 0001 movs r1, r0

ba40: d668 bvs.n bb14 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x134>

ba42: 0001 movs r1, r0

ba44: d650 bvs.n bae8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x108>

ba46: 0001 movs r1, r0

ba48: ffcb ffff ; <UNDEFINED> instruction: 0xffcbffff

ba4c: 0050 lsls r0, r2, #1

ba4e: 0000 movs r0, r0

ba50: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

ba54: f5ad 7d0b sub.w sp, sp, #556 ; 0x22c

ba58: af02 add r7, sp, #8

ba5a: 2400 movs r4, #0

ba5c: 4625 mov r5, r4

ba5e: 4698 mov r8, r3

ba60: 60fc str r4, [r7, #12]

ba62: 4614 mov r4, r2

ba64: 7823 ldrb r3, [r4, #0]

ba66: 4689 mov r9, r1

ba68: 6038 str r0, [r7, #0]

ba6a: f107 0620 add.w r6, r7, #32

ba6e: f107 0a14 add.w sl, r7, #20

ba72: 2b00 cmp r3, #0

ba74: d03a beq.n baec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x10c>

ba76: f1b9 0f01 cmp.w r9, #1

ba7a: d051 beq.n bb20 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x140>

ba7c: f1b3 0225 subs.w r2, r3, #37 ; 0x25

ba80: bf18 it ne

ba82: 2201 movne r2, #1

ba84: 2b00 cmp r3, #0

ba86: bf08 it eq

ba88: 2200 moveq r2, #0

ba8a: 2a00 cmp r2, #0

ba8c: f000 80eb beq.w bc66 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x286>

ba90: fab9 f189 clz r1, r9

ba94: 4622 mov r2, r4

ba96: 0949 lsrs r1, r1, #5

ba98: 2b0a cmp r3, #10

ba9a: bf0c ite eq

ba9c: 460b moveq r3, r1

ba9e: f041 0301 orrne.w r3, r1, #1

baa2: b943 cbnz r3, bab6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xd6>

baa4: e0fe b.n bca4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2c4>

baa6: 2b0a cmp r3, #10

baa8: bf0c ite eq

baaa: 460b moveq r3, r1

baac: f041 0301 orrne.w r3, r1, #1

bab0: 2b00 cmp r3, #0

bab2: f000 8097 beq.w bbe4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x204>

bab6: f812 3f01 ldrb.w r3, [r2, #1]!

baba: 2b00 cmp r3, #0

babc: bf18 it ne

babe: 2b25 cmpne r3, #37 ; 0x25

bac0: d1f1 bne.n baa6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xc6>

bac2: 2d3f cmp r5, #63 ; 0x3f

bac4: f300 8100 bgt.w bcc8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2e8>

bac8: 1b10 subs r0, r2, r4

baca: eb06 0cc5 add.w ip, r6, r5, lsl #3

bace: 4631 mov r1, r6

bad0: f8cc 0004 str.w r0, [ip, #4]

bad4: b110 cbz r0, badc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfc>

bad6: f846 4035 str.w r4, [r6, r5, lsl #3]

bada: 3501 adds r5, #1

badc: 2b25 cmp r3, #37 ; 0x25

bade: d031 beq.n bb44 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x164>

bae0: 2b0a cmp r3, #10

bae2: d010 beq.n bb06 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x126>

bae4: 4614 mov r4, r2

bae6: 7823 ldrb r3, [r4, #0]

bae8: 2b00 cmp r3, #0

baea: d1c4 bne.n ba76 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x96>

baec: f107 0120 add.w r1, r7, #32

baf0: 6838 ldr r0, [r7, #0]

baf2: 462a mov r2, r5

baf4: f04f 0c92 mov.w ip, #146 ; 0x92

baf8: f005 fa62 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

bafc: f507 7709 add.w r7, r7, #548 ; 0x224

bb00: 46bd mov sp, r7

bb02: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

bb06: 4294 cmp r4, r2

bb08: f000 80bf beq.w bc8a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2aa>

bb0c: 1e6b subs r3, r5, #1

bb0e: eb06 03c3 add.w r3, r6, r3, lsl #3

bb12: 6858 ldr r0, [r3, #4]

bb14: 3001 adds r0, #1

bb16: 6058 str r0, [r3, #4]

bb18: 7853 ldrb r3, [r2, #1]

bb1a: 1c54 adds r4, r2, #1

bb1c: 2b00 cmp r3, #0

bb1e: d0e7 beq.n baf0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x110>

bb20: 68fb ldr r3, [r7, #12]

bb22: 2b00 cmp r3, #0

bb24: f000 8081 beq.w bc2a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x24a>

bb28: 2d3f cmp r5, #63 ; 0x3f

bb2a: f300 80c4 bgt.w bcb6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2d6>

bb2e: eb06 02c5 add.w r2, r6, r5, lsl #3

bb32: f846 a035 str.w sl, [r6, r5, lsl #3]

bb36: 210c movs r1, #12

bb38: 7823 ldrb r3, [r4, #0]

bb3a: 6051 str r1, [r2, #4]

bb3c: 3501 adds r5, #1

bb3e: f04f 39ff mov.w r9, #4294967295 ; 0xffffffff

bb42: e79b b.n ba7c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9c>

bb44: 7853 ldrb r3, [r2, #1]

bb46: 2b30 cmp r3, #48 ; 0x30

bb48: bf0b itete eq

bb4a: 60bb streq r3, [r7, #8]

bb4c: 1c54 addne r4, r2, #1

bb4e: 7893 ldrbeq r3, [r2, #2]

bb50: 2220 movne r2, #32

bb52: bf0c ite eq

bb54: 1c94 addeq r4, r2, #2

bb56: 60ba strne r2, [r7, #8]

bb58: 2b2a cmp r3, #42 ; 0x2a

bb5a: bf09 itett eq

bb5c: 7863 ldrbeq r3, [r4, #1]

bb5e: f04f 3bff movne.w fp, #4294967295 ; 0xffffffff

bb62: f858 bb04 ldreq.w fp, [r8], #4

bb66: 3401 addeq r4, #1

bb68: 2b2e cmp r3, #46 ; 0x2e

bb6a: bf18 it ne

bb6c: f04f 32ff movne.w r2, #4294967295 ; 0xffffffff

bb70: d03a beq.n bbe8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x208>

bb72: 2b5a cmp r3, #90 ; 0x5a

bb74: bf18 it ne

bb76: 2b6c cmpne r3, #108 ; 0x6c

bb78: bf04 itt eq

bb7a: 7863 ldrbeq r3, [r4, #1]

bb7c: 3401 addeq r4, #1

bb7e: 2b73 cmp r3, #115 ; 0x73

bb80: d03e beq.n bc00 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x220>

bb82: d80b bhi.n bb9c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1bc>

bb84: 2b25 cmp r3, #37 ; 0x25

bb86: d132 bne.n bbee <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x20e>

bb88: eb06 03c5 add.w r3, r6, r5, lsl #3

bb8c: 2201 movs r2, #1

bb8e: f846 4035 str.w r4, [r6, r5, lsl #3]

bb92: 4415 add r5, r2

bb94: 605a str r2, [r3, #4]

bb96: 1c62 adds r2, r4, #1

bb98: 4614 mov r4, r2

bb9a: e7a4 b.n bae6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x106>

bb9c: 2b75 cmp r3, #117 ; 0x75

bb9e: d11e bne.n bbde <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1fe>

bba0: 4642 mov r2, r8

bba2: b084 sub sp, #16

bba4: f852 0b04 ldr.w r0, [r2], #4

bba8: 2b78 cmp r3, #120 ; 0x78

bbaa: f10d 0814 add.w r8, sp, #20

bbae: f04f 0100 mov.w r1, #0

bbb2: bf0c ite eq

bbb4: 2310 moveq r3, #16

bbb6: 230a movne r3, #10

bbb8: 607a str r2, [r7, #4]

bbba: 4642 mov r2, r8

bbbc: 9100 str r1, [sp, #0]

bbbe: f004 ff51 bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

bbc2: f1bb 3fff cmp.w fp, #4294967295 ; 0xffffffff

bbc6: eba8 0300 sub.w r3, r8, r0

bbca: d154 bne.n bc76 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x296>

bbcc: eb06 02c5 add.w r2, r6, r5, lsl #3

bbd0: f846 0035 str.w r0, [r6, r5, lsl #3]

bbd4: f8d7 8004 ldr.w r8, [r7, #4]

bbd8: 3501 adds r5, #1

bbda: 6053 str r3, [r2, #4]

bbdc: e7db b.n bb96 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1b6>

bbde: 2b78 cmp r3, #120 ; 0x78

bbe0: d0de beq.n bba0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1c0>

bbe2: e004 b.n bbee <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x20e>

bbe4: 230a movs r3, #10

bbe6: e76c b.n bac2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe2>

bbe8: 7863 ldrb r3, [r4, #1]

bbea: 2b2a cmp r3, #42 ; 0x2a

bbec: d055 beq.n bc9a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2ba>

bbee: 4b3f ldr r3, [pc, #252] ; (bcec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x30c>)

bbf0: 22d8 movs r2, #216 ; 0xd8

bbf2: 493f ldr r1, [pc, #252] ; (bcf0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x310>)

bbf4: 483f ldr r0, [pc, #252] ; (bcf4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x314>)

bbf6: 447b add r3, pc

bbf8: 4479 add r1, pc

bbfa: 4478 add r0, pc

bbfc: f004 fec6 bl 1098c <free@@GLIBC\_2.4+0x12c>

bc00: f858 3b04 ldr.w r3, [r8], #4

bc04: 60ba str r2, [r7, #8]

bc06: 4618 mov r0, r3

bc08: f846 3035 str.w r3, [r6, r5, lsl #3]

bc0c: f006 fd98 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

bc10: 68ba ldr r2, [r7, #8]

bc12: eb06 03c5 add.w r3, r6, r5, lsl #3

bc16: 1c51 adds r1, r2, #1

bc18: bf08 it eq

bc1a: 6058 streq r0, [r3, #4]

bc1c: d003 beq.n bc26 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x246>

bc1e: 4282 cmp r2, r0

bc20: bf28 it cs

bc22: 4602 movcs r2, r0

bc24: 605a str r2, [r3, #4]

bc26: 3501 adds r5, #1

bc28: e7b5 b.n bb96 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1b6>

bc2a: f006 f931 bl 11e90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1128>

bc2e: 1e03 subs r3, r0, #0

bc30: 60fb str r3, [r7, #12]

bc32: db52 blt.n bcda <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2fa>

bc34: 68f9 ldr r1, [r7, #12]

bc36: 2200 movs r2, #0

bc38: 230a movs r3, #10

bc3a: 9200 str r2, [sp, #0]

bc3c: f107 021e add.w r2, r7, #30

bc40: ea4f 7ce1 mov.w ip, r1, asr #31

bc44: 4608 mov r0, r1

bc46: 468b mov fp, r1

bc48: 4661 mov r1, ip

bc4a: f004 ff0b bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

bc4e: 4550 cmp r0, sl

bc50: d904 bls.n bc5c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x27c>

bc52: 2320 movs r3, #32

bc54: f800 3d01 strb.w r3, [r0, #-1]!

bc58: 4550 cmp r0, sl

bc5a: d1fb bne.n bc54 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x274>

bc5c: f640 133a movw r3, #2362 ; 0x93a

bc60: f8aa 300a strh.w r3, [sl, #10]

bc64: e760 b.n bb28 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x148>

bc66: 2d3f cmp r5, #63 ; 0x3f

bc68: dc2e bgt.n bcc8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2e8>

bc6a: eb06 00c5 add.w r0, r6, r5, lsl #3

bc6e: 4631 mov r1, r6

bc70: 6042 str r2, [r0, #4]

bc72: 4622 mov r2, r4

bc74: e732 b.n badc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfc>

bc76: 459b cmp fp, r3

bc78: dda8 ble.n bbcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1ec>

bc7a: 68ba ldr r2, [r7, #8]

bc7c: f800 2d01 strb.w r2, [r0, #-1]!

bc80: eba8 0300 sub.w r3, r8, r0

bc84: 455b cmp r3, fp

bc86: dbf9 blt.n bc7c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x29c>

bc88: e7a0 b.n bbcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1ec>

bc8a: 00e8 lsls r0, r5, #3

bc8c: 4430 add r0, r6

bc8e: 2301 movs r3, #1

bc90: f846 4035 str.w r4, [r6, r5, lsl #3]

bc94: 441d add r5, r3

bc96: 6043 str r3, [r0, #4]

bc98: e73e b.n bb18 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x138>

bc9a: 78a3 ldrb r3, [r4, #2]

bc9c: 3402 adds r4, #2

bc9e: f858 2b04 ldr.w r2, [r8], #4

bca2: e766 b.n bb72 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x192>

bca4: 2d3f cmp r5, #63 ; 0x3f

bca6: dc0f bgt.n bcc8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2e8>

bca8: 00e8 lsls r0, r5, #3

bcaa: 4631 mov r1, r6

bcac: eb06 0c00 add.w ip, r6, r0

bcb0: f8cc 3004 str.w r3, [ip, #4]

bcb4: e7ea b.n bc8c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x2ac>

bcb6: 4b10 ldr r3, [pc, #64] ; (bcf8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x318>)

bcb8: 226b movs r2, #107 ; 0x6b

bcba: 4910 ldr r1, [pc, #64] ; (bcfc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x31c>)

bcbc: 4810 ldr r0, [pc, #64] ; (bd00 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x320>)

bcbe: 447b add r3, pc

bcc0: 4479 add r1, pc

bcc2: 4478 add r0, pc

bcc4: f004 fe62 bl 1098c <free@@GLIBC\_2.4+0x12c>

bcc8: 4b0e ldr r3, [pc, #56] ; (bd04 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x324>)

bcca: 2278 movs r2, #120 ; 0x78

bccc: 490e ldr r1, [pc, #56] ; (bd08 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x328>)

bcce: 480f ldr r0, [pc, #60] ; (bd0c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x32c>)

bcd0: 447b add r3, pc

bcd2: 4479 add r1, pc

bcd4: 4478 add r0, pc

bcd6: f004 fe59 bl 1098c <free@@GLIBC\_2.4+0x12c>

bcda: 4b0d ldr r3, [pc, #52] ; (bd10 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x330>)

bcdc: 2262 movs r2, #98 ; 0x62

bcde: 490d ldr r1, [pc, #52] ; (bd14 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x334>)

bce0: 480d ldr r0, [pc, #52] ; (bd18 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x338>)

bce2: 447b add r3, pc

bce4: 4479 add r1, pc

bce6: 4478 add r0, pc

bce8: f004 fe50 bl 1098c <free@@GLIBC\_2.4+0x12c>

bcec: adc6 add r5, sp, #792 ; 0x318

bcee: 0000 movs r0, r0

bcf0: ad68 add r5, sp, #416 ; 0x1a0

bcf2: 0000 movs r0, r0

bcf4: ada2 add r5, sp, #648 ; 0x288

bcf6: 0000 movs r0, r0

bcf8: acfe add r4, sp, #1016 ; 0x3f8

bcfa: 0000 movs r0, r0

bcfc: aca0 add r4, sp, #640 ; 0x280

bcfe: 0000 movs r0, r0

bd00: acca add r4, sp, #808 ; 0x328

bd02: 0000 movs r0, r0

bd04: acec add r4, sp, #944 ; 0x3b0

bd06: 0000 movs r0, r0

bd08: ac8e add r4, sp, #568 ; 0x238

bd0a: 0000 movs r0, r0

bd0c: acb8 add r4, sp, #736 ; 0x2e0

bd0e: 0000 movs r0, r0

bd10: acda add r4, sp, #872 ; 0x368

bd12: 0000 movs r0, r0

bd14: ac7c add r4, sp, #496 ; 0x1f0

bd16: 0000 movs r0, r0

bd18: ac86 add r4, sp, #536 ; 0x218

bd1a: 0000 movs r0, r0

bd1c: b570 push {r4, r5, r6, lr}

bd1e: 460c mov r4, r1

bd20: b09c sub sp, #112 ; 0x70

bd22: f44f 2100 mov.w r1, #524288 ; 0x80000

bd26: 4616 mov r6, r2

bd28: f005 ff68 bl 11bfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe94>

bd2c: 1e05 subs r5, r0, #0

bd2e: bfb8 it lt

bd30: f04f 34ff movlt.w r4, #4294967295 ; 0xffffffff

bd34: da02 bge.n bd3c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x35c>

bd36: 4620 mov r0, r4

bd38: b01c add sp, #112 ; 0x70

bd3a: bd70 pop {r4, r5, r6, pc}

bd3c: aa02 add r2, sp, #8

bd3e: 4629 mov r1, r5

bd40: 2003 movs r0, #3

bd42: f005 fe3b bl 119bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc54>

bd46: 2800 cmp r0, #0

bd48: db02 blt.n bd50 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x370>

bd4a: 990e ldr r1, [sp, #56] ; 0x38

bd4c: 6021 str r1, [r4, #0]

bd4e: b939 cbnz r1, bd60 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x380>

bd50: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

bd54: 4628 mov r0, r5

bd56: f005 fecf bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

bd5a: 4620 mov r0, r4

bd5c: b01c add sp, #112 ; 0x70

bd5e: bd70 pop {r4, r5, r6, pc}

bd60: 2000 movs r0, #0

bd62: 9500 str r5, [sp, #0]

bd64: 4632 mov r2, r6

bd66: 9001 str r0, [sp, #4]

bd68: 2302 movs r3, #2

bd6a: f006 f803 bl 11d74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x100c>

bd6e: 4604 mov r4, r0

bd70: e7f0 b.n bd54 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x374>

bd72: bf00 nop

bd74: b40f push {r0, r1, r2, r3}

bd76: 2101 movs r1, #1

bd78: b510 push {r4, lr}

bd7a: b082 sub sp, #8

bd7c: ac04 add r4, sp, #16

bd7e: 4b07 ldr r3, [pc, #28] ; (bd9c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3bc>)

bd80: f854 2b04 ldr.w r2, [r4], #4

bd84: 447b add r3, pc

bd86: 6a98 ldr r0, [r3, #40] ; 0x28

bd88: 4623 mov r3, r4

bd8a: 9401 str r4, [sp, #4]

bd8c: f7ff fe60 bl ba50 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x70>

bd90: b002 add sp, #8

bd92: e8bd 4010 ldmia.w sp!, {r4, lr}

bd96: b004 add sp, #16

bd98: 4770 bx lr

bd9a: bf00 nop

bd9c: cbe8 ldmia r3, {r3, r5, r6, r7}

bd9e: 0001 movs r1, r0

bda0: b40f push {r0, r1, r2, r3}

bda2: f04f 31ff mov.w r1, #4294967295 ; 0xffffffff

bda6: b510 push {r4, lr}

bda8: b082 sub sp, #8

bdaa: ac04 add r4, sp, #16

bdac: 4b06 ldr r3, [pc, #24] ; (bdc8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3e8>)

bdae: f854 2b04 ldr.w r2, [r4], #4

bdb2: 447b add r3, pc

bdb4: 6a98 ldr r0, [r3, #40] ; 0x28

bdb6: 4623 mov r3, r4

bdb8: 9401 str r4, [sp, #4]

bdba: f7ff fe49 bl ba50 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x70>

bdbe: b002 add sp, #8

bdc0: e8bd 4010 ldmia.w sp!, {r4, lr}

bdc4: b004 add sp, #16

bdc6: 4770 bx lr

bdc8: cbba ldmia r3, {r1, r3, r4, r5, r7}

bdca: 0001 movs r1, r0

bdcc: b40e push {r1, r2, r3}

bdce: 2100 movs r1, #0

bdd0: b510 push {r4, lr}

bdd2: b083 sub sp, #12

bdd4: ac05 add r4, sp, #20

bdd6: f854 2b04 ldr.w r2, [r4], #4

bdda: 4623 mov r3, r4

bddc: 9401 str r4, [sp, #4]

bdde: f7ff fe37 bl ba50 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x70>

bde2: b003 add sp, #12

bde4: e8bd 4010 ldmia.w sp!, {r4, lr}

bde8: b003 add sp, #12

bdea: 4770 bx lr

bdec: b538 push {r3, r4, r5, lr}

bdee: 460c mov r4, r1

bdf0: 6849 ldr r1, [r1, #4]

bdf2: 4605 mov r5, r0

bdf4: f006 f9ac bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

bdf8: b150 cbz r0, be10 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x430>

bdfa: 69e4 ldr r4, [r4, #28]

bdfc: b914 cbnz r4, be04 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x424>

bdfe: e009 b.n be14 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x434>

be00: 6864 ldr r4, [r4, #4]

be02: b13c cbz r4, be14 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x434>

be04: 6821 ldr r1, [r4, #0]

be06: 4628 mov r0, r5

be08: f006 f9a2 bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

be0c: 2800 cmp r0, #0

be0e: d1f7 bne.n be00 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x420>

be10: 2001 movs r0, #1

be12: bd38 pop {r3, r4, r5, pc}

be14: 4620 mov r0, r4

be16: bd38 pop {r3, r4, r5, pc}

be18: 4a17 ldr r2, [pc, #92] ; (be78 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x498>)

be1a: b410 push {r4}

be1c: 447a add r2, pc

be1e: f102 0478 add.w r4, r2, #120 ; 0x78

be22: 1aa1 subs r1, r4, r2

be24: 1089 asrs r1, r1, #2

be26: eb01 71d1 add.w r1, r1, r1, lsr #31

be2a: 1049 asrs r1, r1, #1

be2c: f852 3021 ldr.w r3, [r2, r1, lsl #2]

be30: eb02 0181 add.w r1, r2, r1, lsl #2

be34: 4298 cmp r0, r3

be36: d90a bls.n be4e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x46e>

be38: e01b b.n be72 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x492>

be3a: eb03 73d3 add.w r3, r3, r3, lsr #31

be3e: 105b asrs r3, r3, #1

be40: f852 4023 ldr.w r4, [r2, r3, lsl #2]

be44: eb02 0383 add.w r3, r2, r3, lsl #2

be48: 4284 cmp r4, r0

be4a: d309 bcc.n be60 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x480>

be4c: 4619 mov r1, r3

be4e: 1a8b subs r3, r1, r2

be50: 428a cmp r2, r1

be52: ea4f 03a3 mov.w r3, r3, asr #2

be56: d1f0 bne.n be3a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x45a>

be58: 6808 ldr r0, [r1, #0]

be5a: f85d 4b04 ldr.w r4, [sp], #4

be5e: 4770 bx lr

be60: 460c mov r4, r1

be62: 1d1a adds r2, r3, #4

be64: 42a2 cmp r2, r4

be66: d1dc bne.n be22 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x442>

be68: 4621 mov r1, r4

be6a: f85d 4b04 ldr.w r4, [sp], #4

be6e: 6808 ldr r0, [r1, #0]

be70: 4770 bx lr

be72: 460b mov r3, r1

be74: e7f5 b.n be62 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x482>

be76: bf00 nop

be78: 8430 strh r0, [r6, #32]

be7a: 0000 movs r0, r0

be7c: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

be80: b087 sub sp, #28

be82: 7803 ldrb r3, [r0, #0]

be84: 4680 mov r8, r0

be86: 9104 str r1, [sp, #16]

be88: 2b09 cmp r3, #9

be8a: bf18 it ne

be8c: 2b20 cmpne r3, #32

be8e: d105 bne.n be9c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4bc>

be90: f818 3f01 ldrb.w r3, [r8, #1]!

be94: 2b09 cmp r3, #9

be96: bf18 it ne

be98: 2b20 cmpne r3, #32

be9a: d0f9 beq.n be90 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4b0>

be9c: 2b2d cmp r3, #45 ; 0x2d

be9e: bf02 ittt eq

bea0: f108 0801 addeq.w r8, r8, #1

bea4: 2300 moveq r3, #0

bea6: 9305 streq r3, [sp, #20]

bea8: d006 beq.n beb8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4d8>

beaa: 2b2b cmp r3, #43 ; 0x2b

beac: f04f 0301 mov.w r3, #1

beb0: bf08 it eq

beb2: f108 0801 addeq.w r8, r8, #1

beb6: 9305 str r3, [sp, #20]

beb8: f898 3000 ldrb.w r3, [r8]

bebc: f1a3 0230 sub.w r2, r3, #48 ; 0x30

bec0: 2a09 cmp r2, #9

bec2: d873 bhi.n bfac <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5cc>

bec4: 2b30 cmp r3, #48 ; 0x30

bec6: bf1c itt ne

bec8: f04f 090a movne.w r9, #10

becc: f04f 0a09 movne.w sl, #9

bed0: d049 beq.n bf66 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x586>

bed2: 4649 mov r1, r9

bed4: f10a 0230 add.w r2, sl, #48 ; 0x30

bed8: 2400 movs r4, #0

beda: 9203 str r2, [sp, #12]

bedc: 2500 movs r5, #0

bede: 17ca asrs r2, r1, #31

bee0: e9cd 1200 strd r1, r2, [sp]

bee4: e005 b.n bef2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x512>

bee6: eb16 040a adds.w r4, r6, sl

beea: f818 3f01 ldrb.w r3, [r8, #1]!

beee: eb47 050b adc.w r5, r7, fp

bef2: 2b2f cmp r3, #47 ; 0x2f

bef4: f1a3 0261 sub.w r2, r3, #97 ; 0x61

bef8: d904 bls.n bf04 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x524>

befa: 9903 ldr r1, [sp, #12]

befc: f1a3 0030 sub.w r0, r3, #48 ; 0x30

bf00: 428b cmp r3, r1

bf02: d90d bls.n bf20 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x540>

bf04: f1b9 0f10 cmp.w r9, #16

bf08: f1a3 0141 sub.w r1, r3, #65 ; 0x41

bf0c: f1a3 0037 sub.w r0, r3, #55 ; 0x37

bf10: d13d bne.n bf8e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5ae>

bf12: 2a05 cmp r2, #5

bf14: bf98 it ls

bf16: f1a3 0057 subls.w r0, r3, #87 ; 0x57

bf1a: d901 bls.n bf20 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x540>

bf1c: 2905 cmp r1, #5

bf1e: d836 bhi.n bf8e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5ae>

bf20: e9dd ab00 ldrd sl, fp, [sp]

bf24: 17c7 asrs r7, r0, #31

bf26: 4606 mov r6, r0

bf28: 43c0 mvns r0, r0

bf2a: 43f9 mvns r1, r7

bf2c: 465b mov r3, fp

bf2e: 4652 mov r2, sl

bf30: f007 fe2e bl 13b90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e28>

bf34: fb09 f305 mul.w r3, r9, r5

bf38: fb04 330b mla r3, r4, fp, r3

bf3c: fba9 ab04 umull sl, fp, r9, r4

bf40: 449b add fp, r3

bf42: 428d cmp r5, r1

bf44: bf08 it eq

bf46: 4284 cmpeq r4, r0

bf48: d3cd bcc.n bee6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x506>

bf4a: 9b04 ldr r3, [sp, #16]

bf4c: 2b00 cmp r3, #0

bf4e: d03f beq.n bfd0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5f0>

bf50: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

bf54: f04f 35ff mov.w r5, #4294967295 ; 0xffffffff

bf58: f8c3 8000 str.w r8, [r3]

bf5c: 4620 mov r0, r4

bf5e: 4629 mov r1, r5

bf60: b007 add sp, #28

bf62: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

bf66: f898 2001 ldrb.w r2, [r8, #1]

bf6a: f002 02df and.w r2, r2, #223 ; 0xdf

bf6e: 2a58 cmp r2, #88 ; 0x58

bf70: bf01 itttt eq

bf72: f898 3002 ldrbeq.w r3, [r8, #2]

bf76: f04f 0910 moveq.w r9, #16

bf7a: f108 0802 addeq.w r8, r8, #2

bf7e: f04f 0a09 moveq.w sl, #9

bf82: bf1c itt ne

bf84: f04f 0908 movne.w r9, #8

bf88: f04f 0a07 movne.w sl, #7

bf8c: e7a1 b.n bed2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4f2>

bf8e: 9b04 ldr r3, [sp, #16]

bf90: b10b cbz r3, bf96 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5b6>

bf92: f8c3 8000 str.w r8, [r3]

bf96: 9b05 ldr r3, [sp, #20]

bf98: 2b00 cmp r3, #0

bf9a: d1df bne.n bf5c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x57c>

bf9c: 4264 negs r4, r4

bf9e: eb65 0545 sbc.w r5, r5, r5, lsl #1

bfa2: 4620 mov r0, r4

bfa4: 4629 mov r1, r5

bfa6: b007 add sp, #28

bfa8: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

bfac: 9b04 ldr r3, [sp, #16]

bfae: b143 cbz r3, bfc2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5e2>

bfb0: 2400 movs r4, #0

bfb2: 2500 movs r5, #0

bfb4: 4620 mov r0, r4

bfb6: 4629 mov r1, r5

bfb8: f8c3 8000 str.w r8, [r3]

bfbc: b007 add sp, #28

bfbe: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

bfc2: 2400 movs r4, #0

bfc4: 2500 movs r5, #0

bfc6: 4620 mov r0, r4

bfc8: 4629 mov r1, r5

bfca: b007 add sp, #28

bfcc: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

bfd0: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

bfd4: f04f 35ff mov.w r5, #4294967295 ; 0xffffffff

bfd8: e7c0 b.n bf5c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x57c>

bfda: bf00 nop

bfdc: 6b43 ldr r3, [r0, #52] ; 0x34

bfde: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

bfe2: b099 sub sp, #100 ; 0x64

bfe4: 2b00 cmp r3, #0

bfe6: f000 8186 beq.w c2f6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x916>

bfea: 685b ldr r3, [r3, #4]

bfec: 4683 mov fp, r0

bfee: e9cd 120b strd r1, r2, [sp, #44] ; 0x2c

bff2: f8d0 20b0 ldr.w r2, [r0, #176] ; 0xb0

bff6: 9303 str r3, [sp, #12]

bff8: f8d0 30b8 ldr.w r3, [r0, #184] ; 0xb8

bffc: 920f str r2, [sp, #60] ; 0x3c

bffe: 930e str r3, [sp, #56] ; 0x38

c000: 4613 mov r3, r2

c002: 2a00 cmp r2, #0

c004: f000 8174 beq.w c2f0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x910>

c008: 6802 ldr r2, [r0, #0]

c00a: 685b ldr r3, [r3, #4]

c00c: 18d1 adds r1, r2, r3

c00e: 9109 str r1, [sp, #36] ; 0x24

c010: 5ad1 ldrh r1, [r2, r3]

c012: 2901 cmp r1, #1

c014: f040 81da bne.w c3cc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9ec>

c018: f8df 3434 ldr.w r3, [pc, #1076] ; c450 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa70>

c01c: f04f 0a00 mov.w sl, #0

c020: f8df 8430 ldr.w r8, [pc, #1072] ; c454 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa74>

c024: 46d3 mov fp, sl

c026: 447b add r3, pc

c028: 930d str r3, [sp, #52] ; 0x34

c02a: f8df 342c ldr.w r3, [pc, #1068] ; c458 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa78>

c02e: 44f8 add r8, pc

c030: f8cd a028 str.w sl, [sp, #40] ; 0x28

c034: 447b add r3, pc

c036: 9307 str r3, [sp, #28]

c038: f8df 3420 ldr.w r3, [pc, #1056] ; c45c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa7c>

c03c: 46c2 mov sl, r8

c03e: 4680 mov r8, r0

c040: 447b add r3, pc

c042: 9308 str r3, [sp, #32]

c044: f8d8 2018 ldr.w r2, [r8, #24]

c048: 234c movs r3, #76 ; 0x4c

c04a: 9909 ldr r1, [sp, #36] ; 0x24

c04c: fb03 f302 mul.w r3, r3, r2

c050: 684c ldr r4, [r1, #4]

c052: 9a03 ldr r2, [sp, #12]

c054: 4414 add r4, r2

c056: 9a0d ldr r2, [sp, #52] ; 0x34

c058: 58d5 ldr r5, [r2, r3]

c05a: b925 cbnz r5, c066 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x686>

c05c: e0c8 b.n c1f0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x810>

c05e: 68ed ldr r5, [r5, #12]

c060: 2d00 cmp r5, #0

c062: f000 80c5 beq.w c1f0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x810>

c066: 4629 mov r1, r5

c068: 4620 mov r0, r4

c06a: f7ff febf bl bdec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x40c>

c06e: 2800 cmp r0, #0

c070: d0f5 beq.n c05e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x67e>

c072: 9b0c ldr r3, [sp, #48] ; 0x30

c074: 2b00 cmp r3, #0

c076: f040 8101 bne.w c27c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x89c>

c07a: 9b09 ldr r3, [sp, #36] ; 0x24

c07c: 9504 str r5, [sp, #16]

c07e: f8cd 8008 str.w r8, [sp, #8]

c082: 689c ldr r4, [r3, #8]

c084: 441c add r4, r3

c086: 9b02 ldr r3, [sp, #8]

c088: 685e ldr r6, [r3, #4]

c08a: 7833 ldrb r3, [r6, #0]

c08c: b933 cbnz r3, c09c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x6bc>

c08e: 9b07 ldr r3, [sp, #28]

c090: 681b ldr r3, [r3, #0]

c092: 681e ldr r6, [r3, #0]

c094: 9b08 ldr r3, [sp, #32]

c096: 2e00 cmp r6, #0

c098: bf08 it eq

c09a: 461e moveq r6, r3

c09c: 9b04 ldr r3, [sp, #16]

c09e: 9903 ldr r1, [sp, #12]

c0a0: 68a7 ldr r7, [r4, #8]

c0a2: 695d ldr r5, [r3, #20]

c0a4: f8da 3000 ldr.w r3, [sl]

c0a8: 440f add r7, r1

c0aa: 88a1 ldrh r1, [r4, #4]

c0ac: 6b6a ldr r2, [r5, #52] ; 0x34

c0ae: f8d4 8000 ldr.w r8, [r4]

c0b2: 9105 str r1, [sp, #20]

c0b4: f8d2 9004 ldr.w r9, [r2, #4]

c0b8: 06da lsls r2, r3, #27

c0ba: f100 8085 bmi.w c1c8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7e8>

c0be: f8d5 30b8 ldr.w r3, [r5, #184] ; 0xb8

c0c2: 2b00 cmp r3, #0

c0c4: d06c beq.n c1a0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7c0>

c0c6: 685a ldr r2, [r3, #4]

c0c8: 2a00 cmp r2, #0

c0ca: f000 81a4 beq.w c416 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa36>

c0ce: 6829 ldr r1, [r5, #0]

c0d0: 1853 adds r3, r2, r1

c0d2: 5a52 ldrh r2, [r2, r1]

c0d4: 2a01 cmp r2, #1

c0d6: d11a bne.n c10e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x72e>

c0d8: 9606 str r6, [sp, #24]

c0da: 464e mov r6, r9

c0dc: 46c1 mov r9, r8

c0de: 4698 mov r8, r3

c0e0: 4653 mov r3, sl

c0e2: 46a2 mov sl, r4

c0e4: 462c mov r4, r5

c0e6: 461d mov r5, r3

c0e8: f8d8 3008 ldr.w r3, [r8, #8]

c0ec: 4599 cmp r9, r3

c0ee: d041 beq.n c174 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x794>

c0f0: f8d8 3010 ldr.w r3, [r8, #16]

c0f4: 2b00 cmp r3, #0

c0f6: f000 809e beq.w c236 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x856>

c0fa: 4498 add r8, r3

c0fc: f8b8 3000 ldrh.w r3, [r8]

c100: 2b01 cmp r3, #1

c102: d0f1 beq.n c0e8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x708>

c104: 462a mov r2, r5

c106: 4643 mov r3, r8

c108: 4625 mov r5, r4

c10a: 4654 mov r4, sl

c10c: 4692 mov sl, r2

c10e: 2200 movs r2, #0

c110: f88d 205f strb.w r2, [sp, #95] ; 0x5f

c114: 686e ldr r6, [r5, #4]

c116: 7832 ldrb r2, [r6, #0]

c118: b932 cbnz r2, c128 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x748>

c11a: 4ad1 ldr r2, [pc, #836] ; (c460 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa80>)

c11c: 447a add r2, pc

c11e: 6812 ldr r2, [r2, #0]

c120: 6816 ldr r6, [r2, #0]

c122: 2e00 cmp r6, #0

c124: f000 80a4 beq.w c270 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x890>

c128: 8818 ldrh r0, [r3, #0]

c12a: 2100 movs r1, #0

c12c: 2300 movs r3, #0

c12e: f10d 025f add.w r2, sp, #95 ; 0x5f

c132: 9300 str r3, [sp, #0]

c134: 230a movs r3, #10

c136: f004 fc95 bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

c13a: ad10 add r5, sp, #64 ; 0x40

c13c: 4ac9 ldr r2, [pc, #804] ; (c464 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa84>)

c13e: 2301 movs r3, #1

c140: 4631 mov r1, r6

c142: 930a str r3, [sp, #40] ; 0x28

c144: 447a add r2, pc

c146: 4603 mov r3, r0

c148: 4628 mov r0, r5

c14a: f003 f837 bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

c14e: 4ac6 ldr r2, [pc, #792] ; (c468 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa88>)

c150: 2000 movs r0, #0

c152: 4629 mov r1, r5

c154: 447a add r2, pc

c156: f004 fd61 bl 10c1c <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x3c>

c15a: 4628 mov r0, r5

c15c: f003 f8e0 bl f320 <\_dl\_exception\_free@@GLIBC\_PRIVATE>

c160: 88e3 ldrh r3, [r4, #6]

c162: 68e2 ldr r2, [r4, #12]

c164: f3c3 030e ubfx r3, r3, #0, #15

c168: 459b cmp fp, r3

c16a: bf38 it cc

c16c: 469b movcc fp, r3

c16e: b172 cbz r2, c18e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7ae>

c170: 4414 add r4, r2

c172: e788 b.n c086 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x6a6>

c174: f8d8 300c ldr.w r3, [r8, #12]

c178: 4638 mov r0, r7

c17a: f858 1003 ldr.w r1, [r8, r3]

c17e: 4431 add r1, r6

c180: f005 ffe6 bl 12150 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13e8>

c184: 2800 cmp r0, #0

c186: d1b3 bne.n c0f0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x710>

c188: 4654 mov r4, sl

c18a: 46aa mov sl, r5

c18c: e7e8 b.n c160 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x780>

c18e: f8dd 8008 ldr.w r8, [sp, #8]

c192: 9a09 ldr r2, [sp, #36] ; 0x24

c194: 68d3 ldr r3, [r2, #12]

c196: 2b00 cmp r3, #0

c198: d078 beq.n c28c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8ac>

c19a: 441a add r2, r3

c19c: 9209 str r2, [sp, #36] ; 0x24

c19e: e751 b.n c044 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x664>

c1a0: 9b0b ldr r3, [sp, #44] ; 0x2c

c1a2: 2b00 cmp r3, #0

c1a4: d0dc beq.n c160 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x780>

c1a6: 6869 ldr r1, [r5, #4]

c1a8: 780b ldrb r3, [r1, #0]

c1aa: b92b cbnz r3, c1b8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7d8>

c1ac: 4baf ldr r3, [pc, #700] ; (c46c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa8c>)

c1ae: 447b add r3, pc

c1b0: 681b ldr r3, [r3, #0]

c1b2: 6819 ldr r1, [r3, #0]

c1b4: 2900 cmp r1, #0

c1b6: d066 beq.n c286 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8a6>

c1b8: ad10 add r5, sp, #64 ; 0x40

c1ba: 4aad ldr r2, [pc, #692] ; (c470 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa90>)

c1bc: 4633 mov r3, r6

c1be: 447a add r2, pc

c1c0: 4628 mov r0, r5

c1c2: f002 fffb bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

c1c6: e7c2 b.n c14e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x76e>

c1c8: 686a ldr r2, [r5, #4]

c1ca: 7813 ldrb r3, [r2, #0]

c1cc: b92b cbnz r3, c1da <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7fa>

c1ce: 4ba9 ldr r3, [pc, #676] ; (c474 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa94>)

c1d0: 447b add r3, pc

c1d2: 681b ldr r3, [r3, #0]

c1d4: 681a ldr r2, [r3, #0]

c1d6: 2a00 cmp r2, #0

c1d8: d04d beq.n c276 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x896>

c1da: 9902 ldr r1, [sp, #8]

c1dc: 48a6 ldr r0, [pc, #664] ; (c478 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa98>)

c1de: 69ab ldr r3, [r5, #24]

c1e0: 6989 ldr r1, [r1, #24]

c1e2: 4478 add r0, pc

c1e4: 9600 str r6, [sp, #0]

c1e6: 9101 str r1, [sp, #4]

c1e8: 4639 mov r1, r7

c1ea: f7ff fdc3 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

c1ee: e766 b.n c0be <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x6de>

c1f0: f8d8 3164 ldr.w r3, [r8, #356] ; 0x164

c1f4: b1b3 cbz r3, c224 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x844>

c1f6: 2600 movs r6, #0

c1f8: e004 b.n c204 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x824>

c1fa: f8d8 3164 ldr.w r3, [r8, #356] ; 0x164

c1fe: 3601 adds r6, #1

c200: 429e cmp r6, r3

c202: d20f bcs.n c224 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x844>

c204: f8d8 3160 ldr.w r3, [r8, #352] ; 0x160

c208: 4620 mov r0, r4

c20a: 00b5 lsls r5, r6, #2

c20c: f853 1026 ldr.w r1, [r3, r6, lsl #2]

c210: f7ff fdec bl bdec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x40c>

c214: 2800 cmp r0, #0

c216: d0f0 beq.n c1fa <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x81a>

c218: f8d8 3160 ldr.w r3, [r8, #352] ; 0x160

c21c: 595d ldr r5, [r3, r5]

c21e: 2d00 cmp r5, #0

c220: f47f af27 bne.w c072 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x692>

c224: 4b95 ldr r3, [pc, #596] ; (c47c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa9c>)

c226: 22cd movs r2, #205 ; 0xcd

c228: 4995 ldr r1, [pc, #596] ; (c480 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xaa0>)

c22a: 4896 ldr r0, [pc, #600] ; (c484 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xaa4>)

c22c: 447b add r3, pc

c22e: 4479 add r1, pc

c230: 4478 add r0, pc

c232: f004 fbab bl 1098c <free@@GLIBC\_2.4+0x12c>

c236: 462b mov r3, r5

c238: 4625 mov r5, r4

c23a: 4654 mov r4, sl

c23c: 469a mov sl, r3

c23e: 9b05 ldr r3, [sp, #20]

c240: 9e06 ldr r6, [sp, #24]

c242: 079b lsls r3, r3, #30

c244: d53d bpl.n c2c2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8e2>

c246: 9b0b ldr r3, [sp, #44] ; 0x2c

c248: 2b00 cmp r3, #0

c24a: d089 beq.n c160 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x780>

c24c: 6869 ldr r1, [r5, #4]

c24e: 780b ldrb r3, [r1, #0]

c250: b92b cbnz r3, c25e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x87e>

c252: 4b8d ldr r3, [pc, #564] ; (c488 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xaa8>)

c254: 447b add r3, pc

c256: 681b ldr r3, [r3, #0]

c258: 6819 ldr r1, [r3, #0]

c25a: 2900 cmp r1, #0

c25c: d045 beq.n c2ea <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x90a>

c25e: ad10 add r5, sp, #64 ; 0x40

c260: 4a8a ldr r2, [pc, #552] ; (c48c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xaac>)

c262: 9600 str r6, [sp, #0]

c264: 463b mov r3, r7

c266: 4628 mov r0, r5

c268: 447a add r2, pc

c26a: f002 ffa7 bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

c26e: e76e b.n c14e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x76e>

c270: 4e87 ldr r6, [pc, #540] ; (c490 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xab0>)

c272: 447e add r6, pc

c274: e758 b.n c128 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x748>

c276: 4a87 ldr r2, [pc, #540] ; (c494 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xab4>)

c278: 447a add r2, pc

c27a: e7ae b.n c1da <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7fa>

c27c: f895 3199 ldrb.w r3, [r5, #409] ; 0x199

c280: 0799 lsls r1, r3, #30

c282: d486 bmi.n c192 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7b2>

c284: e6f9 b.n c07a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x69a>

c286: 4984 ldr r1, [pc, #528] ; (c498 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xab8>)

c288: 4479 add r1, pc

c28a: e795 b.n c1b8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x7d8>

c28c: 9b0e ldr r3, [sp, #56] ; 0x38

c28e: 46da mov sl, fp

c290: 46c3 mov fp, r8

c292: b17b cbz r3, c2b4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8d4>

c294: 9a0e ldr r2, [sp, #56] ; 0x38

c296: f8db 3000 ldr.w r3, [fp]

c29a: 6852 ldr r2, [r2, #4]

c29c: 4413 add r3, r2

c29e: e000 b.n c2a2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8c2>

c2a0: 440b add r3, r1

c2a2: 889a ldrh r2, [r3, #4]

c2a4: 6919 ldr r1, [r3, #16]

c2a6: f3c2 020e ubfx r2, r2, #0, #15

c2aa: 4592 cmp sl, r2

c2ac: bf38 it cc

c2ae: 4692 movcc sl, r2

c2b0: 2900 cmp r1, #0

c2b2: d1f5 bne.n c2a0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8c0>

c2b4: f1ba 0f00 cmp.w sl, #0

c2b8: d122 bne.n c300 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x920>

c2ba: 980a ldr r0, [sp, #40] ; 0x28

c2bc: b019 add sp, #100 ; 0x64

c2be: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c2c2: 6869 ldr r1, [r5, #4]

c2c4: 780b ldrb r3, [r1, #0]

c2c6: b92b cbnz r3, c2d4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8f4>

c2c8: 4b74 ldr r3, [pc, #464] ; (c49c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xabc>)

c2ca: 447b add r3, pc

c2cc: 681b ldr r3, [r3, #0]

c2ce: 6819 ldr r1, [r3, #0]

c2d0: 2900 cmp r1, #0

c2d2: d078 beq.n c3c6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9e6>

c2d4: ad10 add r5, sp, #64 ; 0x40

c2d6: 4a72 ldr r2, [pc, #456] ; (c4a0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xac0>)

c2d8: 463b mov r3, r7

c2da: 9600 str r6, [sp, #0]

c2dc: 4628 mov r0, r5

c2de: 447a add r2, pc

c2e0: f002 ff6c bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

c2e4: 2301 movs r3, #1

c2e6: 930a str r3, [sp, #40] ; 0x28

c2e8: e731 b.n c14e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x76e>

c2ea: 496e ldr r1, [pc, #440] ; (c4a4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xac4>)

c2ec: 4479 add r1, pc

c2ee: e7b6 b.n c25e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x87e>

c2f0: 9b0e ldr r3, [sp, #56] ; 0x38

c2f2: 2b00 cmp r3, #0

c2f4: d163 bne.n c3be <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9de>

c2f6: 930a str r3, [sp, #40] ; 0x28

c2f8: 980a ldr r0, [sp, #40] ; 0x28

c2fa: b019 add sp, #100 ; 0x64

c2fc: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c300: f10a 0401 add.w r4, sl, #1

c304: 2110 movs r1, #16

c306: 4620 mov r0, r4

c308: f7f4 eb72 blx 9f0 <calloc@plt>

c30c: f8cb 0174 str.w r0, [fp, #372] ; 0x174

c310: 2800 cmp r0, #0

c312: f000 8089 beq.w c428 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa48>

c316: f8db 30e8 ldr.w r3, [fp, #232] ; 0xe8

c31a: f8cb 4178 str.w r4, [fp, #376] ; 0x178

c31e: 685b ldr r3, [r3, #4]

c320: f8cb 31a8 str.w r3, [fp, #424] ; 0x1a8

c324: 9b0f ldr r3, [sp, #60] ; 0x3c

c326: b34b cbz r3, c37c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x99c>

c328: 685b ldr r3, [r3, #4]

c32a: f44f 4c00 mov.w ip, #32768 ; 0x8000

c32e: f8db 7000 ldr.w r7, [fp]

c332: f6cf 7cff movt ip, #65535 ; 0xffff

c336: f8dd e00c ldr.w lr, [sp, #12]

c33a: 441f add r7, r3

c33c: 68bb ldr r3, [r7, #8]

c33e: 443b add r3, r7

c340: e000 b.n c344 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x964>

c342: 4413 add r3, r2

c344: 88d9 ldrh r1, [r3, #6]

c346: f3c1 020e ubfx r2, r1, #0, #15

c34a: 4294 cmp r4, r2

c34c: d90f bls.n c36e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x98e>

c34e: 0112 lsls r2, r2, #4

c350: ea01 090c and.w r9, r1, ip

c354: 6819 ldr r1, [r3, #0]

c356: 1885 adds r5, r0, r2

c358: f8d3 8008 ldr.w r8, [r3, #8]

c35c: 687e ldr r6, [r7, #4]

c35e: 44f0 add r8, lr

c360: 6069 str r1, [r5, #4]

c362: f8c5 9008 str.w r9, [r5, #8]

c366: 4476 add r6, lr

c368: f840 8002 str.w r8, [r0, r2]

c36c: 60ee str r6, [r5, #12]

c36e: 68da ldr r2, [r3, #12]

c370: 2a00 cmp r2, #0

c372: d1e6 bne.n c342 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x962>

c374: 68fb ldr r3, [r7, #12]

c376: b10b cbz r3, c37c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x99c>

c378: 441f add r7, r3

c37a: e7df b.n c33c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x95c>

c37c: 9b0e ldr r3, [sp, #56] ; 0x38

c37e: 2b00 cmp r3, #0

c380: d09b beq.n c2ba <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8da>

c382: f8db 2000 ldr.w r2, [fp]

c386: 685b ldr r3, [r3, #4]

c388: 9f03 ldr r7, [sp, #12]

c38a: 4413 add r3, r2

c38c: e000 b.n c390 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9b0>

c38e: 4413 add r3, r2

c390: 885a ldrh r2, [r3, #2]

c392: f012 0201 ands.w r2, r2, #1

c396: d10b bne.n c3b0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9d0>

c398: 8899 ldrh r1, [r3, #4]

c39a: 68dc ldr r4, [r3, #12]

c39c: f3c1 010e ubfx r1, r1, #0, #15

c3a0: 689e ldr r6, [r3, #8]

c3a2: 0109 lsls r1, r1, #4

c3a4: 591c ldr r4, [r3, r4]

c3a6: 1845 adds r5, r0, r1

c3a8: 443c add r4, r7

c3aa: 606e str r6, [r5, #4]

c3ac: 5044 str r4, [r0, r1]

c3ae: 60ea str r2, [r5, #12]

c3b0: 691a ldr r2, [r3, #16]

c3b2: 2a00 cmp r2, #0

c3b4: d1eb bne.n c38e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9ae>

c3b6: 980a ldr r0, [sp, #40] ; 0x28

c3b8: b019 add sp, #100 ; 0x64

c3ba: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c3be: 9b0f ldr r3, [sp, #60] ; 0x3c

c3c0: 469a mov sl, r3

c3c2: 930a str r3, [sp, #40] ; 0x28

c3c4: e766 b.n c294 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8b4>

c3c6: 4938 ldr r1, [pc, #224] ; (c4a8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xac8>)

c3c8: 4479 add r1, pc

c3ca: e783 b.n c2d4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x8f4>

c3cc: 6845 ldr r5, [r0, #4]

c3ce: 2100 movs r1, #0

c3d0: f88d 105f strb.w r1, [sp, #95] ; 0x5f

c3d4: 7829 ldrb r1, [r5, #0]

c3d6: b1a9 cbz r1, c404 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa24>

c3d8: 5ad0 ldrh r0, [r2, r3]

c3da: 2100 movs r1, #0

c3dc: 2400 movs r4, #0

c3de: 230a movs r3, #10

c3e0: f10d 025f add.w r2, sp, #95 ; 0x5f

c3e4: 9400 str r4, [sp, #0]

c3e6: f004 fb3d bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

c3ea: ae10 add r6, sp, #64 ; 0x40

c3ec: 4a2f ldr r2, [pc, #188] ; (c4ac <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xacc>)

c3ee: 4629 mov r1, r5

c3f0: 447a add r2, pc

c3f2: 4603 mov r3, r0

c3f4: 4630 mov r0, r6

c3f6: f002 fee1 bl f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>

c3fa: 4620 mov r0, r4

c3fc: 4631 mov r1, r6

c3fe: 2200 movs r2, #0

c400: f7f4 eaea blx 9d8 <\_dl\_signal\_exception@plt>

c404: 492a ldr r1, [pc, #168] ; (c4b0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xad0>)

c406: 4479 add r1, pc

c408: 6809 ldr r1, [r1, #0]

c40a: 680d ldr r5, [r1, #0]

c40c: 2d00 cmp r5, #0

c40e: d1e3 bne.n c3d8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9f8>

c410: 4d28 ldr r5, [pc, #160] ; (c4b4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xad4>)

c412: 447d add r5, pc

c414: e7e0 b.n c3d8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9f8>

c416: 4b28 ldr r3, [pc, #160] ; (c4b8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xad8>)

c418: 2258 movs r2, #88 ; 0x58

c41a: 4928 ldr r1, [pc, #160] ; (c4bc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xadc>)

c41c: 4828 ldr r0, [pc, #160] ; (c4c0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xae0>)

c41e: 447b add r3, pc

c420: 4479 add r1, pc

c422: 4478 add r0, pc

c424: f004 fab2 bl 1098c <free@@GLIBC\_2.4+0x12c>

c428: f8db 1004 ldr.w r1, [fp, #4]

c42c: 780b ldrb r3, [r1, #0]

c42e: b923 cbnz r3, c43a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa5a>

c430: 4b24 ldr r3, [pc, #144] ; (c4c4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xae4>)

c432: 447b add r3, pc

c434: 681b ldr r3, [r3, #0]

c436: 6819 ldr r1, [r3, #0]

c438: b139 cbz r1, c44a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa6a>

c43a: ae10 add r6, sp, #64 ; 0x40

c43c: 4a22 ldr r2, [pc, #136] ; (c4c8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xae8>)

c43e: 4630 mov r0, r6

c440: 447a add r2, pc

c442: f002 fe75 bl f130 <\_dl\_exception\_create@@GLIBC\_PRIVATE>

c446: 200c movs r0, #12

c448: e7d8 b.n c3fc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa1c>

c44a: 4920 ldr r1, [pc, #128] ; (c4cc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xaec>)

c44c: 4479 add r1, pc

c44e: e7f4 b.n c43a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa5a>

c450: d036 beq.n c4c0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xae0>

c452: 0001 movs r1, r0

c454: c93e ldmia r1, {r1, r2, r3, r4, r5}

c456: 0001 movs r1, r0

c458: d864 bhi.n c524 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb44>

c45a: 0001 movs r1, r0

c45c: 8a98 ldrh r0, [r3, #20]

c45e: 0000 movs r0, r0

c460: d77c bvc.n c55c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb7c>

c462: 0001 movs r1, r0

c464: a964 add r1, sp, #400 ; 0x190

c466: 0000 movs r0, r0

c468: a9d4 add r1, sp, #848 ; 0x350

c46a: 0000 movs r0, r0

c46c: d6ea bvs.n c444 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa64>

c46e: 0001 movs r1, r0

c470: a8a6 add r0, sp, #664 ; 0x298

c472: 0000 movs r0, r0

c474: d6c8 bvs.n c408 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa28>

c476: 0001 movs r1, r0

c478: a83a add r0, sp, #232 ; 0xe8

c47a: 0000 movs r0, r0

c47c: a94c add r1, sp, #304 ; 0x130

c47e: 0000 movs r0, r0

c480: a7ce add r7, pc, #824 ; (adr r7, c7bc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xddc>)

c482: 0000 movs r0, r0

c484: a7dc add r7, pc, #880 ; (adr r7, c7f8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe18>)

c486: 0000 movs r0, r0

c488: d644 bvs.n c514 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb34>

c48a: 0001 movs r1, r0

c48c: a868 add r0, sp, #416 ; 0x1a0

c48e: 0000 movs r0, r0

c490: 8866 ldrh r6, [r4, #2]

c492: 0000 movs r0, r0

c494: 8860 ldrh r0, [r4, #2]

c496: 0000 movs r0, r0

c498: 8850 ldrh r0, [r2, #2]

c49a: 0000 movs r0, r0

c49c: d5ce bpl.n c43c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xa5c>

c49e: 0001 movs r1, r0

c4a0: a822 add r0, sp, #136 ; 0x88

c4a2: 0000 movs r0, r0

c4a4: 87ec strh r4, [r5, #62] ; 0x3e

c4a6: 0000 movs r0, r0

c4a8: 8710 strh r0, [r2, #56] ; 0x38

c4aa: 0000 movs r0, r0

c4ac: a5e0 add r5, pc, #896 ; (adr r5, c830 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe50>)

c4ae: 0000 movs r0, r0

c4b0: d492 bmi.n c3d8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x9f8>

c4b2: 0001 movs r1, r0

c4b4: 86c6 strh r6, [r0, #54] ; 0x36

c4b6: 0000 movs r0, r0

c4b8: a74a add r7, pc, #296 ; (adr r7, c5e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xc04>)

c4ba: 0000 movs r0, r0

c4bc: a5dc add r5, pc, #880 ; (adr r5, c830 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe50>)

c4be: 0000 movs r0, r0

c4c0: a676 add r6, pc, #472 ; (adr r6, c69c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xcbc>)

c4c2: 0000 movs r0, r0

c4c4: d466 bmi.n c594 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xbb4>

c4c6: 0001 movs r1, r0

c4c8: a700 add r7, pc, #0 ; (adr r7, c4cc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xaec>)

c4ca: 0000 movs r0, r0

c4cc: 868c strh r4, [r1, #52] ; 0x34

c4ce: 0000 movs r0, r0

c4d0: b5f8 push {r3, r4, r5, r6, r7, lr}

c4d2: 4604 mov r4, r0

c4d4: b1b8 cbz r0, c506 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb26>

c4d6: 460e mov r6, r1

c4d8: 4617 mov r7, r2

c4da: 2500 movs r5, #0

c4dc: e001 b.n c4e2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb02>

c4de: 68e4 ldr r4, [r4, #12]

c4e0: b17c cbz r4, c502 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb22>

c4e2: f894 3199 ldrb.w r3, [r4, #409] ; 0x199

c4e6: 079b lsls r3, r3, #30

c4e8: d4f9 bmi.n c4de <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xafe>

c4ea: 4620 mov r0, r4

c4ec: 463a mov r2, r7

c4ee: 4631 mov r1, r6

c4f0: f7ff fd74 bl bfdc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5fc>

c4f4: 68e4 ldr r4, [r4, #12]

c4f6: 2800 cmp r0, #0

c4f8: bf18 it ne

c4fa: f045 0501 orrne.w r5, r5, #1

c4fe: 2c00 cmp r4, #0

c500: d1ef bne.n c4e2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb02>

c502: 4628 mov r0, r5

c504: bdf8 pop {r3, r4, r5, r6, r7, pc}

c506: 4605 mov r5, r0

c508: 4628 mov r0, r5

c50a: bdf8 pop {r3, r4, r5, r6, r7, pc}

c50c: f8df 34d8 ldr.w r3, [pc, #1240] ; c9e8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1008>

c510: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

c514: 447b add r3, pc

c516: f5ad 7d19 sub.w sp, sp, #612 ; 0x264

c51a: f8d3 6504 ldr.w r6, [r3, #1284] ; 0x504

c51e: af02 add r7, sp, #8

c520: f8b6 215c ldrh.w r2, [r6, #348] ; 0x15c

c524: f8d6 3154 ldr.w r3, [r6, #340] ; 0x154

c528: eb03 1242 add.w r2, r3, r2, lsl #5

c52c: 4293 cmp r3, r2

c52e: f080 8222 bcs.w c976 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf96>

c532: f8df 14b8 ldr.w r1, [pc, #1208] ; c9ec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x100c>

c536: 43d8 mvns r0, r3

c538: 4402 add r2, r0

c53a: f103 0040 add.w r0, r3, #64 ; 0x40

c53e: 4479 add r1, pc

c540: f022 021f bic.w r2, r2, #31

c544: 4402 add r2, r0

c546: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

c54a: 690d ldr r5, [r1, #16]

c54c: 3320 adds r3, #32

c54e: 2000 movs r0, #0

c550: 46b0 mov r8, r6

c552: f1c5 0e00 rsb lr, r5, #0

c556: 4425 add r5, r4

c558: e002 b.n c560 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb80>

c55a: 3320 adds r3, #32

c55c: 429a cmp r2, r3

c55e: d01a beq.n c596 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xbb6>

c560: f853 1c20 ldr.w r1, [r3, #-32]

c564: 2901 cmp r1, #1

c566: d1f8 bne.n c55a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb7a>

c568: f853 1c08 ldr.w r1, [r3, #-8]

c56c: 07c9 lsls r1, r1, #31

c56e: d5f4 bpl.n c55a <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb7a>

c570: f853 1c18 ldr.w r1, [r3, #-24]

c574: 3320 adds r3, #32

c576: f853 6c2c ldr.w r6, [r3, #-44]

c57a: ea01 0c0e and.w ip, r1, lr

c57e: 4431 add r1, r6

c580: 4564 cmp r4, ip

c582: 4429 add r1, r5

c584: ea01 010e and.w r1, r1, lr

c588: bf28 it cs

c58a: 4664 movcs r4, ip

c58c: 4288 cmp r0, r1

c58e: bf38 it cc

c590: 4608 movcc r0, r1

c592: 429a cmp r2, r3

c594: d1e4 bne.n c560 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb80>

c596: 4646 mov r6, r8

c598: 4621 mov r1, r4

c59a: 4602 mov r2, r0

c59c: 6833 ldr r3, [r6, #0]

c59e: f04f 0c00 mov.w ip, #0

c5a2: f8df 544c ldr.w r5, [pc, #1100] ; c9f0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1010>

c5a6: 2604 movs r6, #4

c5a8: 441c add r4, r3

c5aa: 3303 adds r3, #3

c5ac: 4418 add r0, r3

c5ae: f024 0403 bic.w r4, r4, #3

c5b2: f020 0303 bic.w r3, r0, #3

c5b6: 447d add r5, pc

c5b8: 1b18 subs r0, r3, r4

c5ba: 6278 str r0, [r7, #36] ; 0x24

c5bc: 60a8 str r0, [r5, #8]

c5be: eb00 0040 add.w r0, r0, r0, lsl #1

c5c2: 613b str r3, [r7, #16]

c5c4: f241 3387 movw r3, #4999 ; 0x1387

c5c8: 4298 cmp r0, r3

c5ca: 606c str r4, [r5, #4]

c5cc: f8c5 c000 str.w ip, [r5]

c5d0: 60ee str r6, [r5, #12]

c5d2: f200 80ce bhi.w c772 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xd92>

c5d6: f44f 5998 mov.w r9, #4864 ; 0x1300

c5da: 2332 movs r3, #50 ; 0x32

c5dc: 612b str r3, [r5, #16]

c5de: f44f 73c8 mov.w r3, #400 ; 0x190

c5e2: 61bb str r3, [r7, #24]

c5e4: f107 032c add.w r3, r7, #44 ; 0x2c

c5e8: f646 5667 movw r6, #28007 ; 0x6d67

c5ec: 623b str r3, [r7, #32]

c5ee: f6c6 666f movt r6, #28271 ; 0x6e6f

c5f2: 601e str r6, [r3, #0]

c5f4: f107 0540 add.w r5, r7, #64 ; 0x40

c5f8: 6a7b ldr r3, [r7, #36] ; 0x24

c5fa: 2600 movs r6, #0

c5fc: 6029 str r1, [r5, #0]

c5fe: f64f 70ff movw r0, #65535 ; 0xffff

c602: e9c7 660d strd r6, r6, [r7, #52] ; 0x34

c606: f2c0 0001 movt r0, #1

c60a: 63fe str r6, [r7, #60] ; 0x3c

c60c: 6338 str r0, [r7, #48] ; 0x30

c60e: 0898 lsrs r0, r3, #2

c610: e9c5 2001 strd r2, r0, [r5, #4]

c614: f005 f9b6 bl 11984 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc1c>

c618: 4cf6 ldr r4, [pc, #984] ; (c9f4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1014>)

c61a: 2173 movs r1, #115 ; 0x73

c61c: 4af6 ldr r2, [pc, #984] ; (c9f8 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1018>)

c61e: 447c add r4, pc

c620: 77e9 strb r1, [r5, #31]

c622: 447a add r2, pc

c624: 65be str r6, [r7, #88] ; 0x58

c626: f8d4 a16c ldr.w sl, [r4, #364] ; 0x16c

c62a: f8c7 605b str.w r6, [r7, #91] ; 0x5b

c62e: 617c str r4, [r7, #20]

c630: 60e8 str r0, [r5, #12]

c632: e892 0003 ldmia.w r2, {r0, r1}

c636: f107 0250 add.w r2, r7, #80 ; 0x50

c63a: e882 0003 stmia.w r2, {r0, r1}

c63e: 4650 mov r0, sl

c640: f006 f87e bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

c644: f8d4 b168 ldr.w fp, [r4, #360] ; 0x168

c648: 4680 mov r8, r0

c64a: 4658 mov r0, fp

c64c: f006 f878 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

c650: 4651 mov r1, sl

c652: 4440 add r0, r8

c654: 3011 adds r0, #17

c656: f020 0007 bic.w r0, r0, #7

c65a: ebad 0d00 sub.w sp, sp, r0

c65e: f10d 0a08 add.w sl, sp, #8

c662: 4650 mov r0, sl

c664: f005 feec bl 12440 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16d8>

c668: 222f movs r2, #47 ; 0x2f

c66a: 4659 mov r1, fp

c66c: f800 2b01 strb.w r2, [r0], #1

c670: f005 fee6 bl 12440 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16d8>

c674: f8df c384 ldr.w ip, [pc, #900] ; c9fc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x101c>

c678: f44f 72db mov.w r2, #438 ; 0x1b6

c67c: 44fc add ip, pc

c67e: 4686 mov lr, r0

c680: e8bc 0003 ldmia.w ip!, {r0, r1}

c684: f89c c000 ldrb.w ip, [ip]

c688: f8ce 0000 str.w r0, [lr]

c68c: 4650 mov r0, sl

c68e: f8ce 1004 str.w r1, [lr, #4]

c692: f248 0142 movw r1, #32834 ; 0x8042

c696: f88e c008 strb.w ip, [lr, #8]

c69a: f005 faaf bl 11bfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe94>

c69e: f1b0 3fff cmp.w r0, #4294967295 ; 0xffffffff

c6a2: 4680 mov r8, r0

c6a4: d07e beq.n c7a4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xdc4>

c6a6: f107 0b60 add.w fp, r7, #96 ; 0x60

c6aa: 4601 mov r1, r0

c6ac: 2003 movs r0, #3

c6ae: 465a mov r2, fp

c6b0: f005 f984 bl 119bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc54>

c6b4: 2800 cmp r0, #0

c6b6: f2c0 8164 blt.w c982 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfa2>

c6ba: f8db 2010 ldr.w r2, [fp, #16]

c6be: f402 4270 and.w r2, r2, #61440 ; 0xf000

c6c2: f5b2 4f00 cmp.w r2, #32768 ; 0x8000

c6c6: f040 8084 bne.w c7d2 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xdf2>

c6ca: 6a7b ldr r3, [r7, #36] ; 0x24

c6cc: e9db 120c ldrd r1, r2, [fp, #48] ; 0x30

c6d0: 085b lsrs r3, r3, #1

c6d2: 60fb str r3, [r7, #12]

c6d4: 444b add r3, r9

c6d6: 61fb str r3, [r7, #28]

c6d8: ea51 0302 orrs.w r3, r1, r2

c6dc: e9c7 1200 strd r1, r2, [r7]

c6e0: f000 8112 beq.w c908 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf28>

c6e4: 69fb ldr r3, [r7, #28]

c6e6: 17da asrs r2, r3, #31

c6e8: 4619 mov r1, r3

c6ea: 4613 mov r3, r2

c6ec: 460a mov r2, r1

c6ee: e9d7 0100 ldrd r0, r1, [r7]

c6f2: 4299 cmp r1, r3

c6f4: bf08 it eq

c6f6: 4290 cmpeq r0, r2

c6f8: d174 bne.n c7e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe04>

c6fa: f04f 0900 mov.w r9, #0

c6fe: 2301 movs r3, #1

c700: e9cd 8900 strd r8, r9, [sp]

c704: 4648 mov r0, r9

c706: 2203 movs r2, #3

c708: 69f9 ldr r1, [r7, #28]

c70a: f005 fb33 bl 11d74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x100c>

c70e: 1c43 adds r3, r0, #1

c710: 4606 mov r6, r0

c712: f000 813e beq.w c992 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfb2>

c716: 4640 mov r0, r8

c718: f106 0438 add.w r4, r6, #56 ; 0x38

c71c: 617c str r4, [r7, #20]

c71e: f005 f9eb bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

c722: 4bb7 ldr r3, [pc, #732] ; (ca00 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1020>)

c724: 68fa ldr r2, [r7, #12]

c726: 447b add r3, pc

c728: e9db 010c ldrd r0, r1, [fp, #48] ; 0x30

c72c: 3204 adds r2, #4

c72e: 603a str r2, [r7, #0]

c730: eb04 0802 add.w r8, r4, r2

c734: f108 0204 add.w r2, r8, #4

c738: e9c3 8205 strd r8, r2, [r3, #20]

c73c: ea50 0301 orrs.w r3, r0, r1

c740: d062 beq.n c808 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe28>

c742: 6a39 ldr r1, [r7, #32]

c744: 2214 movs r2, #20

c746: 4630 mov r0, r6

c748: f006 f978 bl 12a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cd4>

c74c: b960 cbnz r0, c768 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xd88>

c74e: 6973 ldr r3, [r6, #20]

c750: b953 cbnz r3, c768 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xd88>

c752: 4629 mov r1, r5

c754: 2220 movs r2, #32

c756: f106 0018 add.w r0, r6, #24

c75a: f006 f96f bl 12a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cd4>

c75e: b918 cbnz r0, c768 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xd88>

c760: f858 3c04 ldr.w r3, [r8, #-4]

c764: 2b01 cmp r3, #1

c766: d06a beq.n c83e <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe5e>

c768: 69f9 ldr r1, [r7, #28]

c76a: 4630 mov r0, r6

c76c: f005 fb28 bl 11dc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1058>

c770: e03b b.n c7ea <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xe0a>

c772: f248 531f movw r3, #34079 ; 0x851f

c776: f2c5 13eb movt r3, #20971 ; 0x51eb

c77a: 2663 movs r6, #99 ; 0x63

c77c: f2c0 6640 movt r6, #1600 ; 0x640

c780: fba3 4300 umull r4, r3, r3, r0

c784: 42b0 cmp r0, r6

c786: ea4f 1353 mov.w r3, r3, lsr #5

c78a: f240 8123 bls.w c9d4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xff4>

c78e: f04f 0940 mov.w r9, #64 ; 0x40

c792: f44f 0300 mov.w r3, #8388608 ; 0x800000

c796: f44f 1080 mov.w r0, #1048576 ; 0x100000

c79a: f2c0 6900 movt r9, #1536 ; 0x600

c79e: 61bb str r3, [r7, #24]

c7a0: 6128 str r0, [r5, #16]

c7a2: e71f b.n c5e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xc04>

c7a4: 4b97 ldr r3, [pc, #604] ; (ca04 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1024>)

c7a6: 4c98 ldr r4, [pc, #608] ; (ca08 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1028>)

c7a8: 447b add r3, pc

c7aa: 447c add r4, pc

c7ac: 681d ldr r5, [r3, #0]

c7ae: f107 01c8 add.w r1, r7, #200 ; 0xc8

c7b2: 4628 mov r0, r5

c7b4: f44f 72c8 mov.w r2, #400 ; 0x190

c7b8: f004 f868 bl 1088c <free@@GLIBC\_2.4+0x2c>

c7bc: 4652 mov r2, sl

c7be: 4621 mov r1, r4

c7c0: 4603 mov r3, r0

c7c2: 2002 movs r0, #2

c7c4: f7ff fb02 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

c7c8: f507 7717 add.w r7, r7, #604 ; 0x25c

c7cc: 46bd mov sp, r7

c7ce: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c7d2: 4c8e ldr r4, [pc, #568] ; (ca0c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x102c>)

c7d4: 447c add r4, pc

c7d6: 4b8e ldr r3, [pc, #568] ; (ca10 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1030>)

c7d8: 4640 mov r0, r8

c7da: 447b add r3, pc

c7dc: 681d ldr r5, [r3, #0]

c7de: f005 f98b bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

c7e2: e7e4 b.n c7ae <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xdce>

c7e4: 4640 mov r0, r8

c7e6: f005 f987 bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

c7ea: 4b8a ldr r3, [pc, #552] ; (ca14 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1034>)

c7ec: 4652 mov r2, sl

c7ee: 498a ldr r1, [pc, #552] ; (ca18 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1038>)

c7f0: 2002 movs r0, #2

c7f2: 447b add r3, pc

c7f4: 4479 add r1, pc

c7f6: f8d3 3168 ldr.w r3, [r3, #360] ; 0x168

c7fa: f7ff fae7 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

c7fe: f507 7717 add.w r7, r7, #604 ; 0x25c

c802: 46bd mov sp, r7

c804: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c808: f8d7 c020 ldr.w ip, [r7, #32]

c80c: f8c6 9014 str.w r9, [r6, #20]

c810: f04f 0901 mov.w r9, #1

c814: e8bc 000f ldmia.w ip!, {r0, r1, r2, r3}

c818: 6030 str r0, [r6, #0]

c81a: f8dc 0000 ldr.w r0, [ip]

c81e: 6071 str r1, [r6, #4]

c820: 60b2 str r2, [r6, #8]

c822: 6130 str r0, [r6, #16]

c824: 60f3 str r3, [r6, #12]

c826: cd0f ldmia r5!, {r0, r1, r2, r3}

c828: 61b0 str r0, [r6, #24]

c82a: 61f1 str r1, [r6, #28]

c82c: 6232 str r2, [r6, #32]

c82e: 6273 str r3, [r6, #36] ; 0x24

c830: cd0f ldmia r5!, {r0, r1, r2, r3}

c832: 62b0 str r0, [r6, #40] ; 0x28

c834: 62f1 str r1, [r6, #44] ; 0x2c

c836: 6332 str r2, [r6, #48] ; 0x30

c838: 6373 str r3, [r6, #52] ; 0x34

c83a: f848 9c04 str.w r9, [r8, #-4]

c83e: 68fa ldr r2, [r7, #12]

c840: 2101 movs r1, #1

c842: 69bb ldr r3, [r7, #24]

c844: 4413 add r3, r2

c846: 4618 mov r0, r3

c848: f7f4 e8d2 blx 9f0 <calloc@plt>

c84c: 4a73 ldr r2, [pc, #460] ; (ca1c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x103c>)

c84e: 447a add r2, pc

c850: 61d0 str r0, [r2, #28]

c852: 2800 cmp r0, #0

c854: f000 80b2 beq.w c9bc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfdc>

c858: 683d ldr r5, [r7, #0]

c85a: 2300 movs r3, #0

c85c: 697c ldr r4, [r7, #20]

c85e: 6253 str r3, [r2, #36] ; 0x24

c860: 68fb ldr r3, [r7, #12]

c862: 5961 ldr r1, [r4, r5]

c864: 18c6 adds r6, r0, r3

c866: 6913 ldr r3, [r2, #16]

c868: 6216 str r6, [r2, #32]

c86a: 4299 cmp r1, r3

c86c: bf38 it cc

c86e: 5963 ldrcc r3, [r4, r5]

c870: 4d6b ldr r5, [pc, #428] ; (ca20 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1040>)

c872: 447d add r5, pc

c874: 62ab str r3, [r5, #40] ; 0x28

c876: b1eb cbz r3, c8b4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xed4>

c878: 220c movs r2, #12

c87a: f8d7 a024 ldr.w sl, [r7, #36] ; 0x24

c87e: fb02 8303 mla r3, r2, r3, r8

c882: f1a8 0808 sub.w r8, r8, #8

c886: 3b08 subs r3, #8

c888: 685a ldr r2, [r3, #4]

c88a: 6a69 ldr r1, [r5, #36] ; 0x24

c88c: 0892 lsrs r2, r2, #2

c88e: 1c4c adds r4, r1, #1

c890: 626c str r4, [r5, #36] ; 0x24

c892: eb06 0ec1 add.w lr, r6, r1, lsl #3

c896: f830 9012 ldrh.w r9, [r0, r2, lsl #1]

c89a: fa1f fc81 uxth.w ip, r1

c89e: f846 3031 str.w r3, [r6, r1, lsl #3]

c8a2: 3b0c subs r3, #12

c8a4: 4543 cmp r3, r8

c8a6: f8ae 9004 strh.w r9, [lr, #4]

c8aa: f820 c012 strh.w ip, [r0, r2, lsl #1]

c8ae: d1eb bne.n c888 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xea8>

c8b0: f8c7 a024 str.w sl, [r7, #36] ; 0x24

c8b4: 4b5b ldr r3, [pc, #364] ; (ca24 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1044>)

c8b6: 447b add r3, pc

c8b8: 685e ldr r6, [r3, #4]

c8ba: 693b ldr r3, [r7, #16]

c8bc: 1b9d subs r5, r3, r6

c8be: 68fb ldr r3, [r7, #12]

c8c0: 429d cmp r5, r3

c8c2: bf98 it ls

c8c4: f44f 3380 movls.w r3, #65536 ; 0x10000

c8c8: d910 bls.n c8ec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf0c>

c8ca: 68f9 ldr r1, [r7, #12]

c8cc: 4628 mov r0, r5

c8ce: f006 fecb bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

c8d2: f5b0 3f80 cmp.w r0, #65536 ; 0x10000

c8d6: bf28 it cs

c8d8: 2301 movcs r3, #1

c8da: d207 bcs.n c8ec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf0c>

c8dc: 28ff cmp r0, #255 ; 0xff

c8de: d95b bls.n c998 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfb8>

c8e0: 4601 mov r1, r0

c8e2: f44f 3080 mov.w r0, #65536 ; 0x10000

c8e6: f006 febf bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

c8ea: 4603 mov r3, r0

c8ec: 4632 mov r2, r6

c8ee: 68f9 ldr r1, [r7, #12]

c8f0: 6978 ldr r0, [r7, #20]

c8f2: f005 f803 bl 118fc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb94>

c8f6: 4b4c ldr r3, [pc, #304] ; (ca28 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1048>)

c8f8: 2201 movs r2, #1

c8fa: f507 7717 add.w r7, r7, #604 ; 0x25c

c8fe: 447b add r3, pc

c900: 601a str r2, [r3, #0]

c902: 46bd mov sp, r7

c904: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

c908: 697c ldr r4, [r7, #20]

c90a: 4631 mov r1, r6

c90c: f8c7 d014 str.w sp, [r7, #20]

c910: 6923 ldr r3, [r4, #16]

c912: 1dd8 adds r0, r3, #7

c914: f020 0007 bic.w r0, r0, #7

c918: 461a mov r2, r3

c91a: ebad 0d00 sub.w sp, sp, r0

c91e: 603b str r3, [r7, #0]

c920: ab02 add r3, sp, #8

c922: 60bb str r3, [r7, #8]

c924: 4618 mov r0, r3

c926: f006 eb94 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

c92a: 683b ldr r3, [r7, #0]

c92c: 69fa ldr r2, [r7, #28]

c92e: 4640 mov r0, r8

c930: 425b negs r3, r3

c932: ea03 0102 and.w r1, r3, r2

c936: 4632 mov r2, r6

c938: f005 f880 bl 11a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xcd4>

c93c: 3001 adds r0, #1

c93e: d023 beq.n c988 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfa8>

c940: 4e3a ldr r6, [pc, #232] ; (ca2c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x104c>)

c942: 46a1 mov r9, r4

c944: 603d str r5, [r7, #0]

c946: 447e add r6, pc

c948: 69fc ldr r4, [r7, #28]

c94a: 4635 mov r5, r6

c94c: 68be ldr r6, [r7, #8]

c94e: e002 b.n c956 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf76>

c950: 682a ldr r2, [r5, #0]

c952: 2a04 cmp r2, #4

c954: d118 bne.n c988 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfa8>

c956: f8d9 2010 ldr.w r2, [r9, #16]

c95a: 4631 mov r1, r6

c95c: 4640 mov r0, r8

c95e: 3a01 subs r2, #1

c960: 4022 ands r2, r4

c962: f005 f9b3 bl 11ccc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf64>

c966: 1c42 adds r2, r0, #1

c968: d0f2 beq.n c950 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf70>

c96a: 2800 cmp r0, #0

c96c: 683d ldr r5, [r7, #0]

c96e: db0b blt.n c988 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xfa8>

c970: f8d7 d014 ldr.w sp, [r7, #20]

c974: e6c1 b.n c6fa <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xd1a>

c976: 2200 movs r2, #0

c978: f04f 31ff mov.w r1, #4294967295 ; 0xffffffff

c97c: 4610 mov r0, r2

c97e: 460c mov r4, r1

c980: e60c b.n c59c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xbbc>

c982: 4c2b ldr r4, [pc, #172] ; (ca30 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1050>)

c984: 447c add r4, pc

c986: e726 b.n c7d6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xdf6>

c988: 4c2a ldr r4, [pc, #168] ; (ca34 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1054>)

c98a: f8d7 d014 ldr.w sp, [r7, #20]

c98e: 447c add r4, pc

c990: e721 b.n c7d6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xdf6>

c992: 4c29 ldr r4, [pc, #164] ; (ca38 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x1058>)

c994: 447c add r4, pc

c996: e71e b.n c7d6 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xdf6>

c998: f1b5 7f80 cmp.w r5, #16777216 ; 0x1000000

c99c: bf33 iteet cc

c99e: 0228 lslcc r0, r5, #8

c9a0: 6a7b ldrcs r3, [r7, #36] ; 0x24

c9a2: 4628 movcs r0, r5

c9a4: 68f9 ldrcc r1, [r7, #12]

c9a6: bf28 it cs

c9a8: 0a59 lsrcs r1, r3, #9

c9aa: f006 fe5d bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

c9ae: 4601 mov r1, r0

c9b0: f04f 7080 mov.w r0, #16777216 ; 0x1000000

c9b4: f006 fe58 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

c9b8: 4603 mov r3, r0

c9ba: e797 b.n c8ec <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf0c>

c9bc: 69f9 ldr r1, [r7, #28]

c9be: 4630 mov r0, r6

c9c0: f005 f9fe bl 11dc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1058>

c9c4: 491d ldr r1, [pc, #116] ; (ca3c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x105c>)

c9c6: 2002 movs r0, #2

c9c8: 4479 add r1, pc

c9ca: f7ff f9ff bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

c9ce: 207f movs r0, #127 ; 0x7f

c9d0: f005 fa3a bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

c9d4: 2060 movs r0, #96 ; 0x60

c9d6: 612b str r3, [r5, #16]

c9d8: fb00 f003 mul.w r0, r0, r3

c9dc: 00db lsls r3, r3, #3

c9de: 61bb str r3, [r7, #24]

c9e0: f100 0940 add.w r9, r0, #64 ; 0x40

c9e4: e5fe b.n c5e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xc04>

c9e6: bf00 nop

c9e8: cb48 ldmia r3, {r3, r6}

c9ea: 0001 movs r1, r0

c9ec: c42e stmia r4!, {r1, r2, r3, r5}

c9ee: 0001 movs r1, r0

c9f0: d326 bcc.n ca40 <\_dl\_mcount@@GLIBC\_2.4>

c9f2: 0001 movs r1, r0

c9f4: c34e stmia r3!, {r1, r2, r3, r6}

c9f6: 0001 movs r1, r0

c9f8: a5de add r5, pc, #888 ; (adr r5, cd74 <\_dl\_mcount@@GLIBC\_2.4+0x334>)

c9fa: 0000 movs r0, r0

c9fc: a58c add r5, pc, #560 ; (adr r5, cc30 <\_dl\_mcount@@GLIBC\_2.4+0x1f0>)

c9fe: 0000 movs r0, r0

ca00: d1b6 bne.n c970 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf90>

ca02: 0001 movs r1, r0

ca04: d1ac bne.n c960 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf80>

ca06: 0001 movs r1, r0

ca08: a3e6 add r3, pc, #920 ; (adr r3, cda4 <\_dl\_mcount@@GLIBC\_2.4+0x364>)

ca0a: 0000 movs r0, r0

ca0c: a3d8 add r3, pc, #864 ; (adr r3, cd70 <\_dl\_mcount@@GLIBC\_2.4+0x330>)

ca0e: 0000 movs r0, r0

ca10: d17a bne.n cb08 <\_dl\_mcount@@GLIBC\_2.4+0xc8>

ca12: 0001 movs r1, r0

ca14: c17a stmia r1!, {r1, r3, r4, r5, r6}

ca16: 0001 movs r1, r0

ca18: a420 add r4, pc, #128 ; (adr r4, ca9c <\_dl\_mcount@@GLIBC\_2.4+0x5c>)

ca1a: 0000 movs r0, r0

ca1c: d08e beq.n c93c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xf5c>

ca1e: 0001 movs r1, r0

ca20: d06a beq.n caf8 <\_dl\_mcount@@GLIBC\_2.4+0xb8>

ca22: 0001 movs r1, r0

ca24: d026 beq.n ca74 <\_dl\_mcount@@GLIBC\_2.4+0x34>

ca26: 0001 movs r1, r0

ca28: cfde ldmia r7, {r1, r2, r3, r4, r6, r7}

ca2a: 0001 movs r1, r0

ca2c: d00e beq.n ca4c <\_dl\_mcount@@GLIBC\_2.4+0xc>

ca2e: 0001 movs r1, r0

ca30: a228 add r2, pc, #160 ; (adr r2, cad4 <\_dl\_mcount@@GLIBC\_2.4+0x94>)

ca32: 0000 movs r0, r0

ca34: a23a add r2, pc, #232 ; (adr r2, cb20 <\_dl\_mcount@@GLIBC\_2.4+0xe0>)

ca36: 0000 movs r0, r0

ca38: a250 add r2, pc, #320 ; (adr r2, cb7c <\_dl\_mcount@@GLIBC\_2.4+0x13c>)

ca3a: 0000 movs r0, r0

ca3c: a280 add r2, pc, #512 ; (adr r2, cc40 <\_dl\_mcount@@GLIBC\_2.4+0x200>)

...

0000ca40 <\_dl\_mcount@@GLIBC\_2.4>:

ca40: 4ba0 ldr r3, [pc, #640] ; (ccc4 <\_dl\_mcount@@GLIBC\_2.4+0x284>)

ca42: 447b add r3, pc

ca44: 681a ldr r2, [r3, #0]

ca46: 2a00 cmp r2, #0

ca48: f000 8136 beq.w ccb8 <\_dl\_mcount@@GLIBC\_2.4+0x278>

ca4c: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

ca50: b097 sub sp, #92 ; 0x5c

ca52: e9d3 4201 ldrd r4, r2, [r3, #4]

ca56: 1b00 subs r0, r0, r4

ca58: eba1 0c04 sub.w ip, r1, r4

ca5c: 4290 cmp r0, r2

ca5e: bf28 it cs

ca60: 2000 movcs r0, #0

ca62: 4562 cmp r2, ip

ca64: f240 8125 bls.w ccb2 <\_dl\_mcount@@GLIBC\_2.4+0x272>

ca68: 68d9 ldr r1, [r3, #12]

ca6a: 69dc ldr r4, [r3, #28]

ca6c: fa2c f101 lsr.w r1, ip, r1

ca70: f834 2011 ldrh.w r2, [r4, r1, lsl #1]

ca74: eb04 0141 add.w r1, r4, r1, lsl #1

ca78: 9102 str r1, [sp, #8]

ca7a: b291 uxth r1, r2

ca7c: b1e1 cbz r1, cab8 <\_dl\_mcount@@GLIBC\_2.4+0x78>

ca7e: 6a1a ldr r2, [r3, #32]

ca80: eb02 02c1 add.w r2, r2, r1, lsl #3

ca84: 6811 ldr r1, [r2, #0]

ca86: 680b ldr r3, [r1, #0]

ca88: 4298 cmp r0, r3

ca8a: f000 80f2 beq.w cc72 <\_dl\_mcount@@GLIBC\_2.4+0x232>

ca8e: 8893 ldrh r3, [r2, #4]

ca90: b163 cbz r3, caac <\_dl\_mcount@@GLIBC\_2.4+0x6c>

ca92: 4a8d ldr r2, [pc, #564] ; (ccc8 <\_dl\_mcount@@GLIBC\_2.4+0x288>)

ca94: 447a add r2, pc

ca96: 6a14 ldr r4, [r2, #32]

ca98: e002 b.n caa0 <\_dl\_mcount@@GLIBC\_2.4+0x60>

ca9a: 680d ldr r5, [r1, #0]

ca9c: 4285 cmp r5, r0

ca9e: d005 beq.n caac <\_dl\_mcount@@GLIBC\_2.4+0x6c>

caa0: 00db lsls r3, r3, #3

caa2: 18e2 adds r2, r4, r3

caa4: 58e1 ldr r1, [r4, r3]

caa6: 8893 ldrh r3, [r2, #4]

caa8: 2b00 cmp r3, #0

caaa: d1f6 bne.n ca9a <\_dl\_mcount@@GLIBC\_2.4+0x5a>

caac: 680b ldr r3, [r1, #0]

caae: 4298 cmp r0, r3

cab0: f000 80df beq.w cc72 <\_dl\_mcount@@GLIBC\_2.4+0x232>

cab4: 1d13 adds r3, r2, #4

cab6: 9302 str r3, [sp, #8]

cab8: f8df a210 ldr.w sl, [pc, #528] ; cccc <\_dl\_mcount@@GLIBC\_2.4+0x28c>

cabc: f04f 0e0c mov.w lr, #12

cac0: 4c83 ldr r4, [pc, #524] ; (ccd0 <\_dl\_mcount@@GLIBC\_2.4+0x290>)

cac2: 44fa add sl, pc

cac4: 4d83 ldr r5, [pc, #524] ; (ccd4 <\_dl\_mcount@@GLIBC\_2.4+0x294>)

cac6: 4b84 ldr r3, [pc, #528] ; (ccd8 <\_dl\_mcount@@GLIBC\_2.4+0x298>)

cac8: f10a 0224 add.w r2, sl, #36 ; 0x24

cacc: 447c add r4, pc

cace: 447d add r5, pc

cad0: 447b add r3, pc

cad2: e9cd 0c03 strd r0, ip, [sp, #12]

cad6: 4694 mov ip, r2

cad8: 9305 str r3, [sp, #20]

cada: 3328 adds r3, #40 ; 0x28

cadc: 9301 str r3, [sp, #4]

cade: 6963 ldr r3, [r4, #20]

cae0: 6aa1 ldr r1, [r4, #40] ; 0x28

cae2: 681a ldr r2, [r3, #0]

cae4: 4291 cmp r1, r2

cae6: d04b beq.n cb80 <\_dl\_mcount@@GLIBC\_2.4+0x140>

cae8: 6922 ldr r2, [r4, #16]

caea: 4291 cmp r1, r2

caec: d248 bcs.n cb80 <\_dl\_mcount@@GLIBC\_2.4+0x140>

caee: 69a3 ldr r3, [r4, #24]

caf0: aa09 add r2, sp, #36 ; 0x24

caf2: 2001 movs r0, #1

caf4: 9007 str r0, [sp, #28]

caf6: fb0e 3301 mla r3, lr, r1, r3

cafa: 6859 ldr r1, [r3, #4]

cafc: 0889 lsrs r1, r1, #2

cafe: f8da 3024 ldr.w r3, [sl, #36] ; 0x24

cb02: 9306 str r3, [sp, #24]

cb04: 9b06 ldr r3, [sp, #24]

cb06: 9308 str r3, [sp, #32]

cb08: 9b08 ldr r3, [sp, #32]

cb0a: 9309 str r3, [sp, #36] ; 0x24

cb0c: 9b06 ldr r3, [sp, #24]

cb0e: 9e07 ldr r6, [sp, #28]

cb10: 6810 ldr r0, [r2, #0]

cb12: 4433 add r3, r6

cb14: e85c 6f00 ldrex r6, [ip]

cb18: 4286 cmp r6, r0

cb1a: d105 bne.n cb28 <\_dl\_mcount@@GLIBC\_2.4+0xe8>

cb1c: e84c 3700 strex r7, r3, [ip]

cb20: 2f00 cmp r7, #0

cb22: d1f7 bne.n cb14 <\_dl\_mcount@@GLIBC\_2.4+0xd4>

cb24: f3bf 8f5b dmb ish

cb28: bf18 it ne

cb2a: 6016 strne r6, [r2, #0]

cb2c: 9809 ldr r0, [sp, #36] ; 0x24

cb2e: 9b08 ldr r3, [sp, #32]

cb30: 4298 cmp r0, r3

cb32: d1e4 bne.n cafe <\_dl\_mcount@@GLIBC\_2.4+0xbe>

cb34: 6aaa ldr r2, [r5, #40] ; 0x28

cb36: 69a8 ldr r0, [r5, #24]

cb38: 9b06 ldr r3, [sp, #24]

cb3a: 69ee ldr r6, [r5, #28]

cb3c: fb0e 0002 mla r0, lr, r2, r0

cb40: 3301 adds r3, #1

cb42: 6a2f ldr r7, [r5, #32]

cb44: f836 b011 ldrh.w fp, [r6, r1, lsl #1]

cb48: fa1f f883 uxth.w r8, r3

cb4c: eb07 09c3 add.w r9, r7, r3, lsl #3

cb50: f847 0033 str.w r0, [r7, r3, lsl #3]

cb54: f8a9 b004 strh.w fp, [r9, #4]

cb58: f826 8011 strh.w r8, [r6, r1, lsl #1]

cb5c: 9b01 ldr r3, [sp, #4]

cb5e: 1c51 adds r1, r2, #1

cb60: 4618 mov r0, r3

cb62: e850 3f00 ldrex r3, [r0]

cb66: 4293 cmp r3, r2

cb68: d105 bne.n cb76 <\_dl\_mcount@@GLIBC\_2.4+0x136>

cb6a: e840 1600 strex r6, r1, [r0]

cb6e: 2e00 cmp r6, #0

cb70: d1f7 bne.n cb62 <\_dl\_mcount@@GLIBC\_2.4+0x122>

cb72: f3bf 8f5b dmb ish

cb76: 4293 cmp r3, r2

cb78: d0b1 beq.n cade <\_dl\_mcount@@GLIBC\_2.4+0x9e>

cb7a: 9b05 ldr r3, [sp, #20]

cb7c: 6a9a ldr r2, [r3, #40] ; 0x28

cb7e: e7ed b.n cb5c <\_dl\_mcount@@GLIBC\_2.4+0x11c>

cb80: 9a02 ldr r2, [sp, #8]

cb82: e9dd 0c03 ldrd r0, ip, [sp, #12]

cb86: 8812 ldrh r2, [r2, #0]

cb88: b292 uxth r2, r2

cb8a: 2a00 cmp r2, #0

cb8c: f040 8095 bne.w ccba <\_dl\_mcount@@GLIBC\_2.4+0x27a>

cb90: a90d add r1, sp, #52 ; 0x34

cb92: 2201 movs r2, #1

cb94: 920b str r2, [sp, #44] ; 0x2c

cb96: 681a ldr r2, [r3, #0]

cb98: 920a str r2, [sp, #40] ; 0x28

cb9a: 9a0a ldr r2, [sp, #40] ; 0x28

cb9c: 920c str r2, [sp, #48] ; 0x30

cb9e: 9a0c ldr r2, [sp, #48] ; 0x30

cba0: 920d str r2, [sp, #52] ; 0x34

cba2: 9a0a ldr r2, [sp, #40] ; 0x28

cba4: 9d0b ldr r5, [sp, #44] ; 0x2c

cba6: 680c ldr r4, [r1, #0]

cba8: 442a add r2, r5

cbaa: e853 5f00 ldrex r5, [r3]

cbae: 42a5 cmp r5, r4

cbb0: d105 bne.n cbbe <\_dl\_mcount@@GLIBC\_2.4+0x17e>

cbb2: e843 2600 strex r6, r2, [r3]

cbb6: 2e00 cmp r6, #0

cbb8: d1f7 bne.n cbaa <\_dl\_mcount@@GLIBC\_2.4+0x16a>

cbba: f3bf 8f5b dmb ish

cbbe: bf18 it ne

cbc0: 600d strne r5, [r1, #0]

cbc2: 9c0d ldr r4, [sp, #52] ; 0x34

cbc4: 9a0c ldr r2, [sp, #48] ; 0x30

cbc6: 4294 cmp r4, r2

cbc8: d1e5 bne.n cb96 <\_dl\_mcount@@GLIBC\_2.4+0x156>

cbca: 4b44 ldr r3, [pc, #272] ; (ccdc <\_dl\_mcount@@GLIBC\_2.4+0x29c>)

cbcc: 990a ldr r1, [sp, #40] ; 0x28

cbce: 447b add r3, pc

cbd0: 691a ldr r2, [r3, #16]

cbd2: 4291 cmp r1, r2

cbd4: d26d bcs.n ccb2 <\_dl\_mcount@@GLIBC\_2.4+0x272>

cbd6: ae11 add r6, sp, #68 ; 0x44

cbd8: 461a mov r2, r3

cbda: f103 0524 add.w r5, r3, #36 ; 0x24

cbde: 2301 movs r3, #1

cbe0: 930f str r3, [sp, #60] ; 0x3c

cbe2: 6a53 ldr r3, [r2, #36] ; 0x24

cbe4: 930e str r3, [sp, #56] ; 0x38

cbe6: 9b0e ldr r3, [sp, #56] ; 0x38

cbe8: 9310 str r3, [sp, #64] ; 0x40

cbea: 9b10 ldr r3, [sp, #64] ; 0x40

cbec: 9311 str r3, [sp, #68] ; 0x44

cbee: 9b0e ldr r3, [sp, #56] ; 0x38

cbf0: 9f0f ldr r7, [sp, #60] ; 0x3c

cbf2: 6834 ldr r4, [r6, #0]

cbf4: 443b add r3, r7

cbf6: e855 7f00 ldrex r7, [r5]

cbfa: 42a7 cmp r7, r4

cbfc: d106 bne.n cc0c <\_dl\_mcount@@GLIBC\_2.4+0x1cc>

cbfe: e845 3e00 strex lr, r3, [r5]

cc02: f1be 0f00 cmp.w lr, #0

cc06: d1f6 bne.n cbf6 <\_dl\_mcount@@GLIBC\_2.4+0x1b6>

cc08: f3bf 8f5b dmb ish

cc0c: bf18 it ne

cc0e: 6037 strne r7, [r6, #0]

cc10: 9c11 ldr r4, [sp, #68] ; 0x44

cc12: 9b10 ldr r3, [sp, #64] ; 0x40

cc14: 429c cmp r4, r3

cc16: d1e4 bne.n cbe2 <\_dl\_mcount@@GLIBC\_2.4+0x1a2>

cc18: 9b0e ldr r3, [sp, #56] ; 0x38

cc1a: 250c movs r5, #12

cc1c: fb05 f101 mul.w r1, r5, r1

cc20: 9d02 ldr r5, [sp, #8]

cc22: 4a2f ldr r2, [pc, #188] ; (cce0 <\_dl\_mcount@@GLIBC\_2.4+0x2a0>)

cc24: 3301 adds r3, #1

cc26: 2400 movs r4, #0

cc28: 447a add r2, pc

cc2a: b29b uxth r3, r3

cc2c: 802b strh r3, [r5, #0]

cc2e: f102 0728 add.w r7, r2, #40 ; 0x28

cc32: 882b ldrh r3, [r5, #0]

cc34: f8d2 e018 ldr.w lr, [r2, #24]

cc38: 6a15 ldr r5, [r2, #32]

cc3a: b29b uxth r3, r3

cc3c: eb0e 0601 add.w r6, lr, r1

cc40: f845 6033 str.w r6, [r5, r3, lsl #3]

cc44: f84e 0001 str.w r0, [lr, r1]

cc48: eb05 01c3 add.w r1, r5, r3, lsl #3

cc4c: e9c6 c401 strd ip, r4, [r6, #4]

cc50: 808c strh r4, [r1, #4]

cc52: 6a91 ldr r1, [r2, #40] ; 0x28

cc54: 1c4c adds r4, r1, #1

cc56: e857 0f00 ldrex r0, [r7]

cc5a: 4288 cmp r0, r1

cc5c: d105 bne.n cc6a <\_dl\_mcount@@GLIBC\_2.4+0x22a>

cc5e: e847 4600 strex r6, r4, [r7]

cc62: 2e00 cmp r6, #0

cc64: d1f7 bne.n cc56 <\_dl\_mcount@@GLIBC\_2.4+0x216>

cc66: f3bf 8f5b dmb ish

cc6a: 4281 cmp r1, r0

cc6c: d1f1 bne.n cc52 <\_dl\_mcount@@GLIBC\_2.4+0x212>

cc6e: f855 1033 ldr.w r1, [r5, r3, lsl #3]

cc72: f101 0208 add.w r2, r1, #8

cc76: a815 add r0, sp, #84 ; 0x54

cc78: 2301 movs r3, #1

cc7a: 9313 str r3, [sp, #76] ; 0x4c

cc7c: 688b ldr r3, [r1, #8]

cc7e: 9312 str r3, [sp, #72] ; 0x48

cc80: 9b12 ldr r3, [sp, #72] ; 0x48

cc82: 9314 str r3, [sp, #80] ; 0x50

cc84: 9b14 ldr r3, [sp, #80] ; 0x50

cc86: 9315 str r3, [sp, #84] ; 0x54

cc88: 9b12 ldr r3, [sp, #72] ; 0x48

cc8a: 9d13 ldr r5, [sp, #76] ; 0x4c

cc8c: 6804 ldr r4, [r0, #0]

cc8e: 442b add r3, r5

cc90: e852 5f00 ldrex r5, [r2]

cc94: 42a5 cmp r5, r4

cc96: d105 bne.n cca4 <\_dl\_mcount@@GLIBC\_2.4+0x264>

cc98: e842 3600 strex r6, r3, [r2]

cc9c: 2e00 cmp r6, #0

cc9e: d1f7 bne.n cc90 <\_dl\_mcount@@GLIBC\_2.4+0x250>

cca0: f3bf 8f5b dmb ish

cca4: bf18 it ne

cca6: 6005 strne r5, [r0, #0]

cca8: 9c15 ldr r4, [sp, #84] ; 0x54

ccaa: 9b14 ldr r3, [sp, #80] ; 0x50

ccac: 429c cmp r4, r3

ccae: d1e5 bne.n cc7c <\_dl\_mcount@@GLIBC\_2.4+0x23c>

ccb0: 9b12 ldr r3, [sp, #72] ; 0x48

ccb2: b017 add sp, #92 ; 0x5c

ccb4: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

ccb8: 4770 bx lr

ccba: 9a02 ldr r2, [sp, #8]

ccbc: 4b09 ldr r3, [pc, #36] ; (cce4 <\_dl\_mcount@@GLIBC\_2.4+0x2a4>)

ccbe: 8811 ldrh r1, [r2, #0]

ccc0: 447b add r3, pc

ccc2: e6dc b.n ca7e <\_dl\_mcount@@GLIBC\_2.4+0x3e>

ccc4: ce9a ldmia r6!, {r1, r3, r4, r7}

ccc6: 0001 movs r1, r0

ccc8: ce48 ldmia r6, {r3, r6}

ccca: 0001 movs r1, r0

cccc: ce1a ldmia r6!, {r1, r3, r4}

ccce: 0001 movs r1, r0

ccd0: ce10 ldmia r6!, {r4}

ccd2: 0001 movs r1, r0

ccd4: ce0e ldmia r6!, {r1, r2, r3}

ccd6: 0001 movs r1, r0

ccd8: ce0c ldmia r6!, {r2, r3}

ccda: 0001 movs r1, r0

ccdc: cd0e ldmia r5!, {r1, r2, r3}

ccde: 0001 movs r1, r0

cce0: ccb4 ldmia r4, {r2, r4, r5, r7}

cce2: 0001 movs r1, r0

cce4: cc1c ldmia r4, {r2, r3, r4}

cce6: 0001 movs r1, r0

cce8: b538 push {r3, r4, r5, lr}

ccea: 2108 movs r1, #8

ccec: 4b08 ldr r3, [pc, #32] ; (cd10 <\_dl\_mcount@@GLIBC\_2.4+0x2d0>)

ccee: 4605 mov r5, r0

ccf0: 447b add r3, pc

ccf2: f8d3 480c ldr.w r4, [r3, #2060] ; 0x80c

ccf6: f104 0010 add.w r0, r4, #16

ccfa: f7f3 ee7a blx 9f0 <calloc@plt>

ccfe: b128 cbz r0, cd0c <\_dl\_mcount@@GLIBC\_2.4+0x2cc>

cd00: 4603 mov r3, r0

cd02: 4628 mov r0, r5

cd04: 340e adds r4, #14

cd06: f843 4b08 str.w r4, [r3], #8

cd0a: 602b str r3, [r5, #0]

cd0c: bd38 pop {r3, r4, r5, pc}

cd0e: bf00 nop

cd10: c36c stmia r3!, {r2, r3, r5, r6}

cd12: 0001 movs r1, r0

cd14: 4904 ldr r1, [pc, #16] ; (cd28 <\_dl\_mcount@@GLIBC\_2.4+0x2e8>)

cd16: 2002 movs r0, #2

cd18: b508 push {r3, lr}

cd1a: 4479 add r1, pc

cd1c: f7ff f856 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

cd20: 207f movs r0, #127 ; 0x7f

cd22: f005 f891 bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

cd26: bf00 nop

cd28: 9f5a ldr r7, [sp, #360] ; 0x168

cd2a: 0000 movs r0, r0

cd2c: 4a1a ldr r2, [pc, #104] ; (cd98 <\_dl\_mcount@@GLIBC\_2.4+0x358>)

cd2e: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

cd32: 447a add r2, pc

cd34: f8d2 380c ldr.w r3, [r2, #2060] ; 0x80c

cd38: f3bf 8f5b dmb ish

cd3c: f8d2 8824 ldr.w r8, [r2, #2084] ; 0x824

cd40: 4580 cmp r8, r0

cd42: f850 4c08 ldr.w r4, [r0, #-8]

cd46: f103 060e add.w r6, r3, #14

cd4a: d014 beq.n cd76 <\_dl\_mcount@@GLIBC\_2.4+0x336>

cd4c: f103 0110 add.w r1, r3, #16

cd50: 3808 subs r0, #8

cd52: 00c9 lsls r1, r1, #3

cd54: f7f3 ee52 blx 9fc <realloc@plt>

cd58: 4607 mov r7, r0

cd5a: b1d8 cbz r0, cd94 <\_dl\_mcount@@GLIBC\_2.4+0x354>

cd5c: 1ca5 adds r5, r4, #2

cd5e: 00ed lsls r5, r5, #3

cd60: 1b32 subs r2, r6, r4

cd62: 603e str r6, [r7, #0]

cd64: 1978 adds r0, r7, r5

cd66: 2100 movs r1, #0

cd68: 00d2 lsls r2, r2, #3

cd6a: f006 e972 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

cd6e: f107 0008 add.w r0, r7, #8

cd72: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

cd76: f103 0010 add.w r0, r3, #16

cd7a: 00c0 lsls r0, r0, #3

cd7c: f7f3 ee26 blx 9cc <malloc@plt+0x4>

cd80: 4607 mov r7, r0

cd82: b138 cbz r0, cd94 <\_dl\_mcount@@GLIBC\_2.4+0x354>

cd84: 1ca5 adds r5, r4, #2

cd86: f1a8 0108 sub.w r1, r8, #8

cd8a: 00ed lsls r5, r5, #3

cd8c: 462a mov r2, r5

cd8e: f006 e9b8 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

cd92: e7e5 b.n cd60 <\_dl\_mcount@@GLIBC\_2.4+0x320>

cd94: f7ff ffbe bl cd14 <\_dl\_mcount@@GLIBC\_2.4+0x2d4>

cd98: c32a stmia r3!, {r1, r3, r5}

cd9a: 0001 movs r1, r0

cd9c: e92d 47f0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, lr}

cda0: 4606 mov r6, r0

cda2: 460f mov r7, r1

cda4: 4615 mov r5, r2

cda6: 4698 mov r8, r3

cda8: 2b00 cmp r3, #0

cdaa: d037 beq.n ce1c <\_dl\_mcount@@GLIBC\_2.4+0x3dc>

cdac: f8d8 3244 ldr.w r3, [r8, #580] ; 0x244

cdb0: 3301 adds r3, #1

cdb2: d148 bne.n ce46 <\_dl\_mcount@@GLIBC\_2.4+0x406>

cdb4: e9d8 048e ldrd r0, r4, [r8, #568] ; 0x238

cdb8: f104 3aff add.w sl, r4, #4294967295 ; 0xffffffff

cdbc: ea04 030a and.w r3, r4, sl

cdc0: 2c08 cmp r4, #8

cdc2: bf98 it ls

cdc4: 2b00 cmpls r3, #0

cdc6: d052 beq.n ce6e <\_dl\_mcount@@GLIBC\_2.4+0x42e>

cdc8: 1820 adds r0, r4, r0

cdca: d274 bcs.n ceb6 <\_dl\_mcount@@GLIBC\_2.4+0x476>

cdcc: f7f3 edfe blx 9cc <malloc@plt+0x4>

cdd0: 4681 mov r9, r0

cdd2: 2800 cmp r0, #0

cdd4: d06f beq.n ceb6 <\_dl\_mcount@@GLIBC\_2.4+0x476>

cdd6: 4621 mov r1, r4

cdd8: 4450 add r0, sl

cdda: f006 fc45 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

cdde: fb04 f400 mul.w r4, r4, r0

cde2: 2c00 cmp r4, #0

cde4: d067 beq.n ceb6 <\_dl\_mcount@@GLIBC\_2.4+0x476>

cde6: f8d8 2234 ldr.w r2, [r8, #564] ; 0x234

cdea: 4620 mov r0, r4

cdec: f8d8 1230 ldr.w r1, [r8, #560] ; 0x230

cdf0: f006 f96c bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

cdf4: e9d8 328d ldrd r3, r2, [r8, #564] ; 0x234

cdf8: 2100 movs r1, #0

cdfa: 1ad2 subs r2, r2, r3

cdfc: f006 e928 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

ce00: 6833 ldr r3, [r6, #0]

ce02: eb05 02c3 add.w r2, r5, r3, lsl #3

ce06: f845 4033 str.w r4, [r5, r3, lsl #3]

ce0a: f8c2 9004 str.w r9, [r2, #4]

ce0e: f1b9 0f00 cmp.w r9, #0

ce12: d046 beq.n cea2 <\_dl\_mcount@@GLIBC\_2.4+0x462>

ce14: 6838 ldr r0, [r7, #0]

ce16: 4420 add r0, r4

ce18: e8bd 87f0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, pc}

ce1c: 4b27 ldr r3, [pc, #156] ; (cebc <\_dl\_mcount@@GLIBC\_2.4+0x47c>)

ce1e: 6802 ldr r2, [r0, #0]

ce20: 447b add r3, pc

ce22: f8d3 3810 ldr.w r3, [r3, #2064] ; 0x810

ce26: 6819 ldr r1, [r3, #0]

ce28: 428a cmp r2, r1

ce2a: d304 bcc.n ce36 <\_dl\_mcount@@GLIBC\_2.4+0x3f6>

ce2c: 685b ldr r3, [r3, #4]

ce2e: 1a52 subs r2, r2, r1

ce30: 6819 ldr r1, [r3, #0]

ce32: 4291 cmp r1, r2

ce34: d9fa bls.n ce2c <\_dl\_mcount@@GLIBC\_2.4+0x3ec>

ce36: eb03 03c2 add.w r3, r3, r2, lsl #3

ce3a: f8d3 800c ldr.w r8, [r3, #12]

ce3e: f8d8 3244 ldr.w r3, [r8, #580] ; 0x244

ce42: 3301 adds r3, #1

ce44: d0b6 beq.n cdb4 <\_dl\_mcount@@GLIBC\_2.4+0x374>

ce46: 4c1e ldr r4, [pc, #120] ; (cec0 <\_dl\_mcount@@GLIBC\_2.4+0x480>)

ce48: 447c add r4, pc

ce4a: f204 49c4 addw r9, r4, #1220 ; 0x4c4

ce4e: f8d4 37f8 ldr.w r3, [r4, #2040] ; 0x7f8

ce52: 4648 mov r0, r9

ce54: 4798 blx r3

ce56: f8d8 3244 ldr.w r3, [r8, #580] ; 0x244

ce5a: b96b cbnz r3, ce78 <\_dl\_mcount@@GLIBC\_2.4+0x438>

ce5c: f8d4 37fc ldr.w r3, [r4, #2044] ; 0x7fc

ce60: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

ce64: 4648 mov r0, r9

ce66: f8c8 2244 str.w r2, [r8, #580] ; 0x244

ce6a: 4798 blx r3

ce6c: e7a2 b.n cdb4 <\_dl\_mcount@@GLIBC\_2.4+0x374>

ce6e: f7f3 edae blx 9cc <malloc@plt+0x4>

ce72: 4604 mov r4, r0

ce74: 4681 mov r9, r0

ce76: e7b4 b.n cde2 <\_dl\_mcount@@GLIBC\_2.4+0x3a2>

ce78: 1c59 adds r1, r3, #1

ce7a: f8d4 27fc ldr.w r2, [r4, #2044] ; 0x7fc

ce7e: 4648 mov r0, r9

ce80: d00d beq.n ce9e <\_dl\_mcount@@GLIBC\_2.4+0x45e>

ce82: ee1d 4f70 mrc 15, 0, r4, cr13, cr0, {3}

ce86: 441c add r4, r3

ce88: 4790 blx r2

ce8a: 6833 ldr r3, [r6, #0]

ce8c: 6838 ldr r0, [r7, #0]

ce8e: 2100 movs r1, #0

ce90: eb05 02c3 add.w r2, r5, r3, lsl #3

ce94: 4420 add r0, r4

ce96: 6051 str r1, [r2, #4]

ce98: f845 4033 str.w r4, [r5, r3, lsl #3]

ce9c: e7bc b.n ce18 <\_dl\_mcount@@GLIBC\_2.4+0x3d8>

ce9e: 4790 blx r2

cea0: e788 b.n cdb4 <\_dl\_mcount@@GLIBC\_2.4+0x374>

cea2: 4b08 ldr r3, [pc, #32] ; (cec4 <\_dl\_mcount@@GLIBC\_2.4+0x484>)

cea4: f240 3215 movw r2, #789 ; 0x315

cea8: 4907 ldr r1, [pc, #28] ; (cec8 <\_dl\_mcount@@GLIBC\_2.4+0x488>)

ceaa: 4808 ldr r0, [pc, #32] ; (cecc <\_dl\_mcount@@GLIBC\_2.4+0x48c>)

ceac: 447b add r3, pc

ceae: 4479 add r1, pc

ceb0: 4478 add r0, pc

ceb2: f003 fd6b bl 1098c <free@@GLIBC\_2.4+0x12c>

ceb6: f7ff ff2d bl cd14 <\_dl\_mcount@@GLIBC\_2.4+0x2d4>

ceba: bf00 nop

cebc: c23c stmia r2!, {r2, r3, r4, r5}

cebe: 0001 movs r1, r0

cec0: c214 stmia r2!, {r2, r4}

cec2: 0001 movs r1, r0

cec4: a008 add r0, pc, #32 ; (adr r0, cee8 <\_dl\_mcount@@GLIBC\_2.4+0x4a8>)

cec6: 0000 movs r0, r0

cec8: 9dfe ldr r5, [sp, #1016] ; 0x3f8

ceca: 0000 movs r0, r0

cecc: 9e08 ldr r6, [sp, #32]

cece: 0000 movs r0, r0

ced0: 4b21 ldr r3, [pc, #132] ; (cf58 <\_dl\_mcount@@GLIBC\_2.4+0x518>)

ced2: b570 push {r4, r5, r6, lr}

ced4: 447b add r3, pc

ced6: f893 2808 ldrb.w r2, [r3, #2056] ; 0x808

ceda: f8d3 580c ldr.w r5, [r3, #2060] ; 0x80c

cede: b92a cbnz r2, ceec <\_dl\_mcount@@GLIBC\_2.4+0x4ac>

cee0: 1c68 adds r0, r5, #1

cee2: 4b1e ldr r3, [pc, #120] ; (cf5c <\_dl\_mcount@@GLIBC\_2.4+0x51c>)

cee4: 447b add r3, pc

cee6: f8c3 080c str.w r0, [r3, #2060] ; 0x80c

ceea: bd70 pop {r4, r5, r6, pc}

ceec: f8d3 0814 ldr.w r0, [r3, #2068] ; 0x814

cef0: 3001 adds r0, #1

cef2: 42a8 cmp r0, r5

cef4: d81e bhi.n cf34 <\_dl\_mcount@@GLIBC\_2.4+0x4f4>

cef6: f8d3 2810 ldr.w r2, [r3, #2064] ; 0x810

cefa: 1c6e adds r6, r5, #1

cefc: 2100 movs r1, #0

cefe: 6814 ldr r4, [r2, #0]

cf00: 1a43 subs r3, r0, r1

cf02: 42a3 cmp r3, r4

cf04: d20f bcs.n cf26 <\_dl\_mcount@@GLIBC\_2.4+0x4e6>

cf06: eb02 03c3 add.w r3, r2, r3, lsl #3

cf0a: 68db ldr r3, [r3, #12]

cf0c: b183 cbz r3, cf30 <\_dl\_mcount@@GLIBC\_2.4+0x4f0>

cf0e: 3001 adds r0, #1

cf10: 4286 cmp r6, r0

cf12: d2f5 bcs.n cf00 <\_dl\_mcount@@GLIBC\_2.4+0x4c0>

cf14: 4b12 ldr r3, [pc, #72] ; (cf60 <\_dl\_mcount@@GLIBC\_2.4+0x520>)

cf16: 2249 movs r2, #73 ; 0x49

cf18: 4912 ldr r1, [pc, #72] ; (cf64 <\_dl\_mcount@@GLIBC\_2.4+0x524>)

cf1a: 4813 ldr r0, [pc, #76] ; (cf68 <\_dl\_mcount@@GLIBC\_2.4+0x528>)

cf1c: 447b add r3, pc

cf1e: 4479 add r1, pc

cf20: 4478 add r0, pc

cf22: f003 fd33 bl 1098c <free@@GLIBC\_2.4+0x12c>

cf26: 6852 ldr r2, [r2, #4]

cf28: 4421 add r1, r4

cf2a: b10a cbz r2, cf30 <\_dl\_mcount@@GLIBC\_2.4+0x4f0>

cf2c: 6814 ldr r4, [r2, #0]

cf2e: e7e7 b.n cf00 <\_dl\_mcount@@GLIBC\_2.4+0x4c0>

cf30: 42a8 cmp r0, r5

cf32: d9da bls.n ceea <\_dl\_mcount@@GLIBC\_2.4+0x4aa>

cf34: 3501 adds r5, #1

cf36: 4285 cmp r5, r0

cf38: d105 bne.n cf46 <\_dl\_mcount@@GLIBC\_2.4+0x506>

cf3a: 4b0c ldr r3, [pc, #48] ; (cf6c <\_dl\_mcount@@GLIBC\_2.4+0x52c>)

cf3c: 2200 movs r2, #0

cf3e: 447b add r3, pc

cf40: f883 2808 strb.w r2, [r3, #2056] ; 0x808

cf44: e7cd b.n cee2 <\_dl\_mcount@@GLIBC\_2.4+0x4a2>

cf46: 4b0a ldr r3, [pc, #40] ; (cf70 <\_dl\_mcount@@GLIBC\_2.4+0x530>)

cf48: 2257 movs r2, #87 ; 0x57

cf4a: 490a ldr r1, [pc, #40] ; (cf74 <\_dl\_mcount@@GLIBC\_2.4+0x534>)

cf4c: 480a ldr r0, [pc, #40] ; (cf78 <\_dl\_mcount@@GLIBC\_2.4+0x538>)

cf4e: 447b add r3, pc

cf50: 4479 add r1, pc

cf52: 4478 add r0, pc

cf54: f003 fd1a bl 1098c <free@@GLIBC\_2.4+0x12c>

cf58: c188 stmia r1!, {r3, r7}

cf5a: 0001 movs r1, r0

cf5c: c178 stmia r1!, {r3, r4, r5, r6}

cf5e: 0001 movs r1, r0

cf60: 9ff0 ldr r7, [sp, #960] ; 0x3c0

cf62: 0000 movs r0, r0

cf64: 9d8e ldr r5, [sp, #568] ; 0x238

cf66: 0000 movs r0, r0

cf68: 9db0 ldr r5, [sp, #704] ; 0x2c0

cf6a: 0000 movs r0, r0

cf6c: c11e stmia r1!, {r1, r2, r3, r4}

cf6e: 0001 movs r1, r0

cf70: 9fbe ldr r7, [sp, #760] ; 0x2f8

cf72: 0000 movs r0, r0

cf74: 9d5c ldr r5, [sp, #368] ; 0x170

cf76: 0000 movs r0, r0

cf78: 9da6 ldr r5, [sp, #664] ; 0x298

cf7a: 0000 movs r0, r0

cf7c: 4b0f ldr r3, [pc, #60] ; (cfbc <\_dl\_mcount@@GLIBC\_2.4+0x57c>)

cf7e: 447b add r3, pc

cf80: f893 2808 ldrb.w r2, [r3, #2056] ; 0x808

cf84: b912 cbnz r2, cf8c <\_dl\_mcount@@GLIBC\_2.4+0x54c>

cf86: f8d3 080c ldr.w r0, [r3, #2060] ; 0x80c

cf8a: 4770 bx lr

cf8c: b430 push {r4, r5}

cf8e: f8d3 5810 ldr.w r5, [r3, #2064] ; 0x810

cf92: 4628 mov r0, r5

cf94: b17d cbz r5, cfb6 <\_dl\_mcount@@GLIBC\_2.4+0x576>

cf96: 2000 movs r0, #0

cf98: 6829 ldr r1, [r5, #0]

cf9a: f105 040c add.w r4, r5, #12

cf9e: 2300 movs r3, #0

cfa0: b131 cbz r1, cfb0 <\_dl\_mcount@@GLIBC\_2.4+0x570>

cfa2: f854 2033 ldr.w r2, [r4, r3, lsl #3]

cfa6: 3301 adds r3, #1

cfa8: b102 cbz r2, cfac <\_dl\_mcount@@GLIBC\_2.4+0x56c>

cfaa: 3001 adds r0, #1

cfac: 428b cmp r3, r1

cfae: d1f8 bne.n cfa2 <\_dl\_mcount@@GLIBC\_2.4+0x562>

cfb0: 686d ldr r5, [r5, #4]

cfb2: 2d00 cmp r5, #0

cfb4: d1f0 bne.n cf98 <\_dl\_mcount@@GLIBC\_2.4+0x558>

cfb6: bc30 pop {r4, r5}

cfb8: 4770 bx lr

cfba: bf00 nop

cfbc: c0de stmia r0!, {r1, r2, r3, r4, r6, r7}

cfbe: 0001 movs r1, r0

cfc0: 4b4d ldr r3, [pc, #308] ; (d0f8 <\_dl\_mcount@@GLIBC\_2.4+0x6b8>)

cfc2: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

cfc6: 447b add r3, pc

cfc8: b085 sub sp, #20

cfca: f8d3 3810 ldr.w r3, [r3, #2064] ; 0x810

cfce: 2b00 cmp r3, #0

cfd0: f000 8088 beq.w d0e4 <\_dl\_mcount@@GLIBC\_2.4+0x6a4>

cfd4: 685a ldr r2, [r3, #4]

cfd6: 2a00 cmp r2, #0

cfd8: d17b bne.n d0d2 <\_dl\_mcount@@GLIBC\_2.4+0x692>

cfda: 68de ldr r6, [r3, #12]

cfdc: 2e00 cmp r6, #0

cfde: d069 beq.n d0b4 <\_dl\_mcount@@GLIBC\_2.4+0x674>

cfe0: 6819 ldr r1, [r3, #0]

cfe2: 9103 str r1, [sp, #12]

cfe4: 2900 cmp r1, #0

cfe6: d06b beq.n d0c0 <\_dl\_mcount@@GLIBC\_2.4+0x680>

cfe8: 330c adds r3, #12

cfea: 4690 mov r8, r2

cfec: 9302 str r3, [sp, #8]

cfee: 4691 mov r9, r2

cff0: 2310 movs r3, #16

cff2: 4693 mov fp, r2

cff4: 2708 movs r7, #8

cff6: 9301 str r3, [sp, #4]

cff8: e002 b.n d000 <\_dl\_mcount@@GLIBC\_2.4+0x5c0>

cffa: 9b03 ldr r3, [sp, #12]

cffc: 4543 cmp r3, r8

cffe: d95f bls.n d0c0 <\_dl\_mcount@@GLIBC\_2.4+0x680>

d000: 9b01 ldr r3, [sp, #4]

d002: ebab 0a09 sub.w sl, fp, r9

d006: f8d6 523c ldr.w r5, [r6, #572] ; 0x23c

d00a: f8d6 1238 ldr.w r1, [r6, #568] ; 0x238

d00e: 42ab cmp r3, r5

d010: f8d6 4240 ldr.w r4, [r6, #576] ; 0x240

d014: f105 30ff add.w r0, r5, #4294967295 ; 0xffffffff

d018: bf38 it cc

d01a: 462b movcc r3, r5

d01c: 4264 negs r4, r4

d01e: 4551 cmp r1, sl

d020: ea04 0400 and.w r4, r4, r0

d024: 9301 str r3, [sp, #4]

d026: eba1 0304 sub.w r3, r1, r4

d02a: 9300 str r3, [sp, #0]

d02c: d815 bhi.n d05a <\_dl\_mcount@@GLIBC\_2.4+0x61a>

d02e: f109 30ff add.w r0, r9, #4294967295 ; 0xffffffff

d032: 4629 mov r1, r5

d034: 4428 add r0, r5

d036: f006 fb17 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

d03a: 9b00 ldr r3, [sp, #0]

d03c: fb05 f000 mul.w r0, r5, r0

d040: eba0 0109 sub.w r1, r0, r9

d044: 42a1 cmp r1, r4

d046: bf38 it cc

d048: 1940 addcc r0, r0, r5

d04a: 18c1 adds r1, r0, r3

d04c: 4559 cmp r1, fp

d04e: bf9e ittt ls

d050: 1b00 subls r0, r0, r4

d052: f8c6 0244 strls.w r0, [r6, #580] ; 0x244

d056: 4689 movls r9, r1

d058: d915 bls.n d086 <\_dl\_mcount@@GLIBC\_2.4+0x646>

d05a: 1e78 subs r0, r7, #1

d05c: 4629 mov r1, r5

d05e: 4428 add r0, r5

d060: f006 fb02 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

d064: 9b00 ldr r3, [sp, #0]

d066: fb05 f000 mul.w r0, r5, r0

d06a: 1bc1 subs r1, r0, r7

d06c: 42a1 cmp r1, r4

d06e: bf38 it cc

d070: 1940 addcc r0, r0, r5

d072: 1b04 subs r4, r0, r4

d074: 4403 add r3, r0

d076: 1be1 subs r1, r4, r7

d078: f8c6 4244 str.w r4, [r6, #580] ; 0x244

d07c: 458a cmp sl, r1

d07e: bf3c itt cc

d080: 46b9 movcc r9, r7

d082: 46a3 movcc fp, r4

d084: 461f mov r7, r3

d086: 9b02 ldr r3, [sp, #8]

d088: f108 0801 add.w r8, r8, #1

d08c: f853 6038 ldr.w r6, [r3, r8, lsl #3]

d090: 2e00 cmp r6, #0

d092: d1b2 bne.n cffa <\_dl\_mcount@@GLIBC\_2.4+0x5ba>

d094: f207 628f addw r2, r7, #1679 ; 0x68f

d098: f022 020f bic.w r2, r2, #15

d09c: 4b17 ldr r3, [pc, #92] ; (d0fc <\_dl\_mcount@@GLIBC\_2.4+0x6bc>)

d09e: 447b add r3, pc

d0a0: f8c3 2818 str.w r2, [r3, #2072] ; 0x818

d0a4: 9a01 ldr r2, [sp, #4]

d0a6: f8c3 781c str.w r7, [r3, #2076] ; 0x81c

d0aa: f8c3 2820 str.w r2, [r3, #2080] ; 0x820

d0ae: b005 add sp, #20

d0b0: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

d0b4: 2310 movs r3, #16

d0b6: f44f 62d2 mov.w r2, #1680 ; 0x690

d0ba: 2708 movs r7, #8

d0bc: 9301 str r3, [sp, #4]

d0be: e7ed b.n d09c <\_dl\_mcount@@GLIBC\_2.4+0x65c>

d0c0: 4b0f ldr r3, [pc, #60] ; (d100 <\_dl\_mcount@@GLIBC\_2.4+0x6c0>)

d0c2: 22e5 movs r2, #229 ; 0xe5

d0c4: 490f ldr r1, [pc, #60] ; (d104 <\_dl\_mcount@@GLIBC\_2.4+0x6c4>)

d0c6: 4810 ldr r0, [pc, #64] ; (d108 <\_dl\_mcount@@GLIBC\_2.4+0x6c8>)

d0c8: 447b add r3, pc

d0ca: 4479 add r1, pc

d0cc: 4478 add r0, pc

d0ce: f003 fc5d bl 1098c <free@@GLIBC\_2.4+0x12c>

d0d2: 4b0e ldr r3, [pc, #56] ; (d10c <\_dl\_mcount@@GLIBC\_2.4+0x6cc>)

d0d4: 2290 movs r2, #144 ; 0x90

d0d6: 490e ldr r1, [pc, #56] ; (d110 <\_dl\_mcount@@GLIBC\_2.4+0x6d0>)

d0d8: 480e ldr r0, [pc, #56] ; (d114 <\_dl\_mcount@@GLIBC\_2.4+0x6d4>)

d0da: 447b add r3, pc

d0dc: 4479 add r1, pc

d0de: 4478 add r0, pc

d0e0: f003 fc54 bl 1098c <free@@GLIBC\_2.4+0x12c>

d0e4: 4b0c ldr r3, [pc, #48] ; (d118 <\_dl\_mcount@@GLIBC\_2.4+0x6d8>)

d0e6: 228d movs r2, #141 ; 0x8d

d0e8: 490c ldr r1, [pc, #48] ; (d11c <\_dl\_mcount@@GLIBC\_2.4+0x6dc>)

d0ea: 480d ldr r0, [pc, #52] ; (d120 <\_dl\_mcount@@GLIBC\_2.4+0x6e0>)

d0ec: 447b add r3, pc

d0ee: 4479 add r1, pc

d0f0: 4478 add r0, pc

d0f2: f003 fc4b bl 1098c <free@@GLIBC\_2.4+0x12c>

d0f6: bf00 nop

d0f8: c096 stmia r0!, {r1, r2, r4, r7}

d0fa: 0001 movs r1, r0

d0fc: bfbe ittt lt

d0fe: 0001 movlt r1, r0

d100: 9e2c ldrlt r6, [sp, #176] ; 0xb0

d102: 0000 movlt r0, r0

d104: 9be2 ldr r3, [sp, #904] ; 0x388

d106: 0000 movs r0, r0

d108: 9ca8 ldr r4, [sp, #672] ; 0x2a0

d10a: 0000 movs r0, r0

d10c: 9e1a ldr r6, [sp, #104] ; 0x68

d10e: 0000 movs r0, r0

d110: 9bd0 ldr r3, [sp, #832] ; 0x340

d112: 0000 movs r0, r0

d114: 9c6a ldr r4, [sp, #424] ; 0x1a8

d116: 0000 movs r0, r0

d118: 9e08 ldr r6, [sp, #32]

d11a: 0000 movs r0, r0

d11c: 9bbe ldr r3, [sp, #760] ; 0x2f8

d11e: 0000 movs r0, r0

d120: 9c30 ldr r4, [sp, #192] ; 0xc0

...

0000d124 <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE>:

d124: 4b04 ldr r3, [pc, #16] ; (d138 <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE+0x14>)

d126: 447b add r3, pc

d128: f8d3 2818 ldr.w r2, [r3, #2072] ; 0x818

d12c: 6002 str r2, [r0, #0]

d12e: f8d3 3820 ldr.w r3, [r3, #2080] ; 0x820

d132: 600b str r3, [r1, #0]

d134: 4770 bx lr

d136: bf00 nop

d138: bf36 itet cc

d13a: 0001 movcc r1, r0

d13c: b538 pushcs {r3, r4, r5, lr}

d13e: 4b17 ldrcc r3, [pc, #92] ; (d19c <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE+0x78>)

d140: 447b add r3, pc

d142: f8d3 4820 ldr.w r4, [r3, #2080] ; 0x820

d146: f8d3 0818 ldr.w r0, [r3, #2072] ; 0x818

d14a: 4420 add r0, r4

d14c: f200 40c4 addw r0, r0, #1220 ; 0x4c4

d150: f7f3 ec3c blx 9cc <malloc@plt+0x4>

d154: 4605 mov r5, r0

d156: b1e8 cbz r0, d194 <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE+0x70>

d158: f204 40c3 addw r0, r4, #1219 ; 0x4c3

d15c: 4621 mov r1, r4

d15e: 4428 add r0, r5

d160: f006 fa82 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

d164: f44f 6299 mov.w r2, #1224 ; 0x4c8

d168: 2100 movs r1, #0

d16a: fb04 f400 mul.w r4, r4, r0

d16e: f5a4 6098 sub.w r0, r4, #1216 ; 0x4c0

d172: f005 ef6e blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

d176: f2a4 43c4 subw r3, r4, #1220 ; 0x4c4

d17a: 4620 mov r0, r4

d17c: 601d str r5, [r3, #0]

d17e: f7ff fdb3 bl cce8 <\_dl\_mcount@@GLIBC\_2.4+0x2a8>

d182: 4604 mov r4, r0

d184: b108 cbz r0, d18a <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE+0x66>

d186: 4620 mov r0, r4

d188: bd38 pop {r3, r4, r5, pc}

d18a: 4628 mov r0, r5

d18c: f7f3 ec4a blx a24 <free@plt+0x4>

d190: 4620 mov r0, r4

d192: bd38 pop {r3, r4, r5, pc}

d194: 4604 mov r4, r0

d196: 4620 mov r0, r4

d198: bd38 pop {r3, r4, r5, pc}

d19a: bf00 nop

d19c: bf1c itt ne

d19e: 0001 movne r1, r0

0000d1a0 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE>:

d1a0: e92d 4ff0 stmdbne sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

d1a4: b085 sub sp, #20

d1a6: 9003 str r0, [sp, #12]

d1a8: 2800 cmp r0, #0

d1aa: d078 beq.n d29e <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xfe>

d1ac: 4c51 ldr r4, [pc, #324] ; (d2f4 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x154>)

d1ae: f8d0 8000 ldr.w r8, [r0]

d1b2: 447c add r4, pc

d1b4: f858 2c08 ldr.w r2, [r8, #-8]

d1b8: f8d4 380c ldr.w r3, [r4, #2060] ; 0x80c

d1bc: 429a cmp r2, r3

d1be: d372 bcc.n d2a6 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x106>

d1c0: 4a4d ldr r2, [pc, #308] ; (d2f8 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x158>)

d1c2: f04f 0900 mov.w r9, #0

d1c6: f8df b134 ldr.w fp, [pc, #308] ; d2fc <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x15c>

d1ca: 46ca mov sl, r9

d1cc: 447a add r2, pc

d1ce: 494c ldr r1, [pc, #304] ; (d300 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x160>)

d1d0: 44fb add fp, pc

d1d2: f8d2 2810 ldr.w r2, [r2, #2064] ; 0x810

d1d6: 4479 add r1, pc

d1d8: 9101 str r1, [sp, #4]

d1da: 9202 str r2, [sp, #8]

d1dc: 9902 ldr r1, [sp, #8]

d1de: faba f58a clz r5, sl

d1e2: 096d lsrs r5, r5, #5

d1e4: f8d1 c000 ldr.w ip, [r1]

d1e8: eb0a 0205 add.w r2, sl, r5

d1ec: 4565 cmp r5, ip

d1ee: d242 bcs.n d276 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xd6>

d1f0: 4293 cmp r3, r2

d1f2: d352 bcc.n d29a <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xfa>

d1f4: eb01 06c5 add.w r6, r1, r5, lsl #3

d1f8: e003 b.n d202 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x62>

d1fa: 429a cmp r2, r3

d1fc: f106 0608 add.w r6, r6, #8

d200: d84b bhi.n d29a <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xfa>

d202: 68f4 ldr r4, [r6, #12]

d204: b37c cbz r4, d266 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xc6>

d206: 68b1 ldr r1, [r6, #8]

d208: f8db 3828 ldr.w r3, [fp, #2088] ; 0x828

d20c: 4299 cmp r1, r3

d20e: d853 bhi.n d2b8 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x118>

d210: f8d4 3248 ldr.w r3, [r4, #584] ; 0x248

d214: 4589 cmp r9, r1

d216: f8d4 0244 ldr.w r0, [r4, #580] ; 0x244

d21a: f04f 37ff mov.w r7, #4294967295 ; 0xffffffff

d21e: bf38 it cc

d220: 4689 movcc r9, r1

d222: f100 0e01 add.w lr, r0, #1

d226: eb08 01c3 add.w r1, r8, r3, lsl #3

d22a: f1be 0f01 cmp.w lr, #1

d22e: f848 7033 str.w r7, [r8, r3, lsl #3]

d232: f04f 0700 mov.w r7, #0

d236: 604f str r7, [r1, #4]

d238: d915 bls.n d266 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xc6>

d23a: 4293 cmp r3, r2

d23c: d150 bne.n d2e0 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x140>

d23e: e9d4 238d ldrd r2, r3, [r4, #564] ; 0x234

d242: 4293 cmp r3, r2

d244: d342 bcc.n d2cc <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x12c>

d246: 9b03 ldr r3, [sp, #12]

d248: 4418 add r0, r3

d24a: 6008 str r0, [r1, #0]

d24c: f8d4 1230 ldr.w r1, [r4, #560] ; 0x230

d250: f005 ff3c bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

d254: e9d4 328d ldrd r3, r2, [r4, #564] ; 0x234

d258: 2100 movs r1, #0

d25a: 1ad2 subs r2, r2, r3

d25c: f005 eef8 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

d260: 9b02 ldr r3, [sp, #8]

d262: f8d3 c000 ldr.w ip, [r3]

d266: 3501 adds r5, #1

d268: 9b01 ldr r3, [sp, #4]

d26a: 45ac cmp ip, r5

d26c: eb0a 0205 add.w r2, sl, r5

d270: f8d3 380c ldr.w r3, [r3, #2060] ; 0x80c

d274: d8c1 bhi.n d1fa <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x5a>

d276: 4293 cmp r3, r2

d278: 4692 mov sl, r2

d27a: d90e bls.n d29a <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0xfa>

d27c: 9a02 ldr r2, [sp, #8]

d27e: 6852 ldr r2, [r2, #4]

d280: 9202 str r2, [sp, #8]

d282: 2a00 cmp r2, #0

d284: d1aa bne.n d1dc <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x3c>

d286: 4b1f ldr r3, [pc, #124] ; (d304 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x164>)

d288: f240 2205 movw r2, #517 ; 0x205

d28c: 491e ldr r1, [pc, #120] ; (d308 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x168>)

d28e: 481f ldr r0, [pc, #124] ; (d30c <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x16c>)

d290: 447b add r3, pc

d292: 4479 add r1, pc

d294: 4478 add r0, pc

d296: f003 fb79 bl 1098c <free@@GLIBC\_2.4+0x12c>

d29a: f8c8 9000 str.w r9, [r8]

d29e: 9803 ldr r0, [sp, #12]

d2a0: b005 add sp, #20

d2a2: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

d2a6: 4640 mov r0, r8

d2a8: f7ff fd40 bl cd2c <\_dl\_mcount@@GLIBC\_2.4+0x2ec>

d2ac: 9a03 ldr r2, [sp, #12]

d2ae: f8d4 380c ldr.w r3, [r4, #2060] ; 0x80c

d2b2: 4680 mov r8, r0

d2b4: 6010 str r0, [r2, #0]

d2b6: e783 b.n d1c0 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x20>

d2b8: 4b15 ldr r3, [pc, #84] ; (d310 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x170>)

d2ba: f240 12e1 movw r2, #481 ; 0x1e1

d2be: 4915 ldr r1, [pc, #84] ; (d314 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x174>)

d2c0: 4815 ldr r0, [pc, #84] ; (d318 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x178>)

d2c2: 447b add r3, pc

d2c4: 4479 add r1, pc

d2c6: 4478 add r0, pc

d2c8: f003 fb60 bl 1098c <free@@GLIBC\_2.4+0x12c>

d2cc: 4b13 ldr r3, [pc, #76] ; (d31c <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x17c>)

d2ce: f44f 72f6 mov.w r2, #492 ; 0x1ec

d2d2: 4913 ldr r1, [pc, #76] ; (d320 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x180>)

d2d4: 4813 ldr r0, [pc, #76] ; (d324 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x184>)

d2d6: 447b add r3, pc

d2d8: 4479 add r1, pc

d2da: 4478 add r0, pc

d2dc: f003 fb56 bl 1098c <free@@GLIBC\_2.4+0x12c>

d2e0: 4b11 ldr r3, [pc, #68] ; (d328 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x188>)

d2e2: f240 12eb movw r2, #491 ; 0x1eb

d2e6: 4911 ldr r1, [pc, #68] ; (d32c <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x18c>)

d2e8: 4811 ldr r0, [pc, #68] ; (d330 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE+0x190>)

d2ea: 447b add r3, pc

d2ec: 4479 add r1, pc

d2ee: 4478 add r0, pc

d2f0: f003 fb4c bl 1098c <free@@GLIBC\_2.4+0x12c>

d2f4: beaa bkpt 0x00aa

d2f6: 0001 movs r1, r0

d2f8: be90 bkpt 0x0090

d2fa: 0001 movs r1, r0

d2fc: be8c bkpt 0x008c

d2fe: 0001 movs r1, r0

d300: be86 bkpt 0x0086

d302: 0001 movs r1, r0

d304: 9c4c ldr r4, [sp, #304] ; 0x130

d306: 0000 movs r0, r0

d308: 9a1a ldr r2, [sp, #104] ; 0x68

d30a: 0000 movs r0, r0

d30c: 9b90 ldr r3, [sp, #576] ; 0x240

d30e: 0000 movs r0, r0

d310: 9c1a ldr r4, [sp, #104] ; 0x68

d312: 0000 movs r0, r0

d314: 99e8 ldr r1, [sp, #928] ; 0x3a0

d316: 0000 movs r0, r0

d318: 9ad6 ldr r2, [sp, #856] ; 0x358

d31a: 0000 movs r0, r0

d31c: 9c06 ldr r4, [sp, #24]

d31e: 0000 movs r0, r0

d320: 99d4 ldr r1, [sp, #848] ; 0x350

d322: 0000 movs r0, r0

d324: 9b16 ldr r3, [sp, #88] ; 0x58

d326: 0000 movs r0, r0

d328: 9bf2 ldr r3, [sp, #968] ; 0x3c8

d32a: 0000 movs r0, r0

d32c: 99c0 ldr r1, [sp, #768] ; 0x300

d32e: 0000 movs r0, r0

d330: 9ae2 ldr r2, [sp, #904] ; 0x388

...

0000d334 <\_dl\_allocate\_tls@@GLIBC\_PRIVATE>:

d334: b508 push {r3, lr}

d336: b120 cbz r0, d342 <\_dl\_allocate\_tls@@GLIBC\_PRIVATE+0xe>

d338: f7ff fcd6 bl cce8 <\_dl\_mcount@@GLIBC\_2.4+0x2a8>

d33c: e8bd 4008 ldmia.w sp!, {r3, lr}

d340: e72e b.n d1a0 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE>

d342: f7ff fefb bl d13c <\_dl\_get\_tls\_static\_info@@GLIBC\_PRIVATE+0x18>

d346: e8bd 4008 ldmia.w sp!, {r3, lr}

d34a: e729 b.n d1a0 <\_dl\_allocate\_tls\_init@@GLIBC\_PRIVATE>

0000d34c <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE>:

d34c: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

d350: 4607 mov r7, r0

d352: 6805 ldr r5, [r0, #0]

d354: 4688 mov r8, r1

d356: f855 3c08 ldr.w r3, [r5, #-8]

d35a: b153 cbz r3, d372 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x26>

d35c: 1d2e adds r6, r5, #4

d35e: 2400 movs r4, #0

d360: 3401 adds r4, #1

d362: f856 0034 ldr.w r0, [r6, r4, lsl #3]

d366: f7f3 eb5e blx a24 <free@plt+0x4>

d36a: f855 3c08 ldr.w r3, [r5, #-8]

d36e: 429c cmp r4, r3

d370: d3f6 bcc.n d360 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x14>

d372: 4b0b ldr r3, [pc, #44] ; (d3a0 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x54>)

d374: 447b add r3, pc

d376: f8d3 3824 ldr.w r3, [r3, #2084] ; 0x824

d37a: 42ab cmp r3, r5

d37c: d003 beq.n d386 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x3a>

d37e: f1a5 0008 sub.w r0, r5, #8

d382: f7f3 eb50 blx a24 <free@plt+0x4>

d386: f1b8 0f00 cmp.w r8, #0

d38a: d101 bne.n d390 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x44>

d38c: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

d390: f2a7 47c4 subw r7, r7, #1220 ; 0x4c4

d394: 6838 ldr r0, [r7, #0]

d396: e8bd 41f0 ldmia.w sp!, {r4, r5, r6, r7, r8, lr}

d39a: f7f3 bb41 b.w a20 <free@plt>

d39e: bf00 nop

d3a0: bce8 pop {r3, r5, r6, r7}

d3a2: 0001 movs r1, r0

d3a4: 4b54 ldr r3, [pc, #336] ; (d4f8 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1ac>)

d3a6: ee1d 2f70 mrc 15, 0, r2, cr13, cr0, {3}

d3aa: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

d3ae: 447b add r3, pc

d3b0: b085 sub sp, #20

d3b2: 6816 ldr r6, [r2, #0]

d3b4: f8d3 a810 ldr.w sl, [r3, #2064] ; 0x810

d3b8: 4601 mov r1, r0

d3ba: 9201 str r2, [sp, #4]

d3bc: f8da 2000 ldr.w r2, [sl]

d3c0: 4654 mov r4, sl

d3c2: 4290 cmp r0, r2

d3c4: d305 bcc.n d3d2 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x86>

d3c6: 4615 mov r5, r2

d3c8: 6864 ldr r4, [r4, #4]

d3ca: 1b49 subs r1, r1, r5

d3cc: 6825 ldr r5, [r4, #0]

d3ce: 428d cmp r5, r1

d3d0: d9fa bls.n d3c8 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x7c>

d3d2: 3101 adds r1, #1

d3d4: 6835 ldr r5, [r6, #0]

d3d6: f854 7031 ldr.w r7, [r4, r1, lsl #3]

d3da: 42bd cmp r5, r7

d3dc: d272 bcs.n d4c4 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x178>

d3de: 2300 movs r3, #0

d3e0: 9000 str r0, [sp, #0]

d3e2: 4698 mov r8, r3

d3e4: 9302 str r3, [sp, #8]

d3e6: 9b02 ldr r3, [sp, #8]

d3e8: fab3 f583 clz r5, r3

d3ec: 096d lsrs r5, r5, #5

d3ee: 4295 cmp r5, r2

d3f0: d258 bcs.n d4a4 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x158>

d3f2: 1c6c adds r4, r5, #1

d3f4: 441d add r5, r3

d3f6: 4653 mov r3, sl

d3f8: f10a 0904 add.w r9, sl, #4

d3fc: 46b2 mov sl, r6

d3fe: 461e mov r6, r3

d400: e028 b.n d454 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x108>

d402: f8db 3248 ldr.w r3, [fp, #584] ; 0x248

d406: 42ab cmp r3, r5

d408: d162 bne.n d4d0 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x184>

d40a: f85a 3c08 ldr.w r3, [sl, #-8]

d40e: 42ab cmp r3, r5

d410: d209 bcs.n d426 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0xda>

d412: 4650 mov r0, sl

d414: f7ff fc8a bl cd2c <\_dl\_mcount@@GLIBC\_2.4+0x2ec>

d418: f850 3c08 ldr.w r3, [r0, #-8]

d41c: 4682 mov sl, r0

d41e: 42ab cmp r3, r5

d420: d360 bcc.n d4e4 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x198>

d422: 9b01 ldr r3, [sp, #4]

d424: 6018 str r0, [r3, #0]

d426: eb0a 03c5 add.w r3, sl, r5, lsl #3

d42a: 9a00 ldr r2, [sp, #0]

d42c: 6858 ldr r0, [r3, #4]

d42e: 42aa cmp r2, r5

d430: bf08 it eq

d432: 46d8 moveq r8, fp

d434: 469b mov fp, r3

d436: f7f3 eaf6 blx a24 <free@plt+0x4>

d43a: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

d43e: f84a 3035 str.w r3, [sl, r5, lsl #3]

d442: 2300 movs r3, #0

d444: f8cb 3004 str.w r3, [fp, #4]

d448: 6832 ldr r2, [r6, #0]

d44a: 3501 adds r5, #1

d44c: 1c63 adds r3, r4, #1

d44e: 42a2 cmp r2, r4

d450: d925 bls.n d49e <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x152>

d452: 461c mov r4, r3

d454: f856 3034 ldr.w r3, [r6, r4, lsl #3]

d458: 429f cmp r7, r3

d45a: d3f5 bcc.n d448 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0xfc>

d45c: f8da 2000 ldr.w r2, [sl]

d460: 429a cmp r2, r3

d462: d2f1 bcs.n d448 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0xfc>

d464: f859 b034 ldr.w fp, [r9, r4, lsl #3]

d468: f1bb 0f00 cmp.w fp, #0

d46c: d1c9 bne.n d402 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0xb6>

d46e: f85a 3c08 ldr.w r3, [sl, #-8]

d472: 42ab cmp r3, r5

d474: d3e8 bcc.n d448 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0xfc>

d476: eb0a 03c5 add.w r3, sl, r5, lsl #3

d47a: 9303 str r3, [sp, #12]

d47c: 6858 ldr r0, [r3, #4]

d47e: f7f3 ead2 blx a24 <free@plt+0x4>

d482: 6832 ldr r2, [r6, #0]

d484: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

d488: f84a 3035 str.w r3, [sl, r5, lsl #3]

d48c: 9b03 ldr r3, [sp, #12]

d48e: 42a2 cmp r2, r4

d490: f105 0501 add.w r5, r5, #1

d494: f8c3 b004 str.w fp, [r3, #4]

d498: f104 0301 add.w r3, r4, #1

d49c: d8d9 bhi.n d452 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x106>

d49e: 4633 mov r3, r6

d4a0: 4656 mov r6, sl

d4a2: 469a mov sl, r3

d4a4: 9b02 ldr r3, [sp, #8]

d4a6: f8da a004 ldr.w sl, [sl, #4]

d4aa: 4413 add r3, r2

d4ac: 9302 str r3, [sp, #8]

d4ae: f1ba 0f00 cmp.w sl, #0

d4b2: d002 beq.n d4ba <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x16e>

d4b4: f8da 2000 ldr.w r2, [sl]

d4b8: e795 b.n d3e6 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x9a>

d4ba: 4640 mov r0, r8

d4bc: 6037 str r7, [r6, #0]

d4be: b005 add sp, #20

d4c0: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

d4c4: f04f 0800 mov.w r8, #0

d4c8: 4640 mov r0, r8

d4ca: b005 add sp, #20

d4cc: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

d4d0: 4b0a ldr r3, [pc, #40] ; (d4fc <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1b0>)

d4d2: f240 22b9 movw r2, #697 ; 0x2b9

d4d6: 490a ldr r1, [pc, #40] ; (d500 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1b4>)

d4d8: 480a ldr r0, [pc, #40] ; (d504 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1b8>)

d4da: 447b add r3, pc

d4dc: 4479 add r1, pc

d4de: 4478 add r0, pc

d4e0: f003 fa54 bl 1098c <free@@GLIBC\_2.4+0x12c>

d4e4: 4b08 ldr r3, [pc, #32] ; (d508 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1bc>)

d4e6: f240 22bf movw r2, #703 ; 0x2bf

d4ea: 4908 ldr r1, [pc, #32] ; (d50c <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1c0>)

d4ec: 4808 ldr r0, [pc, #32] ; (d510 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1c4>)

d4ee: 447b add r3, pc

d4f0: 4479 add r1, pc

d4f2: 4478 add r0, pc

d4f4: f003 fa4a bl 1098c <free@@GLIBC\_2.4+0x12c>

d4f8: bcae pop {r1, r2, r3, r5, r7}

d4fa: 0001 movs r1, r0

d4fc: 99ee ldr r1, [sp, #952] ; 0x3b8

d4fe: 0000 movs r0, r0

d500: 97d0 str r7, [sp, #832] ; 0x340

d502: 0000 movs r0, r0

d504: 9956 ldr r1, [sp, #344] ; 0x158

d506: 0000 movs r0, r0

d508: 99da ldr r1, [sp, #872] ; 0x368

d50a: 0000 movs r0, r0

d50c: 97bc str r7, [sp, #752] ; 0x2f0

d50e: 0000 movs r0, r0

d510: 995a ldr r1, [sp, #360] ; 0x168

d512: 0000 movs r0, r0

d514: b510 push {r4, lr}

d516: 4604 mov r4, r0

d518: 6800 ldr r0, [r0, #0]

d51a: f7ff ff43 bl d3a4 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x58>

d51e: ee1d 2f70 mrc 15, 0, r2, cr13, cr0, {3}

d522: 6821 ldr r1, [r4, #0]

d524: 6812 ldr r2, [r2, #0]

d526: f852 1031 ldr.w r1, [r2, r1, lsl #3]

d52a: 1c4b adds r3, r1, #1

d52c: d002 beq.n d534 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1e8>

d52e: 6860 ldr r0, [r4, #4]

d530: 4408 add r0, r1

d532: bd10 pop {r4, pc}

d534: 4603 mov r3, r0

d536: 1d21 adds r1, r4, #4

d538: 4620 mov r0, r4

d53a: e8bd 4010 ldmia.w sp!, {r4, lr}

d53e: e42d b.n cd9c <\_dl\_mcount@@GLIBC\_2.4+0x35c>

0000d540 <\_\_tls\_get\_addr@@GLIBC\_2.4>:

d540: ee1d 2f70 mrc 15, 0, r2, cr13, cr0, {3}

d544: 4b0d ldr r3, [pc, #52] ; (d57c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x3c>)

d546: 6812 ldr r2, [r2, #0]

d548: 447b add r3, pc

d54a: b410 push {r4}

d54c: f8d3 3828 ldr.w r3, [r3, #2088] ; 0x828

d550: 6814 ldr r4, [r2, #0]

d552: 429c cmp r4, r3

d554: d10a bne.n d56c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x2c>

d556: 6803 ldr r3, [r0, #0]

d558: 4601 mov r1, r0

d55a: f852 3033 ldr.w r3, [r2, r3, lsl #3]

d55e: 1c5c adds r4, r3, #1

d560: d007 beq.n d572 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x32>

d562: 6840 ldr r0, [r0, #4]

d564: f85d 4b04 ldr.w r4, [sp], #4

d568: 4418 add r0, r3

d56a: 4770 bx lr

d56c: f85d 4b04 ldr.w r4, [sp], #4

d570: e7d0 b.n d514 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x1c8>

d572: 3104 adds r1, #4

d574: 2300 movs r3, #0

d576: f85d 4b04 ldr.w r4, [sp], #4

d57a: e40f b.n cd9c <\_dl\_mcount@@GLIBC\_2.4+0x35c>

d57c: bb14 cbnz r4, d5c4 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x84>

d57e: 0001 movs r1, r0

d580: f8d0 0248 ldr.w r0, [r0, #584] ; 0x248

d584: b188 cbz r0, d5aa <\_\_tls\_get\_addr@@GLIBC\_2.4+0x6a>

d586: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

d58a: 4a13 ldr r2, [pc, #76] ; (d5d8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x98>)

d58c: 681b ldr r3, [r3, #0]

d58e: 447a add r2, pc

d590: b430 push {r4, r5}

d592: f8d2 1828 ldr.w r1, [r2, #2088] ; 0x828

d596: 681d ldr r5, [r3, #0]

d598: 428d cmp r5, r1

d59a: d107 bne.n d5ac <\_\_tls\_get\_addr@@GLIBC\_2.4+0x6c>

d59c: f853 0030 ldr.w r0, [r3, r0, lsl #3]

d5a0: 1c43 adds r3, r0, #1

d5a2: bf08 it eq

d5a4: 2000 moveq r0, #0

d5a6: bc30 pop {r4, r5}

d5a8: 4770 bx lr

d5aa: 4770 bx lr

d5ac: f853 1c08 ldr.w r1, [r3, #-8]

d5b0: 4288 cmp r0, r1

d5b2: d20f bcs.n d5d4 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x94>

d5b4: f8d2 1810 ldr.w r1, [r2, #2064] ; 0x810

d5b8: 4602 mov r2, r0

d5ba: 680c ldr r4, [r1, #0]

d5bc: 42a0 cmp r0, r4

d5be: d304 bcc.n d5ca <\_\_tls\_get\_addr@@GLIBC\_2.4+0x8a>

d5c0: 6849 ldr r1, [r1, #4]

d5c2: 1b12 subs r2, r2, r4

d5c4: 680c ldr r4, [r1, #0]

d5c6: 4294 cmp r4, r2

d5c8: d9fa bls.n d5c0 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x80>

d5ca: 3201 adds r2, #1

d5cc: f851 2032 ldr.w r2, [r1, r2, lsl #3]

d5d0: 4295 cmp r5, r2

d5d2: d2e3 bcs.n d59c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x5c>

d5d4: 2000 movs r0, #0

d5d6: e7e6 b.n d5a6 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x66>

d5d8: bace revsh r6, r1

d5da: 0001 movs r1, r0

d5dc: 4b27 ldr r3, [pc, #156] ; (d67c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x13c>)

d5de: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

d5e2: 447b add r3, pc

d5e4: 4607 mov r7, r0

d5e6: f8d0 5248 ldr.w r5, [r0, #584] ; 0x248

d5ea: f8d3 4810 ldr.w r4, [r3, #2064] ; 0x810

d5ee: e003 b.n d5f8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0xb8>

d5f0: 6862 ldr r2, [r4, #4]

d5f2: 1aed subs r5, r5, r3

d5f4: b172 cbz r2, d614 <\_\_tls\_get\_addr@@GLIBC\_2.4+0xd4>

d5f6: 4614 mov r4, r2

d5f8: 6823 ldr r3, [r4, #0]

d5fa: 42ab cmp r3, r5

d5fc: d9f8 bls.n d5f0 <\_\_tls\_get\_addr@@GLIBC\_2.4+0xb0>

d5fe: 4b20 ldr r3, [pc, #128] ; (d680 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x140>)

d600: eb04 04c5 add.w r4, r4, r5, lsl #3

d604: 447b add r3, pc

d606: f8d3 6828 ldr.w r6, [r3, #2088] ; 0x828

d60a: 3601 adds r6, #1

d60c: e9c4 6702 strd r6, r7, [r4, #8]

d610: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

d614: b9f5 cbnz r5, d654 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x114>

d616: f44f 70fc mov.w r0, #504 ; 0x1f8

d61a: f7f3 e9d8 blx 9cc <malloc@plt+0x4>

d61e: f8df c064 ldr.w ip, [pc, #100] ; d684 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x144>

d622: 44fc add ip, pc

d624: f8dc 6828 ldr.w r6, [ip, #2088] ; 0x828

d628: 3601 adds r6, #1

d62a: 4680 mov r8, r0

d62c: 6060 str r0, [r4, #4]

d62e: b1d8 cbz r0, d668 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x128>

d630: 4644 mov r4, r8

d632: eb04 04c5 add.w r4, r4, r5, lsl #3

d636: f44f 72f8 mov.w r2, #496 ; 0x1f0

d63a: 4629 mov r1, r5

d63c: 3008 adds r0, #8

d63e: 233e movs r3, #62 ; 0x3e

d640: f8c8 5004 str.w r5, [r8, #4]

d644: f8c8 3000 str.w r3, [r8]

d648: f005 ed02 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

d64c: e9c4 6702 strd r6, r7, [r4, #8]

d650: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

d654: 4b0c ldr r3, [pc, #48] ; (d688 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x148>)

d656: f240 3292 movw r2, #914 ; 0x392

d65a: 490c ldr r1, [pc, #48] ; (d68c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x14c>)

d65c: 480c ldr r0, [pc, #48] ; (d690 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x150>)

d65e: 447b add r3, pc

d660: 4479 add r1, pc

d662: 4478 add r0, pc

d664: f003 f992 bl 1098c <free@@GLIBC\_2.4+0x12c>

d668: 4b0a ldr r3, [pc, #40] ; (d694 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x154>)

d66a: 462a mov r2, r5

d66c: 490a ldr r1, [pc, #40] ; (d698 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x158>)

d66e: 200c movs r0, #12

d670: 447b add r3, pc

d672: f8cc 6828 str.w r6, [ip, #2088] ; 0x828

d676: 4479 add r1, pc

d678: f7f3 e9c6 blx a08 <\_dl\_signal\_error@plt>

d67c: ba7a rev16 r2, r7

d67e: 0001 movs r1, r0

d680: ba58 rev16 r0, r3

d682: 0001 movs r1, r0

d684: ba3a rev r2, r7

d686: 0001 movs r1, r0

d688: 9842 ldr r0, [sp, #264] ; 0x108

d68a: 0000 movs r0, r0

d68c: 964c str r6, [sp, #304] ; 0x130

d68e: 0000 movs r0, r0

d690: 9806 ldr r0, [sp, #24]

d692: 0000 movs r0, r0

d694: 9804 ldr r0, [sp, #16]

d696: 0000 movs r0, r0

d698: 9822 ldr r0, [sp, #136] ; 0x88

d69a: 0000 movs r0, r0

d69c: b570 push {r4, r5, r6, lr}

d69e: f5ad 5d80 sub.w sp, sp, #4096 ; 0x1000

d6a2: 483c ldr r0, [pc, #240] ; (d794 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x254>)

d6a4: f44f 5280 mov.w r2, #4096 ; 0x1000

d6a8: 466d mov r5, sp

d6aa: f04f 0c55 mov.w ip, #85 ; 0x55

d6ae: 4478 add r0, pc

d6b0: 4629 mov r1, r5

d6b2: f003 fc85 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

d6b6: 2800 cmp r0, #0

d6b8: bfd4 ite le

d6ba: 2300 movle r3, #0

d6bc: 2301 movgt r3, #1

d6be: f510 5f80 cmn.w r0, #4096 ; 0x1000

d6c2: bf88 it hi

d6c4: 2300 movhi r3, #0

d6c6: b1e3 cbz r3, d702 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x1c2>

d6c8: 782b ldrb r3, [r5, #0]

d6ca: 2b5b cmp r3, #91 ; 0x5b

d6cc: d019 beq.n d702 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x1c2>

d6ce: 2b2f cmp r3, #47 ; 0x2f

d6d0: d157 bne.n d782 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x242>

d6d2: 2801 cmp r0, #1

d6d4: d00d beq.n d6f2 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x1b2>

d6d6: 1e44 subs r4, r0, #1

d6d8: 5d2b ldrb r3, [r5, r4]

d6da: 2b2f cmp r3, #47 ; 0x2f

d6dc: d040 beq.n d760 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x220>

d6de: 192b adds r3, r5, r4

d6e0: e005 b.n d6ee <\_\_tls\_get\_addr@@GLIBC\_2.4+0x1ae>

d6e2: f813 2d01 ldrb.w r2, [r3, #-1]!

d6e6: 1e61 subs r1, r4, #1

d6e8: 2a2f cmp r2, #47 ; 0x2f

d6ea: d03a beq.n d762 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x222>

d6ec: 460c mov r4, r1

d6ee: 2c01 cmp r4, #1

d6f0: d1f7 bne.n d6e2 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x1a2>

d6f2: 2002 movs r0, #2

d6f4: f7f3 e96a blx 9cc <malloc@plt+0x4>

d6f8: 4606 mov r6, r0

d6fa: b358 cbz r0, d754 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x214>

d6fc: 232f movs r3, #47 ; 0x2f

d6fe: 8033 strh r3, [r6, #0]

d700: e02a b.n d758 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x218>

d702: 4b25 ldr r3, [pc, #148] ; (d798 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x258>)

d704: 447b add r3, pc

d706: f8d3 4160 ldr.w r4, [r3, #352] ; 0x160

d70a: b31c cbz r4, d754 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x214>

d70c: 4620 mov r0, r4

d70e: f005 f817 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

d712: 4605 mov r5, r0

d714: 3001 adds r0, #1

d716: f7f3 e95a blx 9cc <malloc@plt+0x4>

d71a: 4606 mov r6, r0

d71c: b1d0 cbz r0, d754 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x214>

d71e: 462a mov r2, r5

d720: 4621 mov r1, r4

d722: f005 fcd3 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

d726: 1c71 adds r1, r6, #1

d728: 4288 cmp r0, r1

d72a: 4602 mov r2, r0

d72c: d90c bls.n d748 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x208>

d72e: f810 3c01 ldrb.w r3, [r0, #-1]

d732: 2b2f cmp r3, #47 ; 0x2f

d734: d108 bne.n d748 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x208>

d736: 3801 subs r0, #1

d738: e003 b.n d742 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x202>

d73a: f810 3d01 ldrb.w r3, [r0, #-1]!

d73e: 2b2f cmp r3, #47 ; 0x2f

d740: d102 bne.n d748 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x208>

d742: 4281 cmp r1, r0

d744: 4602 mov r2, r0

d746: d1f8 bne.n d73a <\_\_tls\_get\_addr@@GLIBC\_2.4+0x1fa>

d748: 2300 movs r3, #0

d74a: 4630 mov r0, r6

d74c: 7013 strb r3, [r2, #0]

d74e: f50d 5d80 add.w sp, sp, #4096 ; 0x1000

d752: bd70 pop {r4, r5, r6, pc}

d754: f04f 36ff mov.w r6, #4294967295 ; 0xffffffff

d758: 4630 mov r0, r6

d75a: f50d 5d80 add.w sp, sp, #4096 ; 0x1000

d75e: bd70 pop {r4, r5, r6, pc}

d760: 4604 mov r4, r0

d762: 1c60 adds r0, r4, #1

d764: f7f3 e932 blx 9cc <malloc@plt+0x4>

d768: 4606 mov r6, r0

d76a: 2800 cmp r0, #0

d76c: d0f2 beq.n d754 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x214>

d76e: 1e62 subs r2, r4, #1

d770: 4629 mov r1, r5

d772: f005 fcab bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

d776: 2300 movs r3, #0

d778: 7003 strb r3, [r0, #0]

d77a: 4630 mov r0, r6

d77c: f50d 5d80 add.w sp, sp, #4096 ; 0x1000

d780: bd70 pop {r4, r5, r6, pc}

d782: 4b06 ldr r3, [pc, #24] ; (d79c <\_\_tls\_get\_addr@@GLIBC\_2.4+0x25c>)

d784: 222f movs r2, #47 ; 0x2f

d786: 4906 ldr r1, [pc, #24] ; (d7a0 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x260>)

d788: 4806 ldr r0, [pc, #24] ; (d7a4 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x264>)

d78a: 447b add r3, pc

d78c: 4479 add r1, pc

d78e: 4478 add r0, pc

d790: f003 f8fc bl 1098c <free@@GLIBC\_2.4+0x12c>

d794: 9872 ldr r0, [sp, #456] ; 0x1c8

d796: 0000 movs r0, r0

d798: b268 sxtb r0, r5

d79a: 0001 movs r1, r0

d79c: 97e2 str r7, [sp, #904] ; 0x388

d79e: 0000 movs r0, r0

d7a0: 97a4 str r7, [sp, #656] ; 0x290

d7a2: 0000 movs r0, r0

d7a4: 97ca str r7, [sp, #808] ; 0x328

d7a6: 0000 movs r0, r0

d7a8: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

d7ac: f5a3 6398 sub.w r3, r3, #1216 ; 0x4c0

d7b0: b570 push {r4, r5, r6, lr}

d7b2: 681c ldr r4, [r3, #0]

d7b4: b91c cbnz r4, d7be <\_\_tls\_get\_addr@@GLIBC\_2.4+0x27e>

d7b6: f7f3 e936 blx a24 <free@plt+0x4>

d7ba: 4620 mov r0, r4

d7bc: bd70 pop {r4, r5, r6, pc}

d7be: 4c19 ldr r4, [pc, #100] ; (d824 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x2e4>)

d7c0: 4606 mov r6, r0

d7c2: 447c add r4, pc

d7c4: f8d4 5834 ldr.w r5, [r4, #2100] ; 0x834

d7c8: b1d5 cbz r5, d800 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x2c0>

d7ca: 682b ldr r3, [r5, #0]

d7cc: 2b31 cmp r3, #49 ; 0x31

d7ce: d806 bhi.n d7de <\_\_tls\_get\_addr@@GLIBC\_2.4+0x29e>

d7d0: eb05 0283 add.w r2, r5, r3, lsl #2

d7d4: 2400 movs r4, #0

d7d6: 3301 adds r3, #1

d7d8: 602b str r3, [r5, #0]

d7da: 6050 str r0, [r2, #4]

d7dc: e7ed b.n d7ba <\_\_tls\_get\_addr@@GLIBC\_2.4+0x27a>

d7de: f8d4 3830 ldr.w r3, [r4, #2096] ; 0x830

d7e2: 4798 blx r3

d7e4: 682b ldr r3, [r5, #0]

d7e6: b14b cbz r3, d7fc <\_\_tls\_get\_addr@@GLIBC\_2.4+0x2bc>

d7e8: 3b01 subs r3, #1

d7ea: 602b str r3, [r5, #0]

d7ec: eb05 0383 add.w r3, r5, r3, lsl #2

d7f0: 6858 ldr r0, [r3, #4]

d7f2: f7f3 e918 blx a24 <free@plt+0x4>

d7f6: 682b ldr r3, [r5, #0]

d7f8: 2b00 cmp r3, #0

d7fa: d1f5 bne.n d7e8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x2a8>

d7fc: 2401 movs r4, #1

d7fe: e7dc b.n d7ba <\_\_tls\_get\_addr@@GLIBC\_2.4+0x27a>

d800: 20cc movs r0, #204 ; 0xcc

d802: f7f3 e8e4 blx 9cc <malloc@plt+0x4>

d806: f8c4 0834 str.w r0, [r4, #2100] ; 0x834

d80a: b120 cbz r0, d816 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x2d6>

d80c: 2301 movs r3, #1

d80e: 6046 str r6, [r0, #4]

d810: 462c mov r4, r5

d812: 6003 str r3, [r0, #0]

d814: e7d1 b.n d7ba <\_\_tls\_get\_addr@@GLIBC\_2.4+0x27a>

d816: f8d4 3830 ldr.w r3, [r4, #2096] ; 0x830

d81a: 2401 movs r4, #1

d81c: 4798 blx r3

d81e: 4630 mov r0, r6

d820: e7c9 b.n d7b6 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x276>

d822: bf00 nop

d824: b89a ; <UNDEFINED> instruction: 0xb89a

d826: 0001 movs r1, r0

0000d828 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE>:

d828: 4a0d ldr r2, [pc, #52] ; (d860 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x38>)

d82a: 4b0e ldr r3, [pc, #56] ; (d864 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x3c>)

d82c: 447a add r2, pc

d82e: 447b add r3, pc

d830: 6911 ldr r1, [r2, #16]

d832: b510 push {r4, lr}

d834: 4604 mov r4, r0

d836: 6800 ldr r0, [r0, #0]

d838: 681a ldr r2, [r3, #0]

d83a: 424b negs r3, r1

d83c: 4018 ands r0, r3

d83e: f004 fad7 bl 11df0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1088>

d842: b948 cbnz r0, d858 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x30>

d844: 4a08 ldr r2, [pc, #32] ; (d868 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x40>)

d846: 6020 str r0, [r4, #0]

d848: 447a add r2, pc

d84a: f8d2 3804 ldr.w r3, [r2, #2052] ; 0x804

d84e: f043 0301 orr.w r3, r3, #1

d852: f8c2 3804 str.w r3, [r2, #2052] ; 0x804

d856: bd10 pop {r4, pc}

d858: 4b04 ldr r3, [pc, #16] ; (d86c <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x44>)

d85a: 447b add r3, pc

d85c: 6818 ldr r0, [r3, #0]

d85e: bd10 pop {r4, pc}

d860: b140 cbz r0, d874 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x4c>

d862: 0001 movs r1, r0

d864: b2f6 uxtb r6, r6

d866: 0001 movs r1, r0

d868: b814 ; <UNDEFINED> instruction: 0xb814

d86a: 0001 movs r1, r0

d86c: c0fa stmia r0!, {r1, r3, r4, r5, r6, r7}

d86e: 0001 movs r1, r0

d870: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

d874: 4605 mov r5, r0

d876: f8d0 8164 ldr.w r8, [r0, #356] ; 0x164

d87a: b083 sub sp, #12

d87c: f1b8 0f00 cmp.w r8, #0

d880: f000 80b2 beq.w d9e8 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1c0>

d884: f8d0 3160 ldr.w r3, [r0, #352] ; 0x160

d888: 2400 movs r4, #0

d88a: eb03 0188 add.w r1, r3, r8, lsl #2

d88e: f853 2b04 ldr.w r2, [r3], #4

d892: f892 2198 ldrb.w r2, [r2, #408] ; 0x198

d896: 06d6 lsls r6, r2, #27

d898: bf58 it pl

d89a: 3401 addpl r4, #1

d89c: 4299 cmp r1, r3

d89e: d1f6 bne.n d88e <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x66>

d8a0: f8df a154 ldr.w sl, [pc, #340] ; d9f8 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1d0>

d8a4: 264c movs r6, #76 ; 0x4c

d8a6: 69af ldr r7, [r5, #24]

d8a8: 44fa add sl, pc

d8aa: fb06 a607 mla r6, r6, r7, sl

d8ae: e9d6 9302 ldrd r9, r3, [r6, #8]

d8b2: 2b00 cmp r3, #0

d8b4: d156 bne.n d964 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x13c>

d8b6: f1b9 0f00 cmp.w r9, #0

d8ba: f000 8084 beq.w d9c6 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x19e>

d8be: 4e4f ldr r6, [pc, #316] ; (d9fc <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1d4>)

d8c0: 234c movs r3, #76 ; 0x4c

d8c2: f8d9 a004 ldr.w sl, [r9, #4]

d8c6: 3408 adds r4, #8

d8c8: 447e add r6, pc

d8ca: 4454 add r4, sl

d8cc: fb03 6607 mla r6, r3, r7, r6

d8d0: 00a0 lsls r0, r4, #2

d8d2: 60f4 str r4, [r6, #12]

d8d4: f7f3 e87a blx 9cc <malloc@plt+0x4>

d8d8: 2800 cmp r0, #0

d8da: f000 808b beq.w d9f4 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1cc>

d8de: ea4f 028a mov.w r2, sl, lsl #2

d8e2: f8d9 1000 ldr.w r1, [r9]

d8e6: f005 ec0c blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

d8ea: 68b3 ldr r3, [r6, #8]

d8ec: 685a ldr r2, [r3, #4]

d8ee: f8c9 0000 str.w r0, [r9]

d8f2: f1b8 0f00 cmp.w r8, #0

d8f6: d028 beq.n d94a <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x122>

d8f8: 4e41 ldr r6, [pc, #260] ; (da00 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1d8>)

d8fa: 234c movs r3, #76 ; 0x4c

d8fc: f8df 8104 ldr.w r8, [pc, #260] ; da04 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1dc>

d900: 2400 movs r4, #0

d902: 447e add r6, pc

d904: f8df 9100 ldr.w r9, [pc, #256] ; da08 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1e0>

d908: 44f8 add r8, pc

d90a: fb03 6607 mla r6, r3, r7, r6

d90e: 44f9 add r9, pc

d910: f8d5 3160 ldr.w r3, [r5, #352] ; 0x160

d914: f853 1024 ldr.w r1, [r3, r4, lsl #2]

d918: f891 3198 ldrb.w r3, [r1, #408] ; 0x198

d91c: 06d8 lsls r0, r3, #27

d91e: d40f bmi.n d940 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x118>

d920: 68b0 ldr r0, [r6, #8]

d922: f043 0310 orr.w r3, r3, #16

d926: f881 3198 strb.w r3, [r1, #408] ; 0x198

d92a: f102 0a01 add.w sl, r2, #1

d92e: f8d8 3000 ldr.w r3, [r8]

d932: 6800 ldr r0, [r0, #0]

d934: 059b lsls r3, r3, #22

d936: f840 1022 str.w r1, [r0, r2, lsl #2]

d93a: bf58 it pl

d93c: 4652 movpl r2, sl

d93e: d43b bmi.n d9b8 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x190>

d940: f8d5 3164 ldr.w r3, [r5, #356] ; 0x164

d944: 3401 adds r4, #1

d946: 42a3 cmp r3, r4

d948: d8e2 bhi.n d910 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0xe8>

d94a: 4b30 ldr r3, [pc, #192] ; (da0c <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1e4>)

d94c: 214c movs r1, #76 ; 0x4c

d94e: f3bf 8f5b dmb ish

d952: 2000 movs r0, #0

d954: 447b add r3, pc

d956: fb01 3707 mla r7, r1, r7, r3

d95a: 68bb ldr r3, [r7, #8]

d95c: 605a str r2, [r3, #4]

d95e: b003 add sp, #12

d960: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

d964: f8d9 2004 ldr.w r2, [r9, #4]

d968: 1911 adds r1, r2, r4

d96a: 428b cmp r3, r1

d96c: d2c1 bcs.n d8f2 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0xca>

d96e: 441c add r4, r3

d970: 9301 str r3, [sp, #4]

d972: f8d9 8000 ldr.w r8, [r9]

d976: 00e0 lsls r0, r4, #3

d978: 0064 lsls r4, r4, #1

d97a: f7f3 e828 blx 9cc <malloc@plt+0x4>

d97e: 4683 mov fp, r0

d980: b350 cbz r0, d9d8 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1b0>

d982: 9b01 ldr r3, [sp, #4]

d984: 4641 mov r1, r8

d986: 009a lsls r2, r3, #2

d988: f005 ebba blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

d98c: ee1d 3f70 mrc 15, 0, r3, cr13, cr0, {3}

d990: f5a3 6398 sub.w r3, r3, #1216 ; 0x4c0

d994: 60f4 str r4, [r6, #12]

d996: f8c9 b000 str.w fp, [r9]

d99a: 681b ldr r3, [r3, #0]

d99c: bb33 cbnz r3, d9ec <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1c4>

d99e: 4640 mov r0, r8

d9a0: f7f3 e840 blx a24 <free@plt+0x4>

d9a4: 4b1a ldr r3, [pc, #104] ; (da10 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1e8>)

d9a6: 224c movs r2, #76 ; 0x4c

d9a8: f8d5 8164 ldr.w r8, [r5, #356] ; 0x164

d9ac: 447b add r3, pc

d9ae: fb02 3307 mla r3, r2, r7, r3

d9b2: 689b ldr r3, [r3, #8]

d9b4: 685a ldr r2, [r3, #4]

d9b6: e79c b.n d8f2 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0xca>

d9b8: 698a ldr r2, [r1, #24]

d9ba: 4648 mov r0, r9

d9bc: 6849 ldr r1, [r1, #4]

d9be: f7fe f9d9 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

d9c2: 4652 mov r2, sl

d9c4: e7bc b.n d940 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x118>

d9c6: 2108 movs r1, #8

d9c8: 2001 movs r0, #1

d9ca: f7f3 e812 blx 9f0 <calloc@plt>

d9ce: 4681 mov r9, r0

d9d0: 60b0 str r0, [r6, #8]

d9d2: 2800 cmp r0, #0

d9d4: f47f af73 bne.w d8be <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x96>

d9d8: 69e9 ldr r1, [r5, #28]

d9da: 2200 movs r2, #0

d9dc: 4b0d ldr r3, [pc, #52] ; (da14 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1ec>)

d9de: 200c movs r0, #12

d9e0: 447b add r3, pc

d9e2: 6809 ldr r1, [r1, #0]

d9e4: f7f3 e810 blx a08 <\_dl\_signal\_error@plt>

d9e8: 4644 mov r4, r8

d9ea: e759 b.n d8a0 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x78>

d9ec: f8da 3830 ldr.w r3, [sl, #2096] ; 0x830

d9f0: 4798 blx r3

d9f2: e7d4 b.n d99e <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x176>

d9f4: 60f0 str r0, [r6, #12]

d9f6: e7ef b.n d9d8 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x1b0>

d9f8: b7b4 ; <UNDEFINED> instruction: 0xb7b4

d9fa: 0001 movs r1, r0

d9fc: b794 ; <UNDEFINED> instruction: 0xb794

d9fe: 0001 movs r1, r0

da00: b75a ; <UNDEFINED> instruction: 0xb75a

da02: 0001 movs r1, r0

da04: b064 add sp, #400 ; 0x190

da06: 0001 movs r1, r0

da08: 968a str r6, [sp, #552] ; 0x228

da0a: 0000 movs r0, r0

da0c: b708 ; <UNDEFINED> instruction: 0xb708

da0e: 0001 movs r1, r0

da10: b6b0 ; <UNDEFINED> instruction: 0xb6b0

da12: 0001 movs r1, r0

da14: 959c str r5, [sp, #624] ; 0x270

...

0000da18 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE>:

da18: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

da1c: 4e1c ldr r6, [pc, #112] ; (da90 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x78>)

da1e: 447e add r6, pc

da20: f8d6 34c0 ldr.w r3, [r6, #1216] ; 0x4c0

da24: b303 cbz r3, da68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x50>

da26: f8df 806c ldr.w r8, [pc, #108] ; da94 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x7c>

da2a: 4605 mov r5, r0

da2c: 2700 movs r7, #0

da2e: 44f8 add r8, pc

da30: 6834 ldr r4, [r6, #0]

da32: b19c cbz r4, da5c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x44>

da34: f8d4 31b0 ldr.w r3, [r4, #432] ; 0x1b0

da38: 42ab cmp r3, r5

da3a: d80c bhi.n da56 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x3e>

da3c: f8d4 31b4 ldr.w r3, [r4, #436] ; 0x1b4

da40: 42ab cmp r3, r5

da42: d908 bls.n da56 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x3e>

da44: f894 3199 ldrb.w r3, [r4, #409] ; 0x199

da48: 065b lsls r3, r3, #25

da4a: d411 bmi.n da70 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x58>

da4c: 4629 mov r1, r5

da4e: 4620 mov r0, r4

da50: f003 ff12 bl 11878 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb10>

da54: b960 cbnz r0, da70 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x58>

da56: 68e4 ldr r4, [r4, #12]

da58: 2c00 cmp r4, #0

da5a: d1eb bne.n da34 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1c>

da5c: f8d8 34c0 ldr.w r3, [r8, #1216] ; 0x4c0

da60: 3701 adds r7, #1

da62: 364c adds r6, #76 ; 0x4c

da64: 42bb cmp r3, r7

da66: d8e3 bhi.n da30 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x18>

da68: 2400 movs r4, #0

da6a: 4620 mov r0, r4

da6c: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

da70: 69a3 ldr r3, [r4, #24]

da72: 42bb cmp r3, r7

da74: d102 bne.n da7c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x64>

da76: 4620 mov r0, r4

da78: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

da7c: 4b06 ldr r3, [pc, #24] ; (da98 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x80>)

da7e: 22b8 movs r2, #184 ; 0xb8

da80: 4906 ldr r1, [pc, #24] ; (da9c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x84>)

da82: 4807 ldr r0, [pc, #28] ; (daa0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x88>)

da84: 447b add r3, pc

da86: 4479 add r1, pc

da88: 4478 add r0, pc

da8a: f002 ff7f bl 1098c <free@@GLIBC\_2.4+0x12c>

da8e: bf00 nop

da90: b63e ; <UNDEFINED> instruction: 0xb63e

da92: 0001 movs r1, r0

da94: b62e ; <UNDEFINED> instruction: 0xb62e

da96: 0001 movs r1, r0

da98: 9740 str r7, [sp, #256] ; 0x100

da9a: 0000 movs r0, r0

da9c: 9532 str r5, [sp, #200] ; 0xc8

da9e: 0000 movs r0, r0

daa0: 953c str r5, [sp, #240] ; 0xf0

daa2: 0000 movs r0, r0

daa4: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

daa8: 4690 mov r8, r2

daaa: f011 0203 ands.w r2, r1, #3

daae: b08c sub sp, #48 ; 0x30

dab0: 4607 mov r7, r0

dab2: f000 80bc beq.w dc2e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x216>

dab6: 4d66 ldr r5, [pc, #408] ; (dc50 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x238>)

dab8: 461c mov r4, r3

daba: 460e mov r6, r1

dabc: 447d add r5, pc

dabe: f205 40c4 addw r0, r5, #1220 ; 0x4c4

dac2: f8d5 37f8 ldr.w r3, [r5, #2040] ; 0x7f8

dac6: 4798 blx r3

dac8: 1c61 adds r1, r4, #1

daca: d02f beq.n db2c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x114>

dacc: 1ca3 adds r3, r4, #2

dace: f033 0302 bics.w r3, r3, #2

dad2: d170 bne.n dbb6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x19e>

dad4: 9408 str r4, [sp, #32]

dad6: ad01 add r5, sp, #4

dad8: 9c12 ldr r4, [sp, #72] ; 0x48

dada: aa04 add r2, sp, #16

dadc: 495d ldr r1, [pc, #372] ; (dc54 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x23c>)

dade: 4628 mov r0, r5

dae0: 2300 movs r3, #0

dae2: 9704 str r7, [sp, #16]

dae4: 9409 str r4, [sp, #36] ; 0x24

dae6: 4479 add r1, pc

dae8: 9c13 ldr r4, [sp, #76] ; 0x4c

daea: 9307 str r3, [sp, #28]

daec: 9605 str r6, [sp, #20]

daee: 940a str r4, [sp, #40] ; 0x28

daf0: 9c14 ldr r4, [sp, #80] ; 0x50

daf2: f8cd 8018 str.w r8, [sp, #24]

daf6: 940b str r4, [sp, #44] ; 0x2c

daf8: f7f2 ef60 blx 9bc <\_dl\_catch\_exception@plt>

dafc: 4604 mov r4, r0

dafe: f001 ff3f bl f980 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x660>

db02: 9802 ldr r0, [sp, #8]

db04: 2800 cmp r0, #0

db06: d13c bne.n db82 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16a>

db08: 9908 ldr r1, [sp, #32]

db0a: f7fd ff6b bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

db0e: 68c3 ldr r3, [r0, #12]

db10: 2b00 cmp r3, #0

db12: f040 8092 bne.w dc3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x222>

db16: 4b50 ldr r3, [pc, #320] ; (dc58 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x240>)

db18: 447b add r3, pc

db1a: f203 40c4 addw r0, r3, #1220 ; 0x4c4

db1e: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

db22: 4798 blx r3

db24: 9807 ldr r0, [sp, #28]

db26: b00c add sp, #48 ; 0x30

db28: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

db2c: f8d5 24c0 ldr.w r2, [r5, #1216] ; 0x4c0

db30: 2a01 cmp r2, #1

db32: d979 bls.n dc28 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x210>

db34: 6ceb ldr r3, [r5, #76] ; 0x4c

db36: 2401 movs r4, #1

db38: b923 cbnz r3, db44 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12c>

db3a: e01b b.n db74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15c>

db3c: 354c adds r5, #76 ; 0x4c

db3e: 6ceb ldr r3, [r5, #76] ; 0x4c

db40: 2b00 cmp r3, #0

db42: d057 beq.n dbf4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1dc>

db44: 3401 adds r4, #1

db46: 42a2 cmp r2, r4

db48: 4623 mov r3, r4

db4a: d1f7 bne.n db3c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x124>

db4c: 2a10 cmp r2, #16

db4e: d053 beq.n dbf8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1e0>

db50: 429a cmp r2, r3

db52: d10f bne.n db74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15c>

db54: 4941 ldr r1, [pc, #260] ; (dc5c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x244>)

db56: 234c movs r3, #76 ; 0x4c

db58: 3201 adds r2, #1

db5a: 2001 movs r0, #1

db5c: 4479 add r1, pc

db5e: fb03 1304 mla r3, r3, r4, r1

db62: f8c1 24c0 str.w r2, [r1, #1216] ; 0x4c0

db66: 2200 movs r2, #0

db68: 61d8 str r0, [r3, #28]

db6a: e9c3 2204 strd r2, r2, [r3, #16]

db6e: 619a str r2, [r3, #24]

db70: e9c3 2208 strd r2, r2, [r3, #32]

db74: 4621 mov r1, r4

db76: 2000 movs r0, #0

db78: f7fd ff34 bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

db7c: 2300 movs r3, #0

db7e: 60c3 str r3, [r0, #12]

db80: e7a8 b.n dad4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xbc>

db82: 9807 ldr r0, [sp, #28]

db84: b120 cbz r0, db90 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x178>

db86: 0133 lsls r3, r6, #4

db88: d52e bpl.n dbe8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1d0>

db8a: 2101 movs r1, #1

db8c: f000 fc2a bl e3e4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9cc>

db90: 9908 ldr r1, [sp, #32]

db92: 2000 movs r0, #0

db94: f7fd ff26 bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

db98: 68c6 ldr r6, [r0, #12]

db9a: 2e00 cmp r6, #0

db9c: d13a bne.n dc14 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1fc>

db9e: 4b30 ldr r3, [pc, #192] ; (dc60 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x248>)

dba0: 447b add r3, pc

dba2: f203 40c4 addw r0, r3, #1220 ; 0x4c4

dba6: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

dbaa: 4798 blx r3

dbac: 4632 mov r2, r6

dbae: 4629 mov r1, r5

dbb0: 4620 mov r0, r4

dbb2: f7f2 ef12 blx 9d8 <\_dl\_signal\_exception@plt>

dbb6: 2c00 cmp r4, #0

dbb8: db0f blt.n dbda <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1c2>

dbba: f8d5 34c0 ldr.w r3, [r5, #1216] ; 0x4c0

dbbe: 429c cmp r4, r3

dbc0: d20b bcs.n dbda <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1c2>

dbc2: 234c movs r3, #76 ; 0x4c

dbc4: fb03 f304 mul.w r3, r3, r4

dbc8: 18ea adds r2, r5, r3

dbca: 6852 ldr r2, [r2, #4]

dbcc: b12a cbz r2, dbda <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1c2>

dbce: 58eb ldr r3, [r5, r3]

dbd0: f893 3199 ldrb.w r3, [r3, #409] ; 0x199

dbd4: 071a lsls r2, r3, #28

dbd6: f57f af7d bpl.w dad4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xbc>

dbda: 4b22 ldr r3, [pc, #136] ; (dc64 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x24c>)

dbdc: 4639 mov r1, r7

dbde: 2200 movs r2, #0

dbe0: 2016 movs r0, #22

dbe2: 447b add r3, pc

dbe4: f7f2 ef10 blx a08 <\_dl\_signal\_error@plt>

dbe8: 4b1f ldr r3, [pc, #124] ; (dc68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x250>)

dbea: 2201 movs r2, #1

dbec: 447b add r3, pc

dbee: f883 2808 strb.w r2, [r3, #2056] ; 0x808

dbf2: e7ca b.n db8a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x172>

dbf4: 2c10 cmp r4, #16

dbf6: d1bd bne.n db74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15c>

dbf8: 4b1c ldr r3, [pc, #112] ; (dc6c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x254>)

dbfa: 447b add r3, pc

dbfc: f203 40c4 addw r0, r3, #1220 ; 0x4c4

dc00: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

dc04: 4798 blx r3

dc06: 4b1a ldr r3, [pc, #104] ; (dc70 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x258>)

dc08: 4639 mov r1, r7

dc0a: 2200 movs r2, #0

dc0c: 447b add r3, pc

dc0e: 2016 movs r0, #22

dc10: f7f2 eefa blx a08 <\_dl\_signal\_error@plt>

dc14: 4b17 ldr r3, [pc, #92] ; (dc74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x25c>)

dc16: f240 2272 movw r2, #626 ; 0x272

dc1a: 4917 ldr r1, [pc, #92] ; (dc78 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x260>)

dc1c: 4817 ldr r0, [pc, #92] ; (dc7c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x264>)

dc1e: 447b add r3, pc

dc20: 4479 add r1, pc

dc22: 4478 add r0, pc

dc24: f002 feb2 bl 1098c <free@@GLIBC\_2.4+0x12c>

dc28: 2301 movs r3, #1

dc2a: 461c mov r4, r3

dc2c: e790 b.n db50 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x138>

dc2e: 4b14 ldr r3, [pc, #80] ; (dc80 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x268>)

dc30: 4601 mov r1, r0

dc32: 2016 movs r0, #22

dc34: 447b add r3, pc

dc36: f7f2 eee8 blx a08 <\_dl\_signal\_error@plt>

dc3a: 4b12 ldr r3, [pc, #72] ; (dc84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x26c>)

dc3c: f240 227b movw r2, #635 ; 0x27b

dc40: 4911 ldr r1, [pc, #68] ; (dc88 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x270>)

dc42: 4812 ldr r0, [pc, #72] ; (dc8c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x274>)

dc44: 447b add r3, pc

dc46: 4479 add r1, pc

dc48: 4478 add r0, pc

dc4a: f002 fe9f bl 1098c <free@@GLIBC\_2.4+0x12c>

dc4e: bf00 nop

dc50: b5a0 push {r5, r7, lr}

dc52: 0001 movs r1, r0

dc54: 02a3 lsls r3, r4, #10

dc56: 0000 movs r0, r0

dc58: b544 push {r2, r6, lr}

dc5a: 0001 movs r1, r0

dc5c: b500 push {lr}

dc5e: 0001 movs r1, r0

dc60: b4bc push {r2, r3, r4, r5, r7}

dc62: 0001 movs r1, r0

dc64: 943a str r4, [sp, #232] ; 0xe8

dc66: 0000 movs r0, r0

dc68: b470 push {r4, r5, r6}

dc6a: 0001 movs r1, r0

dc6c: b462 push {r1, r5, r6}

dc6e: 0001 movs r1, r0

dc70: 93e4 str r3, [sp, #912] ; 0x390

dc72: 0000 movs r0, r0

dc74: 959a str r5, [sp, #616] ; 0x268

dc76: 0000 movs r0, r0

dc78: 9398 str r3, [sp, #608] ; 0x260

dc7a: 0000 movs r0, r0

dc7c: 9422 str r4, [sp, #136] ; 0x88

dc7e: 0000 movs r0, r0

dc80: 93a0 str r3, [sp, #640] ; 0x280

dc82: 0000 movs r0, r0

dc84: 9574 str r5, [sp, #464] ; 0x1d0

dc86: 0000 movs r0, r0

dc88: 9372 str r3, [sp, #456] ; 0x1c8

dc8a: 0000 movs r0, r0

dc8c: 93fc str r3, [sp, #1008] ; 0x3f0

dc8e: 0000 movs r0, r0

dc90: e92d 4ff8 stmdb sp!, {r3, r4, r5, r6, r7, r8, r9, sl, fp, lr}

dc94: 4689 mov r9, r1

dc96: 6841 ldr r1, [r0, #4]

dc98: 4683 mov fp, r0

dc9a: 780b ldrb r3, [r1, #0]

dc9c: b92b cbnz r3, dcaa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x292>

dc9e: 4b30 ldr r3, [pc, #192] ; (dd60 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x348>)

dca0: 447b add r3, pc

dca2: 681b ldr r3, [r3, #0]

dca4: 6819 ldr r1, [r3, #0]

dca6: 2900 cmp r1, #0

dca8: d052 beq.n dd50 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x338>

dcaa: 482e ldr r0, [pc, #184] ; (dd64 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x34c>)

dcac: f8db 2018 ldr.w r2, [fp, #24]

dcb0: 4478 add r0, pc

dcb2: f7fe f85f bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

dcb6: f8db 31d0 ldr.w r3, [fp, #464] ; 0x1d0

dcba: 2b00 cmp r3, #0

dcbc: d04b beq.n dd56 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x33e>

dcbe: f853 3029 ldr.w r3, [r3, r9, lsl #2]

dcc2: ea4f 0589 mov.w r5, r9, lsl #2

dcc6: 2b00 cmp r3, #0

dcc8: d03c beq.n dd44 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x32c>

dcca: f8df a09c ldr.w sl, [pc, #156] ; dd68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x350>

dcce: f8df 809c ldr.w r8, [pc, #156] ; dd6c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x354>

dcd2: 4e27 ldr r6, [pc, #156] ; (dd70 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x358>)

dcd4: 44fa add sl, pc

dcd6: 4f27 ldr r7, [pc, #156] ; (dd74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x35c>)

dcd8: 44f8 add r8, pc

dcda: 447e add r6, pc

dcdc: 447f add r7, pc

dcde: 4649 mov r1, r9

dce0: 4650 mov r0, sl

dce2: f7fe f847 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

dce6: f8db 31d0 ldr.w r3, [fp, #464] ; 0x1d0

dcea: 595b ldr r3, [r3, r5]

dcec: 685a ldr r2, [r3, #4]

dcee: b1ea cbz r2, dd2c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x314>

dcf0: 2400 movs r4, #0

dcf2: e00e b.n dd12 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x2fa>

dcf4: f8d8 3000 ldr.w r3, [r8]

dcf8: 6819 ldr r1, [r3, #0]

dcfa: 2900 cmp r1, #0

dcfc: bf08 it eq

dcfe: 4639 moveq r1, r7

dd00: f7fe f84e bl bda0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3c0>

dd04: f8db 31d0 ldr.w r3, [fp, #464] ; 0x1d0

dd08: 3401 adds r4, #1

dd0a: 595b ldr r3, [r3, r5]

dd0c: 685a ldr r2, [r3, #4]

dd0e: 42a2 cmp r2, r4

dd10: d90c bls.n dd2c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x314>

dd12: 681b ldr r3, [r3, #0]

dd14: 4630 mov r0, r6

dd16: f853 3024 ldr.w r3, [r3, r4, lsl #2]

dd1a: 6859 ldr r1, [r3, #4]

dd1c: 780b ldrb r3, [r1, #0]

dd1e: 2b00 cmp r3, #0

dd20: d0e8 beq.n dcf4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x2dc>

dd22: 4815 ldr r0, [pc, #84] ; (dd78 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x360>)

dd24: 4478 add r0, pc

dd26: f7fe f83b bl bda0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3c0>

dd2a: e7eb b.n dd04 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x2ec>

dd2c: 4813 ldr r0, [pc, #76] ; (dd7c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x364>)

dd2e: 3504 adds r5, #4

dd30: f109 0901 add.w r9, r9, #1

dd34: 4478 add r0, pc

dd36: f7fe f833 bl bda0 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3c0>

dd3a: f8db 31d0 ldr.w r3, [fp, #464] ; 0x1d0

dd3e: 595b ldr r3, [r3, r5]

dd40: 2b00 cmp r3, #0

dd42: d1cc bne.n dcde <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x2c6>

dd44: 480e ldr r0, [pc, #56] ; (dd80 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x368>)

dd46: e8bd 4ff8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, sl, fp, lr}

dd4a: 4478 add r0, pc

dd4c: f7fe b812 b.w bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

dd50: 490c ldr r1, [pc, #48] ; (dd84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x36c>)

dd52: 4479 add r1, pc

dd54: e7a9 b.n dcaa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x292>

dd56: 480c ldr r0, [pc, #48] ; (dd88 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x370>)

dd58: 4478 add r0, pc

dd5a: f7fe f80b bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

dd5e: e7f1 b.n dd44 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x32c>

dd60: bbf8 cbnz r0, dde2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x3ca>

dd62: 0001 movs r1, r0

dd64: 93d4 str r3, [sp, #848] ; 0x350

dd66: 0000 movs r0, r0

dd68: 93c4 str r3, [sp, #784] ; 0x310

dd6a: 0000 movs r0, r0

dd6c: bbc0 cbnz r0, dde0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x3c8>

dd6e: 0001 movs r1, r0

dd70: 93ca str r3, [sp, #808] ; 0x328

dd72: 0000 movs r0, r0

dd74: 690c ldr r4, [r1, #16]

dd76: 0000 movs r0, r0

dd78: 9380 str r3, [sp, #512] ; 0x200

dd7a: 0000 movs r0, r0

dd7c: 85b4 strh r4, [r6, #44] ; 0x2c

dd7e: 0000 movs r0, r0

dd80: 859e strh r6, [r3, #44] ; 0x2c

dd82: 0000 movs r0, r0

dd84: 6d86 ldr r6, [r0, #88] ; 0x58

dd86: 0000 movs r0, r0

dd88: 9350 str r3, [sp, #320] ; 0x140

dd8a: 0000 movs r0, r0

dd8c: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

dd90: b08b sub sp, #44 ; 0x2c

dd92: e9d0 6300 ldrd r6, r3, [r0]

dd96: af02 add r7, sp, #8

dd98: 4682 mov sl, r0

dd9a: 2124 movs r1, #36 ; 0x24

dd9c: 4630 mov r0, r6

dd9e: 61fb str r3, [r7, #28]

dda0: f004 f96e bl 12080 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1318>

dda4: 2800 cmp r0, #0

dda6: f000 817c beq.w e0a2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x68a>

ddaa: f8df 34f4 ldr.w r3, [pc, #1268] ; e2a0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x888>

ddae: f8da 0008 ldr.w r0, [sl, #8]

ddb2: 447b add r3, pc

ddb4: 681c ldr r4, [r3, #0]

ddb6: f7ff fe2f bl da18 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE>

ddba: f8da 5010 ldr.w r5, [sl, #16]

ddbe: 2800 cmp r0, #0

ddc0: bf18 it ne

ddc2: 4604 movne r4, r0

ddc4: 1cab adds r3, r5, #2

ddc6: bf04 itt eq

ddc8: 69a5 ldreq r5, [r4, #24]

ddca: f8ca 5010 streq.w r5, [sl, #16]

ddce: 4629 mov r1, r5

ddd0: 2000 movs r0, #0

ddd2: f7fd fe07 bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

ddd6: f8da 3010 ldr.w r3, [sl, #16]

ddda: 69fa ldr r2, [r7, #28]

dddc: 4631 mov r1, r6

ddde: 4620 mov r0, r4

dde0: f042 5280 orr.w r2, r2, #268435456 ; 0x10000000

dde4: e9cd 2300 strd r2, r3, [sp]

dde8: 2300 movs r3, #0

ddea: 2202 movs r2, #2

ddec: f7f8 fb1e bl 642c <free@plt+0x5a0c>

ddf0: 69fb ldr r3, [r7, #28]

ddf2: 4605 mov r5, r0

ddf4: f8ca 000c str.w r0, [sl, #12]

ddf8: 2800 cmp r0, #0

ddfa: f000 8191 beq.w e120 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x708>

ddfe: 04d8 lsls r0, r3, #19

de00: bf42 ittt mi

de02: f8d5 320c ldrmi.w r3, [r5, #524] ; 0x20c

de06: f043 0308 orrmi.w r3, r3, #8

de0a: f8c5 320c strmi.w r3, [r5, #524] ; 0x20c

de0e: 69fb ldr r3, [r7, #28]

de10: 0059 lsls r1, r3, #1

de12: f100 80fd bmi.w e010 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5f8>

de16: f8d5 4160 ldr.w r4, [r5, #352] ; 0x160

de1a: f8d5 3194 ldr.w r3, [r5, #404] ; 0x194

de1e: 3301 adds r3, #1

de20: f8c5 3194 str.w r3, [r5, #404] ; 0x194

de24: 2c00 cmp r4, #0

de26: f040 81c3 bne.w e1b0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x798>

de2a: 69fa ldr r2, [r7, #28]

de2c: 2308 movs r3, #8

de2e: f6c8 0300 movt r3, #34816 ; 0x8800

de32: 4621 mov r1, r4

de34: 401a ands r2, r3

de36: 4628 mov r0, r5

de38: 9200 str r2, [sp, #0]

de3a: 4623 mov r3, r4

de3c: 4622 mov r2, r4

de3e: f7fc f83f bl 9ec0 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x322c>

de42: f8d5 0164 ldr.w r0, [r5, #356] ; 0x164

de46: 4601 mov r1, r0

de48: b918 cbnz r0, de52 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x43a>

de4a: e013 b.n de74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x45c>

de4c: 3401 adds r4, #1

de4e: 42a1 cmp r1, r4

de50: d910 bls.n de74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x45c>

de52: f8d5 3160 ldr.w r3, [r5, #352] ; 0x160

de56: f853 3024 ldr.w r3, [r3, r4, lsl #2]

de5a: 6958 ldr r0, [r3, #20]

de5c: f8d0 2174 ldr.w r2, [r0, #372] ; 0x174

de60: 2a00 cmp r2, #0

de62: d1f3 bne.n de4c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x434>

de64: 4611 mov r1, r2

de66: 3401 adds r4, #1

de68: f7fe f8b8 bl bfdc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x5fc>

de6c: f8d5 1164 ldr.w r1, [r5, #356] ; 0x164

de70: 42a1 cmp r1, r4

de72: d8ee bhi.n de52 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x43a>

de74: f8df 342c ldr.w r3, [pc, #1068] ; e2a4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x88c>

de78: 447b add r3, pc

de7a: f8d3 21b0 ldr.w r2, [r3, #432] ; 0x1b0

de7e: 2a00 cmp r2, #0

de80: f040 81b8 bne.w e1f4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x7dc>

de84: f8da 1010 ldr.w r1, [sl, #16]

de88: 2000 movs r0, #0

de8a: f7fd fdab bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

de8e: 2400 movs r4, #0

de90: 60c4 str r4, [r0, #12]

de92: f7fd fda5 bl b9e0 <\_dl\_debug\_state@@GLIBC\_PRIVATE>

de96: f8df 3410 ldr.w r3, [pc, #1040] ; e2a8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x890>

de9a: 447b add r3, pc

de9c: 681b ldr r3, [r3, #0]

de9e: 0598 lsls r0, r3, #22

dea0: f100 81c9 bmi.w e236 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x81e>

dea4: f8df 3404 ldr.w r3, [pc, #1028] ; e2ac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x894>

dea8: 447b add r3, pc

deaa: 6adb ldr r3, [r3, #44] ; 0x2c

deac: 2b00 cmp r3, #0

deae: f040 80b3 bne.w e018 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x600>

deb2: 69fb ldr r3, [r7, #28]

deb4: f003 6800 and.w r8, r3, #134217728 ; 0x8000000

deb8: 462c mov r4, r5

deba: 2200 movs r2, #0

debc: 6963 ldr r3, [r4, #20]

debe: 68e4 ldr r4, [r4, #12]

dec0: f893 3198 ldrb.w r3, [r3, #408] ; 0x198

dec4: 0759 lsls r1, r3, #29

dec6: bf58 it pl

dec8: 3201 addpl r2, #1

deca: 2c00 cmp r4, #0

decc: d1f6 bne.n debc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x4a4>

dece: 0092 lsls r2, r2, #2

ded0: f8c7 d014 str.w sp, [r7, #20]

ded4: 3207 adds r2, #7

ded6: 462b mov r3, r5

ded8: f022 0207 bic.w r2, r2, #7

dedc: ebad 0d02 sub.w sp, sp, r2

dee0: ae02 add r6, sp, #8

dee2: 695a ldr r2, [r3, #20]

dee4: f892 2198 ldrb.w r2, [r2, #408] ; 0x198

dee8: 0752 lsls r2, r2, #29

deea: bf58 it pl

deec: f846 3024 strpl.w r3, [r6, r4, lsl #2]

def0: 68db ldr r3, [r3, #12]

def2: bf58 it pl

def4: 3401 addpl r4, #1

def6: 2b00 cmp r3, #0

def8: d1f3 bne.n dee2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x4ca>

defa: f8df 93b4 ldr.w r9, [pc, #948] ; e2b0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x898>

defe: 461a mov r2, r3

df00: f8df b3b0 ldr.w fp, [pc, #944] ; e2b4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x89c>

df04: 4621 mov r1, r4

df06: 4630 mov r0, r6

df08: 44f9 add r9, pc

df0a: f001 fa13 bl f334 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x14>

df0e: 44fb add fp, pc

df10: e00b b.n df2a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x512>

df12: f856 0024 ldr.w r0, [r6, r4, lsl #2]

df16: f8d9 3168 ldr.w r3, [r9, #360] ; 0x168

df1a: f8d0 11d0 ldr.w r1, [r0, #464] ; 0x1d0

df1e: 2b00 cmp r3, #0

df20: f040 80cd bne.w e0be <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x6a6>

df24: 4642 mov r2, r8

df26: f7fa fcbf bl 88a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c14>

df2a: f114 34ff adds.w r4, r4, #4294967295 ; 0xffffffff

df2e: d2f0 bcs.n df12 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x4fa>

df30: f8d5 0164 ldr.w r0, [r5, #356] ; 0x164

df34: 4680 mov r8, r0

df36: 2800 cmp r0, #0

df38: d057 beq.n dfea <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5d2>

df3a: f8df b37c ldr.w fp, [pc, #892] ; e2b8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8a0>

df3e: 2600 movs r6, #0

df40: f8c7 a010 str.w sl, [r7, #16]

df44: 46aa mov sl, r5

df46: 44fb add fp, pc

df48: 61be str r6, [r7, #24]

df4a: e012 b.n df72 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x55a>

df4c: f011 0908 ands.w r9, r1, #8

df50: bf18 it ne

df52: 2100 movne r1, #0

df54: d104 bne.n df60 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x548>

df56: f8d5 1238 ldr.w r1, [r5, #568] ; 0x238

df5a: 2900 cmp r1, #0

df5c: f040 80cb bne.w e0f6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x6de>

df60: f8db 3000 ldr.w r3, [fp]

df64: 0598 lsls r0, r3, #22

df66: d43c bmi.n dfe2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5ca>

df68: f8da 0164 ldr.w r0, [sl, #356] ; 0x164

df6c: 3601 adds r6, #1

df6e: 42b0 cmp r0, r6

df70: d95a bls.n e028 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x610>

df72: f8da 3160 ldr.w r3, [sl, #352] ; 0x160

df76: f853 5026 ldr.w r5, [r3, r6, lsl #2]

df7a: f895 1198 ldrb.w r1, [r5, #408] ; 0x198

df7e: f001 030b and.w r3, r1, #11

df82: 2b0a cmp r3, #10

df84: d1e2 bne.n df4c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x534>

df86: f8d5 11d0 ldr.w r1, [r5, #464] ; 0x1d0

df8a: 680c ldr r4, [r1, #0]

df8c: 2c00 cmp r4, #0

df8e: f000 80da beq.w e146 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x72e>

df92: f50a 7cb0 add.w ip, sl, #352 ; 0x160

df96: 4564 cmp r4, ip

df98: d0e8 beq.n df6c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x554>

df9a: 460a mov r2, r1

df9c: f04f 0900 mov.w r9, #0

dfa0: e002 b.n dfa8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x590>

dfa2: 4563 cmp r3, ip

dfa4: 46a1 mov r9, r4

dfa6: d0e1 beq.n df6c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x554>

dfa8: f852 3f04 ldr.w r3, [r2, #4]!

dfac: f109 0401 add.w r4, r9, #1

dfb0: 2b00 cmp r3, #0

dfb2: d1f6 bne.n dfa2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x58a>

dfb4: f109 0902 add.w r9, r9, #2

dfb8: f8d5 31cc ldr.w r3, [r5, #460] ; 0x1cc

dfbc: 454b cmp r3, r9

dfbe: f240 80c5 bls.w e14c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x734>

dfc2: 00a3 lsls r3, r4, #2

dfc4: 2200 movs r2, #0

dfc6: f841 2029 str.w r2, [r1, r9, lsl #2]

dfca: f3bf 8f5b dmb ish

dfce: f8d5 21d0 ldr.w r2, [r5, #464] ; 0x1d0

dfd2: f50a 70b0 add.w r0, sl, #352 ; 0x160

dfd6: 4621 mov r1, r4

dfd8: 50d0 str r0, [r2, r3]

dfda: f8db 3000 ldr.w r3, [fp]

dfde: 0598 lsls r0, r3, #22

dfe0: d5c2 bpl.n df68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x550>

dfe2: 4628 mov r0, r5

dfe4: f7ff fe54 bl dc90 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x278>

dfe8: e7be b.n df68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x550>

dfea: f8da 301c ldr.w r3, [sl, #28]

dfee: 4628 mov r0, r5

dff0: e9da 1205 ldrd r1, r2, [sl, #20]

dff4: f7fd faf6 bl b5e4 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x4950>

dff8: 69fb ldr r3, [r7, #28]

dffa: 05dc lsls r4, r3, #23

dffc: f100 809c bmi.w e138 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x720>

e000: 4bae ldr r3, [pc, #696] ; (e2bc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8a4>)

e002: 447b add r3, pc

e004: 681b ldr r3, [r3, #0]

e006: 065b lsls r3, r3, #25

e008: f100 811f bmi.w e24a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x832>

e00c: f8d7 d014 ldr.w sp, [r7, #20]

e010: 3724 adds r7, #36 ; 0x24

e012: 46bd mov sp, r7

e014: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

e018: 69fb ldr r3, [r7, #28]

e01a: f04f 0801 mov.w r8, #1

e01e: f6c0 0800 movt r8, #2048 ; 0x800

e022: ea03 0808 and.w r8, r3, r8

e026: e747 b.n deb8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x4a0>

e028: 69bb ldr r3, [r7, #24]

e02a: 4655 mov r5, sl

e02c: f8d7 a010 ldr.w sl, [r7, #16]

e030: b14b cbz r3, e046 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x62e>

e032: 4aa3 ldr r2, [pc, #652] ; (e2c0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8a8>)

e034: 447a add r2, pc

e036: f8d2 3828 ldr.w r3, [r2, #2088] ; 0x828

e03a: 3301 adds r3, #1

e03c: f8c2 3828 str.w r3, [r2, #2088] ; 0x828

e040: 2b00 cmp r3, #0

e042: f000 810b beq.w e25c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x844>

e046: 4540 cmp r0, r8

e048: d9cf bls.n dfea <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5d2>

e04a: 4c9e ldr r4, [pc, #632] ; (e2c4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8ac>)

e04c: 447c add r4, pc

e04e: e003 b.n e058 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x640>

e050: f108 0801 add.w r8, r8, #1

e054: 4540 cmp r0, r8

e056: d9c8 bls.n dfea <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5d2>

e058: f8d5 3160 ldr.w r3, [r5, #352] ; 0x160

e05c: f853 6028 ldr.w r6, [r3, r8, lsl #2]

e060: f8b6 3198 ldrh.w r3, [r6, #408] ; 0x198

e064: f403 6381 and.w r3, r3, #1032 ; 0x408

e068: f5b3 6f80 cmp.w r3, #1024 ; 0x400

e06c: d1f0 bne.n e050 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x638>

e06e: f8d6 3238 ldr.w r3, [r6, #568] ; 0x238

e072: 2b00 cmp r3, #0

e074: d0ec beq.n e050 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x638>

e076: f896 3199 ldrb.w r3, [r6, #409] ; 0x199

e07a: f8d6 0248 ldr.w r0, [r6, #584] ; 0x248

e07e: f36f 0382 bfc r3, #2, #1

e082: f886 3199 strb.w r3, [r6, #409] ; 0x199

e086: f7ff f98d bl d3a4 <\_dl\_deallocate\_tls@@GLIBC\_PRIVATE+0x58>

e08a: f8d4 382c ldr.w r3, [r4, #2092] ; 0x82c

e08e: 4630 mov r0, r6

e090: 4798 blx r3

e092: f896 3199 ldrb.w r3, [r6, #409] ; 0x199

e096: 075a lsls r2, r3, #29

e098: f100 80f7 bmi.w e28a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x872>

e09c: f8d5 0164 ldr.w r0, [r5, #356] ; 0x164

e0a0: e7d6 b.n e050 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x638>

e0a2: f8da 5010 ldr.w r5, [sl, #16]

e0a6: 1caa adds r2, r5, #2

e0a8: f43f ae7f beq.w ddaa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x392>

e0ac: 4604 mov r4, r0

e0ae: 212f movs r1, #47 ; 0x2f

e0b0: 4630 mov r0, r6

e0b2: f003 ffe5 bl 12080 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1318>

e0b6: 2800 cmp r0, #0

e0b8: f47f ae89 bne.w ddce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x3b6>

e0bc: e675 b.n ddaa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x392>

e0be: f8db 3504 ldr.w r3, [fp, #1284] ; 0x504

e0c2: 61bb str r3, [r7, #24]

e0c4: 2301 movs r3, #1

e0c6: ea48 0203 orr.w r2, r8, r3

e0ca: f7fa fbed bl 88a8 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1c14>

e0ce: 69bb ldr r3, [r7, #24]

e0d0: 2b00 cmp r3, #0

e0d2: f47f af2a bne.w df2a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x512>

e0d6: f8db 3504 ldr.w r3, [fp, #1284] ; 0x504

e0da: 2b00 cmp r3, #0

e0dc: f43f af25 beq.w df2a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x512>

e0e0: f7fe fa14 bl c50c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0xb2c>

e0e4: f8db 2504 ldr.w r2, [fp, #1284] ; 0x504

e0e8: f8d2 320c ldr.w r3, [r2, #524] ; 0x20c

e0ec: f043 0308 orr.w r3, r3, #8

e0f0: f8c2 320c str.w r3, [r2, #524] ; 0x20c

e0f4: e719 b.n df2a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x512>

e0f6: 4628 mov r0, r5

e0f8: f7ff fa70 bl d5dc <\_\_tls\_get\_addr@@GLIBC\_2.4+0x9c>

e0fc: f895 1199 ldrb.w r1, [r5, #409] ; 0x199

e100: f011 0104 ands.w r1, r1, #4

e104: d009 beq.n e11a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x702>

e106: f8da 0164 ldr.w r0, [sl, #356] ; 0x164

e10a: 4649 mov r1, r9

e10c: 4540 cmp r0, r8

e10e: bf02 ittt eq

e110: 2301 moveq r3, #1

e112: 46b0 moveq r8, r6

e114: 61bb streq r3, [r7, #24]

e116: f43f af23 beq.w df60 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x548>

e11a: 2301 movs r3, #1

e11c: 61bb str r3, [r7, #24]

e11e: e71f b.n df60 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x548>

e120: 075c lsls r4, r3, #29

e122: f53f af75 bmi.w e010 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5f8>

e126: 4b68 ldr r3, [pc, #416] ; (e2c8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8b0>)

e128: 22eb movs r2, #235 ; 0xeb

e12a: 4968 ldr r1, [pc, #416] ; (e2cc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8b4>)

e12c: 4868 ldr r0, [pc, #416] ; (e2d0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8b8>)

e12e: 447b add r3, pc

e130: 4479 add r1, pc

e132: 4478 add r0, pc

e134: f002 fc2a bl 1098c <free@@GLIBC\_2.4+0x12c>

e138: 4628 mov r0, r5

e13a: f7ff fb99 bl d870 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x48>

e13e: 2800 cmp r0, #0

e140: f43f af5e beq.w e000 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5e8>

e144: e762 b.n e00c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5f4>

e146: f04f 0901 mov.w r9, #1

e14a: e735 b.n dfb8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5a0>

e14c: f505 72de add.w r2, r5, #444 ; 0x1bc

e150: 2b03 cmp r3, #3

e152: 60ba str r2, [r7, #8]

e154: 4610 mov r0, r2

e156: bf8c ite hi

e158: 2200 movhi r2, #0

e15a: 2201 movls r2, #1

e15c: 4288 cmp r0, r1

e15e: bf08 it eq

e160: 2200 moveq r2, #0

e162: 2a00 cmp r2, #0

e164: d16c bne.n e240 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x828>

e166: 00d8 lsls r0, r3, #3

e168: 6079 str r1, [r7, #4]

e16a: 005b lsls r3, r3, #1

e16c: 603b str r3, [r7, #0]

e16e: f7f2 ec2e blx 9cc <malloc@plt+0x4>

e172: 6879 ldr r1, [r7, #4]

e174: 60f8 str r0, [r7, #12]

e176: 2800 cmp r0, #0

e178: d07f beq.n e27a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x862>

e17a: 00a3 lsls r3, r4, #2

e17c: 68f8 ldr r0, [r7, #12]

e17e: 607b str r3, [r7, #4]

e180: 461a mov r2, r3

e182: f004 efbe blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

e186: 68fb ldr r3, [r7, #12]

e188: f8d5 01d0 ldr.w r0, [r5, #464] ; 0x1d0

e18c: f8c5 31d0 str.w r3, [r5, #464] ; 0x1d0

e190: 68bb ldr r3, [r7, #8]

e192: 4283 cmp r3, r0

e194: 687b ldr r3, [r7, #4]

e196: bf08 it eq

e198: 68f9 ldreq r1, [r7, #12]

e19a: d005 beq.n e1a8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x790>

e19c: 60fb str r3, [r7, #12]

e19e: f7ff fb03 bl d7a8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x268>

e1a2: f8d5 11d0 ldr.w r1, [r5, #464] ; 0x1d0

e1a6: 68fb ldr r3, [r7, #12]

e1a8: 683a ldr r2, [r7, #0]

e1aa: f8c5 21cc str.w r2, [r5, #460] ; 0x1cc

e1ae: e709 b.n dfc4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5ac>

e1b0: 4a48 ldr r2, [pc, #288] ; (e2d4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8bc>)

e1b2: 447a add r2, pc

e1b4: 6812 ldr r2, [r2, #0]

e1b6: 0652 lsls r2, r2, #25

e1b8: d458 bmi.n e26c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x854>

e1ba: 69fb ldr r3, [r7, #28]

e1bc: 05de lsls r6, r3, #23

e1be: d506 bpl.n e1ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x7b6>

e1c0: f895 3198 ldrb.w r3, [r5, #408] ; 0x198

e1c4: 06dc lsls r4, r3, #27

e1c6: d402 bmi.n e1ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x7b6>

e1c8: 4628 mov r0, r5

e1ca: f7ff fb51 bl d870 <\_dl\_make\_stack\_executable@@GLIBC\_PRIVATE+0x48>

e1ce: f8da 1010 ldr.w r1, [sl, #16]

e1d2: 2000 movs r0, #0

e1d4: f7fd fc06 bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

e1d8: 68c3 ldr r3, [r0, #12]

e1da: 2b00 cmp r3, #0

e1dc: f43f af18 beq.w e010 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5f8>

e1e0: 4b3d ldr r3, [pc, #244] ; (e2d8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8c0>)

e1e2: f240 1209 movw r2, #265 ; 0x109

e1e6: 493d ldr r1, [pc, #244] ; (e2dc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8c4>)

e1e8: 483d ldr r0, [pc, #244] ; (e2e0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8c8>)

e1ea: 447b add r3, pc

e1ec: 4479 add r1, pc

e1ee: 4478 add r0, pc

e1f0: f002 fbcc bl 1098c <free@@GLIBC\_2.4+0x12c>

e1f4: 69a8 ldr r0, [r5, #24]

e1f6: 224c movs r2, #76 ; 0x4c

e1f8: 493a ldr r1, [pc, #232] ; (e2e4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8cc>)

e1fa: fb02 f200 mul.w r2, r2, r0

e1fe: 4479 add r1, pc

e200: 5889 ldr r1, [r1, r2]

e202: f891 2199 ldrb.w r2, [r1, #409] ; 0x199

e206: f012 0208 ands.w r2, r2, #8

e20a: f47f ae3b bne.w de84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x46c>

e20e: f8d3 91ac ldr.w r9, [r3, #428] ; 0x1ac

e212: f501 7818 add.w r8, r1, #608 ; 0x260

e216: 4616 mov r6, r2

e218: 464c mov r4, r9

e21a: 4699 mov r9, r3

e21c: 6823 ldr r3, [r4, #0]

e21e: b11b cbz r3, e228 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x810>

e220: 2100 movs r1, #0

e222: eb08 00c6 add.w r0, r8, r6, lsl #3

e226: 4798 blx r3

e228: f8d9 31b0 ldr.w r3, [r9, #432] ; 0x1b0

e22c: 3601 adds r6, #1

e22e: 6a24 ldr r4, [r4, #32]

e230: 42b3 cmp r3, r6

e232: d8f3 bhi.n e21c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x804>

e234: e626 b.n de84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x46c>

e236: 4621 mov r1, r4

e238: 4628 mov r0, r5

e23a: f7ff fd29 bl dc90 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x278>

e23e: e631 b.n dea4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x48c>

e240: 68bb ldr r3, [r7, #8]

e242: 60fb str r3, [r7, #12]

e244: 2304 movs r3, #4

e246: 603b str r3, [r7, #0]

e248: e797 b.n e17a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x762>

e24a: 4827 ldr r0, [pc, #156] ; (e2e8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8d0>)

e24c: f8d5 3194 ldr.w r3, [r5, #404] ; 0x194

e250: 69aa ldr r2, [r5, #24]

e252: 4478 add r0, pc

e254: 6869 ldr r1, [r5, #4]

e256: f7fd fd8d bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

e25a: e6d7 b.n e00c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x5f4>

e25c: 4923 ldr r1, [pc, #140] ; (e2ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8d4>)

e25e: 2002 movs r0, #2

e260: 4479 add r1, pc

e262: f7fd fdb3 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

e266: 207f movs r0, #127 ; 0x7f

e268: f003 fdee bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

e26c: 4820 ldr r0, [pc, #128] ; (e2f0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8d8>)

e26e: 69aa ldr r2, [r5, #24]

e270: 6869 ldr r1, [r5, #4]

e272: 4478 add r0, pc

e274: f7fd fd7e bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

e278: e79f b.n e1ba <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x7a2>

e27a: 4b1e ldr r3, [pc, #120] ; (e2f4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8dc>)

e27c: 4602 mov r2, r0

e27e: 491e ldr r1, [pc, #120] ; (e2f8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8e0>)

e280: 200c movs r0, #12

e282: 447b add r3, pc

e284: 4479 add r1, pc

e286: f7f2 ebc0 blx a08 <\_dl\_signal\_error@plt>

e28a: 4b1c ldr r3, [pc, #112] ; (e2fc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8e4>)

e28c: f44f 72fc mov.w r2, #504 ; 0x1f8

e290: 491b ldr r1, [pc, #108] ; (e300 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8e8>)

e292: 481c ldr r0, [pc, #112] ; (e304 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8ec>)

e294: 447b add r3, pc

e296: 4479 add r1, pc

e298: 4478 add r0, pc

e29a: f002 fb77 bl 1098c <free@@GLIBC\_2.4+0x12c>

e29e: bf00 nop

e2a0: b2aa uxth r2, r5

e2a2: 0001 movs r1, r0

e2a4: aaf4 add r2, sp, #976 ; 0x3d0

e2a6: 0001 movs r1, r0

e2a8: aad2 add r2, sp, #840 ; 0x348

e2aa: 0001 movs r1, r0

e2ac: aac4 add r2, sp, #784 ; 0x310

e2ae: 0001 movs r1, r0

e2b0: aa64 add r2, sp, #400 ; 0x190

e2b2: 0001 movs r1, r0

e2b4: b14e cbz r6, e2ca <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8b2>

e2b6: 0001 movs r1, r0

e2b8: aa26 add r2, sp, #152 ; 0x98

e2ba: 0001 movs r1, r0

e2bc: a96a add r1, sp, #424 ; 0x1a8

e2be: 0001 movs r1, r0

e2c0: b028 add sp, #160 ; 0xa0

e2c2: 0001 movs r1, r0

e2c4: b010 add sp, #64 ; 0x40

e2c6: 0001 movs r1, r0

e2c8: 907a str r0, [sp, #488] ; 0x1e8

e2ca: 0000 movs r0, r0

e2cc: 8e88 ldrh r0, [r1, #52] ; 0x34

e2ce: 0000 movs r0, r0

e2d0: 8f82 ldrh r2, [r0, #60] ; 0x3c

e2d2: 0000 movs r0, r0

e2d4: a7ba add r7, pc, #744 ; (adr r7, e5c0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xba8>)

e2d6: 0001 movs r1, r0

e2d8: 8fbe ldrh r6, [r7, #60] ; 0x3c

e2da: 0000 movs r0, r0

e2dc: 8dcc ldrh r4, [r1, #46] ; 0x2e

e2de: 0000 movs r0, r0

e2e0: 8f0a ldrh r2, [r1, #56] ; 0x38

e2e2: 0000 movs r0, r0

e2e4: ae5e add r6, sp, #376 ; 0x178

e2e6: 0001 movs r1, r0

e2e8: 8e76 ldrh r6, [r6, #50] ; 0x32

e2ea: 0000 movs r0, r0

e2ec: 8ef4 ldrh r4, [r6, #54] ; 0x36

e2ee: 0000 movs r0, r0

e2f0: 8e56 ldrh r6, [r2, #50] ; 0x32

e2f2: 0000 movs r0, r0

e2f4: 8eb6 ldrh r6, [r6, #52] ; 0x34

e2f6: 0000 movs r0, r0

e2f8: 8c14 ldrh r4, [r2, #32]

e2fa: 0000 movs r0, r0

e2fc: 8f14 ldrh r4, [r2, #56] ; 0x38

e2fe: 0000 movs r0, r0

e300: 8d22 ldrh r2, [r4, #40] ; 0x28

e302: 0000 movs r0, r0

e304: 8ef4 ldrh r4, [r6, #54] ; 0x36

e306: 0000 movs r0, r0

e308: b5f8 push {r3, r4, r5, r6, r7, lr}

e30a: 4616 mov r6, r2

e30c: 680a ldr r2, [r1, #0]

e30e: 1b84 subs r4, r0, r6

e310: 460d mov r5, r1

e312: 4294 cmp r4, r2

e314: d307 bcc.n e326 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x90e>

e316: 6849 ldr r1, [r1, #4]

e318: b1f1 cbz r1, e358 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x940>

e31a: 4432 add r2, r6

e31c: f7ff fff4 bl e308 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8f0>

e320: b9c0 cbnz r0, e354 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x93c>

e322: 682c ldr r4, [r5, #0]

e324: e019 b.n e35a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x942>

e326: eb05 03c4 add.w r3, r5, r4, lsl #3

e32a: 4601 mov r1, r0

e32c: 68da ldr r2, [r3, #12]

e32e: b15a cbz r2, e348 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x930>

e330: f8d2 2248 ldr.w r2, [r2, #584] ; 0x248

e334: 4282 cmp r2, r0

e336: d12e bne.n e396 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x97e>

e338: 4a20 ldr r2, [pc, #128] ; (e3bc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9a4>)

e33a: 2000 movs r0, #0

e33c: 447a add r2, pc

e33e: f8d2 2828 ldr.w r2, [r2, #2088] ; 0x828

e342: 60d8 str r0, [r3, #12]

e344: 3201 adds r2, #1

e346: 609a str r2, [r3, #8]

e348: 4b1d ldr r3, [pc, #116] ; (e3c0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9a8>)

e34a: 447b add r3, pc

e34c: f8d3 380c ldr.w r3, [r3, #2060] ; 0x80c

e350: 428b cmp r3, r1

e352: d002 beq.n e35a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x942>

e354: 2001 movs r0, #1

e356: bdf8 pop {r3, r4, r5, r6, r7, pc}

e358: bb33 cbnz r3, e3a8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x990>

e35a: 481a ldr r0, [pc, #104] ; (e3c4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9ac>)

e35c: 2e00 cmp r6, #0

e35e: 4623 mov r3, r4

e360: f106 32ff add.w r2, r6, #4294967295 ; 0xffffffff

e364: 4478 add r0, pc

e366: f105 010c add.w r1, r5, #12

e36a: f8d0 4814 ldr.w r4, [r0, #2068] ; 0x814

e36e: f104 0401 add.w r4, r4, #1

e372: bf18 it ne

e374: 2400 movne r4, #0

e376: e004 b.n e382 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x96a>

e378: 18d5 adds r5, r2, r3

e37a: 3b01 subs r3, #1

e37c: f851 0033 ldr.w r0, [r1, r3, lsl #3]

e380: b918 cbnz r0, e38a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x972>

e382: 42a3 cmp r3, r4

e384: d8f8 bhi.n e378 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x960>

e386: 2000 movs r0, #0

e388: bdf8 pop {r3, r4, r5, r6, r7, pc}

e38a: 4b0f ldr r3, [pc, #60] ; (e3c8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9b0>)

e38c: 2001 movs r0, #1

e38e: 447b add r3, pc

e390: f8c3 580c str.w r5, [r3, #2060] ; 0x80c

e394: bdf8 pop {r3, r4, r5, r6, r7, pc}

e396: 4b0d ldr r3, [pc, #52] ; (e3cc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9b4>)

e398: 2250 movs r2, #80 ; 0x50

e39a: 490d ldr r1, [pc, #52] ; (e3d0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9b8>)

e39c: 480d ldr r0, [pc, #52] ; (e3d4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9bc>)

e39e: 447b add r3, pc

e3a0: 4479 add r1, pc

e3a2: 4478 add r0, pc

e3a4: f002 faf2 bl 1098c <free@@GLIBC\_2.4+0x12c>

e3a8: 4b0b ldr r3, [pc, #44] ; (e3d8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9c0>)

e3aa: 223b movs r2, #59 ; 0x3b

e3ac: 490b ldr r1, [pc, #44] ; (e3dc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9c4>)

e3ae: 480c ldr r0, [pc, #48] ; (e3e0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9c8>)

e3b0: 447b add r3, pc

e3b2: 4479 add r1, pc

e3b4: 4478 add r0, pc

e3b6: f002 fae9 bl 1098c <free@@GLIBC\_2.4+0x12c>

e3ba: bf00 nop

e3bc: ad20 add r5, sp, #128 ; 0x80

e3be: 0001 movs r1, r0

e3c0: ad12 add r5, sp, #72 ; 0x48

e3c2: 0001 movs r1, r0

e3c4: acf8 add r4, sp, #992 ; 0x3e0

e3c6: 0001 movs r1, r0

e3c8: acce add r4, sp, #824 ; 0x338

e3ca: 0001 movs r1, r0

e3cc: 904e str r0, [sp, #312] ; 0x138

e3ce: 0000 movs r0, r0

e3d0: 8e3c ldrh r4, [r7, #48] ; 0x30

e3d2: 0000 movs r0, r0

e3d4: 8e5a ldrh r2, [r3, #50] ; 0x32

e3d6: 0000 movs r0, r0

e3d8: 903c str r0, [sp, #240] ; 0xf0

e3da: 0000 movs r0, r0

e3dc: 8e2a ldrh r2, [r5, #48] ; 0x30

e3de: 0000 movs r0, r0

e3e0: 8e34 ldrh r4, [r6, #48] ; 0x30

e3e2: 0000 movs r0, r0

e3e4: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

e3e8: b09f sub sp, #124 ; 0x7c

e3ea: f8d0 2194 ldr.w r2, [r0, #404] ; 0x194

e3ee: af00 add r7, sp, #0

e3f0: 3a01 subs r2, #1

e3f2: 6338 str r0, [r7, #48] ; 0x30

e3f4: 64f9 str r1, [r7, #76] ; 0x4c

e3f6: f8c0 2194 str.w r2, [r0, #404] ; 0x194

e3fa: 2a00 cmp r2, #0

e3fc: f040 81fe bne.w e7fc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xde4>

e400: f890 3198 ldrb.w r3, [r0, #408] ; 0x198

e404: f003 0303 and.w r3, r3, #3

e408: 2b02 cmp r3, #2

e40a: f040 81f7 bne.w e7fc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xde4>

e40e: f8df 1ae0 ldr.w r1, [pc, #2784] ; eef0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14d8>

e412: 4479 add r1, pc

e414: 6808 ldr r0, [r1, #0]

e416: 2800 cmp r0, #0

e418: f040 81ef bne.w e7fa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xde2>

e41c: 6b3b ldr r3, [r7, #48] ; 0x30

e41e: 244c movs r4, #76 ; 0x4c

e420: f8df 2ad0 ldr.w r2, [pc, #2768] ; eef4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14dc>

e424: f04f 0b01 mov.w fp, #1

e428: 6239 str r1, [r7, #32]

e42a: f8d3 a018 ldr.w sl, [r3, #24]

e42e: 447a add r2, pc

e430: f8df 3ac4 ldr.w r3, [pc, #2756] ; eef8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14e0>

e434: f1ba 0f00 cmp.w sl, #0

e438: 60bc str r4, [r7, #8]

e43a: fb04 f00a mul.w r0, r4, sl

e43e: 447b add r3, pc

e440: bf0c ite eq

e442: 2504 moveq r5, #4

e444: 2500 movne r5, #0

e446: f8c7 a06c str.w sl, [r7, #108] ; 0x6c

e44a: f100 0110 add.w r1, r0, #16

e44e: 60fd str r5, [r7, #12]

e450: 4419 add r1, r3

e452: 6479 str r1, [r7, #68] ; 0x44

e454: 6278 str r0, [r7, #36] ; 0x24

e456: 1819 adds r1, r3, r0

e458: 4613 mov r3, r2

e45a: 61f9 str r1, [r7, #28]

e45c: 4403 add r3, r0

e45e: 61b9 str r1, [r7, #24]

e460: 607b str r3, [r7, #4]

e462: 69fb ldr r3, [r7, #28]

e464: f8c7 d028 str.w sp, [r7, #40] ; 0x28

e468: f8d3 9004 ldr.w r9, [r3, #4]

e46c: 6a3b ldr r3, [r7, #32]

e46e: 461a mov r2, r3

e470: 2301 movs r3, #1

e472: 6013 str r3, [r2, #0]

e474: f109 0207 add.w r2, r9, #7

e478: f022 0207 bic.w r2, r2, #7

e47c: ea4f 0389 mov.w r3, r9, lsl #2

e480: ebad 0d02 sub.w sp, sp, r2

e484: 3307 adds r3, #7

e486: f023 0307 bic.w r3, r3, #7

e48a: 46ea mov sl, sp

e48c: ebad 0d02 sub.w sp, sp, r2

e490: 466c mov r4, sp

e492: ebad 0d03 sub.w sp, sp, r3

e496: 6cfb ldr r3, [r7, #76] ; 0x4c

e498: f8c7 d060 str.w sp, [r7, #96] ; 0x60

e49c: b133 cbz r3, e4ac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xa94>

e49e: 6b3a ldr r2, [r7, #48] ; 0x30

e4a0: f8d2 320c ldr.w r3, [r2, #524] ; 0x20c

e4a4: f023 0308 bic.w r3, r3, #8

e4a8: f8c2 320c str.w r3, [r2, #524] ; 0x20c

e4ac: 69bb ldr r3, [r7, #24]

e4ae: 681b ldr r3, [r3, #0]

e4b0: 2b00 cmp r3, #0

e4b2: f000 83df beq.w ec74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x125c>

e4b6: 6e3a ldr r2, [r7, #96] ; 0x60

e4b8: 1f11 subs r1, r2, #4

e4ba: 2200 movs r2, #0

e4bc: f8c3 2214 str.w r2, [r3, #532] ; 0x214

e4c0: 3201 adds r2, #1

e4c2: f841 3f04 str.w r3, [r1, #4]!

e4c6: 68db ldr r3, [r3, #12]

e4c8: 2b00 cmp r3, #0

e4ca: d1f7 bne.n e4bc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xaa4>

e4cc: 4591 cmp r9, r2

e4ce: f040 84ed bne.w eeac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1494>

e4d2: 464a mov r2, r9

e4d4: 2100 movs r1, #0

e4d6: 4650 mov r0, sl

e4d8: f004 edba blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

e4dc: 464a mov r2, r9

e4de: 2100 movs r1, #0

e4e0: 4620 mov r0, r4

e4e2: f004 edb6 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

e4e6: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

e4ea: 4694 mov ip, r2

e4ec: 3201 adds r2, #1

e4ee: 4591 cmp r9, r2

e4f0: d942 bls.n e578 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb60>

e4f2: 5ca3 ldrb r3, [r4, r2]

e4f4: 2b00 cmp r3, #0

e4f6: d1f9 bne.n e4ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xad4>

e4f8: 6e3b ldr r3, [r7, #96] ; 0x60

e4fa: f853 0022 ldr.w r0, [r3, r2, lsl #2]

e4fe: f890 3198 ldrb.w r3, [r0, #408] ; 0x198

e502: f003 0303 and.w r3, r3, #3

e506: 2b02 cmp r3, #2

e508: f000 81ac beq.w e864 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe4c>

e50c: f80a b002 strb.w fp, [sl, r2]

e510: f8d0 11f8 ldr.w r1, [r0, #504] ; 0x1f8

e514: f804 b002 strb.w fp, [r4, r2]

e518: f8c0 c214 str.w ip, [r0, #532] ; 0x214

e51c: 2900 cmp r1, #0

e51e: f040 8178 bne.w e812 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdfa>

e522: f8d0 51fc ldr.w r5, [r0, #508] ; 0x1fc

e526: 2d00 cmp r5, #0

e528: d0e0 beq.n e4ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xad4>

e52a: 682b ldr r3, [r5, #0]

e52c: 2b00 cmp r3, #0

e52e: d0dd beq.n e4ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xad4>

e530: 2100 movs r1, #0

e532: 46a6 mov lr, r4

e534: eb05 0381 add.w r3, r5, r1, lsl #2

e538: 685e ldr r6, [r3, #4]

e53a: f8d6 3214 ldr.w r3, [r6, #532] ; 0x214

e53e: 1c5c adds r4, r3, #1

e540: d012 beq.n e568 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb50>

e542: 2b00 cmp r3, #0

e544: f2c0 8386 blt.w ec54 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x123c>

e548: 4599 cmp r9, r3

e54a: f240 8383 bls.w ec54 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x123c>

e54e: f81a 4003 ldrb.w r4, [sl, r3]

e552: b94c cbnz r4, e568 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb50>

e554: f80a b003 strb.w fp, [sl, r3]

e558: f8d6 3214 ldr.w r3, [r6, #532] ; 0x214

e55c: f8d0 51fc ldr.w r5, [r0, #508] ; 0x1fc

e560: 4293 cmp r3, r2

e562: bfd8 it le

e564: f103 32ff addle.w r2, r3, #4294967295 ; 0xffffffff

e568: 682b ldr r3, [r5, #0]

e56a: 3101 adds r1, #1

e56c: 4299 cmp r1, r3

e56e: d3e1 bcc.n e534 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb1c>

e570: 3201 adds r2, #1

e572: 4674 mov r4, lr

e574: 4591 cmp r9, r2

e576: d8bc bhi.n e4f2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xada>

e578: 6efb ldr r3, [r7, #108] ; 0x6c

e57a: 6e3a ldr r2, [r7, #96] ; 0x60

e57c: fab3 f183 clz r1, r3

e580: 68f8 ldr r0, [r7, #12]

e582: 2301 movs r3, #1

e584: 0949 lsrs r1, r1, #5

e586: 1810 adds r0, r2, r0

e588: eb0a 0201 add.w r2, sl, r1

e58c: eba9 0101 sub.w r1, r9, r1

e590: f000 fed0 bl f334 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x14>

e594: f8df 3964 ldr.w r3, [pc, #2404] ; eefc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14e4>

e598: 447b add r3, pc

e59a: f8d3 31b0 ldr.w r3, [r3, #432] ; 0x1b0

e59e: 2b00 cmp r3, #0

e5a0: f000 81f9 beq.w e996 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf7e>

e5a4: 687b ldr r3, [r7, #4]

e5a6: 681b ldr r3, [r3, #0]

e5a8: f893 3199 ldrb.w r3, [r3, #409] ; 0x199

e5ac: f083 0308 eor.w r3, r3, #8

e5b0: f3c3 03c0 ubfx r3, r3, #3, #1

e5b4: 65bb str r3, [r7, #88] ; 0x58

e5b6: f1b9 0f00 cmp.w r9, #0

e5ba: f000 8114 beq.w e7e6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdce>

e5be: 6e39 ldr r1, [r7, #96] ; 0x60

e5c0: 6efa ldr r2, [r7, #108] ; 0x6c

e5c2: 680d ldr r5, [r1, #0]

e5c4: 69ab ldr r3, [r5, #24]

e5c6: 429a cmp r2, r3

e5c8: f040 83e5 bne.w ed96 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x137e>

e5cc: f109 33ff add.w r3, r9, #4294967295 ; 0xffffffff

e5d0: 2200 movs r2, #0

e5d2: 4453 add r3, sl

e5d4: 66bb str r3, [r7, #104] ; 0x68

e5d6: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

e5da: 673b str r3, [r7, #112] ; 0x70

e5dc: f1ca 0301 rsb r3, sl, #1

e5e0: 667b str r3, [r7, #100] ; 0x64

e5e2: f8df 391c ldr.w r3, [pc, #2332] ; ef00 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14e8>

e5e6: f10a 36ff add.w r6, sl, #4294967295 ; 0xffffffff

e5ea: 657a str r2, [r7, #84] ; 0x54

e5ec: 460c mov r4, r1

e5ee: 447b add r3, pc

e5f0: 65fa str r2, [r7, #92] ; 0x5c

e5f2: 637a str r2, [r7, #52] ; 0x34

e5f4: 62fb str r3, [r7, #44] ; 0x2c

e5f6: e9c7 a904 strd sl, r9, [r7, #16]

e5fa: e040 b.n e67e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc66>

e5fc: 2902 cmp r1, #2

e5fe: f040 83d4 bne.w edaa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1392>

e602: f8d5 220c ldr.w r2, [r5, #524] ; 0x20c

e606: f012 0808 ands.w r8, r2, #8

e60a: f040 83ce bne.w edaa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1392>

e60e: 0719 lsls r1, r3, #28

e610: d511 bpl.n e636 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc1e>

e612: f8df 38f0 ldr.w r3, [pc, #2288] ; ef04 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14ec>

e616: 447b add r3, pc

e618: 681b ldr r3, [r3, #0]

e61a: 079a lsls r2, r3, #30

e61c: f100 817f bmi.w e91e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf06>

e620: f8d5 3088 ldr.w r3, [r5, #136] ; 0x88

e624: 2b00 cmp r3, #0

e626: f040 8134 bne.w e892 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe7a>

e62a: 6d6b ldr r3, [r5, #84] ; 0x54

e62c: b11b cbz r3, e636 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc1e>

e62e: 685b ldr r3, [r3, #4]

e630: 682a ldr r2, [r5, #0]

e632: 4413 add r3, r2

e634: 4798 blx r3

e636: 6dbb ldr r3, [r7, #88] ; 0x58

e638: 2b00 cmp r3, #0

e63a: f040 8147 bne.w e8cc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xeb4>

e63e: f895 3199 ldrb.w r3, [r5, #409] ; 0x199

e642: f895 2198 ldrb.w r2, [r5, #408] ; 0x198

e646: f043 0320 orr.w r3, r3, #32

e64a: f885 3199 strb.w r3, [r5, #409] ; 0x199

e64e: 06d3 lsls r3, r2, #27

e650: d502 bpl.n e658 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc40>

e652: 6d7b ldr r3, [r7, #84] ; 0x54

e654: 3301 adds r3, #1

e656: 657b str r3, [r7, #84] ; 0x54

e658: e9d7 321c ldrd r3, r2, [r7, #112] ; 0x70

e65c: 4293 cmp r3, r2

e65e: bf28 it cs

e660: 4613 movcs r3, r2

e662: 673b str r3, [r7, #112] ; 0x70

e664: 2301 movs r3, #1

e666: 65fb str r3, [r7, #92] ; 0x5c

e668: 6ebb ldr r3, [r7, #104] ; 0x68

e66a: 42b3 cmp r3, r6

e66c: f000 80b5 beq.w e7da <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdc2>

e670: f854 5f04 ldr.w r5, [r4, #4]!

e674: 6efa ldr r2, [r7, #108] ; 0x6c

e676: 69ab ldr r3, [r5, #24]

e678: 429a cmp r2, r3

e67a: f040 838c bne.w ed96 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x137e>

e67e: 6e7b ldr r3, [r7, #100] ; 0x64

e680: 4433 add r3, r6

e682: f816 2f01 ldrb.w r2, [r6, #1]!

e686: 677b str r3, [r7, #116] ; 0x74

e688: f895 3198 ldrb.w r3, [r5, #408] ; 0x198

e68c: f003 0103 and.w r1, r3, #3

e690: 2a00 cmp r2, #0

e692: d0b3 beq.n e5fc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xbe4>

e694: 2902 cmp r1, #2

e696: d1e7 bne.n e668 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc50>

e698: f8d5 3160 ldr.w r3, [r5, #352] ; 0x160

e69c: 2b00 cmp r3, #0

e69e: f000 8146 beq.w e92e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf16>

e6a2: f8d5 31d0 ldr.w r3, [r5, #464] ; 0x1d0

e6a6: 681a ldr r2, [r3, #0]

e6a8: 653b str r3, [r7, #80] ; 0x50

e6aa: 2a00 cmp r2, #0

e6ac: f000 8081 beq.w e7b2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd9a>

e6b0: f04f 0c01 mov.w ip, #1

e6b4: 2300 movs r3, #0

e6b6: 64bb str r3, [r7, #72] ; 0x48

e6b8: 6d38 ldr r0, [r7, #80] ; 0x50

e6ba: f505 78b4 add.w r8, r5, #360 ; 0x168

e6be: 4611 mov r1, r2

e6c0: f04f 0900 mov.w r9, #0

e6c4: f8d7 e06c ldr.w lr, [r7, #108] ; 0x6c

e6c8: 4682 mov sl, r0

e6ca: e00f b.n e6ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xcd4>

e6cc: f5a1 71b0 sub.w r1, r1, #352 ; 0x160

e6d0: 698b ldr r3, [r1, #24]

e6d2: 459e cmp lr, r3

e6d4: f040 83f3 bne.w eebe <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14a6>

e6d8: f8d1 3214 ldr.w r3, [r1, #532] ; 0x214

e6dc: 3301 adds r3, #1

e6de: bf18 it ne

e6e0: f04f 0901 movne.w r9, #1

e6e4: d004 beq.n e6f0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xcd8>

e6e6: f850 1f04 ldr.w r1, [r0, #4]!

e6ea: b139 cbz r1, e6fc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xce4>

e6ec: 4541 cmp r1, r8

e6ee: d1ed bne.n e6cc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xcb4>

e6f0: f850 1f04 ldr.w r1, [r0, #4]!

e6f4: f10c 0c01 add.w ip, ip, #1

e6f8: 2900 cmp r1, #0

e6fa: d1f7 bne.n e6ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xcd4>

e6fc: f1b9 0f00 cmp.w r9, #0

e700: f000 8105 beq.w e90e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xef6>

e704: 6d39 ldr r1, [r7, #80] ; 0x50

e706: f505 73de add.w r3, r5, #444 ; 0x1bc

e70a: 1a59 subs r1, r3, r1

e70c: bf18 it ne

e70e: 2101 movne r1, #1

e710: f1bc 0f03 cmp.w ip, #3

e714: bf88 it hi

e716: 2100 movhi r1, #0

e718: 2900 cmp r1, #0

e71a: f040 8116 bne.w e94a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf32>

e71e: e9c7 320f strd r3, r2, [r7, #60] ; 0x3c

e722: f8d5 31cc ldr.w r3, [r5, #460] ; 0x1cc

e726: 0098 lsls r0, r3, #2

e728: 63bb str r3, [r7, #56] ; 0x38

e72a: f7f2 e950 blx 9cc <malloc@plt+0x4>

e72e: e9d7 320f ldrd r3, r2, [r7, #60] ; 0x3c

e732: 2800 cmp r0, #0

e734: f000 83a6 beq.w ee84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x146c>

e738: e9c7 350f strd r3, r5, [r7, #60] ; 0x3c

e73c: f04f 0e00 mov.w lr, #0

e740: 6cbd ldr r5, [r7, #72] ; 0x48

e742: e00b b.n e75c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd44>

e744: b13d cbz r5, e756 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd3e>

e746: 3104 adds r1, #4

e748: f8cc 5000 str.w r5, [ip]

e74c: f10e 0e01 add.w lr, lr, #1

e750: eb00 0c01 add.w ip, r0, r1

e754: 2500 movs r5, #0

e756: f85a 2f04 ldr.w r2, [sl, #4]!

e75a: b1a2 cbz r2, e786 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd6e>

e75c: 4542 cmp r2, r8

e75e: ea4f 018e mov.w r1, lr, lsl #2

e762: eb00 0c01 add.w ip, r0, r1

e766: d003 beq.n e770 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd58>

e768: f8d2 30b4 ldr.w r3, [r2, #180] ; 0xb4

e76c: 3301 adds r3, #1

e76e: d1e9 bne.n e744 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd2c>

e770: f8cc 2000 str.w r2, [ip]

e774: 3104 adds r1, #4

e776: f85a 2f04 ldr.w r2, [sl, #4]!

e77a: f10e 0e01 add.w lr, lr, #1

e77e: eb00 0c01 add.w ip, r0, r1

e782: 2a00 cmp r2, #0

e784: d1ea bne.n e75c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd44>

e786: e9d7 350f ldrd r3, r5, [r7, #60] ; 0x3c

e78a: f8cc 2000 str.w r2, [ip]

e78e: f8c5 01d0 str.w r0, [r5, #464] ; 0x1d0

e792: 6d38 ldr r0, [r7, #80] ; 0x50

e794: 4283 cmp r3, r0

e796: bf08 it eq

e798: f8c7 9034 streq.w r9, [r7, #52] ; 0x34

e79c: d006 beq.n e7ac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd94>

e79e: f7ff f803 bl d7a8 <\_\_tls\_get\_addr@@GLIBC\_2.4+0x268>

e7a2: 6b7b ldr r3, [r7, #52] ; 0x34

e7a4: 2800 cmp r0, #0

e7a6: bf18 it ne

e7a8: 2300 movne r3, #0

e7aa: 637b str r3, [r7, #52] ; 0x34

e7ac: 6bbb ldr r3, [r7, #56] ; 0x38

e7ae: f8c5 31cc str.w r3, [r5, #460] ; 0x1cc

e7b2: f8d5 3170 ldr.w r3, [r5, #368] ; 0x170

e7b6: b133 cbz r3, e7c6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdae>

e7b8: f8d3 3214 ldr.w r3, [r3, #532] ; 0x214

e7bc: 3301 adds r3, #1

e7be: bf1c itt ne

e7c0: 2300 movne r3, #0

e7c2: f8c5 3170 strne.w r3, [r5, #368] ; 0x170

e7c6: e9d7 321c ldrd r3, r2, [r7, #112] ; 0x70

e7ca: 4293 cmp r3, r2

e7cc: bf28 it cs

e7ce: 4613 movcs r3, r2

e7d0: 673b str r3, [r7, #112] ; 0x70

e7d2: 6ebb ldr r3, [r7, #104] ; 0x68

e7d4: 42b3 cmp r3, r6

e7d6: f47f af4b bne.w e670 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc58>

e7da: 6dfb ldr r3, [r7, #92] ; 0x5c

e7dc: e9d7 a904 ldrd sl, r9, [r7, #16]

e7e0: 2b00 cmp r3, #0

e7e2: f040 80da bne.w e99a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf82>

e7e6: f8df 3720 ldr.w r3, [pc, #1824] ; ef08 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14f0>

e7ea: 447b add r3, pc

e7ec: 681a ldr r2, [r3, #0]

e7ee: 2a02 cmp r2, #2

e7f0: f040 82e5 bne.w edbe <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x13a6>

e7f4: f8d7 d028 ldr.w sp, [r7, #40] ; 0x28

e7f8: e633 b.n e462 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xa4a>

e7fa: 600b str r3, [r1, #0]

e7fc: f8df 370c ldr.w r3, [pc, #1804] ; ef0c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14f4>

e800: 447b add r3, pc

e802: 681b ldr r3, [r3, #0]

e804: 065b lsls r3, r3, #25

e806: f100 822e bmi.w ec66 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x124e>

e80a: 377c adds r7, #124 ; 0x7c

e80c: 46bd mov sp, r7

e80e: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

e812: 684b ldr r3, [r1, #4]

e814: 3104 adds r1, #4

e816: b99b cbnz r3, e840 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe28>

e818: e683 b.n e522 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb0a>

e81a: 4599 cmp r9, r3

e81c: d916 bls.n e84c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe34>

e81e: f81a 5003 ldrb.w r5, [sl, r3]

e822: b945 cbnz r5, e836 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe1e>

e824: f80a b003 strb.w fp, [sl, r3]

e828: 680b ldr r3, [r1, #0]

e82a: f8d3 3214 ldr.w r3, [r3, #532] ; 0x214

e82e: 4293 cmp r3, r2

e830: bfd8 it le

e832: f103 32ff addle.w r2, r3, #4294967295 ; 0xffffffff

e836: f851 3f04 ldr.w r3, [r1, #4]!

e83a: 2b00 cmp r3, #0

e83c: f43f ae71 beq.w e522 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb0a>

e840: f8d3 3214 ldr.w r3, [r3, #532] ; 0x214

e844: 1c5d adds r5, r3, #1

e846: d0f6 beq.n e836 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe1e>

e848: 2b00 cmp r3, #0

e84a: dae6 bge.n e81a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xe02>

e84c: f8df 36c0 ldr.w r3, [pc, #1728] ; ef10 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14f8>

e850: 22cf movs r2, #207 ; 0xcf

e852: f8df 16c0 ldr.w r1, [pc, #1728] ; ef14 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14fc>

e856: f8df 06c0 ldr.w r0, [pc, #1728] ; ef18 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1500>

e85a: 447b add r3, pc

e85c: 4479 add r1, pc

e85e: 4478 add r0, pc

e860: f002 f894 bl 1098c <free@@GLIBC\_2.4+0x12c>

e864: f8d0 3194 ldr.w r3, [r0, #404] ; 0x194

e868: 2b00 cmp r3, #0

e86a: f47f ae4f bne.w e50c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xaf4>

e86e: f8d0 320c ldr.w r3, [r0, #524] ; 0x20c

e872: 071e lsls r6, r3, #28

e874: f53f ae4a bmi.w e50c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xaf4>

e878: f8d0 324c ldr.w r3, [r0, #588] ; 0x24c

e87c: f3bf 8f5b dmb ish

e880: 2b00 cmp r3, #0

e882: f47f ae43 bne.w e50c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xaf4>

e886: f81a 3002 ldrb.w r3, [sl, r2]

e88a: 2b00 cmp r3, #0

e88c: f47f ae3e bne.w e50c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xaf4>

e890: e62c b.n e4ec <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xad4>

e892: f8d5 1090 ldr.w r1, [r5, #144] ; 0x90

e896: 6858 ldr r0, [r3, #4]

e898: 682a ldr r2, [r5, #0]

e89a: 684b ldr r3, [r1, #4]

e89c: 4402 add r2, r0

e89e: ea5f 0a93 movs.w sl, r3, lsr #2

e8a2: f43f aec2 beq.w e62a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc12>

e8a6: f10a 4a80 add.w sl, sl, #1073741824 ; 0x40000000

e8aa: 65fc str r4, [r7, #92] ; 0x5c

e8ac: f10a 3aff add.w sl, sl, #4294967295 ; 0xffffffff

e8b0: eb02 0a8a add.w sl, r2, sl, lsl #2

e8b4: 4654 mov r4, sl

e8b6: 4692 mov sl, r2

e8b8: e000 b.n e8bc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xea4>

e8ba: 464c mov r4, r9

e8bc: 46a1 mov r9, r4

e8be: f859 2904 ldr.w r2, [r9], #-4

e8c2: 4790 blx r2

e8c4: 45a2 cmp sl, r4

e8c6: d1f8 bne.n e8ba <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xea2>

e8c8: 6dfc ldr r4, [r7, #92] ; 0x5c

e8ca: e6ae b.n e62a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc12>

e8cc: 6afa ldr r2, [r7, #44] ; 0x2c

e8ce: f8d2 31b0 ldr.w r3, [r2, #432] ; 0x1b0

e8d2: f8d2 a1ac ldr.w sl, [r2, #428] ; 0x1ac

e8d6: 2b00 cmp r3, #0

e8d8: f43f aeb1 beq.w e63e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc26>

e8dc: f8df 963c ldr.w r9, [pc, #1596] ; ef1c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1504>

e8e0: f505 7318 add.w r3, r5, #608 ; 0x260

e8e4: 65fd str r5, [r7, #92] ; 0x5c

e8e6: 44f9 add r9, pc

e8e8: 464d mov r5, r9

e8ea: 46a1 mov r9, r4

e8ec: 4654 mov r4, sl

e8ee: 469a mov sl, r3

e8f0: 69e3 ldr r3, [r4, #28]

e8f2: b113 cbz r3, e8fa <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xee2>

e8f4: eb0a 00c8 add.w r0, sl, r8, lsl #3

e8f8: 4798 blx r3

e8fa: f8d5 31b0 ldr.w r3, [r5, #432] ; 0x1b0

e8fe: f108 0801 add.w r8, r8, #1

e902: 6a24 ldr r4, [r4, #32]

e904: 4598 cmp r8, r3

e906: d3f3 bcc.n e8f0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xed8>

e908: 464c mov r4, r9

e90a: 6dfd ldr r5, [r7, #92] ; 0x5c

e90c: e697 b.n e63e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc26>

e90e: 6cbb ldr r3, [r7, #72] ; 0x48

e910: 2b00 cmp r3, #0

e912: f43f af4e beq.w e7b2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd9a>

e916: 2300 movs r3, #0

e918: e9c5 3358 strd r3, r3, [r5, #352] ; 0x160

e91c: e749 b.n e7b2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd9a>

e91e: f8df 0600 ldr.w r0, [pc, #1536] ; ef20 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1508>

e922: 6efa ldr r2, [r7, #108] ; 0x6c

e924: 6869 ldr r1, [r5, #4]

e926: 4478 add r0, pc

e928: f7fd fa24 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

e92c: e678 b.n e620 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xc08>

e92e: f8d5 31f8 ldr.w r3, [r5, #504] ; 0x1f8

e932: 64bb str r3, [r7, #72] ; 0x48

e934: b96b cbnz r3, e952 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf3a>

e936: f8d5 31d0 ldr.w r3, [r5, #464] ; 0x1d0

e93a: f04f 0c01 mov.w ip, #1

e93e: 681a ldr r2, [r3, #0]

e940: 653b str r3, [r7, #80] ; 0x50

e942: 2a00 cmp r2, #0

e944: f47f aeb8 bne.w e6b8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xca0>

e948: e733 b.n e7b2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd9a>

e94a: 2104 movs r1, #4

e94c: 4618 mov r0, r3

e94e: 63b9 str r1, [r7, #56] ; 0x38

e950: e6f2 b.n e738 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xd20>

e952: 6cba ldr r2, [r7, #72] ; 0x48

e954: 6853 ldr r3, [r2, #4]

e956: 2b00 cmp r3, #0

e958: f000 81f7 beq.w ed4a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1332>

e95c: 1d11 adds r1, r2, #4

e95e: 2301 movs r3, #1

e960: e000 b.n e964 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf4c>

e962: 4613 mov r3, r2

e964: f851 0f04 ldr.w r0, [r1, #4]!

e968: 1c5a adds r2, r3, #1

e96a: 2800 cmp r0, #0

e96c: d1f9 bne.n e962 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf4a>

e96e: 3302 adds r3, #2

e970: 009b lsls r3, r3, #2

e972: f8d5 11d0 ldr.w r1, [r5, #464] ; 0x1d0

e976: 6cb8 ldr r0, [r7, #72] ; 0x48

e978: f8c5 2164 str.w r2, [r5, #356] ; 0x164

e97c: f505 72b0 add.w r2, r5, #352 ; 0x160

e980: 64ba str r2, [r7, #72] ; 0x48

e982: 4418 add r0, r3

e984: 680a ldr r2, [r1, #0]

e986: 6539 str r1, [r7, #80] ; 0x50

e988: f8c5 0160 str.w r0, [r5, #352] ; 0x160

e98c: 2a00 cmp r2, #0

e98e: d0c2 beq.n e916 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xefe>

e990: f04f 0c02 mov.w ip, #2

e994: e690 b.n e6b8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xca0>

e996: 65bb str r3, [r7, #88] ; 0x58

e998: e60d b.n e5b6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xb9e>

e99a: 6dbb ldr r3, [r7, #88] ; 0x58

e99c: 2b00 cmp r3, #0

e99e: f040 8213 bne.w edc8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x13b0>

e9a2: 6ef9 ldr r1, [r7, #108] ; 0x6c

e9a4: 2000 movs r0, #0

e9a6: f7fd f81d bl b9e4 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x4>

e9aa: 2302 movs r3, #2

e9ac: 60c3 str r3, [r0, #12]

e9ae: 6538 str r0, [r7, #80] ; 0x50

e9b0: f7fd f816 bl b9e0 <\_dl\_debug\_state@@GLIBC\_PRIVATE>

e9b4: 6d7b ldr r3, [r7, #84] ; 0x54

e9b6: ee1d ef70 mrc 15, 0, lr, cr13, cr0, {3}

e9ba: 2b00 cmp r3, #0

e9bc: f040 8183 bne.w ecc6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12ae>

e9c0: f5ae 6e98 sub.w lr, lr, #1216 ; 0x4c0

e9c4: f8de 3000 ldr.w r3, [lr]

e9c8: 2b00 cmp r3, #0

e9ca: f040 8241 bne.w ee50 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1438>

e9ce: f8df 4554 ldr.w r4, [pc, #1364] ; ef24 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x150c>

e9d2: 447c add r4, pc

e9d4: f204 45dc addw r5, r4, #1244 ; 0x4dc

e9d8: f8d4 37f8 ldr.w r3, [r4, #2040] ; 0x7f8

e9dc: 4628 mov r0, r5

e9de: 4798 blx r3

e9e0: 6f3b ldr r3, [r7, #112] ; 0x70

e9e2: 4599 cmp r9, r3

e9e4: f240 827f bls.w eee6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14ce>

e9e8: 6f3a ldr r2, [r7, #112] ; 0x70

e9ea: 6e39 ldr r1, [r7, #96] ; 0x60

e9ec: 1e55 subs r5, r2, #1

e9ee: f8df 3538 ldr.w r3, [pc, #1336] ; ef28 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1510>

e9f2: eb01 0682 add.w r6, r1, r2, lsl #2

e9f6: 6a7a ldr r2, [r7, #36] ; 0x24

e9f8: f8df 9530 ldr.w r9, [pc, #1328] ; ef2c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1514>

e9fc: 4455 add r5, sl

e9fe: f8d7 8068 ldr.w r8, [r7, #104] ; 0x68

ea02: 447b add r3, pc

ea04: 44f9 add r9, pc

ea06: f8c7 9074 str.w r9, [r7, #116] ; 0x74

ea0a: 4691 mov r9, r2

ea0c: 4413 add r3, r2

ea0e: 64bb str r3, [r7, #72] ; 0x48

ea10: 2300 movs r3, #0

ea12: 663b str r3, [r7, #96] ; 0x60

ea14: 657b str r3, [r7, #84] ; 0x54

ea16: 667b str r3, [r7, #100] ; 0x64

ea18: f815 2f01 ldrb.w r2, [r5, #1]!

ea1c: 2a00 cmp r2, #0

ea1e: d179 bne.n eb14 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10fc>

ea20: 6834 ldr r4, [r6, #0]

ea22: f894 3198 ldrb.w r3, [r4, #408] ; 0x198

ea26: f003 0103 and.w r1, r3, #3

ea2a: 2902 cmp r1, #2

ea2c: f040 8251 bne.w eed2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14ba>

ea30: f8d4 1238 ldr.w r1, [r4, #568] ; 0x238

ea34: 2900 cmp r1, #0

ea36: f040 80cd bne.w ebd4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11bc>

ea3a: 6cfb ldr r3, [r7, #76] ; 0x4c

ea3c: b1a3 cbz r3, ea68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1050>

ea3e: f8df 24f0 ldr.w r2, [pc, #1264] ; ef30 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1518>

ea42: 6c78 ldr r0, [r7, #68] ; 0x44

ea44: 447a add r2, pc

ea46: 673a str r2, [r7, #112] ; 0x70

ea48: f8d2 37f8 ldr.w r3, [r2, #2040] ; 0x7f8

ea4c: 4798 blx r3

ea4e: 6f3a ldr r2, [r7, #112] ; 0x70

ea50: 444a add r2, r9

ea52: 6a93 ldr r3, [r2, #40] ; 0x28

ea54: 2b00 cmp r3, #0

ea56: f040 80a1 bne.w eb9c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1184>

ea5a: f8df 34d8 ldr.w r3, [pc, #1240] ; ef34 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x151c>

ea5e: 6c78 ldr r0, [r7, #68] ; 0x44

ea60: 447b add r3, pc

ea62: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

ea66: 4798 blx r3

ea68: 4620 mov r0, r4

ea6a: f001 fb53 bl 10114 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2d4>

ea6e: 6922 ldr r2, [r4, #16]

ea70: 2a00 cmp r2, #0

ea72: f000 8082 beq.w eb7a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1162>

ea76: 68e3 ldr r3, [r4, #12]

ea78: 60d3 str r3, [r2, #12]

ea7a: 68e1 ldr r1, [r4, #12]

ea7c: 6f7b ldr r3, [r7, #116] ; 0x74

ea7e: eb03 0009 add.w r0, r3, r9

ea82: 6843 ldr r3, [r0, #4]

ea84: 3b01 subs r3, #1

ea86: 6043 str r3, [r0, #4]

ea88: b101 cbz r1, ea8c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1074>

ea8a: 610a str r2, [r1, #16]

ea8c: f8d4 0174 ldr.w r0, [r4, #372] ; 0x174

ea90: f7f1 efc8 blx a24 <free@plt+0x4>

ea94: f8d4 01ac ldr.w r0, [r4, #428] ; 0x1ac

ea98: 1c43 adds r3, r0, #1

ea9a: d001 beq.n eaa0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1088>

ea9c: f7f1 efc2 blx a24 <free@plt+0x4>

eaa0: f8d4 01fc ldr.w r0, [r4, #508] ; 0x1fc

eaa4: f7f1 efbe blx a24 <free@plt+0x4>

eaa8: f8df 348c ldr.w r3, [pc, #1164] ; ef38 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1520>

eaac: 447b add r3, pc

eaae: 681b ldr r3, [r3, #0]

eab0: 0659 lsls r1, r3, #25

eab2: f100 80c5 bmi.w ec40 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1228>

eab6: 6860 ldr r0, [r4, #4]

eab8: f7f1 efb4 blx a24 <free@plt+0x4>

eabc: 69e0 ldr r0, [r4, #28]

eabe: e001 b.n eac4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10ac>

eac0: 4650 mov r0, sl

eac2: b140 cbz r0, ead6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10be>

eac4: e9d0 a301 ldrd sl, r3, [r0, #4]

eac8: 2b00 cmp r3, #0

eaca: d1f9 bne.n eac0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10a8>

eacc: f7f1 efaa blx a24 <free@plt+0x4>

ead0: 4650 mov r0, sl

ead2: 2800 cmp r0, #0

ead4: d1f6 bne.n eac4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10ac>

ead6: f8d4 01f8 ldr.w r0, [r4, #504] ; 0x1f8

eada: f7f1 efa4 blx a24 <free@plt+0x4>

eade: f8d4 01d0 ldr.w r0, [r4, #464] ; 0x1d0

eae2: f504 73de add.w r3, r4, #444 ; 0x1bc

eae6: 4298 cmp r0, r3

eae8: d001 beq.n eaee <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10d6>

eaea: f7f1 ef9c blx a24 <free@plt+0x4>

eaee: f994 3198 ldrsb.w r3, [r4, #408] ; 0x198

eaf2: 2b00 cmp r3, #0

eaf4: db4d blt.n eb92 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x117a>

eaf6: f8d4 019c ldr.w r0, [r4, #412] ; 0x19c

eafa: 1c42 adds r2, r0, #1

eafc: d001 beq.n eb02 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10ea>

eafe: f7f1 ef92 blx a24 <free@plt+0x4>

eb02: f8d4 01f0 ldr.w r0, [r4, #496] ; 0x1f0

eb06: 1c43 adds r3, r0, #1

eb08: d001 beq.n eb0e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10f6>

eb0a: f7f1 ef8c blx a24 <free@plt+0x4>

eb0e: 4620 mov r0, r4

eb10: f7f1 ef88 blx a24 <free@plt+0x4>

eb14: 45a8 cmp r8, r5

eb16: f106 0604 add.w r6, r6, #4

eb1a: f47f af7d bne.w ea18 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1000>

eb1e: f8df 441c ldr.w r4, [pc, #1052] ; ef3c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1524>

eb22: f8d7 8064 ldr.w r8, [r7, #100] ; 0x64

eb26: 447c add r4, pc

eb28: f204 40dc addw r0, r4, #1244 ; 0x4dc

eb2c: f8d4 37fc ldr.w r3, [r4, #2044] ; 0x7fc

eb30: 4798 blx r3

eb32: f1b8 0f00 cmp.w r8, #0

eb36: d00f beq.n eb58 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1140>

eb38: f8d4 3828 ldr.w r3, [r4, #2088] ; 0x828

eb3c: 3301 adds r3, #1

eb3e: f8c4 3828 str.w r3, [r4, #2088] ; 0x828

eb42: 2b00 cmp r3, #0

eb44: f000 8164 beq.w ee10 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x13f8>

eb48: f8d4 381c ldr.w r3, [r4, #2076] ; 0x81c

eb4c: 6e3a ldr r2, [r7, #96] ; 0x60

eb4e: 4293 cmp r3, r2

eb50: bf04 itt eq

eb52: 6d7b ldreq r3, [r7, #84] ; 0x54

eb54: f8c4 381c streq.w r3, [r4, #2076] ; 0x81c

eb58: 4bf9 ldr r3, [pc, #996] ; (ef40 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1528>)

eb5a: 6a7a ldr r2, [r7, #36] ; 0x24

eb5c: 447b add r3, pc

eb5e: 589c ldr r4, [r3, r2]

eb60: 6dbb ldr r3, [r7, #88] ; 0x58

eb62: 2b00 cmp r3, #0

eb64: f040 808c bne.w ec80 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1268>

eb68: 2c00 cmp r4, #0

eb6a: f000 8159 beq.w ee20 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1408>

eb6e: 6d3a ldr r2, [r7, #80] ; 0x50

eb70: 2300 movs r3, #0

eb72: 60d3 str r3, [r2, #12]

eb74: f7fc ff34 bl b9e0 <\_dl\_debug\_state@@GLIBC\_PRIVATE>

eb78: e635 b.n e7e6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdce>

eb7a: 6efb ldr r3, [r7, #108] ; 0x6c

eb7c: 2b00 cmp r3, #0

eb7e: f000 8177 beq.w ee70 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1458>

eb82: 4bf0 ldr r3, [pc, #960] ; (ef44 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x152c>)

eb84: 68e1 ldr r1, [r4, #12]

eb86: 447b add r3, pc

eb88: f843 1009 str.w r1, [r3, r9]

eb8c: 6d3b ldr r3, [r7, #80] ; 0x50

eb8e: 6059 str r1, [r3, #4]

eb90: e774 b.n ea7c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1064>

eb92: f8d4 0154 ldr.w r0, [r4, #340] ; 0x154

eb96: f7f1 ef46 blx a24 <free@plt+0x4>

eb9a: e7ac b.n eaf6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x10de>

eb9c: 6ad1 ldr r1, [r2, #44] ; 0x2c

eb9e: 2900 cmp r1, #0

eba0: f43f af5b beq.w ea5a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1042>

eba4: eb03 1101 add.w r1, r3, r1, lsl #4

eba8: 2000 movs r0, #0

ebaa: f8d7 c048 ldr.w ip, [r7, #72] ; 0x48

ebae: e003 b.n ebb8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11a0>

ebb0: 3310 adds r3, #16

ebb2: 4299 cmp r1, r3

ebb4: f43f af51 beq.w ea5a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1042>

ebb8: 685a ldr r2, [r3, #4]

ebba: 2a00 cmp r2, #0

ebbc: d0f8 beq.n ebb0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1198>

ebbe: 68da ldr r2, [r3, #12]

ebc0: 4294 cmp r4, r2

ebc2: d1f5 bne.n ebb0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1198>

ebc4: 6018 str r0, [r3, #0]

ebc6: f8dc 2030 ldr.w r2, [ip, #48] ; 0x30

ebca: 6058 str r0, [r3, #4]

ebcc: 3a01 subs r2, #1

ebce: f8cc 2030 str.w r2, [ip, #48] ; 0x30

ebd2: e7ed b.n ebb0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1198>

ebd4: f8df a370 ldr.w sl, [pc, #880] ; ef48 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1530>

ebd8: 44fa add sl, pc

ebda: f8da 1810 ldr.w r1, [sl, #2064] ; 0x810

ebde: b151 cbz r1, ebf6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11de>

ebe0: f3c3 03c0 ubfx r3, r3, #3, #1

ebe4: f8d4 0248 ldr.w r0, [r4, #584] ; 0x248

ebe8: f7ff fb8e bl e308 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x8f0>

ebec: b918 cbnz r0, ebf6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11de>

ebee: f8da 3814 ldr.w r3, [sl, #2068] ; 0x814

ebf2: f8ca 380c str.w r3, [sl, #2060] ; 0x80c

ebf6: f8d4 3244 ldr.w r3, [r4, #580] ; 0x244

ebfa: 1c5a adds r2, r3, #1

ebfc: 2a01 cmp r2, #1

ebfe: d926 bls.n ec4e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1236>

ec00: f8d4 1238 ldr.w r1, [r4, #568] ; 0x238

ec04: f8d4 2240 ldr.w r2, [r4, #576] ; 0x240

ec08: 440b add r3, r1

ec0a: 6d79 ldr r1, [r7, #84] ; 0x54

ec0c: b199 cbz r1, ec36 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x121e>

ec0e: 6e38 ldr r0, [r7, #96] ; 0x60

ec10: 4282 cmp r2, r0

ec12: d031 beq.n ec78 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1260>

ec14: 428b cmp r3, r1

ec16: f000 80ba beq.w ed8e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1376>

ec1a: 49cc ldr r1, [pc, #816] ; (ef4c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1534>)

ec1c: 4479 add r1, pc

ec1e: f8d1 c81c ldr.w ip, [r1, #2076] ; 0x81c

ec22: 459c cmp ip, r3

ec24: f000 80ae beq.w ed84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x136c>

ec28: 6e38 ldr r0, [r7, #96] ; 0x60

ec2a: 4584 cmp ip, r0

ec2c: f000 8132 beq.w ee94 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x147c>

ec30: 6e39 ldr r1, [r7, #96] ; 0x60

ec32: 428a cmp r2, r1

ec34: d90b bls.n ec4e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1236>

ec36: 663b str r3, [r7, #96] ; 0x60

ec38: 6dfb ldr r3, [r7, #92] ; 0x5c

ec3a: 657a str r2, [r7, #84] ; 0x54

ec3c: 667b str r3, [r7, #100] ; 0x64

ec3e: e6fc b.n ea3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1022>

ec40: 48c3 ldr r0, [pc, #780] ; (ef50 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1538>)

ec42: 69a2 ldr r2, [r4, #24]

ec44: 6861 ldr r1, [r4, #4]

ec46: 4478 add r0, pc

ec48: f7fd f894 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

ec4c: e733 b.n eab6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x109e>

ec4e: 6dfb ldr r3, [r7, #92] ; 0x5c

ec50: 667b str r3, [r7, #100] ; 0x64

ec52: e6f2 b.n ea3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1022>

ec54: 4bbf ldr r3, [pc, #764] ; (ef54 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x153c>)

ec56: 22e8 movs r2, #232 ; 0xe8

ec58: 49bf ldr r1, [pc, #764] ; (ef58 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1540>)

ec5a: 48c0 ldr r0, [pc, #768] ; (ef5c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1544>)

ec5c: 447b add r3, pc

ec5e: 4479 add r1, pc

ec60: 4478 add r0, pc

ec62: f001 fe93 bl 1098c <free@@GLIBC\_2.4+0x12c>

ec66: 6b3b ldr r3, [r7, #48] ; 0x30

ec68: 48bd ldr r0, [pc, #756] ; (ef60 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1548>)

ec6a: 6859 ldr r1, [r3, #4]

ec6c: 4478 add r0, pc

ec6e: f7fd f881 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

ec72: e5ca b.n e80a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdf2>

ec74: 461a mov r2, r3

ec76: e429 b.n e4cc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xab4>

ec78: 663b str r3, [r7, #96] ; 0x60

ec7a: 6dfb ldr r3, [r7, #92] ; 0x5c

ec7c: 667b str r3, [r7, #100] ; 0x64

ec7e: e6dc b.n ea3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1022>

ec80: f894 3199 ldrb.w r3, [r4, #409] ; 0x199

ec84: f013 0308 ands.w r3, r3, #8

ec88: f47f af71 bne.w eb6e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1156>

ec8c: 4eb5 ldr r6, [pc, #724] ; (ef64 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x154c>)

ec8e: 447e add r6, pc

ec90: e9d6 826b ldrd r8, r2, [r6, #428] ; 0x1ac

ec94: 2a00 cmp r2, #0

ec96: f43f af6a beq.w eb6e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1156>

ec9a: f504 7418 add.w r4, r4, #608 ; 0x260

ec9e: 461d mov r5, r3

eca0: f8d8 3000 ldr.w r3, [r8]

eca4: b113 cbz r3, ecac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1294>

eca6: 2100 movs r1, #0

eca8: 4620 mov r0, r4

ecaa: 4798 blx r3

ecac: f8d6 31b0 ldr.w r3, [r6, #432] ; 0x1b0

ecb0: 3501 adds r5, #1

ecb2: f8d8 8020 ldr.w r8, [r8, #32]

ecb6: 3408 adds r4, #8

ecb8: 429d cmp r5, r3

ecba: d3f1 bcc.n eca0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1288>

ecbc: 4baa ldr r3, [pc, #680] ; (ef68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1550>)

ecbe: 6a7a ldr r2, [r7, #36] ; 0x24

ecc0: 447b add r3, pc

ecc2: 589c ldr r4, [r3, r2]

ecc4: e750 b.n eb68 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1150>

ecc6: 4ba9 ldr r3, [pc, #676] ; (ef6c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1554>)

ecc8: 6a7a ldr r2, [r7, #36] ; 0x24

ecca: 447b add r3, pc

eccc: 4413 add r3, r2

ecce: 689e ldr r6, [r3, #8]

ecd0: 6871 ldr r1, [r6, #4]

ecd2: 2900 cmp r1, #0

ecd4: f000 80e6 beq.w eea4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x148c>

ecd8: 6830 ldr r0, [r6, #0]

ecda: f101 4380 add.w r3, r1, #1073741824 ; 0x40000000

ecde: 3b01 subs r3, #1

ece0: f850 4023 ldr.w r4, [r0, r3, lsl #2]

ece4: eb00 0383 add.w r3, r0, r3, lsl #2

ece8: f894 2199 ldrb.w r2, [r4, #409] ; 0x199

ecec: 0695 lsls r5, r2, #26

ecee: f140 80db bpl.w eea8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1490>

ecf2: 461a mov r2, r3

ecf4: 460b mov r3, r1

ecf6: e005 b.n ed04 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12ec>

ecf8: f852 4d04 ldr.w r4, [r2, #-4]!

ecfc: f894 4199 ldrb.w r4, [r4, #409] ; 0x199

ed00: 06a4 lsls r4, r4, #26

ed02: d525 bpl.n ed50 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1338>

ed04: 3b01 subs r3, #1

ed06: d1f7 bne.n ecf8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12e0>

ed08: f5ae 6e98 sub.w lr, lr, #1216 ; 0x4c0

ed0c: 6073 str r3, [r6, #4]

ed0e: f8de 3000 ldr.w r3, [lr]

ed12: 2b00 cmp r3, #0

ed14: f43f ae5b beq.w e9ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xfb6>

ed18: 4c95 ldr r4, [pc, #596] ; (ef70 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1558>)

ed1a: 447c add r4, pc

ed1c: f8d4 3830 ldr.w r3, [r4, #2096] ; 0x830

ed20: 4798 blx r3

ed22: f8d4 4834 ldr.w r4, [r4, #2100] ; 0x834

ed26: 2c00 cmp r4, #0

ed28: f43f ae51 beq.w e9ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xfb6>

ed2c: 6823 ldr r3, [r4, #0]

ed2e: 2b00 cmp r3, #0

ed30: f43f ae4d beq.w e9ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xfb6>

ed34: 3b01 subs r3, #1

ed36: 6023 str r3, [r4, #0]

ed38: eb04 0383 add.w r3, r4, r3, lsl #2

ed3c: 6858 ldr r0, [r3, #4]

ed3e: f7f1 ee72 blx a24 <free@plt+0x4>

ed42: 6823 ldr r3, [r4, #0]

ed44: 2b00 cmp r3, #0

ed46: d1f5 bne.n ed34 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x131c>

ed48: e641 b.n e9ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xfb6>

ed4a: 2308 movs r3, #8

ed4c: 2201 movs r2, #1

ed4e: e610 b.n e972 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf5a>

ed50: 461a mov r2, r3

ed52: 6d7b ldr r3, [r7, #84] ; 0x54

ed54: 4413 add r3, r2

ed56: 4299 cmp r1, r3

ed58: bf08 it eq

ed5a: 4613 moveq r3, r2

ed5c: d0d4 beq.n ed08 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12f0>

ed5e: 2300 movs r3, #0

ed60: f1a0 0c04 sub.w ip, r0, #4

ed64: 4619 mov r1, r3

ed66: f85c 4f04 ldr.w r4, [ip, #4]!

ed6a: f894 5199 ldrb.w r5, [r4, #409] ; 0x199

ed6e: 06ad lsls r5, r5, #26

ed70: d404 bmi.n ed7c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1364>

ed72: 4299 cmp r1, r3

ed74: bf18 it ne

ed76: f840 4023 strne.w r4, [r0, r3, lsl #2]

ed7a: 3301 adds r3, #1

ed7c: 3101 adds r1, #1

ed7e: 4291 cmp r1, r2

ed80: d1f1 bne.n ed66 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x134e>

ed82: e7c1 b.n ed08 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12f0>

ed84: 6dfb ldr r3, [r7, #92] ; 0x5c

ed86: f8c1 281c str.w r2, [r1, #2076] ; 0x81c

ed8a: 667b str r3, [r7, #100] ; 0x64

ed8c: e655 b.n ea3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1022>

ed8e: 6dfb ldr r3, [r7, #92] ; 0x5c

ed90: 657a str r2, [r7, #84] ; 0x54

ed92: 667b str r3, [r7, #100] ; 0x64

ed94: e651 b.n ea3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1022>

ed96: 4b77 ldr r3, [pc, #476] ; (ef74 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x155c>)

ed98: f44f 7283 mov.w r2, #262 ; 0x106

ed9c: 4976 ldr r1, [pc, #472] ; (ef78 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1560>)

ed9e: 4877 ldr r0, [pc, #476] ; (ef7c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1564>)

eda0: 447b add r3, pc

eda2: 4479 add r1, pc

eda4: 4478 add r0, pc

eda6: f001 fdf1 bl 1098c <free@@GLIBC\_2.4+0x12c>

edaa: 4b75 ldr r3, [pc, #468] ; (ef80 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1568>)

edac: f240 120b movw r2, #267 ; 0x10b

edb0: 4974 ldr r1, [pc, #464] ; (ef84 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x156c>)

edb2: 4875 ldr r0, [pc, #468] ; (ef88 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1570>)

edb4: 447b add r3, pc

edb6: 4479 add r1, pc

edb8: 4478 add r0, pc

edba: f001 fde7 bl 1098c <free@@GLIBC\_2.4+0x12c>

edbe: 2200 movs r2, #0

edc0: 601a str r2, [r3, #0]

edc2: f8d7 d028 ldr.w sp, [r7, #40] ; 0x28

edc6: e520 b.n e80a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xdf2>

edc8: 4b70 ldr r3, [pc, #448] ; (ef8c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1574>)

edca: 6a7a ldr r2, [r7, #36] ; 0x24

edcc: 447b add r3, pc

edce: 589d ldr r5, [r3, r2]

edd0: f895 3199 ldrb.w r3, [r5, #409] ; 0x199

edd4: f013 0308 ands.w r3, r3, #8

edd8: f47f ade3 bne.w e9a2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf8a>

eddc: f8df 81b0 ldr.w r8, [pc, #432] ; ef90 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1578>

ede0: 44f8 add r8, pc

ede2: f8d8 21b0 ldr.w r2, [r8, #432] ; 0x1b0

ede6: 2a00 cmp r2, #0

ede8: f43f addb beq.w e9a2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf8a>

edec: f8d8 41ac ldr.w r4, [r8, #428] ; 0x1ac

edf0: f505 7518 add.w r5, r5, #608 ; 0x260

edf4: 461e mov r6, r3

edf6: 6823 ldr r3, [r4, #0]

edf8: b113 cbz r3, ee00 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x13e8>

edfa: 2102 movs r1, #2

edfc: 4628 mov r0, r5

edfe: 4798 blx r3

ee00: f8d8 31b0 ldr.w r3, [r8, #432] ; 0x1b0

ee04: 3601 adds r6, #1

ee06: 6a24 ldr r4, [r4, #32]

ee08: 3508 adds r5, #8

ee0a: 429e cmp r6, r3

ee0c: d3f3 bcc.n edf6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x13de>

ee0e: e5c8 b.n e9a2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xf8a>

ee10: 4960 ldr r1, [pc, #384] ; (ef94 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x157c>)

ee12: 2002 movs r0, #2

ee14: 4479 add r1, pc

ee16: f7fc ffd9 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

ee1a: 207f movs r0, #127 ; 0x7f

ee1c: f003 f814 bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

ee20: 495d ldr r1, [pc, #372] ; (ef98 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1580>)

ee22: 6ef8 ldr r0, [r7, #108] ; 0x6c

ee24: 4479 add r1, pc

ee26: f8d1 34c0 ldr.w r3, [r1, #1216] ; 0x4c0

ee2a: 1e5a subs r2, r3, #1

ee2c: 4290 cmp r0, r2

ee2e: f47f ae9e bne.w eb6e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1156>

ee32: 68b8 ldr r0, [r7, #8]

ee34: fb00 1303 mla r3, r0, r3, r1

ee38: e000 b.n ee3c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1424>

ee3a: 3a01 subs r2, #1

ee3c: 3b4c subs r3, #76 ; 0x4c

ee3e: f853 1c4c ldr.w r1, [r3, #-76]

ee42: 2900 cmp r1, #0

ee44: d0f9 beq.n ee3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1422>

ee46: 4b55 ldr r3, [pc, #340] ; (ef9c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1584>)

ee48: 447b add r3, pc

ee4a: f8c3 24c0 str.w r2, [r3, #1216] ; 0x4c0

ee4e: e68e b.n eb6e <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1156>

ee50: 6b7b ldr r3, [r7, #52] ; 0x34

ee52: 2b00 cmp r3, #0

ee54: f47f af60 bne.w ed18 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1300>

ee58: 4b51 ldr r3, [pc, #324] ; (efa0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1588>)

ee5a: 447b add r3, pc

ee5c: f8d3 3834 ldr.w r3, [r3, #2100] ; 0x834

ee60: 2b00 cmp r3, #0

ee62: f43f adb4 beq.w e9ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xfb6>

ee66: 681b ldr r3, [r3, #0]

ee68: 2b00 cmp r3, #0

ee6a: f47f af55 bne.w ed18 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1300>

ee6e: e5ae b.n e9ce <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0xfb6>

ee70: 4b4c ldr r3, [pc, #304] ; (efa4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x158c>)

ee72: f240 22b6 movw r2, #694 ; 0x2b6

ee76: 494c ldr r1, [pc, #304] ; (efa8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1590>)

ee78: 484c ldr r0, [pc, #304] ; (efac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1594>)

ee7a: 447b add r3, pc

ee7c: 4479 add r1, pc

ee7e: 4478 add r0, pc

ee80: f001 fd84 bl 1098c <free@@GLIBC\_2.4+0x12c>

ee84: 4b4a ldr r3, [pc, #296] ; (efb0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1598>)

ee86: 4602 mov r2, r0

ee88: 494a ldr r1, [pc, #296] ; (efb4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x159c>)

ee8a: 200c movs r0, #12

ee8c: 447b add r3, pc

ee8e: 4479 add r1, pc

ee90: f7f1 edba blx a08 <\_dl\_signal\_error@plt>

ee94: 6d78 ldr r0, [r7, #84] ; 0x54

ee96: 663b str r3, [r7, #96] ; 0x60

ee98: 6dfb ldr r3, [r7, #92] ; 0x5c

ee9a: f8c1 081c str.w r0, [r1, #2076] ; 0x81c

ee9e: 657a str r2, [r7, #84] ; 0x54

eea0: 667b str r3, [r7, #100] ; 0x64

eea2: e5ca b.n ea3a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1022>

eea4: 460b mov r3, r1

eea6: e72f b.n ed08 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x12f0>

eea8: 460a mov r2, r1

eeaa: e752 b.n ed52 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x133a>

eeac: 4b42 ldr r3, [pc, #264] ; (efb8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15a0>)

eeae: 22a5 movs r2, #165 ; 0xa5

eeb0: 4942 ldr r1, [pc, #264] ; (efbc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15a4>)

eeb2: 4843 ldr r0, [pc, #268] ; (efc0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15a8>)

eeb4: 447b add r3, pc

eeb6: 4479 add r1, pc

eeb8: 4478 add r0, pc

eeba: f001 fd67 bl 1098c <free@@GLIBC\_2.4+0x12c>

eebe: 4b41 ldr r3, [pc, #260] ; (efc4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15ac>)

eec0: f240 126d movw r2, #365 ; 0x16d

eec4: 4940 ldr r1, [pc, #256] ; (efc8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15b0>)

eec6: 4841 ldr r0, [pc, #260] ; (efcc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15b4>)

eec8: 447b add r3, pc

eeca: 4479 add r1, pc

eecc: 4478 add r0, pc

eece: f001 fd5d bl 1098c <free@@GLIBC\_2.4+0x12c>

eed2: 4b3f ldr r3, [pc, #252] ; (efd0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15b8>)

eed4: f240 2225 movw r2, #549 ; 0x225

eed8: 493e ldr r1, [pc, #248] ; (efd4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15bc>)

eeda: 483f ldr r0, [pc, #252] ; (efd8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15c0>)

eedc: 447b add r3, pc

eede: 4479 add r1, pc

eee0: 4478 add r0, pc

eee2: f001 fd53 bl 1098c <free@@GLIBC\_2.4+0x12c>

eee6: f8d4 37fc ldr.w r3, [r4, #2044] ; 0x7fc

eeea: 4628 mov r0, r5

eeec: 4798 blx r3

eeee: e633 b.n eb58 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1140>

eef0: b4f6 push {r1, r2, r4, r5, r6, r7}

eef2: 0001 movs r1, r0

eef4: ac2e add r4, sp, #184 ; 0xb8

eef6: 0001 movs r1, r0

eef8: ac1e add r4, sp, #120 ; 0x78

eefa: 0001 movs r1, r0

eefc: a3d4 add r3, pc, #848 ; (adr r3, f250 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x94>)

eefe: 0001 movs r1, r0

ef00: a37e add r3, pc, #504 ; (adr r3, f0fc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16e4>)

ef02: 0001 movs r1, r0

ef04: a356 add r3, pc, #344 ; (adr r3, f060 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1648>)

ef06: 0001 movs r1, r0

ef08: b11e cbz r6, ef12 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x14fa>

ef0a: 0001 movs r1, r0

ef0c: a16c add r1, pc, #432 ; (adr r1, f0c0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16a8>)

ef0e: 0001 movs r1, r0

ef10: 8ba2 ldrh r2, [r4, #28]

ef12: 0000 movs r0, r0

ef14: 8980 ldrh r0, [r0, #12]

ef16: 0000 movs r0, r0

ef18: 89f2 ldrh r2, [r6, #14]

ef1a: 0000 movs r0, r0

ef1c: a086 add r0, pc, #536 ; (adr r0, f138 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x8>)

ef1e: 0001 movs r1, r0

ef20: 7f9a ldrb r2, [r3, #30]

ef22: 0000 movs r0, r0

ef24: a68a add r6, pc, #552 ; (adr r6, f150 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x20>)

ef26: 0001 movs r1, r0

ef28: a65a add r6, pc, #360 ; (adr r6, f094 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x167c>)

ef2a: 0001 movs r1, r0

ef2c: a658 add r6, pc, #352 ; (adr r6, f090 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1678>)

ef2e: 0001 movs r1, r0

ef30: a618 add r6, pc, #96 ; (adr r6, ef94 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x157c>)

ef32: 0001 movs r1, r0

ef34: a5fc add r5, pc, #1008 ; (adr r5, f328 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8>)

ef36: 0001 movs r1, r0

ef38: 9ec0 ldr r6, [sp, #768] ; 0x300

ef3a: 0001 movs r1, r0

ef3c: a536 add r5, pc, #216 ; (adr r5, f018 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1600>)

ef3e: 0001 movs r1, r0

ef40: a500 add r5, pc, #0 ; (adr r5, ef44 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x152c>)

ef42: 0001 movs r1, r0

ef44: a4d6 add r4, pc, #856 ; (adr r4, f2a0 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0xe4>)

ef46: 0001 movs r1, r0

ef48: a484 add r4, pc, #528 ; (adr r4, f15c <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x2c>)

ef4a: 0001 movs r1, r0

ef4c: a440 add r4, pc, #256 ; (adr r4, f050 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1638>)

ef4e: 0001 movs r1, r0

ef50: 8706 strh r6, [r0, #56] ; 0x38

ef52: 0000 movs r0, r0

ef54: 87a0 strh r0, [r4, #60] ; 0x3c

ef56: 0000 movs r0, r0

ef58: 857e strh r6, [r7, #42] ; 0x2a

ef5a: 0000 movs r0, r0

ef5c: 861c strh r4, [r3, #48] ; 0x30

ef5e: 0000 movs r0, r0

ef60: 85ac strh r4, [r5, #44] ; 0x2c

ef62: 0000 movs r0, r0

ef64: 9cde ldr r4, [sp, #888] ; 0x378

ef66: 0001 movs r1, r0

ef68: a39c add r3, pc, #624 ; (adr r3, f1dc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x20>)

ef6a: 0001 movs r1, r0

ef6c: a392 add r3, pc, #584 ; (adr r3, f1b8 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x88>)

ef6e: 0001 movs r1, r0

ef70: a342 add r3, pc, #264 ; (adr r3, f07c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1664>)

ef72: 0001 movs r1, r0

ef74: 865c strh r4, [r3, #50] ; 0x32

ef76: 0000 movs r0, r0

ef78: 843a strh r2, [r7, #32]

ef7a: 0000 movs r0, r0

ef7c: 8504 strh r4, [r0, #40] ; 0x28

ef7e: 0000 movs r0, r0

ef80: 8648 strh r0, [r1, #50] ; 0x32

ef82: 0000 movs r0, r0

ef84: 8426 strh r6, [r4, #32]

ef86: 0000 movs r0, r0

ef88: 8504 strh r4, [r0, #40] ; 0x28

ef8a: 0000 movs r0, r0

ef8c: a290 add r2, pc, #576 ; (adr r2, f1d0 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x14>)

ef8e: 0001 movs r1, r0

ef90: 9b8c ldr r3, [sp, #560] ; 0x230

ef92: 0001 movs r1, r0

ef94: 8560 strh r0, [r4, #42] ; 0x2a

ef96: 0000 movs r0, r0

ef98: a238 add r2, pc, #224 ; (adr r2, f07c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1664>)

ef9a: 0001 movs r1, r0

ef9c: a214 add r2, pc, #80 ; (adr r2, eff0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15d8>)

ef9e: 0001 movs r1, r0

efa0: a202 add r2, pc, #8 ; (adr r2, efac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1594>)

efa2: 0001 movs r1, r0

efa4: 8582 strh r2, [r0, #44] ; 0x2c

efa6: 0000 movs r0, r0

efa8: 8360 strh r0, [r4, #26]

efaa: 0000 movs r0, r0

efac: 84ba strh r2, [r7, #36] ; 0x24

efae: 0000 movs r0, r0

efb0: 82ac strh r4, [r5, #20]

efb2: 0000 movs r0, r0

efb4: 8486 strh r6, [r0, #36] ; 0x24

efb6: 0000 movs r0, r0

efb8: 8548 strh r0, [r1, #42] ; 0x2a

efba: 0000 movs r0, r0

efbc: 8326 strh r6, [r4, #24]

efbe: 0000 movs r0, r0

efc0: 8388 strh r0, [r1, #28]

efc2: 0000 movs r0, r0

efc4: 8534 strh r4, [r6, #40] ; 0x28

efc6: 0000 movs r0, r0

efc8: 8312 strh r2, [r2, #24]

efca: 0000 movs r0, r0

efcc: 8434 strh r4, [r6, #32]

efce: 0000 movs r0, r0

efd0: 8520 strh r0, [r4, #40] ; 0x28

efd2: 0000 movs r0, r0

efd4: 82fe strh r6, [r7, #22]

efd6: 0000 movs r0, r0

efd8: 843c strh r4, [r7, #32]

efda: 0000 movs r0, r0

efdc: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

efe0: 4605 mov r5, r0

efe2: 4c12 ldr r4, [pc, #72] ; (f02c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1614>)

efe4: 447c add r4, pc

efe6: f204 46c4 addw r6, r4, #1220 ; 0x4c4

efea: f8d4 37f8 ldr.w r3, [r4, #2040] ; 0x7f8

efee: 4630 mov r0, r6

eff0: 4798 blx r3

eff2: f8d5 120c ldr.w r1, [r5, #524] ; 0x20c

eff6: f011 0108 ands.w r1, r1, #8

effa: d105 bne.n f008 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15f0>

effc: f8d5 7194 ldr.w r7, [r5, #404] ; 0x194

f000: b147 cbz r7, f014 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15fc>

f002: 4628 mov r0, r5

f004: f7ff f9ee bl e3e4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x9cc>

f008: f8d4 37fc ldr.w r3, [r4, #2044] ; 0x7fc

f00c: 4630 mov r0, r6

f00e: e8bd 41f0 ldmia.w sp!, {r4, r5, r6, r7, r8, lr}

f012: 4718 bx r3

f014: f8d4 37fc ldr.w r3, [r4, #2044] ; 0x7fc

f018: 4630 mov r0, r6

f01a: 4798 blx r3

f01c: 4b04 ldr r3, [pc, #16] ; (f030 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1618>)

f01e: 6869 ldr r1, [r5, #4]

f020: 463a mov r2, r7

f022: 447b add r3, pc

f024: 4638 mov r0, r7

f026: f7f1 ecf0 blx a08 <\_dl\_signal\_error@plt>

f02a: bf00 nop

f02c: a078 add r0, pc, #480 ; (adr r0, f210 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x54>)

f02e: 0001 movs r1, r0

f030: 83b2 strh r2, [r6, #28]

f032: 0000 movs r0, r0

f034: 001f movs r7, r3

f036: e92d 0004 stmdb sp!, {r2}

f03a: e51e b.n ea7a <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1062>

f03c: 100e asrs r6, r1, #32

f03e: e04c b.n f0da <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16c2>

f040: 1004 asrs r4, r0, #32

f042: e241 b.n f4c8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1a8>

f044: 1001 asrs r1, r0, #32

f046: e081 b.n f14c <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x1c>

f048: efb3 faff ; <UNDEFINED> instruction: 0xefb3faff

f04c: c000 stmia r0!, {}

f04e: e1a0 b.n f392 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x72>

f050: 401f ands r7, r3

f052: e8bd ff1c ldmia.w sp!, {r2, r3, r4, r8, r9, sl, fp, ip, sp, lr, pc}

f056: e12f b.n f2b8 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0xfc>

f058: d0c4 beq.n efe4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x15cc>

f05a: e24d b.n f4f8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1d8>

f05c: 000f movs r7, r1

f05e: e88d d010 stmia.w sp, {r4, ip, lr, pc}

f062: e24d b.n f500 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1e0>

f064: 00d8 lsls r0, r3, #3

f066: e28d b.n f584 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x264>

f068: 0020 movs r0, r4

f06a: e58d b.n eb88 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1170>

f06c: 20d4 movs r0, #212 ; 0xd4

f06e: e59d b.n ebac <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1194>

f070: 2024 movs r0, #36 ; 0x24

f072: e58d b.n eb90 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1178>

f074: 0004 movs r4, r0

f076: e51e b.n eab6 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x109e>

f078: 100e asrs r6, r1, #32

f07a: e04c b.n f116 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16fe>

f07c: 1004 asrs r4, r0, #32

f07e: e241 b.n f504 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1e4>

f080: 1001 asrs r1, r0, #32

f082: e081 b.n f188 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x58>

f084: 3008 adds r0, #8

f086: e28d b.n f5a4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x284>

f088: 0003 movs r3, r0

f08a: e8a3 c0d0 stmia.w r3!, {r4, r6, r7, lr, pc}

f08e: e28d b.n f5ac <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x28c>

f090: c000 stmia r0!, {}

f092: e58d b.n ebb0 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1198>

f094: eff0 faff ; <UNDEFINED> instruction: 0xeff0faff

f098: c0d0 stmia r0!, {r4, r6, r7}

f09a: e59d b.n ebd8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11c0>

f09c: 0000 movs r0, r0

f09e: e35c b.n f75a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x43a>

f0a0: 0003 movs r3, r0

f0a2: aa00 add r2, sp, #0

f0a4: c000 stmia r0!, {}

f0a6: e1a0 b.n f3ea <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xca>

f0a8: d010 beq.n f0cc <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16b4>

f0aa: e28d b.n f5c8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2a8>

f0ac: 600f str r7, [r1, #0]

f0ae: e89d ff1c ldmia.w sp, {r2, r3, r4, r8, r9, sl, fp, ip, sp, lr, pc}

f0b2: e12f b.n f314 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x158>

f0b4: 70d4 strb r4, [r2, #3]

f0b6: e58d b.n ebd4 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11bc>

f0b8: 7038 strb r0, [r7, #0]

f0ba: e24d b.n f558 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x238>

f0bc: d00c beq.n f0d8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16c0>

f0be: e04d b.n f15c <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x2c>

f0c0: d007 beq.n f0d2 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x16ba>

f0c2: e3cd b.n f860 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x540>

f0c4: 0108 lsls r0, r1, #4

f0c6: e587 b.n ebd8 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11c0>

f0c8: 000d movs r5, r1

f0ca: e1a0 b.n f40e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xee>

f0cc: 1e11 subs r1, r2, #0

f0ce: e287 b.n f5e0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2c0>

f0d0: 200c movs r0, #12

f0d2: e1a0 b.n f416 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xf6>

f0d4: 1009 asrs r1, r1, #32

f0d6: eb00 c048 ; <UNDEFINED> instruction: 0xeb00c048

f0da: e287 b.n f5ec <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2cc>

f0dc: 000f movs r7, r1

f0de: e89c c108 ldmia.w ip, {r3, r8, lr, pc}

f0e2: e597 b.n ec14 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x11fc>

f0e4: ff3c e12f vrhadd.u<illegal width 64> d14, d12, d31

f0e8: 000f movs r7, r1

f0ea: e887 c040 stmia.w r7, {r6, lr, pc}

f0ee: e287 b.n f600 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2e0>

f0f0: 0003 movs r3, r0

f0f2: e89c 2048 ldmia.w ip, {r3, r6, sp}

f0f6: e287 b.n f608 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2e8>

f0f8: 3000 adds r0, #0

f0fa: e287 b.n f60c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2ec>

f0fc: f0db faff bl ea6fe <\_r\_debug@@GLIBC\_2.4+0xc0da2>

f100: 000f movs r7, r1

f102: e897 d007 ldmia.w r7, {r0, r1, r2, ip, lr, pc}

f106: e1a0 b.n f44a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x12a>

f108: 710c strb r4, [r1, #4]

f10a: e59d b.n ec48 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1230>

f10c: e05c b.n f1c8 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0xc>

f10e: e59d b.n ec4c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1234>

f110: de11 udf #17

f112: e28d b.n f630 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x310>

f114: ff1e e12f vrhadd.u16 d14, d14, d31

f118: 4904 ldr r1, [pc, #16] ; (f12c <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1714>)

f11a: 2002 movs r0, #2

f11c: b508 push {r3, lr}

f11e: 4479 add r1, pc

f120: f7fc fe54 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

f124: 207f movs r0, #127 ; 0x7f

f126: f002 fe8f bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

f12a: bf00 nop

f12c: 82f2 strh r2, [r6, #22]

...

0000f130 <\_dl\_exception\_create@@GLIBC\_PRIVATE>:

f130: e92d 43f8 stmdb sp!, {r3, r4, r5, r6, r7, r8, r9, lr}

f134: 4604 mov r4, r0

f136: 4617 mov r7, r2

f138: b399 cbz r1, f1a2 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x72>

f13a: 4608 mov r0, r1

f13c: 460d mov r5, r1

f13e: f003 faff bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

f142: 1c46 adds r6, r0, #1

f144: 4638 mov r0, r7

f146: f003 fafb bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

f14a: f100 0801 add.w r8, r0, #1

f14e: eb08 0006 add.w r0, r8, r6

f152: f7f1 ec3c blx 9cc <malloc@plt+0x4>

f156: 4681 mov r9, r0

f158: b1d8 cbz r0, f192 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x62>

f15a: 4642 mov r2, r8

f15c: 4639 mov r1, r7

f15e: f003 ffb5 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

f162: 4632 mov r2, r6

f164: 4629 mov r1, r5

f166: f003 efcc blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

f16a: 4b10 ldr r3, [pc, #64] ; (f1ac <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x7c>)

f16c: f8c4 9004 str.w r9, [r4, #4]

f170: 447b add r3, pc

f172: 681b ldr r3, [r3, #0]

f174: 6020 str r0, [r4, #0]

f176: b143 cbz r3, f18a <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x5a>

f178: f893 3198 ldrb.w r3, [r3, #408] ; 0x198

f17c: 075b lsls r3, r3, #29

f17e: bf48 it mi

f180: f8c4 9008 strmi.w r9, [r4, #8]

f184: d501 bpl.n f18a <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x5a>

f186: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

f18a: 2300 movs r3, #0

f18c: 60a3 str r3, [r4, #8]

f18e: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

f192: 4a07 ldr r2, [pc, #28] ; (f1b0 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x80>)

f194: 4b07 ldr r3, [pc, #28] ; (f1b4 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x84>)

f196: 447a add r2, pc

f198: 447b add r3, pc

f19a: e884 020c stmia.w r4, {r2, r3, r9}

f19e: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

f1a2: 4d05 ldr r5, [pc, #20] ; (f1b8 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x88>)

f1a4: 2601 movs r6, #1

f1a6: 447d add r5, pc

f1a8: e7cc b.n f144 <\_dl\_exception\_create@@GLIBC\_PRIVATE+0x14>

f1aa: bf00 nop

f1ac: 9eec ldr r6, [sp, #944] ; 0x3b0

f1ae: 0001 movs r1, r0

f1b0: 6372 str r2, [r6, #52] ; 0x34

f1b2: 0000 movs r0, r0

f1b4: 82ec strh r4, [r5, #22]

f1b6: 0000 movs r0, r0

f1b8: 6362 str r2, [r4, #52] ; 0x34

...

0000f1bc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE>:

f1bc: b40c push {r2, r3}

f1be: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

f1c2: b083 sub sp, #12

f1c4: 4606 mov r6, r0

f1c6: 9d0c ldr r5, [sp, #48] ; 0x30

f1c8: 2900 cmp r1, #0

f1ca: f000 808e beq.w f2ea <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x12e>

f1ce: 4608 mov r0, r1

f1d0: 460f mov r7, r1

f1d2: f003 fab5 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

f1d6: f100 0801 add.w r8, r0, #1

f1da: f100 0902 add.w r9, r0, #2

f1de: 782c ldrb r4, [r5, #0]

f1e0: f10d 0a34 add.w sl, sp, #52 ; 0x34

f1e4: f8cd a004 str.w sl, [sp, #4]

f1e8: b17c cbz r4, f20a <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x4e>

f1ea: 46ab mov fp, r5

f1ec: 465b mov r3, fp

f1ee: 2c25 cmp r4, #37 ; 0x25

f1f0: f813 2f01 ldrb.w r2, [r3, #1]!

f1f4: bf1e ittt ne

f1f6: f109 0901 addne.w r9, r9, #1

f1fa: 469b movne fp, r3

f1fc: 4614 movne r4, r2

f1fe: d056 beq.n f2ae <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0xf2>

f200: 2c00 cmp r4, #0

f202: d1f3 bne.n f1ec <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x30>

f204: f1b9 0f00 cmp.w r9, #0

f208: db62 blt.n f2d0 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x114>

f20a: 4648 mov r0, r9

f20c: f7f1 ebde blx 9cc <malloc@plt+0x4>

f210: 4604 mov r4, r0

f212: 2800 cmp r0, #0

f214: d05c beq.n f2d0 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x114>

f216: 4b3d ldr r3, [pc, #244] ; (f30c <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x150>)

f218: 6070 str r0, [r6, #4]

f21a: 447b add r3, pc

f21c: 681b ldr r3, [r3, #0]

f21e: b133 cbz r3, f22e <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x72>

f220: f893 3198 ldrb.w r3, [r3, #408] ; 0x198

f224: f013 0f04 tst.w r3, #4

f228: bf14 ite ne

f22a: 4603 movne r3, r0

f22c: 2300 moveq r3, #0

f22e: 60b3 str r3, [r6, #8]

f230: 44a1 add r9, r4

f232: 782b ldrb r3, [r5, #0]

f234: f8cd a004 str.w sl, [sp, #4]

f238: b93b cbnz r3, f24a <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x8e>

f23a: e024 b.n f286 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0xca>

f23c: 45a1 cmp r9, r4

f23e: d05b beq.n f2f8 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x13c>

f240: f804 3b01 strb.w r3, [r4], #1

f244: 3501 adds r5, #1

f246: 782b ldrb r3, [r5, #0]

f248: b1eb cbz r3, f286 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0xca>

f24a: 2b25 cmp r3, #37 ; 0x25

f24c: d1f6 bne.n f23c <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x80>

f24e: 786a ldrb r2, [r5, #1]

f250: 3501 adds r5, #1

f252: 2a25 cmp r2, #37 ; 0x25

f254: d0f2 beq.n f23c <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x80>

f256: 2a73 cmp r2, #115 ; 0x73

f258: d150 bne.n f2fc <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x140>

f25a: 9b01 ldr r3, [sp, #4]

f25c: 1d1a adds r2, r3, #4

f25e: 9201 str r2, [sp, #4]

f260: f8d3 a000 ldr.w sl, [r3]

f264: 4650 mov r0, sl

f266: f003 fa6b bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

f26a: eba9 0304 sub.w r3, r9, r4

f26e: 4283 cmp r3, r0

f270: 4602 mov r2, r0

f272: d341 bcc.n f2f8 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x13c>

f274: 4620 mov r0, r4

f276: 4651 mov r1, sl

f278: f003 ff28 bl 130cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2364>

f27c: 786b ldrb r3, [r5, #1]

f27e: 3501 adds r5, #1

f280: 4604 mov r4, r0

f282: 2b00 cmp r3, #0

f284: d1e1 bne.n f24a <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x8e>

f286: 45a1 cmp r9, r4

f288: d036 beq.n f2f8 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x13c>

f28a: 4620 mov r0, r4

f28c: 2300 movs r3, #0

f28e: f800 3b01 strb.w r3, [r0], #1

f292: eba9 0900 sub.w r9, r9, r0

f296: 45c1 cmp r9, r8

f298: d12e bne.n f2f8 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x13c>

f29a: 4642 mov r2, r8

f29c: 4639 mov r1, r7

f29e: f003 ef30 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

f2a2: 6030 str r0, [r6, #0]

f2a4: b003 add sp, #12

f2a6: e8bd 4ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

f2aa: b002 add sp, #8

f2ac: 4770 bx lr

f2ae: 2a73 cmp r2, #115 ; 0x73

f2b0: f81b 4f02 ldrb.w r4, [fp, #2]!

f2b4: bf18 it ne

f2b6: f109 0901 addne.w r9, r9, #1

f2ba: d1a1 bne.n f200 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x44>

f2bc: 9b01 ldr r3, [sp, #4]

f2be: 1d1a adds r2, r3, #4

f2c0: 9201 str r2, [sp, #4]

f2c2: 6818 ldr r0, [r3, #0]

f2c4: f003 fa3c bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

f2c8: 4481 add r9, r0

f2ca: 2c00 cmp r4, #0

f2cc: d18e bne.n f1ec <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x30>

f2ce: e799 b.n f204 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x48>

f2d0: 4a0f ldr r2, [pc, #60] ; (f310 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x154>)

f2d2: 2100 movs r1, #0

f2d4: 4b0f ldr r3, [pc, #60] ; (f314 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x158>)

f2d6: 447a add r2, pc

f2d8: 60b1 str r1, [r6, #8]

f2da: 447b add r3, pc

f2dc: e9c6 2300 strd r2, r3, [r6]

f2e0: b003 add sp, #12

f2e2: e8bd 4ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

f2e6: b002 add sp, #8

f2e8: 4770 bx lr

f2ea: 4f0b ldr r7, [pc, #44] ; (f318 <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x15c>)

f2ec: f04f 0902 mov.w r9, #2

f2f0: f04f 0801 mov.w r8, #1

f2f4: 447f add r7, pc

f2f6: e772 b.n f1de <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x22>

f2f8: f7ff ff0e bl f118 <\_dl\_find\_dso\_for\_object@@GLIBC\_PRIVATE+0x1700>

f2fc: 4907 ldr r1, [pc, #28] ; (f31c <\_dl\_exception\_create\_format@@GLIBC\_PRIVATE+0x160>)

f2fe: 2002 movs r0, #2

f300: 4479 add r1, pc

f302: f7fc fd63 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

f306: 207f movs r0, #127 ; 0x7f

f308: f002 fd9e bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

f30c: 9e42 ldr r6, [sp, #264] ; 0x108

f30e: 0001 movs r1, r0

f310: 6232 str r2, [r6, #32]

f312: 0000 movs r0, r0

f314: 81aa strh r2, [r5, #12]

f316: 0000 movs r0, r0

f318: 6214 str r4, [r2, #32]

f31a: 0000 movs r0, r0

f31c: 8150 strh r0, [r2, #10]

...

0000f320 <\_dl\_exception\_free@@GLIBC\_PRIVATE>:

f320: b510 push {r4, lr}

f322: 4604 mov r4, r0

f324: 6880 ldr r0, [r0, #8]

f326: f7f1 eb7e blx a24 <free@plt+0x4>

f32a: 2300 movs r3, #0

f32c: e9c4 3300 strd r3, r3, [r4]

f330: 60a3 str r3, [r4, #8]

f332: bd10 pop {r4, pc}

f334: 2901 cmp r1, #1

f336: f240 80ba bls.w f4ae <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x18e>

f33a: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

f33e: b08b sub sp, #44 ; 0x2c

f340: af00 add r7, sp, #0

f342: 4605 mov r5, r0

f344: 466c mov r4, sp

f346: f04f 0901 mov.w r9, #1

f34a: 613a str r2, [r7, #16]

f34c: 004a lsls r2, r1, #1

f34e: 627b str r3, [r7, #36] ; 0x24

f350: 1dd3 adds r3, r2, #7

f352: f023 0307 bic.w r3, r3, #7

f356: 603c str r4, [r7, #0]

f358: ebad 0d03 sub.w sp, sp, r3

f35c: 460c mov r4, r1

f35e: 6239 str r1, [r7, #32]

f360: 2100 movs r1, #0

f362: 46ea mov sl, sp

f364: 61b8 str r0, [r7, #24]

f366: 4650 mov r0, sl

f368: 460e mov r6, r1

f36a: 468b mov fp, r1

f36c: 61f9 str r1, [r7, #28]

f36e: f003 ee70 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

f372: f104 4380 add.w r3, r4, #1073741824 ; 0x40000000

f376: 3b01 subs r3, #1

f378: eb05 0383 add.w r3, r5, r3, lsl #2

f37c: 607b str r3, [r7, #4]

f37e: f83a 200b ldrh.w r2, [sl, fp]

f382: eb0a 030b add.w r3, sl, fp

f386: 617b str r3, [r7, #20]

f388: e9d7 3106 ldrd r3, r1, [r7, #24]

f38c: 3201 adds r2, #1

f38e: 1858 adds r0, r3, r1

f390: 585c ldr r4, [r3, r1]

f392: b293 uxth r3, r2

f394: 60bb str r3, [r7, #8]

f396: f82a 300b strh.w r3, [sl, fp]

f39a: 6a7b ldr r3, [r7, #36] ; 0x24

f39c: 2b00 cmp r3, #0

f39e: d176 bne.n f48e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x16e>

f3a0: 6a3b ldr r3, [r7, #32]

f3a2: 1e5d subs r5, r3, #1

f3a4: 42ae cmp r6, r5

f3a6: d20d bcs.n f3c4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa4>

f3a8: 687a ldr r2, [r7, #4]

f3aa: 4684 mov ip, r0

f3ac: 4690 mov r8, r2

f3ae: f852 0904 ldr.w r0, [r2], #-4

f3b2: f8d0 11f8 ldr.w r1, [r0, #504] ; 0x1f8

f3b6: b9d9 cbnz r1, f3f0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xd0>

f3b8: 6a7b ldr r3, [r7, #36] ; 0x24

f3ba: 2b00 cmp r3, #0

f3bc: d14a bne.n f454 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x134>

f3be: 3d01 subs r5, #1

f3c0: 42ae cmp r6, r5

f3c2: d1f3 bne.n f3ac <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8c>

f3c4: 6a3b ldr r3, [r7, #32]

f3c6: 454b cmp r3, r9

f3c8: d06b beq.n f4a2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x182>

f3ca: eb0a 0149 add.w r1, sl, r9, lsl #1

f3ce: 6a3b ldr r3, [r7, #32]

f3d0: 4608 mov r0, r1

f3d2: 2100 movs r1, #0

f3d4: 464e mov r6, r9

f3d6: eba3 0209 sub.w r2, r3, r9

f3da: ea4f 0b49 mov.w fp, r9, lsl #1

f3de: 0052 lsls r2, r2, #1

f3e0: f003 ee36 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

f3e4: ea4f 0389 mov.w r3, r9, lsl #2

f3e8: f109 0901 add.w r9, r9, #1

f3ec: 61fb str r3, [r7, #28]

f3ee: e7c6 b.n f37e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5e>

f3f0: 3904 subs r1, #4

f3f2: f851 3f04 ldr.w r3, [r1, #4]!

f3f6: 2b00 cmp r3, #0

f3f8: d0de beq.n f3b8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x98>

f3fa: 42a3 cmp r3, r4

f3fc: d1f9 bne.n f3f2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xd2>

f3fe: 69fa ldr r2, [r7, #28]

f400: 1bab subs r3, r5, r6

f402: 4660 mov r0, ip

f404: 60fb str r3, [r7, #12]

f406: 1d11 adds r1, r2, #4

f408: 69ba ldr r2, [r7, #24]

f40a: 4411 add r1, r2

f40c: 009a lsls r2, r3, #2

f40e: f003 ec80 blx 12d10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1fa8>

f412: 693b ldr r3, [r7, #16]

f414: f8c8 4000 str.w r4, [r8]

f418: b143 cbz r3, f42c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x10c>

f41a: 4619 mov r1, r3

f41c: 1998 adds r0, r3, r6

f41e: 68fa ldr r2, [r7, #12]

f420: 4449 add r1, r9

f422: 5d9c ldrb r4, [r3, r6]

f424: f003 ec74 blx 12d10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1fa8>

f428: 693b ldr r3, [r7, #16]

f42a: 555c strb r4, [r3, r5]

f42c: 697b ldr r3, [r7, #20]

f42e: ea4f 0049 mov.w r0, r9, lsl #1

f432: eb0a 0100 add.w r1, sl, r0

f436: 885c ldrh r4, [r3, #2]

f438: 6a3b ldr r3, [r7, #32]

f43a: 1b9a subs r2, r3, r6

f43c: 4294 cmp r4, r2

f43e: d8c6 bhi.n f3ce <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xae>

f440: 68fb ldr r3, [r7, #12]

f442: 3802 subs r0, #2

f444: 4450 add r0, sl

f446: 005a lsls r2, r3, #1

f448: f003 ec62 blx 12d10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1fa8>

f44c: 68bb ldr r3, [r7, #8]

f44e: f82a 3015 strh.w r3, [sl, r5, lsl #1]

f452: e794 b.n f37e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5e>

f454: f8d0 31fc ldr.w r3, [r0, #508] ; 0x1fc

f458: 2b00 cmp r3, #0

f45a: d0b0 beq.n f3be <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9e>

f45c: 6819 ldr r1, [r3, #0]

f45e: 4686 mov lr, r0

f460: eb03 0381 add.w r3, r3, r1, lsl #2

f464: 2900 cmp r1, #0

f466: d0aa beq.n f3be <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9e>

f468: f853 0904 ldr.w r0, [r3], #-4

f46c: 3901 subs r1, #1

f46e: 42a0 cmp r0, r4

f470: d1f8 bne.n f464 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x144>

f472: f8d4 11f8 ldr.w r1, [r4, #504] ; 0x1f8

f476: 4670 mov r0, lr

f478: 2900 cmp r1, #0

f47a: d0c0 beq.n f3fe <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xde>

f47c: 3904 subs r1, #4

f47e: e001 b.n f484 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x164>

f480: 4298 cmp r0, r3

f482: d09c beq.n f3be <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9e>

f484: f851 3f04 ldr.w r3, [r1, #4]!

f488: 2b00 cmp r3, #0

f48a: d1f9 bne.n f480 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x160>

f48c: e7b7 b.n f3fe <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xde>

f48e: 6962 ldr r2, [r4, #20]

f490: 42a2 cmp r2, r4

f492: d197 bne.n f3c4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa4>

f494: f8d4 2214 ldr.w r2, [r4, #532] ; 0x214

f498: 3201 adds r2, #1

f49a: d181 bne.n f3a0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x80>

f49c: 6a3b ldr r3, [r7, #32]

f49e: 454b cmp r3, r9

f4a0: d193 bne.n f3ca <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xaa>

f4a2: f8d7 d000 ldr.w sp, [r7]

f4a6: 372c adds r7, #44 ; 0x2c

f4a8: 46bd mov sp, r7

f4aa: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

f4ae: 4770 bx lr

f4b0: b4f0 push {r4, r5, r6, r7}

f4b2: 260a movs r6, #10

f4b4: 7805 ldrb r5, [r0, #0]

f4b6: 780c ldrb r4, [r1, #0]

f4b8: b19d cbz r5, f4e2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1c2>

f4ba: f1a5 0330 sub.w r3, r5, #48 ; 0x30

f4be: f1a4 0230 sub.w r2, r4, #48 ; 0x30

f4c2: fa5f fc83 uxtb.w ip, r3

f4c6: b2d7 uxtb r7, r2

f4c8: f1bc 0f09 cmp.w ip, #9

f4cc: d90c bls.n f4e8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1c8>

f4ce: 2f09 cmp r7, #9

f4d0: d931 bls.n f536 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x216>

f4d2: 42a5 cmp r5, r4

f4d4: d133 bne.n f53e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x21e>

f4d6: 3001 adds r0, #1

f4d8: 7805 ldrb r5, [r0, #0]

f4da: 3101 adds r1, #1

f4dc: 780c ldrb r4, [r1, #0]

f4de: 2d00 cmp r5, #0

f4e0: d1eb bne.n f4ba <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x19a>

f4e2: 4260 negs r0, r4

f4e4: bcf0 pop {r4, r5, r6, r7}

f4e6: 4770 bx lr

f4e8: 2f09 cmp r7, #9

f4ea: d82b bhi.n f544 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x224>

f4ec: 7844 ldrb r4, [r0, #1]

f4ee: 1c4f adds r7, r1, #1

f4f0: 3001 adds r0, #1

f4f2: f1a4 0530 sub.w r5, r4, #48 ; 0x30

f4f6: 2d09 cmp r5, #9

f4f8: d809 bhi.n f50e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1ee>

f4fa: fb06 4303 mla r3, r6, r3, r4

f4fe: f810 4f01 ldrb.w r4, [r0, #1]!

f502: f1a4 0530 sub.w r5, r4, #48 ; 0x30

f506: 2d09 cmp r5, #9

f508: f1a3 0330 sub.w r3, r3, #48 ; 0x30

f50c: d9f5 bls.n f4fa <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1da>

f50e: 784c ldrb r4, [r1, #1]

f510: f1a4 0130 sub.w r1, r4, #48 ; 0x30

f514: 2909 cmp r1, #9

f516: 4639 mov r1, r7

f518: d809 bhi.n f52e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x20e>

f51a: fb06 4202 mla r2, r6, r2, r4

f51e: f811 4f01 ldrb.w r4, [r1, #1]!

f522: f1a4 0530 sub.w r5, r4, #48 ; 0x30

f526: 2d09 cmp r5, #9

f528: f1a2 0230 sub.w r2, r2, #48 ; 0x30

f52c: d9f5 bls.n f51a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1fa>

f52e: 4293 cmp r3, r2

f530: d0c0 beq.n f4b4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x194>

f532: 1a98 subs r0, r3, r2

f534: e7d6 b.n f4e4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1c4>

f536: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

f53a: bcf0 pop {r4, r5, r6, r7}

f53c: 4770 bx lr

f53e: 1b28 subs r0, r5, r4

f540: bcf0 pop {r4, r5, r6, r7}

f542: 4770 bx lr

f544: 2001 movs r0, #1

f546: e7cd b.n f4e4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x1c4>

f548: 4bfd ldr r3, [pc, #1012] ; (f940 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x620>)

f54a: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

f54e: 447b add r3, pc

f550: b08b sub sp, #44 ; 0x2c

f552: 681b ldr r3, [r3, #0]

f554: af00 add r7, sp, #0

f556: 4605 mov r5, r0

f558: 07db lsls r3, r3, #31

f55a: f100 81b5 bmi.w f8c8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5a8>

f55e: 4cf9 ldr r4, [pc, #996] ; (f944 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x624>)

f560: 447c add r4, pc

f562: f8d4 a000 ldr.w sl, [r4]

f566: f1ba 0f00 cmp.w sl, #0

f56a: f000 817e beq.w f86a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x54a>

f56e: f1ba 3fff cmp.w sl, #4294967295 ; 0xffffffff

f572: f000 81a0 beq.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f576: 4cf4 ldr r4, [pc, #976] ; (f948 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x628>)

f578: 447c add r4, pc

f57a: e9d4 3901 ldrd r3, r9, [r4, #4]

f57e: f1b9 3fff cmp.w r9, #4294967295 ; 0xffffffff

f582: eb0a 0803 add.w r8, sl, r3

f586: f000 80dd beq.w f744 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x424>

f58a: 2200 movs r2, #0

f58c: f107 0120 add.w r1, r7, #32

f590: 200b movs r0, #11

f592: eba8 0809 sub.w r8, r8, r9

f596: f000 fc53 bl fe40 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE>

f59a: 4bec ldr r3, [pc, #944] ; (f94c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x62c>)

f59c: 6a3a ldr r2, [r7, #32]

f59e: 447b add r3, pc

f5a0: f8d4 a008 ldr.w sl, [r4, #8]

f5a4: 60ba str r2, [r7, #8]

f5a6: 6c1a ldr r2, [r3, #64] ; 0x40

f5a8: 6c5b ldr r3, [r3, #68] ; 0x44

f5aa: f8da 6014 ldr.w r6, [sl, #20]

f5ae: 60fa str r2, [r7, #12]

f5b0: 61bb str r3, [r7, #24]

f5b2: 3e01 subs r6, #1

f5b4: 6a7b ldr r3, [r7, #36] ; 0x24

f5b6: 607b str r3, [r7, #4]

f5b8: f100 817d bmi.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f5bc: 1074 asrs r4, r6, #1

f5be: f04f 0b18 mov.w fp, #24

f5c2: fb0b a304 mla r3, fp, r4, sl

f5c6: 6b59 ldr r1, [r3, #52] ; 0x34

f5c8: 4541 cmp r1, r8

f5ca: f080 8174 bcs.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f5ce: f8c7 b01c str.w fp, [r7, #28]

f5d2: f04f 0b00 mov.w fp, #0

f5d6: e012 b.n f5fe <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2de>

f5d8: bfb4 ite lt

f5da: f104 0b01 addlt.w fp, r4, #1

f5de: f104 36ff addge.w r6, r4, #4294967295 ; 0xffffffff

f5e2: eb0b 0406 add.w r4, fp, r6

f5e6: 45b3 cmp fp, r6

f5e8: ea4f 0464 mov.w r4, r4, asr #1

f5ec: f300 8163 bgt.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f5f0: 69fb ldr r3, [r7, #28]

f5f2: fb03 a304 mla r3, r3, r4, sl

f5f6: 6b59 ldr r1, [r3, #52] ; 0x34

f5f8: 4588 cmp r8, r1

f5fa: f240 815c bls.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f5fe: 4449 add r1, r9

f600: 4628 mov r0, r5

f602: f7ff ff55 bl f4b0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x190>

f606: 2800 cmp r0, #0

f608: d1e6 bne.n f5d8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x2b8>

f60a: 2c00 cmp r4, #0

f60c: f000 818f beq.w f92e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x60e>

f610: f104 3bff add.w fp, r4, #4294967295 ; 0xffffffff

f614: 2218 movs r2, #24

f616: fb02 a10b mla r1, r2, fp, sl

f61a: 6b49 ldr r1, [r1, #52] ; 0x34

f61c: 4541 cmp r1, r8

f61e: f080 8186 bcs.w f92e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x60e>

f622: fb02 a204 mla r2, r2, r4, sl

f626: 61fc str r4, [r7, #28]

f628: 603e str r6, [r7, #0]

f62a: 465c mov r4, fp

f62c: 4616 mov r6, r2

f62e: e007 b.n f640 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x320>

f630: 2c00 cmp r4, #0

f632: f000 8145 beq.w f8c0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5a0>

f636: 69f1 ldr r1, [r6, #28]

f638: 4588 cmp r8, r1

f63a: f240 8141 bls.w f8c0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5a0>

f63e: 4664 mov r4, ip

f640: 4449 add r1, r9

f642: 4628 mov r0, r5

f644: f7ff ff34 bl f4b0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x190>

f648: f104 0b01 add.w fp, r4, #1

f64c: 3e18 subs r6, #24

f64e: f104 3cff add.w ip, r4, #4294967295 ; 0xffffffff

f652: 2800 cmp r0, #0

f654: d0ec beq.n f630 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x310>

f656: 69fc ldr r4, [r7, #28]

f658: 683e ldr r6, [r7, #0]

f65a: 49bd ldr r1, [pc, #756] ; (f950 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x630>)

f65c: 2218 movs r2, #24

f65e: 687b ldr r3, [r7, #4]

f660: 4479 add r1, pc

f662: fb02 aa0b mla sl, r2, fp, sl

f666: 69ba ldr r2, [r7, #24]

f668: 401a ands r2, r3

f66a: 6bcb ldr r3, [r1, #60] ; 0x3c

f66c: f042 4200 orr.w r2, r2, #2147483648 ; 0x80000000

f670: 61bb str r3, [r7, #24]

f672: 684b ldr r3, [r1, #4]

f674: 603b str r3, [r7, #0]

f676: 2300 movs r3, #0

f678: 61fb str r3, [r7, #28]

f67a: 43d3 mvns r3, r2

f67c: 607b str r3, [r7, #4]

f67e: e008 b.n f692 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x372>

f680: 69ba ldr r2, [r7, #24]

f682: 4282 cmp r2, r0

f684: d01f beq.n f6c6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3a6>

f686: f10b 0b01 add.w fp, fp, #1

f68a: f10a 0a18 add.w sl, sl, #24

f68e: 45b3 cmp fp, r6

f690: dc37 bgt.n f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f692: 45a3 cmp fp, r4

f694: dd08 ble.n f6a8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x388>

f696: f8da 1034 ldr.w r1, [sl, #52] ; 0x34

f69a: 4541 cmp r1, r8

f69c: d231 bcs.n f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f69e: 4449 add r1, r9

f6a0: 4628 mov r0, r5

f6a2: f7ff ff05 bl f4b0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x190>

f6a6: bb60 cbnz r0, f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f6a8: f8da 0030 ldr.w r0, [sl, #48] ; 0x30

f6ac: f640 1203 movw r2, #2307 ; 0x903

f6b0: 2803 cmp r0, #3

f6b2: bf18 it ne

f6b4: 4290 cmpne r0, r2

f6b6: d1e6 bne.n f686 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x366>

f6b8: f8da 3038 ldr.w r3, [sl, #56] ; 0x38

f6bc: 4543 cmp r3, r8

f6be: d2e2 bcs.n f686 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x366>

f6c0: 69fa ldr r2, [r7, #28]

f6c2: 2a00 cmp r2, #0

f6c4: d1dc bne.n f680 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x360>

f6c6: 68fa ldr r2, [r7, #12]

f6c8: f8da 1040 ldr.w r1, [sl, #64] ; 0x40

f6cc: 4694 mov ip, r2

f6ce: 68ba ldr r2, [r7, #8]

f6d0: ea0c 0e02 and.w lr, ip, r2

f6d4: f8da 2044 ldr.w r2, [sl, #68] ; 0x44

f6d8: ea21 010e bic.w r1, r1, lr

f6dc: 6139 str r1, [r7, #16]

f6de: 6879 ldr r1, [r7, #4]

f6e0: 400a ands r2, r1

f6e2: 617a str r2, [r7, #20]

f6e4: e9d7 1204 ldrd r1, r2, [r7, #16]

f6e8: 430a orrs r2, r1

f6ea: d1cc bne.n f686 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x366>

f6ec: 6839 ldr r1, [r7, #0]

f6ee: b119 cbz r1, f6f8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3d8>

f6f0: f8da 203c ldr.w r2, [sl, #60] ; 0x3c

f6f4: 4291 cmp r1, r2

f6f6: d3c6 bcc.n f686 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x366>

f6f8: 444b add r3, r9

f6fa: 61fb str r3, [r7, #28]

f6fc: 69bb ldr r3, [r7, #24]

f6fe: 4283 cmp r3, r0

f700: d1c1 bne.n f686 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x366>

f702: 4b94 ldr r3, [pc, #592] ; (f954 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x634>)

f704: 447b add r3, pc

f706: 681b ldr r3, [r3, #0]

f708: f013 0f01 tst.w r3, #1

f70c: 69fb ldr r3, [r7, #28]

f70e: f040 80e2 bne.w f8d6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5b6>

f712: 2b00 cmp r3, #0

f714: f000 80cf beq.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f718: 69fc ldr r4, [r7, #28]

f71a: 4620 mov r0, r4

f71c: f003 f810 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

f720: 4621 mov r1, r4

f722: f100 0308 add.w r3, r0, #8

f726: 1c42 adds r2, r0, #1

f728: f023 0307 bic.w r3, r3, #7

f72c: ebad 0d03 sub.w sp, sp, r3

f730: 466b mov r3, sp

f732: 4618 mov r0, r3

f734: f003 ece4 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

f738: f002 ffea bl 12710 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19a8>

f73c: 372c adds r7, #44 ; 0x2c

f73e: 46bd mov sp, r7

f740: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

f744: f8da 600c ldr.w r6, [sl, #12]

f748: f04f 0b0c mov.w fp, #12

f74c: fb0b f906 mul.w r9, fp, r6

f750: 3e01 subs r6, #1

f752: f109 0910 add.w r9, r9, #16

f756: eba3 0809 sub.w r8, r3, r9

f75a: 44d1 add r9, sl

f75c: f100 80ab bmi.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f760: 1074 asrs r4, r6, #1

f762: fb0b a304 mla r3, fp, r4, sl

f766: 6959 ldr r1, [r3, #20]

f768: 4588 cmp r8, r1

f76a: f240 80a4 bls.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f76e: f8c7 b01c str.w fp, [r7, #28]

f772: f04f 0b00 mov.w fp, #0

f776: e012 b.n f79e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x47e>

f778: bfb4 ite lt

f77a: f104 0b01 addlt.w fp, r4, #1

f77e: f104 36ff addge.w r6, r4, #4294967295 ; 0xffffffff

f782: eb0b 0406 add.w r4, fp, r6

f786: 45b3 cmp fp, r6

f788: ea4f 0464 mov.w r4, r4, asr #1

f78c: f300 8093 bgt.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f790: 69fb ldr r3, [r7, #28]

f792: fb03 a304 mla r3, r3, r4, sl

f796: 6959 ldr r1, [r3, #20]

f798: 4588 cmp r8, r1

f79a: f240 808c bls.w f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f79e: 4449 add r1, r9

f7a0: 4628 mov r0, r5

f7a2: f7ff fe85 bl f4b0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x190>

f7a6: 2800 cmp r0, #0

f7a8: d1e6 bne.n f778 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x458>

f7aa: 2c00 cmp r4, #0

f7ac: f000 80c1 beq.w f932 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x612>

f7b0: f104 3bff add.w fp, r4, #4294967295 ; 0xffffffff

f7b4: 220c movs r2, #12

f7b6: fb02 a10b mla r1, r2, fp, sl

f7ba: 6949 ldr r1, [r1, #20]

f7bc: 4588 cmp r8, r1

f7be: f240 80b8 bls.w f932 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x612>

f7c2: fb02 a204 mla r2, r2, r4, sl

f7c6: 61fc str r4, [r7, #28]

f7c8: 613e str r6, [r7, #16]

f7ca: 465c mov r4, fp

f7cc: 4616 mov r6, r2

f7ce: e005 b.n f7dc <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4bc>

f7d0: 2c00 cmp r4, #0

f7d2: d077 beq.n f8c4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5a4>

f7d4: 68b1 ldr r1, [r6, #8]

f7d6: 4588 cmp r8, r1

f7d8: d974 bls.n f8c4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5a4>

f7da: 4664 mov r4, ip

f7dc: 4449 add r1, r9

f7de: 4628 mov r0, r5

f7e0: f7ff fe66 bl f4b0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x190>

f7e4: f104 0b01 add.w fp, r4, #1

f7e8: 3e0c subs r6, #12

f7ea: f104 3cff add.w ip, r4, #4294967295 ; 0xffffffff

f7ee: 2800 cmp r0, #0

f7f0: d0ee beq.n f7d0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4b0>

f7f2: 69fc ldr r4, [r7, #28]

f7f4: 693e ldr r6, [r7, #16]

f7f6: 4958 ldr r1, [pc, #352] ; (f958 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x638>)

f7f8: 2300 movs r3, #0

f7fa: 61fb str r3, [r7, #28]

f7fc: 220c movs r2, #12

f7fe: 4479 add r1, pc

f800: fb02 aa0b mla sl, r2, fp, sl

f804: 6bcb ldr r3, [r1, #60] ; 0x3c

f806: 613b str r3, [r7, #16]

f808: e008 b.n f81c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4fc>

f80a: 693b ldr r3, [r7, #16]

f80c: 428b cmp r3, r1

f80e: f000 8092 beq.w f936 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x616>

f812: 45b3 cmp fp, r6

f814: f10a 0a0c add.w sl, sl, #12

f818: f73f af73 bgt.w f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f81c: 45a3 cmp fp, r4

f81e: 4628 mov r0, r5

f820: f10b 0b01 add.w fp, fp, #1

f824: dd0b ble.n f83e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x51e>

f826: f8da 3014 ldr.w r3, [sl, #20]

f82a: 4543 cmp r3, r8

f82c: eb09 0103 add.w r1, r9, r3

f830: f4bf af67 bcs.w f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f834: f7ff fe3c bl f4b0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x190>

f838: 2800 cmp r0, #0

f83a: f47f af62 bne.w f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f83e: f8da 1010 ldr.w r1, [sl, #16]

f842: f640 1303 movw r3, #2307 ; 0x903

f846: 2903 cmp r1, #3

f848: bf18 it ne

f84a: 4299 cmpne r1, r3

f84c: d1e1 bne.n f812 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4f2>

f84e: f8da 0018 ldr.w r0, [sl, #24]

f852: 4540 cmp r0, r8

f854: d2dd bcs.n f812 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4f2>

f856: 69fb ldr r3, [r7, #28]

f858: 2b00 cmp r3, #0

f85a: d1d6 bne.n f80a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4ea>

f85c: eb09 0300 add.w r3, r9, r0

f860: 61fb str r3, [r7, #28]

f862: 693b ldr r3, [r7, #16]

f864: 428b cmp r3, r1

f866: d1d4 bne.n f812 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4f2>

f868: e74b b.n f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f86a: 483c ldr r0, [pc, #240] ; (f95c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x63c>)

f86c: 2201 movs r2, #1

f86e: 1d21 adds r1, r4, #4

f870: 4478 add r0, pc

f872: f7fc fa53 bl bd1c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x33c>

f876: f1b0 3fff cmp.w r0, #4294967295 ; 0xffffffff

f87a: 4682 mov sl, r0

f87c: d016 beq.n f8ac <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x58c>

f87e: 6866 ldr r6, [r4, #4]

f880: 2e10 cmp r6, #16

f882: d90f bls.n f8a4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x584>

f884: 4936 ldr r1, [pc, #216] ; (f960 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x640>)

f886: 220b movs r2, #11

f888: 4479 add r1, pc

f88a: f003 f8d7 bl 12a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cd4>

f88e: b350 cbz r0, f8e6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5c6>

f890: 2e30 cmp r6, #48 ; 0x30

f892: d907 bls.n f8a4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x584>

f894: 4933 ldr r1, [pc, #204] ; (f964 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x644>)

f896: 2214 movs r2, #20

f898: 4650 mov r0, sl

f89a: 4479 add r1, pc

f89c: f003 f8ce bl 12a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cd4>

f8a0: 2800 cmp r0, #0

f8a2: d03f beq.n f924 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x604>

f8a4: 4631 mov r1, r6

f8a6: 4650 mov r0, sl

f8a8: f002 fa8a bl 11dc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1058>

f8ac: 4b2e ldr r3, [pc, #184] ; (f968 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x648>)

f8ae: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

f8b2: 447b add r3, pc

f8b4: 601a str r2, [r3, #0]

f8b6: 2000 movs r0, #0

f8b8: 372c adds r7, #44 ; 0x2c

f8ba: 46bd mov sp, r7

f8bc: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

f8c0: 46a3 mov fp, r4

f8c2: e6c8 b.n f656 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x336>

f8c4: 46a3 mov fp, r4

f8c6: e794 b.n f7f2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4d2>

f8c8: 4928 ldr r1, [pc, #160] ; (f96c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x64c>)

f8ca: 4829 ldr r0, [pc, #164] ; (f970 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x650>)

f8cc: 4479 add r1, pc

f8ce: 4478 add r0, pc

f8d0: f7fc fa50 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

f8d4: e643 b.n f55e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x23e>

f8d6: 2b00 cmp r3, #0

f8d8: d0ed beq.n f8b6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x596>

f8da: 4826 ldr r0, [pc, #152] ; (f974 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x654>)

f8dc: 4619 mov r1, r3

f8de: 4478 add r0, pc

f8e0: f7fc fa48 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

f8e4: e718 b.n f718 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3f8>

f8e6: f8da 200c ldr.w r2, [sl, #12]

f8ea: 230c movs r3, #12

f8ec: f8c4 a000 str.w sl, [r4]

f8f0: fb03 f302 mul.w r3, r3, r2

f8f4: 3317 adds r3, #23

f8f6: f023 0307 bic.w r3, r3, #7

f8fa: f103 0230 add.w r2, r3, #48 ; 0x30

f8fe: eb0a 0003 add.w r0, sl, r3

f902: 4296 cmp r6, r2

f904: 60a0 str r0, [r4, #8]

f906: d307 bcc.n f918 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x5f8>

f908: 491b ldr r1, [pc, #108] ; (f978 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x658>)

f90a: 2214 movs r2, #20

f90c: 4479 add r1, pc

f90e: f003 f895 bl 12a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cd4>

f912: 2800 cmp r0, #0

f914: f43f ae2f beq.w f576 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x256>

f918: 4b18 ldr r3, [pc, #96] ; (f97c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x65c>)

f91a: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

f91e: 447b add r3, pc

f920: 609a str r2, [r3, #8]

f922: e628 b.n f576 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x256>

f924: f8c4 a008 str.w sl, [r4, #8]

f928: f8c4 a000 str.w sl, [r4]

f92c: e623 b.n f576 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x256>

f92e: 46a3 mov fp, r4

f930: e693 b.n f65a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x33a>

f932: 46a3 mov fp, r4

f934: e75f b.n f7f6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x4d6>

f936: eb09 0300 add.w r3, r9, r0

f93a: 61fb str r3, [r7, #28]

f93c: e6e1 b.n f702 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x3e2>

f93e: bf00 nop

f940: 941e str r4, [sp, #120] ; 0x78

f942: 0001 movs r1, r0

f944: a3ac add r3, pc, #688 ; (adr r3, fbf8 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8d8>)

f946: 0001 movs r1, r0

f948: a394 add r3, pc, #592 ; (adr r3, fb9c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x87c>)

f94a: 0001 movs r1, r0

f94c: 93ce str r3, [sp, #824] ; 0x338

f94e: 0001 movs r1, r0

f950: 930c str r3, [sp, #48] ; 0x30

f952: 0001 movs r1, r0

f954: 9268 str r2, [sp, #416] ; 0x1a0

f956: 0001 movs r1, r0

f958: 916e str r1, [sp, #440] ; 0x1b8

f95a: 0001 movs r1, r0

f95c: 7c24 ldrb r4, [r4, #16]

f95e: 0000 movs r0, r0

f960: 7c34 ldrb r4, [r6, #16]

f962: 0000 movs r0, r0

f964: 7c2e ldrb r6, [r5, #16]

f966: 0000 movs r0, r0

f968: a05a add r0, pc, #360 ; (adr r0, fad4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x7b4>)

f96a: 0001 movs r1, r0

f96c: 7bc8 ldrb r0, [r1, #15]

f96e: 0000 movs r0, r0

f970: 7bda ldrb r2, [r3, #15]

f972: 0000 movs r0, r0

f974: 652e str r6, [r5, #80] ; 0x50

f976: 0000 movs r0, r0

f978: 7bbc ldrb r4, [r7, #14]

f97a: 0000 movs r0, r0

f97c: 9fee ldr r7, [sp, #952] ; 0x3b8

f97e: 0001 movs r1, r0

f980: b510 push {r4, lr}

f982: 4c06 ldr r4, [pc, #24] ; (f99c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x67c>)

f984: 447c add r4, pc

f986: 6820 ldr r0, [r4, #0]

f988: 1e43 subs r3, r0, #1

f98a: 3303 adds r3, #3

f98c: d900 bls.n f990 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x670>

f98e: bd10 pop {r4, pc}

f990: 6861 ldr r1, [r4, #4]

f992: f002 fa15 bl 11dc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1058>

f996: 2300 movs r3, #0

f998: 6023 str r3, [r4, #0]

f99a: bd10 pop {r4, pc}

f99c: 9f88 ldr r7, [sp, #544] ; 0x220

f99e: 0001 movs r1, r0

f9a0: e92d 0370 stmdb sp!, {r4, r5, r6, r8, r9}

f9a4: 6886 ldr r6, [r0, #8]

f9a6: 2e03 cmp r6, #3

f9a8: bf08 it eq

f9aa: 6201 streq r1, [r0, #32]

f9ac: d01b beq.n f9e6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6c6>

f9ae: 2e01 cmp r6, #1

f9b0: e9d1 8900 ldrd r8, r9, [r1]

f9b4: e9d0 4504 ldrd r4, r5, [r0, #16]

f9b8: e9d0 2306 ldrd r2, r3, [r0, #24]

f9bc: d02b beq.n fa16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6f6>

f9be: b1ae cbz r6, f9ec <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6cc>

f9c0: 45a9 cmp r9, r5

f9c2: bf08 it eq

f9c4: 45a0 cmpeq r8, r4

f9c6: bf2c ite cs

f9c8: 2101 movcs r1, #1

f9ca: 2100 movcc r1, #0

f9cc: 454b cmp r3, r9

f9ce: f001 0101 and.w r1, r1, #1

f9d2: bf08 it eq

f9d4: 4542 cmpeq r2, r8

f9d6: bf38 it cc

f9d8: 2100 movcc r1, #0

f9da: b121 cbz r1, f9e6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6c6>

f9dc: 2301 movs r3, #1

f9de: e9c0 8908 strd r8, r9, [r0, #32]

f9e2: f880 3028 strb.w r3, [r0, #40] ; 0x28

f9e6: e8bd 0370 ldmia.w sp!, {r4, r5, r6, r8, r9}

f9ea: 4770 bx lr

f9ec: 45a0 cmp r8, r4

f9ee: eb79 0105 sbcs.w r1, r9, r5

f9f2: bfac ite ge

f9f4: 2101 movge r1, #1

f9f6: 2100 movlt r1, #0

f9f8: 4542 cmp r2, r8

f9fa: f001 0101 and.w r1, r1, #1

f9fe: eb73 0309 sbcs.w r3, r3, r9

fa02: bfb8 it lt

fa04: 2100 movlt r1, #0

fa06: 2900 cmp r1, #0

fa08: d0ed beq.n f9e6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6c6>

fa0a: 2301 movs r3, #1

fa0c: e9c0 8908 strd r8, r9, [r0, #32]

fa10: f880 3028 strb.w r3, [r0, #40] ; 0x28

fa14: e7e7 b.n f9e6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6c6>

fa16: 45a9 cmp r9, r5

fa18: bf08 it eq

fa1a: 45a0 cmpeq r8, r4

fa1c: bf2c ite cs

fa1e: 2101 movcs r1, #1

fa20: 2100 movcc r1, #0

fa22: 454b cmp r3, r9

fa24: f001 0101 and.w r1, r1, #1

fa28: bf08 it eq

fa2a: 4542 cmpeq r2, r8

fa2c: bf38 it cc

fa2e: 2100 movcc r1, #0

fa30: 2900 cmp r1, #0

fa32: d0d8 beq.n f9e6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x6c6>

fa34: e9c0 8908 strd r8, r9, [r0, #32]

fa38: f880 6028 strb.w r6, [r0, #40] ; 0x28

fa3c: e8bd 0370 ldmia.w sp!, {r4, r5, r6, r8, r9}

fa40: 4770 bx lr

fa42: bf00 nop

fa44: 6882 ldr r2, [r0, #8]

fa46: 460b mov r3, r1

fa48: b510 push {r4, lr}

fa4a: 2a03 cmp r2, #3

fa4c: b082 sub sp, #8

fa4e: 4604 mov r4, r0

fa50: bf04 itt eq

fa52: 2201 moveq r2, #1

fa54: f880 2028 strbeq.w r2, [r0, #40] ; 0x28

fa58: d006 beq.n fa68 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x748>

fa5a: 4608 mov r0, r1

fa5c: 2100 movs r1, #0

fa5e: f7fc fa0d bl be7c <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x49c>

fa62: 466b mov r3, sp

fa64: e9cd 0100 strd r0, r1, [sp]

fa68: 4619 mov r1, r3

fa6a: 4620 mov r0, r4

fa6c: f7ff ff98 bl f9a0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x680>

fa70: b002 add sp, #8

fa72: bd10 pop {r4, pc}

fa74: 4b02 ldr r3, [pc, #8] ; (fa80 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x760>)

fa76: 2238 movs r2, #56 ; 0x38

fa78: 447b add r3, pc

fa7a: fb02 3000 mla r0, r2, r0, r3

fa7e: e78f b.n f9a0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x680>

fa80: 90cc str r0, [sp, #816] ; 0x330

fa82: 0001 movs r1, r0

fa84: 4be2 ldr r3, [pc, #904] ; (fe10 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xaf0>)

fa86: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

fa8a: 447b add r3, pc

fa8c: b089 sub sp, #36 ; 0x24

fa8e: 4682 mov sl, r0

fa90: 681b ldr r3, [r3, #0]

fa92: 2b00 cmp r3, #0

fa94: f040 81a7 bne.w fde6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xac6>

fa98: 4bde ldr r3, [pc, #888] ; (fe14 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xaf4>)

fa9a: 447b add r3, pc

fa9c: 9303 str r3, [sp, #12]

fa9e: 4bde ldr r3, [pc, #888] ; (fe18 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xaf8>)

faa0: 447b add r3, pc

faa2: 9304 str r3, [sp, #16]

faa4: 4bdd ldr r3, [pc, #884] ; (fe1c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xafc>)

faa6: 447b add r3, pc

faa8: 9307 str r3, [sp, #28]

faaa: 4bdd ldr r3, [pc, #884] ; (fe20 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb00>)

faac: 447b add r3, pc

faae: 9305 str r3, [sp, #20]

fab0: f1ba 0f00 cmp.w sl, #0

fab4: d069 beq.n fb8a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x86a>

fab6: f8da 4000 ldr.w r4, [sl]

faba: 2c00 cmp r4, #0

fabc: d065 beq.n fb8a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x86a>

fabe: 7827 ldrb r7, [r4, #0]

fac0: f10a 0004 add.w r0, sl, #4

fac4: f1b7 063d subs.w r6, r7, #61 ; 0x3d

fac8: bf18 it ne

faca: 2601 movne r6, #1

facc: 2f00 cmp r7, #0

face: bf08 it eq

fad0: 2600 moveq r6, #0

fad2: 2e00 cmp r6, #0

fad4: f000 8160 beq.w fd98 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa78>

fad8: f1c4 0101 rsb r1, r4, #1

fadc: 4622 mov r2, r4

fade: 188e adds r6, r1, r2

fae0: f812 3f01 ldrb.w r3, [r2, #1]!

fae4: 2b00 cmp r3, #0

fae6: bf18 it ne

fae8: 2b3d cmpne r3, #61 ; 0x3d

faea: d1f8 bne.n fade <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x7be>

faec: 2b00 cmp r3, #0

faee: f000 8117 beq.w fd20 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa00>

faf2: 3601 adds r6, #1

faf4: 4681 mov r9, r0

faf6: 19a3 adds r3, r4, r6

faf8: 9301 str r3, [sp, #4]

fafa: 2800 cmp r0, #0

fafc: d045 beq.n fb8a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x86a>

fafe: 2f47 cmp r7, #71 ; 0x47

fb00: d113 bne.n fb2a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x80a>

fb02: 9b03 ldr r3, [sp, #12]

fb04: 1c60 adds r0, r4, #1

fb06: 224c movs r2, #76 ; 0x4c

fb08: 1c5d adds r5, r3, #1

fb0a: e003 b.n fb14 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x7f4>

fb0c: f815 2f01 ldrb.w r2, [r5, #1]!

fb10: 2a00 cmp r2, #0

fb12: d050 beq.n fbb6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x896>

fb14: f810 1b01 ldrb.w r1, [r0], #1

fb18: fab1 f381 clz r3, r1

fb1c: 095b lsrs r3, r3, #5

fb1e: 4291 cmp r1, r2

fb20: bf18 it ne

fb22: f043 0301 orrne.w r3, r3, #1

fb26: 2b00 cmp r3, #0

fb28: d0f0 beq.n fb0c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x7ec>

fb2a: 4bbe ldr r3, [pc, #760] ; (fe24 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb04>)

fb2c: 2600 movs r6, #0

fb2e: 4dbe ldr r5, [pc, #760] ; (fe28 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb08>)

fb30: 46a4 mov ip, r4

fb32: 447b add r3, pc

fb34: 447d add r5, pc

fb36: 681f ldr r7, [r3, #0]

fb38: f895 3028 ldrb.w r3, [r5, #40] ; 0x28

fb3c: b9eb cbnz r3, fb7a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x85a>

fb3e: 6b2c ldr r4, [r5, #48] ; 0x30

fb40: b1dc cbz r4, fb7a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x85a>

fb42: 7823 ldrb r3, [r4, #0]

fb44: f89c 2000 ldrb.w r2, [ip]

fb48: b313 cbz r3, fb90 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x870>

fb4a: 1a9b subs r3, r3, r2

fb4c: bf18 it ne

fb4e: 2301 movne r3, #1

fb50: 2a00 cmp r2, #0

fb52: bf14 ite ne

fb54: 461a movne r2, r3

fb56: 2201 moveq r2, #1

fb58: b97a cbnz r2, fb7a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x85a>

fb5a: 46e6 mov lr, ip

fb5c: 4620 mov r0, r4

fb5e: f810 1f01 ldrb.w r1, [r0, #1]!

fb62: f81e 2f01 ldrb.w r2, [lr, #1]!

fb66: fab2 f382 clz r3, r2

fb6a: b189 cbz r1, fb90 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x870>

fb6c: 095b lsrs r3, r3, #5

fb6e: 4291 cmp r1, r2

fb70: bf18 it ne

fb72: f043 0301 orrne.w r3, r3, #1

fb76: 2b00 cmp r3, #0

fb78: d0f1 beq.n fb5e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x83e>

fb7a: 3601 adds r6, #1

fb7c: 3538 adds r5, #56 ; 0x38

fb7e: 2e12 cmp r6, #18

fb80: d1da bne.n fb38 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x818>

fb82: 46ca mov sl, r9

fb84: f1ba 0f00 cmp.w sl, #0

fb88: d195 bne.n fab6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x796>

fb8a: b009 add sp, #36 ; 0x24

fb8c: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

fb90: 2a3d cmp r2, #61 ; 0x3d

fb92: d1f2 bne.n fb7a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x85a>

fb94: b12f cbz r7, fba2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x882>

fb96: 6aeb ldr r3, [r5, #44] ; 0x2c

fb98: 2b00 cmp r3, #0

fb9a: f000 80cd beq.w fd38 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa18>

fb9e: 2b02 cmp r3, #2

fba0: d1eb bne.n fb7a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x85a>

fba2: 4ba2 ldr r3, [pc, #648] ; (fe2c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb0c>)

fba4: 2038 movs r0, #56 ; 0x38

fba6: 9901 ldr r1, [sp, #4]

fba8: 46ca mov sl, r9

fbaa: 447b add r3, pc

fbac: fb00 3006 mla r0, r0, r6, r3

fbb0: f7ff ff48 bl fa44 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x724>

fbb4: e77c b.n fab0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x790>

fbb6: 7803 ldrb r3, [r0, #0]

fbb8: 2b3d cmp r3, #61 ; 0x3d

fbba: bf04 itt eq

fbbc: 4615 moveq r5, r2

fbbe: 4623 moveq r3, r4

fbc0: d1b3 bne.n fb2a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x80a>

fbc2: 1c68 adds r0, r5, #1

fbc4: b12f cbz r7, fbd2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8b2>

fbc6: f813 7f01 ldrb.w r7, [r3, #1]!

fbca: 4605 mov r5, r0

fbcc: 1c68 adds r0, r5, #1

fbce: 2f00 cmp r7, #0

fbd0: d1f9 bne.n fbc6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8a6>

fbd2: f001 fa25 bl 11020 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b8>

fbd6: 1c43 adds r3, r0, #1

fbd8: 9002 str r0, [sp, #8]

fbda: f000 80e3 beq.w fda4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa84>

fbde: 9a02 ldr r2, [sp, #8]

fbe0: 1963 adds r3, r4, r5

fbe2: 442a add r2, r5

fbe4: b12d cbz r5, fbf2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8d2>

fbe6: f813 1d01 ldrb.w r1, [r3, #-1]!

fbea: 42a3 cmp r3, r4

fbec: f802 1d01 strb.w r1, [r2, #-1]!

fbf0: d1f9 bne.n fbe6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8c6>

fbf2: 9b02 ldr r3, [sp, #8]

fbf4: 2b00 cmp r3, #0

fbf6: f000 80d5 beq.w fda4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa84>

fbfa: 9b02 ldr r3, [sp, #8]

fbfc: 199a adds r2, r3, r6

fbfe: 9206 str r2, [sp, #24]

fc00: f000 8089 beq.w fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fc04: 5d98 ldrb r0, [r3, r6]

fc06: 4690 mov r8, r2

fc08: 2800 cmp r0, #0

fc0a: f000 8084 beq.w fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fc0e: 283a cmp r0, #58 ; 0x3a

fc10: bf18 it ne

fc12: 283d cmpne r0, #61 ; 0x3d

fc14: bf14 ite ne

fc16: 2101 movne r1, #1

fc18: 2100 moveq r1, #0

fc1a: f000 80e2 beq.w fde2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xac2>

fc1e: 2800 cmp r0, #0

fc20: d079 beq.n fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fc22: f1c8 0401 rsb r4, r8, #1

fc26: 4642 mov r2, r8

fc28: e001 b.n fc2e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x90e>

fc2a: 2b00 cmp r3, #0

fc2c: d073 beq.n fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fc2e: 18a1 adds r1, r4, r2

fc30: f812 3f01 ldrb.w r3, [r2, #1]!

fc34: 2b3d cmp r3, #61 ; 0x3d

fc36: bf18 it ne

fc38: 2b3a cmpne r3, #58 ; 0x3a

fc3a: d1f6 bne.n fc2a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x90a>

fc3c: 2b00 cmp r3, #0

fc3e: d06a beq.n fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fc40: 3101 adds r1, #1

fc42: 2b3a cmp r3, #58 ; 0x3a

fc44: eb08 0e01 add.w lr, r8, r1

fc48: f818 6001 ldrb.w r6, [r8, r1]

fc4c: d071 beq.n fd32 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa12>

fc4e: f1b6 053a subs.w r5, r6, #58 ; 0x3a

fc52: bf18 it ne

fc54: 2501 movne r5, #1

fc56: 2e00 cmp r6, #0

fc58: bf08 it eq

fc5a: 2500 moveq r5, #0

fc5c: b14d cbz r5, fc72 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x952>

fc5e: f1ce 0201 rsb r2, lr, #1

fc62: 4673 mov r3, lr

fc64: 18d5 adds r5, r2, r3

fc66: f813 6f01 ldrb.w r6, [r3, #1]!

fc6a: 2e3a cmp r6, #58 ; 0x3a

fc6c: bf18 it ne

fc6e: 2e00 cmpne r6, #0

fc70: d1f8 bne.n fc64 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x944>

fc72: f8df c1bc ldr.w ip, [pc, #444] ; fe30 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb10>

fc76: 2700 movs r7, #0

fc78: 46b3 mov fp, r6

fc7a: 44fc add ip, pc

fc7c: f8dc 1000 ldr.w r1, [ip]

fc80: 780b ldrb r3, [r1, #0]

fc82: b323 cbz r3, fcce <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9ae>

fc84: 1a1b subs r3, r3, r0

fc86: bf18 it ne

fc88: 2301 movne r3, #1

fc8a: 2800 cmp r0, #0

fc8c: bf08 it eq

fc8e: 2301 moveq r3, #1

fc90: b973 cbnz r3, fcb0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x990>

fc92: 4644 mov r4, r8

fc94: f811 6f01 ldrb.w r6, [r1, #1]!

fc98: f814 2f01 ldrb.w r2, [r4, #1]!

fc9c: fab2 f382 clz r3, r2

fca0: b1b6 cbz r6, fcd0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9b0>

fca2: 095b lsrs r3, r3, #5

fca4: 4296 cmp r6, r2

fca6: bf18 it ne

fca8: f043 0301 orrne.w r3, r3, #1

fcac: 2b00 cmp r3, #0

fcae: d0f1 beq.n fc94 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x974>

fcb0: 3701 adds r7, #1

fcb2: f10c 0c38 add.w ip, ip, #56 ; 0x38

fcb6: 2f12 cmp r7, #18

fcb8: d1e0 bne.n fc7c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x95c>

fcba: eb0e 0405 add.w r4, lr, r5

fcbe: 46f0 mov r8, lr

fcc0: 7823 ldrb r3, [r4, #0]

fcc2: b343 cbz r3, fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fcc4: 3501 adds r5, #1

fcc6: 44a8 add r8, r5

fcc8: f898 0000 ldrb.w r0, [r8]

fccc: e79f b.n fc0e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8ee>

fcce: 4602 mov r2, r0

fcd0: 2a3d cmp r2, #61 ; 0x3d

fcd2: d1ed bne.n fcb0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x990>

fcd4: 9b04 ldr r3, [sp, #16]

fcd6: 465e mov r6, fp

fcd8: 681b ldr r3, [r3, #0]

fcda: 2b00 cmp r3, #0

fcdc: d05e beq.n fd9c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa7c>

fcde: 9a05 ldr r2, [sp, #20]

fce0: 2338 movs r3, #56 ; 0x38

fce2: fb03 2307 mla r3, r3, r7, r2

fce6: 6adb ldr r3, [r3, #44] ; 0x2c

fce8: 2b00 cmp r3, #0

fcea: d062 beq.n fdb2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa92>

fcec: eb0e 0405 add.w r4, lr, r5

fcf0: 46f0 mov r8, lr

fcf2: 2b02 cmp r3, #2

fcf4: d1e4 bne.n fcc0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9a0>

fcf6: 9b06 ldr r3, [sp, #24]

fcf8: 2038 movs r0, #56 ; 0x38

fcfa: 9a01 ldr r2, [sp, #4]

fcfc: ebae 0103 sub.w r1, lr, r3

fd00: 2300 movs r3, #0

fd02: 4411 add r1, r2

fd04: 9a07 ldr r2, [sp, #28]

fd06: 554b strb r3, [r1, r5]

fd08: fb00 2007 mla r0, r0, r7, r2

fd0c: f7ff fe9a bl fa44 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x724>

fd10: 7823 ldrb r3, [r4, #0]

fd12: 2b00 cmp r3, #0

fd14: d1d6 bne.n fcc4 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9a4>

fd16: 9b02 ldr r3, [sp, #8]

fd18: f8ca 3000 str.w r3, [sl]

fd1c: 46ca mov sl, r9

fd1e: e6c7 b.n fab0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x790>

fd20: 2800 cmp r0, #0

fd22: f43f af32 beq.w fb8a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x86a>

fd26: 6804 ldr r4, [r0, #0]

fd28: 4682 mov sl, r0

fd2a: 2c00 cmp r4, #0

fd2c: f47f aec7 bne.w fabe <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x79e>

fd30: e72b b.n fb8a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x86a>

fd32: 4630 mov r0, r6

fd34: 46f0 mov r8, lr

fd36: e76a b.n fc0e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x8ee>

fd38: 46d0 mov r8, sl

fd3a: f8d8 0000 ldr.w r0, [r8]

fd3e: b1f8 cbz r0, fd80 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa60>

fd40: 7821 ldrb r1, [r4, #0]

fd42: 7802 ldrb r2, [r0, #0]

fd44: b1f9 cbz r1, fd86 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa66>

fd46: fab2 f382 clz r3, r2

fd4a: 095b lsrs r3, r3, #5

fd4c: 4291 cmp r1, r2

fd4e: bf18 it ne

fd50: f043 0301 orrne.w r3, r3, #1

fd54: b973 cbnz r3, fd74 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa54>

fd56: 46a6 mov lr, r4

fd58: f81e 1f01 ldrb.w r1, [lr, #1]!

fd5c: f810 2f01 ldrb.w r2, [r0, #1]!

fd60: fab2 f382 clz r3, r2

fd64: b179 cbz r1, fd86 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa66>

fd66: 095b lsrs r3, r3, #5

fd68: 4291 cmp r1, r2

fd6a: bf18 it ne

fd6c: f043 0301 orrne.w r3, r3, #1

fd70: 2b00 cmp r3, #0

fd72: d0f1 beq.n fd58 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa38>

fd74: f108 0804 add.w r8, r8, #4

fd78: f8d8 0000 ldr.w r0, [r8]

fd7c: 2800 cmp r0, #0

fd7e: d1df bne.n fd40 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa20>

fd80: 6aeb ldr r3, [r5, #44] ; 0x2c

fd82: 46d1 mov r9, sl

fd84: e70b b.n fb9e <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x87e>

fd86: 2a3d cmp r2, #61 ; 0x3d

fd88: d1f4 bne.n fd74 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa54>

fd8a: 4643 mov r3, r8

fd8c: 685a ldr r2, [r3, #4]

fd8e: f843 2b04 str.w r2, [r3], #4

fd92: 2a00 cmp r2, #0

fd94: d1fa bne.n fd8c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa6c>

fd96: e7d0 b.n fd3a <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xa1a>

fd98: 463b mov r3, r7

fd9a: e6a7 b.n faec <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x7cc>

fd9c: eb0e 0405 add.w r4, lr, r5

fda0: 46f0 mov r8, lr

fda2: e7a8 b.n fcf6 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9d6>

fda4: 2300 movs r3, #0

fda6: 9302 str r3, [sp, #8]

fda8: 9b02 ldr r3, [sp, #8]

fdaa: f8ca 3000 str.w r3, [sl]

fdae: 46ca mov sl, r9

fdb0: e67e b.n fab0 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x790>

fdb2: b34e cbz r6, fe08 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xae8>

fdb4: 3501 adds r5, #1

fdb6: 4641 mov r1, r8

fdb8: eb0e 0205 add.w r2, lr, r5

fdbc: f81e 3005 ldrb.w r3, [lr, r5]

fdc0: b12b cbz r3, fdce <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xaae>

fdc2: f801 3b01 strb.w r3, [r1], #1

fdc6: f812 3f01 ldrb.w r3, [r2, #1]!

fdca: 2b00 cmp r3, #0

fdcc: d1f9 bne.n fdc2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xaa2>

fdce: 4a19 ldr r2, [pc, #100] ; (fe34 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb14>)

fdd0: 2338 movs r3, #56 ; 0x38

fdd2: 2500 movs r5, #0

fdd4: 4644 mov r4, r8

fdd6: 447a add r2, pc

fdd8: 700d strb r5, [r1, #0]

fdda: fb03 2307 mla r3, r3, r7, r2

fdde: 6adb ldr r3, [r3, #44] ; 0x2c

fde0: e787 b.n fcf2 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9d2>

fde2: 4603 mov r3, r0

fde4: e72a b.n fc3c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x91c>

fde6: 4814 ldr r0, [pc, #80] ; (fe38 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb18>)

fde8: 2100 movs r1, #0

fdea: f04f 0c21 mov.w ip, #33 ; 0x21

fdee: 4478 add r0, pc

fdf0: f001 f8e6 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

fdf4: f510 5f80 cmn.w r0, #4096 ; 0x1000

fdf8: f63f ae4e bhi.w fa98 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x778>

fdfc: 4b0f ldr r3, [pc, #60] ; (fe3c <\_dl\_exception\_free@@GLIBC\_PRIVATE+0xb1c>)

fdfe: 2202 movs r2, #2

fe00: 447b add r3, pc

fe02: f8c3 23e4 str.w r2, [r3, #996] ; 0x3e4

fe06: e647 b.n fa98 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x778>

fe08: f888 b000 strb.w fp, [r8]

fe0c: e783 b.n fd16 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x9f6>

fe0e: bf00 nop

fe10: 94b2 str r4, [sp, #712] ; 0x2c8

fe12: 0001 movs r1, r0

fe14: 7a46 ldrb r6, [r0, #9]

fe16: 0000 movs r0, r0

fe18: 949c str r4, [sp, #624] ; 0x270

fe1a: 0001 movs r1, r0

fe1c: 909e str r0, [sp, #632] ; 0x278

fe1e: 0001 movs r1, r0

fe20: 9098 str r0, [sp, #608] ; 0x260

fe22: 0001 movs r1, r0

fe24: 940a str r4, [sp, #40] ; 0x28

fe26: 0001 movs r1, r0

fe28: 9010 str r0, [sp, #64] ; 0x40

fe2a: 0001 movs r1, r0

fe2c: 8f9a ldrh r2, [r3, #60] ; 0x3c

fe2e: 0001 movs r1, r0

fe30: 8eca ldrh r2, [r1, #54] ; 0x36

fe32: 0001 movs r1, r0

fe34: 8d6e ldrh r6, [r5, #42] ; 0x2a

fe36: 0001 movs r1, r0

fe38: 4dea ldr r5, [pc, #936] ; (101e4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x3a4>)

fe3a: 0000 movs r0, r0

fe3c: 8d44 ldrh r4, [r0, #42] ; 0x2a

fe3e: 0001 movs r1, r0

0000fe40 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE>:

fe40: b430 push {r4, r5}

fe42: 2338 movs r3, #56 ; 0x38

fe44: 4c1f ldr r4, [pc, #124] ; (fec4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x84>)

fe46: 447c add r4, pc

fe48: fb03 4300 mla r3, r3, r0, r4

fe4c: 689b ldr r3, [r3, #8]

fe4e: 2b03 cmp r3, #3

fe50: d82c bhi.n feac <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x6c>

fe52: e8df f003 tbb [pc, r3]

fe56: 1923 adds r3, r4, r4

fe58: 2b02 cmp r3, #2

fe5a: 4b1b ldr r3, [pc, #108] ; (fec8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x88>)

fe5c: 2438 movs r4, #56 ; 0x38

fe5e: 447b add r3, pc

fe60: fb04 3300 mla r3, r4, r0, r3

fe64: 6a1b ldr r3, [r3, #32]

fe66: 600b str r3, [r1, #0]

fe68: 2338 movs r3, #56 ; 0x38

fe6a: 4918 ldr r1, [pc, #96] ; (fecc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8c>)

fe6c: fb03 f000 mul.w r0, r3, r0

fe70: 4479 add r1, pc

fe72: 180b adds r3, r1, r0

fe74: f893 3028 ldrb.w r3, [r3, #40] ; 0x28

fe78: 2a00 cmp r2, #0

fe7a: bf0c ite eq

fe7c: 2300 moveq r3, #0

fe7e: f003 0301 andne.w r3, r3, #1

fe82: b9db cbnz r3, febc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7c>

fe84: bc30 pop {r4, r5}

fe86: 4770 bx lr

fe88: 4b11 ldr r3, [pc, #68] ; (fed0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x90>)

fe8a: 2438 movs r4, #56 ; 0x38

fe8c: 447b add r3, pc

fe8e: fb04 3300 mla r3, r4, r0, r3

fe92: e9d3 4508 ldrd r4, r5, [r3, #32]

fe96: e9c1 4500 strd r4, r5, [r1]

fe9a: e7e5 b.n fe68 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x28>

fe9c: 4b0d ldr r3, [pc, #52] ; (fed4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x94>)

fe9e: 2438 movs r4, #56 ; 0x38

fea0: 447b add r3, pc

fea2: fb04 3300 mla r3, r4, r0, r3

fea6: 6a1b ldr r3, [r3, #32]

fea8: 600b str r3, [r1, #0]

feaa: e7dd b.n fe68 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x28>

feac: 4b0a ldr r3, [pc, #40] ; (fed8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x98>)

feae: 2438 movs r4, #56 ; 0x38

feb0: 447b add r3, pc

feb2: fb04 3300 mla r3, r4, r0, r3

feb6: 6a1b ldr r3, [r3, #32]

feb8: 600b str r3, [r1, #0]

feba: e7d5 b.n fe68 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x28>

febc: 3020 adds r0, #32

febe: 4408 add r0, r1

fec0: bc30 pop {r4, r5}

fec2: 4710 bx r2

fec4: 8cfe ldrh r6, [r7, #38] ; 0x26

fec6: 0001 movs r1, r0

fec8: 8ce6 ldrh r6, [r4, #38] ; 0x26

feca: 0001 movs r1, r0

fecc: 8cd4 ldrh r4, [r2, #38] ; 0x26

fece: 0001 movs r1, r0

fed0: 8cb8 ldrh r0, [r7, #36] ; 0x24

fed2: 0001 movs r1, r0

fed4: 8ca4 ldrh r4, [r4, #36] ; 0x24

fed6: 0001 movs r1, r0

fed8: 8c94 ldrh r4, [r2, #36] ; 0x24

feda: 0001 movs r1, r0

fedc: 4b84 ldr r3, [pc, #528] ; (100f0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2b0>)

fede: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

fee2: 447b add r3, pc

fee4: b087 sub sp, #28

fee6: 4a83 ldr r2, [pc, #524] ; (100f4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2b4>)

fee8: 4604 mov r4, r0

feea: 468a mov sl, r1

feec: 9000 str r0, [sp, #0]

feee: f203 40c4 addw r0, r3, #1220 ; 0x4c4

fef2: f8d3 37f8 ldr.w r3, [r3, #2040] ; 0x7f8

fef6: 447a add r2, pc

fef8: 9201 str r2, [sp, #4]

fefa: 4798 blx r3

fefc: f8d4 821c ldr.w r8, [r4, #540] ; 0x21c

ff00: f1b8 0f00 cmp.w r8, #0

ff04: f000 80b9 beq.w 1007a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x23a>

ff08: 9b00 ldr r3, [sp, #0]

ff0a: e9d8 5001 ldrd r5, r0, [r8, #4]

ff0e: f8d8 7000 ldr.w r7, [r8]

ff12: f8d3 3248 ldr.w r3, [r3, #584] ; 0x248

ff16: 9303 str r3, [sp, #12]

ff18: eb05 0345 add.w r3, r5, r5, lsl #1

ff1c: ebb3 0f80 cmp.w r3, r0, lsl #2

ff20: d95f bls.n ffe2 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x1a2>

ff22: 4629 mov r1, r5

ff24: 4650 mov r0, sl

ff26: f003 fccd bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

ff2a: f857 6021 ldr.w r6, [r7, r1, lsl #2]

ff2e: 460c mov r4, r1

ff30: eb07 0981 add.w r9, r7, r1, lsl #2

ff34: b1ae cbz r6, ff62 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x122>

ff36: 6873 ldr r3, [r6, #4]

ff38: 459a cmp sl, r3

ff3a: d047 beq.n ffcc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x18c>

ff3c: 1ea9 subs r1, r5, #2

ff3e: 4650 mov r0, sl

ff40: f003 fcc0 bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

ff44: 3101 adds r1, #1

ff46: e002 b.n ff4e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x10e>

ff48: 6873 ldr r3, [r6, #4]

ff4a: 459a cmp sl, r3

ff4c: d03e beq.n ffcc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x18c>

ff4e: 440c add r4, r1

ff50: 42ac cmp r4, r5

ff52: bf28 it cs

ff54: 1b64 subcs r4, r4, r5

ff56: f857 6024 ldr.w r6, [r7, r4, lsl #2]

ff5a: eb07 0984 add.w r9, r7, r4, lsl #2

ff5e: 2e00 cmp r6, #0

ff60: d1f2 bne.n ff48 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x108>

ff62: f8d8 3008 ldr.w r3, [r8, #8]

ff66: 200c movs r0, #12

ff68: 3301 adds r3, #1

ff6a: f8c8 3008 str.w r3, [r8, #8]

ff6e: f7f0 ed2e blx 9cc <malloc@plt+0x4>

ff72: 4b61 ldr r3, [pc, #388] ; (100f8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2b8>)

ff74: 9a00 ldr r2, [sp, #0]

ff76: 447b add r3, pc

ff78: f8d2 2248 ldr.w r2, [r2, #584] ; 0x248

ff7c: f8c9 0000 str.w r0, [r9]

ff80: 4606 mov r6, r0

ff82: f8d3 3810 ldr.w r3, [r3, #2064] ; 0x810

ff86: e002 b.n ff8e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x14e>

ff88: 685b ldr r3, [r3, #4]

ff8a: 1a52 subs r2, r2, r1

ff8c: b14b cbz r3, ffa2 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x162>

ff8e: 6819 ldr r1, [r3, #0]

ff90: 4291 cmp r1, r2

ff92: d9f9 bls.n ff88 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x148>

ff94: eb03 03c2 add.w r3, r3, r2, lsl #3

ff98: 9900 ldr r1, [sp, #0]

ff9a: 68da ldr r2, [r3, #12]

ff9c: 4291 cmp r1, r2

ff9e: f000 8094 beq.w 100ca <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x28a>

ffa2: 4b56 ldr r3, [pc, #344] ; (100fc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2bc>)

ffa4: 447b add r3, pc

ffa6: f8d3 2828 ldr.w r2, [r3, #2088] ; 0x828

ffaa: 3201 adds r2, #1

ffac: 4b54 ldr r3, [pc, #336] ; (10100 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2c0>)

ffae: 60b2 str r2, [r6, #8]

ffb0: 9a03 ldr r2, [sp, #12]

ffb2: 447b add r3, pc

ffb4: f203 40c4 addw r0, r3, #1220 ; 0x4c4

ffb8: f8c6 a004 str.w sl, [r6, #4]

ffbc: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

ffc0: 6032 str r2, [r6, #0]

ffc2: 4798 blx r3

ffc4: 4630 mov r0, r6

ffc6: b007 add sp, #28

ffc8: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

ffcc: 4b4d ldr r3, [pc, #308] ; (10104 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2c4>)

ffce: 447b add r3, pc

ffd0: f203 40c4 addw r0, r3, #1220 ; 0x4c4

ffd4: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

ffd8: 4798 blx r3

ffda: 4630 mov r0, r6

ffdc: b007 add sp, #28

ffde: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

ffe2: 0040 lsls r0, r0, #1

ffe4: eb07 0385 add.w r3, r7, r5, lsl #2

ffe8: 4285 cmp r5, r0

ffea: 461c mov r4, r3

ffec: d361 bcc.n 100b2 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x272>

ffee: 4629 mov r1, r5

fff0: 2004 movs r0, #4

fff2: f7f0 ecfe blx 9f0 <calloc@plt>

fff6: 4606 mov r6, r0

fff8: 2800 cmp r0, #0

fffa: d05e beq.n 100ba <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x27a>

fffc: 46bb mov fp, r7

fffe: 9704 str r7, [sp, #16]

10000: 46a1 mov r9, r4

10002: 462f mov r7, r5

10004: 1eab subs r3, r5, #2

10006: e9c8 0500 strd r0, r5, [r8]

1000a: 9302 str r3, [sp, #8]

1000c: f8cd a014 str.w sl, [sp, #20]

10010: f85b 5b04 ldr.w r5, [fp], #4

10014: b1ed cbz r5, 10052 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x212>

10016: f8d5 a004 ldr.w sl, [r5, #4]

1001a: 4639 mov r1, r7

1001c: f8d8 6000 ldr.w r6, [r8]

10020: 4650 mov r0, sl

10022: f003 fc4f bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

10026: 460c mov r4, r1

10028: f856 1021 ldr.w r1, [r6, r1, lsl #2]

1002c: eb06 0084 add.w r0, r6, r4, lsl #2

10030: b171 cbz r1, 10050 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x210>

10032: 4650 mov r0, sl

10034: 9902 ldr r1, [sp, #8]

10036: f003 fc45 bl 138c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b5c>

1003a: 3101 adds r1, #1

1003c: 440c add r4, r1

1003e: 42a7 cmp r7, r4

10040: bf98 it ls

10042: 1be4 subls r4, r4, r7

10044: f856 3024 ldr.w r3, [r6, r4, lsl #2]

10048: eb06 0084 add.w r0, r6, r4, lsl #2

1004c: 2b00 cmp r3, #0

1004e: d1f5 bne.n 1003c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x1fc>

10050: 6005 str r5, [r0, #0]

10052: 45d9 cmp r9, fp

10054: d8dc bhi.n 10010 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x1d0>

10056: f8d8 300c ldr.w r3, [r8, #12]

1005a: 463d mov r5, r7

1005c: e9dd 7a04 ldrd r7, sl, [sp, #16]

10060: b11b cbz r3, 1006a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x22a>

10062: 4638 mov r0, r7

10064: 4798 blx r3

10066: f8d8 5004 ldr.w r5, [r8, #4]

1006a: 4b27 ldr r3, [pc, #156] ; (10108 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2c8>)

1006c: 9a01 ldr r2, [sp, #4]

1006e: f8d8 7000 ldr.w r7, [r8]

10072: 58d3 ldr r3, [r2, r3]

10074: f8c8 300c str.w r3, [r8, #12]

10078: e753 b.n ff22 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0xe2>

1007a: 2010 movs r0, #16

1007c: f7f0 eca6 blx 9cc <malloc@plt+0x4>

10080: 4604 mov r4, r0

10082: b358 cbz r0, 100dc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x29c>

10084: 2503 movs r5, #3

10086: 2101 movs r1, #1

10088: 6045 str r5, [r0, #4]

1008a: 200c movs r0, #12

1008c: f7f0 ecb0 blx 9f0 <calloc@plt>

10090: 4b1d ldr r3, [pc, #116] ; (10108 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2c8>)

10092: 9a01 ldr r2, [sp, #4]

10094: 6020 str r0, [r4, #0]

10096: 4607 mov r7, r0

10098: 58d3 ldr r3, [r2, r3]

1009a: 60e3 str r3, [r4, #12]

1009c: b1d0 cbz r0, 100d4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x294>

1009e: 9b00 ldr r3, [sp, #0]

100a0: f8c4 8008 str.w r8, [r4, #8]

100a4: 46a0 mov r8, r4

100a6: f8c3 421c str.w r4, [r3, #540] ; 0x21c

100aa: f8d3 3248 ldr.w r3, [r3, #584] ; 0x248

100ae: 9303 str r3, [sp, #12]

100b0: e737 b.n ff22 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0xe2>

100b2: f7fb feb1 bl be18 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x438>

100b6: 4605 mov r5, r0

100b8: e799 b.n ffee <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x1ae>

100ba: 4b14 ldr r3, [pc, #80] ; (1010c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2cc>)

100bc: 447b add r3, pc

100be: f203 40c4 addw r0, r3, #1220 ; 0x4c4

100c2: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

100c6: 4798 blx r3

100c8: e787 b.n ffda <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x19a>

100ca: 689a ldr r2, [r3, #8]

100cc: 2a00 cmp r2, #0

100ce: f43f af68 beq.w ffa2 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x162>

100d2: e76b b.n ffac <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x16c>

100d4: b113 cbz r3, 100dc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x29c>

100d6: 4620 mov r0, r4

100d8: f7f0 eca4 blx a24 <free@plt+0x4>

100dc: 4b0c ldr r3, [pc, #48] ; (10110 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2d0>)

100de: 2600 movs r6, #0

100e0: 447b add r3, pc

100e2: f203 40c4 addw r0, r3, #1220 ; 0x4c4

100e6: f8d3 37fc ldr.w r3, [r3, #2044] ; 0x7fc

100ea: 4798 blx r3

100ec: e775 b.n ffda <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x19a>

100ee: bf00 nop

100f0: 917a str r1, [sp, #488] ; 0x1e8

100f2: 0001 movs r1, r0

100f4: 9106 str r1, [sp, #24]

100f6: 0001 movs r1, r0

100f8: 90e6 str r0, [sp, #920] ; 0x398

100fa: 0001 movs r1, r0

100fc: 90b8 str r0, [sp, #736] ; 0x2e0

100fe: 0001 movs r1, r0

10100: 90aa str r0, [sp, #680] ; 0x2a8

10102: 0001 movs r1, r0

10104: 908e str r0, [sp, #568] ; 0x238

10106: 0001 movs r1, r0

10108: 0058 lsls r0, r3, #1

1010a: 0000 movs r0, r0

1010c: 8fa0 ldrh r0, [r4, #60] ; 0x3c

1010e: 0001 movs r1, r0

10110: 8f7c ldrh r4, [r7, #58] ; 0x3a

10112: 0001 movs r1, r0

10114: b538 push {r3, r4, r5, lr}

10116: 4604 mov r4, r0

10118: e9d0 316c ldrd r3, r1, [r0, #432] ; 0x1b0

1011c: 1ac9 subs r1, r1, r3

1011e: 4618 mov r0, r3

10120: f001 fe4e bl 11dc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1058>

10124: f8d4 521c ldr.w r5, [r4, #540] ; 0x21c

10128: b19d cbz r5, 10152 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x312>

1012a: 686c ldr r4, [r5, #4]

1012c: 3c01 subs r4, #1

1012e: d407 bmi.n 10140 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x300>

10130: 682b ldr r3, [r5, #0]

10132: f853 0024 ldr.w r0, [r3, r4, lsl #2]

10136: 3c01 subs r4, #1

10138: f7f0 ec74 blx a24 <free@plt+0x4>

1013c: 1c63 adds r3, r4, #1

1013e: d1f7 bne.n 10130 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x2f0>

10140: 68eb ldr r3, [r5, #12]

10142: b10b cbz r3, 10148 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x308>

10144: 6828 ldr r0, [r5, #0]

10146: 4798 blx r3

10148: 4628 mov r0, r5

1014a: e8bd 4038 ldmia.w sp!, {r3, r4, r5, lr}

1014e: f7f0 bc67 b.w a20 <free@plt>

10152: bd38 pop {r3, r4, r5, pc}

10154: 6800 ldr r0, [r0, #0]

10156: 4770 bx lr

10158: ee1d 0f70 mrc 15, 0, r0, cr13, cr0, {3}

1015c: f1c0 0000 rsb r0, r0, #0

10160: 4770 bx lr

10162: bf00 nop

10164: b51c push {r2, r3, r4, lr}

10166: 6801 ldr r1, [r0, #0]

10168: ee1d 0f70 mrc 15, 0, r0, cr13, cr0, {3}

1016c: 4604 mov r4, r0

1016e: 6800 ldr r0, [r0, #0]

10170: 688a ldr r2, [r1, #8]

10172: 6803 ldr r3, [r0, #0]

10174: 429a cmp r2, r3

10176: d80a bhi.n 1018e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x34e>

10178: 680b ldr r3, [r1, #0]

1017a: f850 2033 ldr.w r2, [r0, r3, lsl #3]

1017e: f112 0f01 cmn.w r2, #1

10182: bf1e ittt ne

10184: 684b ldrne r3, [r1, #4]

10186: 18d3 addne r3, r2, r3

10188: ebc4 0003 rsbne r0, r4, r3

1018c: d104 bne.n 10198 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x358>

1018e: 4608 mov r0, r1

10190: f7f0 ec28 blx 9e4 <\_\_tls\_get\_addr@plt>

10194: ebc4 0000 rsb r0, r4, r0

10198: bd1c pop {r2, r3, r4, pc}

1019a: bf00 nop

1019c: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

101a0: 4603 mov r3, r0

101a2: 4c8e ldr r4, [pc, #568] ; (103dc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x59c>)

101a4: 460f mov r7, r1

101a6: 4a8e ldr r2, [pc, #568] ; (103e0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5a0>)

101a8: b093 sub sp, #76 ; 0x4c

101aa: f853 5b04 ldr.w r5, [r3], #4

101ae: 447c add r4, pc

101b0: 498c ldr r1, [pc, #560] ; (103e4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5a4>)

101b2: 447a add r2, pc

101b4: 6020 str r0, [r4, #0]

101b6: 1c6c adds r4, r5, #1

101b8: 4479 add r1, pc

101ba: 6013 str r3, [r2, #0]

101bc: 4a8a ldr r2, [pc, #552] ; (103e8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5a8>)

101be: eb03 0084 add.w r0, r3, r4, lsl #2

101c2: 6008 str r0, [r1, #0]

101c4: f853 1024 ldr.w r1, [r3, r4, lsl #2]

101c8: 447a add r2, pc

101ca: 6015 str r5, [r2, #0]

101cc: 4602 mov r2, r0

101ce: b119 cbz r1, 101d8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x398>

101d0: f852 3f04 ldr.w r3, [r2, #4]!

101d4: 2b00 cmp r3, #0

101d6: d1fb bne.n 101d0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x390>

101d8: 4d84 ldr r5, [pc, #528] ; (103ec <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5ac>)

101da: 1d16 adds r6, r2, #4

101dc: 6853 ldr r3, [r2, #4]

101de: 2100 movs r1, #0

101e0: 447d add r5, pc

101e2: 4c83 ldr r4, [pc, #524] ; (103f0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5b0>)

101e4: 64ae str r6, [r5, #72] ; 0x48

101e6: 447c add r4, pc

101e8: 60a9 str r1, [r5, #8]

101ea: 9411 str r4, [sp, #68] ; 0x44

101ec: 9300 str r3, [sp, #0]

101ee: 2b00 cmp r3, #0

101f0: f000 80c0 beq.w 10374 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x534>

101f4: f8d5 317c ldr.w r3, [r5, #380] ; 0x17c

101f8: 3208 adds r2, #8

101fa: 4e7e ldr r6, [pc, #504] ; (103f4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5b4>)

101fc: 468b mov fp, r1

101fe: f8d5 8010 ldr.w r8, [r5, #16]

10202: 468a mov sl, r1

10204: 930e str r3, [sp, #56] ; 0x38

10206: 447e add r6, pc

10208: f8d5 3188 ldr.w r3, [r5, #392] ; 0x188

1020c: 4689 mov r9, r1

1020e: 468e mov lr, r1

10210: 468c mov ip, r1

10212: 910d str r1, [sp, #52] ; 0x34

10214: 9308 str r3, [sp, #32]

10216: 6873 ldr r3, [r6, #4]

10218: 910b str r1, [sp, #44] ; 0x2c

1021a: 9107 str r1, [sp, #28]

1021c: 930f str r3, [sp, #60] ; 0x3c

1021e: 68b3 ldr r3, [r6, #8]

10220: 460e mov r6, r1

10222: 9104 str r1, [sp, #16]

10224: 9103 str r1, [sp, #12]

10226: 9301 str r3, [sp, #4]

10228: f8d5 318c ldr.w r3, [r5, #396] ; 0x18c

1022c: 9002 str r0, [sp, #8]

1022e: 9309 str r3, [sp, #36] ; 0x24

10230: 6bab ldr r3, [r5, #56] ; 0x38

10232: 930c str r3, [sp, #48] ; 0x30

10234: 6a2b ldr r3, [r5, #32]

10236: 930a str r3, [sp, #40] ; 0x28

10238: 6c2b ldr r3, [r5, #64] ; 0x40

1023a: 9305 str r3, [sp, #20]

1023c: 6c6b ldr r3, [r5, #68] ; 0x44

1023e: 460d mov r5, r1

10240: 9306 str r3, [sp, #24]

10242: 9b00 ldr r3, [sp, #0]

10244: 3b03 subs r3, #3

10246: 2b1e cmp r3, #30

10248: d815 bhi.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

1024a: e8df f003 tbb [pc, r3]

1024e: 148f asrs r7, r1, #18

10250: 8a91 ldrh r1, [r2, #20]

10252: 1414 asrs r4, r2, #16

10254: 1486 asrs r6, r0, #18

10256: 1414 asrs r4, r2, #16

10258: 1414 asrs r4, r2, #16

1025a: 7a81 ldrb r1, [r0, #10]

1025c: 6c75 ldr r5, [r6, #68] ; 0x44

1025e: 1414 asrs r4, r2, #16

10260: 1414 asrs r4, r2, #16

10262: 1467 asrs r7, r4, #17

10264: 6071 str r1, [r6, #4]

10266: 1414 asrs r4, r2, #16

10268: 1414 asrs r4, r2, #16

1026a: 1414 asrs r4, r2, #16

1026c: 0010 movs r0, r2

1026e: 6813 ldr r3, [r2, #0]

10270: 930e str r3, [sp, #56] ; 0x38

10272: 2301 movs r3, #1

10274: 930d str r3, [sp, #52] ; 0x34

10276: 3208 adds r2, #8

10278: f852 3c04 ldr.w r3, [r2, #-4]

1027c: 2b00 cmp r3, #0

1027e: d1e1 bne.n 10244 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x404>

10280: 9802 ldr r0, [sp, #8]

10282: f1bc 0f00 cmp.w ip, #0

10286: d003 beq.n 10290 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x450>

10288: 4b5b ldr r3, [pc, #364] ; (103f8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5b8>)

1028a: 447b add r3, pc

1028c: f8c3 8010 str.w r8, [r3, #16]

10290: f1be 0f00 cmp.w lr, #0

10294: d000 beq.n 10298 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x458>

10296: 9411 str r4, [sp, #68] ; 0x44

10298: f1b9 0f00 cmp.w r9, #0

1029c: d003 beq.n 102a6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x466>

1029e: 4b57 ldr r3, [pc, #348] ; (103fc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5bc>)

102a0: 9a01 ldr r2, [sp, #4]

102a2: 447b add r3, pc

102a4: 609a str r2, [r3, #8]

102a6: f1ba 0f00 cmp.w sl, #0

102aa: d003 beq.n 102b4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x474>

102ac: 4b54 ldr r3, [pc, #336] ; (10400 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5c0>)

102ae: 9a03 ldr r2, [sp, #12]

102b0: 447b add r3, pc

102b2: 609a str r2, [r3, #8]

102b4: f1bb 0f00 cmp.w fp, #0

102b8: d005 beq.n 102c6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x486>

102ba: 4b52 ldr r3, [pc, #328] ; (10404 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5c4>)

102bc: 9a05 ldr r2, [sp, #20]

102be: 447b add r3, pc

102c0: 641a str r2, [r3, #64] ; 0x40

102c2: 9a06 ldr r2, [sp, #24]

102c4: 645a str r2, [r3, #68] ; 0x44

102c6: 9b04 ldr r3, [sp, #16]

102c8: b13b cbz r3, 102da <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x49a>

102ca: 4b4f ldr r3, [pc, #316] ; (10408 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5c8>)

102cc: 9a08 ldr r2, [sp, #32]

102ce: 447b add r3, pc

102d0: f8c3 2188 str.w r2, [r3, #392] ; 0x188

102d4: 9a09 ldr r2, [sp, #36] ; 0x24

102d6: f8c3 218c str.w r2, [r3, #396] ; 0x18c

102da: 9b07 ldr r3, [sp, #28]

102dc: b11b cbz r3, 102e6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x4a6>

102de: 4b4b ldr r3, [pc, #300] ; (1040c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5cc>)

102e0: 9a0a ldr r2, [sp, #40] ; 0x28

102e2: 447b add r3, pc

102e4: 621a str r2, [r3, #32]

102e6: 9b0b ldr r3, [sp, #44] ; 0x2c

102e8: b11b cbz r3, 102f2 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x4b2>

102ea: 4b49 ldr r3, [pc, #292] ; (10410 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5d0>)

102ec: 9a0c ldr r2, [sp, #48] ; 0x30

102ee: 447b add r3, pc

102f0: 639a str r2, [r3, #56] ; 0x38

102f2: 9b0d ldr r3, [sp, #52] ; 0x34

102f4: b123 cbz r3, 10300 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x4c0>

102f6: 4b47 ldr r3, [pc, #284] ; (10414 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5d4>)

102f8: 9a0e ldr r2, [sp, #56] ; 0x38

102fa: 447b add r3, pc

102fc: f8c3 217c str.w r2, [r3, #380] ; 0x17c

10300: 2900 cmp r1, #0

10302: d039 beq.n 10378 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x538>

10304: 4b44 ldr r3, [pc, #272] ; (10418 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5d8>)

10306: 9a0f ldr r2, [sp, #60] ; 0x3c

10308: 447b add r3, pc

1030a: 605a str r2, [r3, #4]

1030c: e034 b.n 10378 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x538>

1030e: 6813 ldr r3, [r2, #0]

10310: 9308 str r3, [sp, #32]

10312: 2300 movs r3, #0

10314: 9309 str r3, [sp, #36] ; 0x24

10316: 2301 movs r3, #1

10318: 9304 str r3, [sp, #16]

1031a: e7ac b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

1031c: 6813 ldr r3, [r2, #0]

1031e: f04f 0901 mov.w r9, #1

10322: 9301 str r3, [sp, #4]

10324: e7a7 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10326: 6813 ldr r3, [r2, #0]

10328: 930c str r3, [sp, #48] ; 0x30

1032a: 2301 movs r3, #1

1032c: 930b str r3, [sp, #44] ; 0x2c

1032e: e7a2 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10330: 6813 ldr r3, [r2, #0]

10332: 2101 movs r1, #1

10334: 930f str r3, [sp, #60] ; 0x3c

10336: e79e b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10338: 6813 ldr r3, [r2, #0]

1033a: 930a str r3, [sp, #40] ; 0x28

1033c: 2301 movs r3, #1

1033e: 9307 str r3, [sp, #28]

10340: e799 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10342: 6813 ldr r3, [r2, #0]

10344: f04f 0b01 mov.w fp, #1

10348: 9305 str r3, [sp, #20]

1034a: 2300 movs r3, #0

1034c: 9306 str r3, [sp, #24]

1034e: e792 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10350: 6813 ldr r3, [r2, #0]

10352: f04f 0a01 mov.w sl, #1

10356: 9303 str r3, [sp, #12]

10358: e78d b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

1035a: 6814 ldr r4, [r2, #0]

1035c: f04f 0e01 mov.w lr, #1

10360: e789 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10362: f8d2 8000 ldr.w r8, [r2]

10366: f04f 0c01 mov.w ip, #1

1036a: e784 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

1036c: 6816 ldr r6, [r2, #0]

1036e: e782 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10370: 6815 ldr r5, [r2, #0]

10372: e780 b.n 10276 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x436>

10374: 9d00 ldr r5, [sp, #0]

10376: 462e mov r6, r5

10378: 4c28 ldr r4, [pc, #160] ; (1041c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5dc>)

1037a: f7ff fb83 bl fa84 <\_dl\_exception\_free@@GLIBC\_PRIVATE+0x764>

1037e: 2000 movs r0, #0

10380: 447c add r4, pc

10382: f000 fe21 bl 10fc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x260>

10386: 68a0 ldr r0, [r4, #8]

10388: b110 cbz r0, 10390 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x550>

1038a: 7803 ldrb r3, [r0, #0]

1038c: b9b3 cbnz r3, 103bc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x57c>

1038e: 60a3 str r3, [r4, #8]

10390: 2000 movs r0, #0

10392: f000 fe45 bl 11020 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b8>

10396: 4b22 ldr r3, [pc, #136] ; (10420 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5e0>)

10398: 447b add r3, pc

1039a: 4298 cmp r0, r3

1039c: d012 beq.n 103c4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x584>

1039e: 4b21 ldr r3, [pc, #132] ; (10424 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5e4>)

103a0: 447b add r3, pc

103a2: 689b ldr r3, [r3, #8]

103a4: b9bb cbnz r3, 103d6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x596>

103a6: 4b20 ldr r3, [pc, #128] ; (10428 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5e8>)

103a8: 4630 mov r0, r6

103aa: 4629 mov r1, r5

103ac: aa11 add r2, sp, #68 ; 0x44

103ae: 447b add r3, pc

103b0: 6c9b ldr r3, [r3, #72] ; 0x48

103b2: 47b8 blx r7

103b4: 9811 ldr r0, [sp, #68] ; 0x44

103b6: b013 add sp, #76 ; 0x4c

103b8: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

103bc: f002 f9c0 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

103c0: 60e0 str r0, [r4, #12]

103c2: e7e5 b.n 10390 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x550>

103c4: 4b19 ldr r3, [pc, #100] ; (1042c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x5ec>)

103c6: 447b add r3, pc

103c8: 691a ldr r2, [r3, #16]

103ca: 1e53 subs r3, r2, #1

103cc: 4018 ands r0, r3

103ce: 1a10 subs r0, r2, r0

103d0: f000 fe26 bl 11020 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b8>

103d4: e7e3 b.n 1039e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x55e>

103d6: f001 f933 bl 11640 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8d8>

103da: e7e4 b.n 103a6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x566>

103dc: 8d86 ldrh r6, [r0, #44] ; 0x2c

103de: 0001 movs r1, r0

103e0: 96e6 str r6, [sp, #920] ; 0x398

103e2: 0001 movs r1, r0

103e4: 9794 str r7, [sp, #592] ; 0x250

103e6: 0001 movs r1, r0

103e8: 8744 strh r4, [r0, #58] ; 0x3a

103ea: 0001 movs r1, r0

103ec: 878c strh r4, [r1, #60] ; 0x3c

103ee: 0001 movs r1, r0

103f0: 0856 lsrs r6, r2, #1

103f2: ffff 8d2e ; <UNDEFINED> instruction: 0xffff8d2e

103f6: 0001 movs r1, r0

103f8: 86e2 strh r2, [r4, #54] ; 0x36

103fa: 0001 movs r1, r0

103fc: 8c92 ldrh r2, [r2, #36] ; 0x24

103fe: 0001 movs r1, r0

10400: 86bc strh r4, [r7, #52] ; 0x34

10402: 0001 movs r1, r0

10404: 86ae strh r6, [r5, #52] ; 0x34

10406: 0001 movs r1, r0

10408: 869e strh r6, [r3, #52] ; 0x34

1040a: 0001 movs r1, r0

1040c: 868a strh r2, [r1, #52] ; 0x34

1040e: 0001 movs r1, r0

10410: 867e strh r6, [r7, #50] ; 0x32

10412: 0001 movs r1, r0

10414: 8672 strh r2, [r6, #50] ; 0x32

10416: 0001 movs r1, r0

10418: 8c2c ldrh r4, [r5, #32]

1041a: 0001 movs r1, r0

1041c: 85ec strh r4, [r5, #46] ; 0x2e

1041e: 0001 movs r1, r0

10420: 95d8 str r5, [sp, #864] ; 0x360

10422: 0001 movs r1, r0

10424: 8b94 ldrh r4, [r2, #28]

10426: 0001 movs r1, r0

10428: 85be strh r6, [r7, #44] ; 0x2c

1042a: 0001 movs r1, r0

1042c: 85a6 strh r6, [r4, #44] ; 0x2c

1042e: 0001 movs r1, r0

10430: 4770 bx lr

10432: bf00 nop

10434: 4b52 ldr r3, [pc, #328] ; (10580 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x740>)

10436: 2200 movs r2, #0

10438: e92d 43f0 stmdb sp!, {r4, r5, r6, r7, r8, r9, lr}

1043c: 447b add r3, pc

1043e: b099 sub sp, #100 ; 0x64

10440: 6c9d ldr r5, [r3, #72] ; 0x48

10442: f88d 205f strb.w r2, [sp, #95] ; 0x5f

10446: 682b ldr r3, [r5, #0]

10448: 2b00 cmp r3, #0

1044a: d038 beq.n 104be <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x67e>

1044c: 4e4d ldr r6, [pc, #308] ; (10584 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x744>)

1044e: 4f4e ldr r7, [pc, #312] ; (10588 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x748>)

10450: 447e add r6, pc

10452: 447f add r7, pc

10454: 364c adds r6, #76 ; 0x4c

10456: f507 7794 add.w r7, r7, #296 ; 0x128

1045a: 2b01 cmp r3, #1

1045c: d92b bls.n 104b6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x676>

1045e: 1e9c subs r4, r3, #2

10460: 2c1f cmp r4, #31

10462: d830 bhi.n 104c6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x686>

10464: 4a49 ldr r2, [pc, #292] ; (1058c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x74c>)

10466: 2114 movs r1, #20

10468: 447a add r2, pc

1046a: fb01 2204 mla r2, r1, r4, r2

1046e: 7c52 ldrb r2, [r2, #17]

10470: 2a04 cmp r2, #4

10472: d020 beq.n 104b6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x676>

10474: 2b1a cmp r3, #26

10476: bf18 it ne

10478: 2b10 cmpne r3, #16

1047a: d123 bne.n 104c4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x684>

1047c: 2b10 cmp r3, #16

1047e: f8d5 8004 ldr.w r8, [r5, #4]

10482: d058 beq.n 10536 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x6f6>

10484: 2b1a cmp r3, #26

10486: d11d bne.n 104c4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x684>

10488: 4941 ldr r1, [pc, #260] ; (10590 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x750>)

1048a: 2001 movs r0, #1

1048c: f8df 9104 ldr.w r9, [pc, #260] ; 10594 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x754>

10490: 2400 movs r4, #0

10492: 4479 add r1, pc

10494: f7fb fc9a bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10498: 44f9 add r9, pc

1049a: 2001 movs r0, #1

1049c: fa00 f304 lsl.w r3, r0, r4

104a0: ea13 0f08 tst.w r3, r8

104a4: d140 bne.n 10528 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x6e8>

104a6: 3401 adds r4, #1

104a8: 2c05 cmp r4, #5

104aa: d1f6 bne.n 1049a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x65a>

104ac: 493a ldr r1, [pc, #232] ; (10598 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x758>)

104ae: 2001 movs r0, #1

104b0: 4479 add r1, pc

104b2: f7fb fc8b bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

104b6: f855 3f08 ldr.w r3, [r5, #8]!

104ba: 2b00 cmp r3, #0

104bc: d1cd bne.n 1045a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x61a>

104be: b019 add sp, #100 ; 0x64

104c0: e8bd 83f0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, pc}

104c4: b9d2 cbnz r2, 104fc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x6bc>

104c6: 2400 movs r4, #0

104c8: aa07 add r2, sp, #28

104ca: f88d 401c strb.w r4, [sp, #28]

104ce: 4621 mov r1, r4

104d0: 6868 ldr r0, [r5, #4]

104d2: 2310 movs r3, #16

104d4: 9400 str r4, [sp, #0]

104d6: f000 fac5 bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

104da: 4621 mov r1, r4

104dc: f10d 025f add.w r2, sp, #95 ; 0x5f

104e0: 2310 movs r3, #16

104e2: 4680 mov r8, r0

104e4: 6828 ldr r0, [r5, #0]

104e6: 9400 str r4, [sp, #0]

104e8: f000 fabc bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

104ec: 492b ldr r1, [pc, #172] ; (1059c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x75c>)

104ee: 4643 mov r3, r8

104f0: 4479 add r1, pc

104f2: 4602 mov r2, r0

104f4: 2001 movs r0, #1

104f6: f7fb fc69 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

104fa: e7dc b.n 104b6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x676>

104fc: 2a01 cmp r2, #1

104fe: 6868 ldr r0, [r5, #4]

10500: d133 bne.n 1056a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x72a>

10502: 2100 movs r1, #0

10504: 230a movs r3, #10

10506: 9100 str r1, [sp, #0]

10508: f10d 025f add.w r2, sp, #95 ; 0x5f

1050c: f000 faaa bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

10510: 4603 mov r3, r0

10512: 4a23 ldr r2, [pc, #140] ; (105a0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x760>)

10514: 2014 movs r0, #20

10516: 4923 ldr r1, [pc, #140] ; (105a4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x764>)

10518: 447a add r2, pc

1051a: 4479 add r1, pc

1051c: fb00 2204 mla r2, r0, r4, r2

10520: 2001 movs r0, #1

10522: f7fb fc53 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10526: e7c6 b.n 104b6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x676>

10528: 220a movs r2, #10

1052a: 4649 mov r1, r9

1052c: fb02 7204 mla r2, r2, r4, r7

10530: f7fb fc4c bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10534: e7b7 b.n 104a6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x666>

10536: 491c ldr r1, [pc, #112] ; (105a8 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x768>)

10538: 2001 movs r0, #1

1053a: f8df 9070 ldr.w r9, [pc, #112] ; 105ac <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x76c>

1053e: 2400 movs r4, #0

10540: 4479 add r1, pc

10542: f7fb fc43 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10546: 44f9 add r9, pc

10548: e002 b.n 10550 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x710>

1054a: 3401 adds r4, #1

1054c: 2c16 cmp r4, #22

1054e: d0ad beq.n 104ac <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x66c>

10550: 2001 movs r0, #1

10552: fa00 f304 lsl.w r3, r0, r4

10556: ea13 0f08 tst.w r3, r8

1055a: d0f6 beq.n 1054a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x70a>

1055c: 220a movs r2, #10

1055e: 4649 mov r1, r9

10560: fb02 6204 mla r2, r2, r4, r6

10564: f7fb fc32 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10568: e7ef b.n 1054a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x70a>

1056a: 2a02 cmp r2, #2

1056c: d106 bne.n 1057c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x73c>

1056e: 2100 movs r1, #0

10570: 2310 movs r3, #16

10572: 9100 str r1, [sp, #0]

10574: f10d 025f add.w r2, sp, #95 ; 0x5f

10578: f000 fa74 bl 10a64 <realloc@@GLIBC\_2.4+0x9c>

1057c: 4603 mov r3, r0

1057e: e7c8 b.n 10512 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x6d2>

10580: 8530 strh r0, [r6, #40] ; 0x28

10582: 0001 movs r1, r0

10584: 851c strh r4, [r3, #40] ; 0x28

10586: 0001 movs r1, r0

10588: 851a strh r2, [r3, #40] ; 0x28

1058a: 0001 movs r1, r0

1058c: 3e5c subs r6, #92 ; 0x5c

1058e: 0000 movs r0, r0

10590: 731a strb r2, [r3, #12]

10592: 0000 movs r0, r0

10594: 6c0c ldr r4, [r1, #64] ; 0x40

10596: 0000 movs r0, r0

10598: 5e38 ldrsh r0, [r7, r0]

1059a: 0000 movs r0, r0

1059c: 72dc strb r4, [r3, #11]

1059e: 0000 movs r0, r0

105a0: 3dac subs r5, #172 ; 0xac

105a2: 0000 movs r0, r0

105a4: 72a6 strb r6, [r4, #10]

105a6: 0000 movs r0, r0

105a8: 7258 strb r0, [r3, #9]

105aa: 0000 movs r0, r0

105ac: 6b5e ldr r6, [r3, #52] ; 0x34

105ae: 0000 movs r0, r0

105b0: 4b4a ldr r3, [pc, #296] ; (106dc <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x89c>)

105b2: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

105b6: 447b add r3, pc

105b8: b0f3 sub sp, #460 ; 0x1cc

105ba: f8d3 a180 ldr.w sl, [r3, #384] ; 0x180

105be: f1ba 0f00 cmp.w sl, #0

105c2: d039 beq.n 10638 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7f8>

105c4: f8ba 915c ldrh.w r9, [sl, #348] ; 0x15c

105c8: f8da 5154 ldr.w r5, [sl, #340] ; 0x154

105cc: f1b9 0f00 cmp.w r9, #0

105d0: d032 beq.n 10638 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7f8>

105d2: 4e43 ldr r6, [pc, #268] ; (106e0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8a0>)

105d4: f04f 0b00 mov.w fp, #0

105d8: 447e add r6, pc

105da: f506 7620 add.w r6, r6, #640 ; 0x280

105de: e004 b.n 105ea <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7aa>

105e0: f10b 0b01 add.w fp, fp, #1

105e4: 3520 adds r5, #32

105e6: 45d9 cmp r9, fp

105e8: d026 beq.n 10638 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7f8>

105ea: 682b ldr r3, [r5, #0]

105ec: 2b04 cmp r3, #4

105ee: d1f7 bne.n 105e0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7a0>

105f0: 696f ldr r7, [r5, #20]

105f2: 68ac ldr r4, [r5, #8]

105f4: f8da 3000 ldr.w r3, [sl]

105f8: 2f0c cmp r7, #12

105fa: 441c add r4, r3

105fc: f1c4 080c rsb r8, r4, #12

10600: d80f bhi.n 10622 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7e2>

10602: e7ed b.n 105e0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7a0>

10604: e9d4 3200 ldrd r3, r2, [r4]

10608: 3303 adds r3, #3

1060a: 3203 adds r2, #3

1060c: f023 0303 bic.w r3, r3, #3

10610: f022 0203 bic.w r2, r2, #3

10614: 4413 add r3, r2

10616: 330c adds r3, #12

10618: 441c add r4, r3

1061a: eb04 0308 add.w r3, r4, r8

1061e: 42bb cmp r3, r7

10620: d2de bcs.n 105e0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7a0>

10622: 2214 movs r2, #20

10624: 4631 mov r1, r6

10626: 4620 mov r0, r4

10628: f002 fa08 bl 12a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cd4>

1062c: 2800 cmp r0, #0

1062e: d1e9 bne.n 10604 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7c4>

10630: 6960 ldr r0, [r4, #20]

10632: b073 add sp, #460 ; 0x1cc

10634: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

10638: a810 add r0, sp, #64 ; 0x40

1063a: f001 fbf1 bl 11e20 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10b8>

1063e: f10d 01c2 add.w r1, sp, #194 ; 0xc2

10642: bb68 cbnz r0, 106a0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x860>

10644: 2700 movs r7, #0

10646: 260a movs r6, #10

10648: 4638 mov r0, r7

1064a: 780a ldrb r2, [r1, #0]

1064c: 3a30 subs r2, #48 ; 0x30

1064e: b2d3 uxtb r3, r2

10650: 2b09 cmp r3, #9

10652: d81e bhi.n 10692 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x852>

10654: 784c ldrb r4, [r1, #1]

10656: 3101 adds r1, #1

10658: f1a4 0330 sub.w r3, r4, #48 ; 0x30

1065c: b2dd uxtb r5, r3

1065e: 2d09 cmp r5, #9

10660: d808 bhi.n 10674 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x834>

10662: f811 4f01 ldrb.w r4, [r1, #1]!

10666: fb06 3202 mla r2, r6, r2, r3

1066a: f1a4 0330 sub.w r3, r4, #48 ; 0x30

1066e: b2dd uxtb r5, r3

10670: 2d09 cmp r5, #9

10672: d9f6 bls.n 10662 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x822>

10674: 2c2e cmp r4, #46 ; 0x2e

10676: f107 0701 add.w r7, r7, #1

1067a: ea42 2000 orr.w r0, r2, r0, lsl #8

1067e: f101 0101 add.w r1, r1, #1

10682: d104 bne.n 1068e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x84e>

10684: 2f03 cmp r7, #3

10686: d1e0 bne.n 1064a <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x80a>

10688: b073 add sp, #460 ; 0x1cc

1068a: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1068e: 2f03 cmp r7, #3

10690: d0cf beq.n 10632 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7f2>

10692: f1c7 0703 rsb r7, r7, #3

10696: 00ff lsls r7, r7, #3

10698: 40b8 lsls r0, r7

1069a: b073 add sp, #460 ; 0x1cc

1069c: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

106a0: 4810 ldr r0, [pc, #64] ; (106e4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8a4>)

106a2: 2100 movs r1, #0

106a4: 4478 add r0, pc

106a6: f001 faa9 bl 11bfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe94>

106aa: 1e06 subs r6, r0, #0

106ac: db12 blt.n 106d4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x894>

106ae: 466d mov r5, sp

106b0: 2240 movs r2, #64 ; 0x40

106b2: 4629 mov r1, r5

106b4: f001 faf6 bl 11ca4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf3c>

106b8: 4604 mov r4, r0

106ba: 4630 mov r0, r6

106bc: f001 fa1c bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

106c0: 2c00 cmp r4, #0

106c2: dd07 ble.n 106d4 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x894>

106c4: 2c3f cmp r4, #63 ; 0x3f

106c6: f04f 0300 mov.w r3, #0

106ca: 4629 mov r1, r5

106cc: bfa8 it ge

106ce: 243f movge r4, #63 ; 0x3f

106d0: 552b strb r3, [r5, r4]

106d2: e7b7 b.n 10644 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x804>

106d4: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

106d8: e7ab b.n 10632 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x7f2>

106da: bf00 nop

106dc: 83b6 strh r6, [r6, #28]

106de: 0001 movs r1, r0

106e0: 3cec subs r4, #236 ; 0xec

106e2: 0000 movs r0, r0

106e4: 7140 strb r0, [r0, #5]

106e6: 0000 movs r0, r0

106e8: 6802 ldr r2, [r0, #0]

106ea: 6813 ldr r3, [r2, #0]

106ec: b91b cbnz r3, 106f6 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8b6>

106ee: e00e b.n 1070e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8ce>

106f0: f852 3f04 ldr.w r3, [r2, #4]!

106f4: b15b cbz r3, 1070e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8ce>

106f6: 7819 ldrb r1, [r3, #0]

106f8: 294c cmp r1, #76 ; 0x4c

106fa: d1f9 bne.n 106f0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8b0>

106fc: 7859 ldrb r1, [r3, #1]

106fe: 2944 cmp r1, #68 ; 0x44

10700: d1f6 bne.n 106f0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8b0>

10702: 7899 ldrb r1, [r3, #2]

10704: 295f cmp r1, #95 ; 0x5f

10706: d1f3 bne.n 106f0 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8b0>

10708: 3303 adds r3, #3

1070a: 3204 adds r2, #4

1070c: 6002 str r2, [r0, #0]

1070e: 4618 mov r0, r3

10710: 4770 bx lr

10712: bf00 nop

10714: 4b12 ldr r3, [pc, #72] ; (10760 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x920>)

10716: b430 push {r4, r5}

10718: 447b add r3, pc

1071a: 681d ldr r5, [r3, #0]

1071c: 682a ldr r2, [r5, #0]

1071e: b192 cbz r2, 10746 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x906>

10720: 7813 ldrb r3, [r2, #0]

10722: 7801 ldrb r1, [r0, #0]

10724: 428b cmp r3, r1

10726: d109 bne.n 1073c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8fc>

10728: 4604 mov r4, r0

1072a: b90b cbnz r3, 10730 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8f0>

1072c: e007 b.n 1073e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8fe>

1072e: b133 cbz r3, 1073e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8fe>

10730: f812 3f01 ldrb.w r3, [r2, #1]!

10734: f814 1f01 ldrb.w r1, [r4, #1]!

10738: 428b cmp r3, r1

1073a: d0f8 beq.n 1072e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8ee>

1073c: b131 cbz r1, 1074c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x90c>

1073e: 3504 adds r5, #4

10740: 682a ldr r2, [r5, #0]

10742: 2a00 cmp r2, #0

10744: d1ec bne.n 10720 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8e0>

10746: 4610 mov r0, r2

10748: bc30 pop {r4, r5}

1074a: 4770 bx lr

1074c: 2b3d cmp r3, #61 ; 0x3d

1074e: d1f6 bne.n 1073e <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8fe>

10750: 462b mov r3, r5

10752: 685a ldr r2, [r3, #4]

10754: f843 2b04 str.w r2, [r3], #4

10758: 2a00 cmp r2, #0

1075a: d1fa bne.n 10752 <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x912>

1075c: e7de b.n 1071c <\_\_tunable\_get\_val@@GLIBC\_PRIVATE+0x8dc>

1075e: bf00 nop

10760: 9234 str r2, [sp, #208] ; 0xd0

10762: 0001 movs r1, r0

00010764 <malloc@@GLIBC\_2.4>:

10764: 4928 ldr r1, [pc, #160] ; (10808 <malloc@@GLIBC\_2.4+0xa4>)

10766: b530 push {r4, r5, lr}

10768: 4479 add r1, pc

1076a: b083 sub sp, #12

1076c: 680a ldr r2, [r1, #0]

1076e: 2a00 cmp r2, #0

10770: d03b beq.n 107ea <malloc@@GLIBC\_2.4+0x86>

10772: 684b ldr r3, [r1, #4]

10774: 3307 adds r3, #7

10776: 4925 ldr r1, [pc, #148] ; (1080c <malloc@@GLIBC\_2.4+0xa8>)

10778: f023 0307 bic.w r3, r3, #7

1077c: 181c adds r4, r3, r0

1077e: 4479 add r1, pc

10780: 4294 cmp r4, r2

10782: 604b str r3, [r1, #4]

10784: d202 bcs.n 1078c <malloc@@GLIBC\_2.4+0x28>

10786: 425a negs r2, r3

10788: 4282 cmp r2, r0

1078a: d827 bhi.n 107dc <malloc@@GLIBC\_2.4+0x78>

1078c: 4b20 ldr r3, [pc, #128] ; (10810 <malloc@@GLIBC\_2.4+0xac>)

1078e: 447b add r3, pc

10790: 691d ldr r5, [r3, #16]

10792: 1e6b subs r3, r5, #1

10794: 426a negs r2, r5

10796: 4403 add r3, r0

10798: 4013 ands r3, r2

1079a: bf0c ite eq

1079c: 2201 moveq r2, #1

1079e: 2200 movne r2, #0

107a0: 2800 cmp r0, #0

107a2: bf08 it eq

107a4: 2200 moveq r2, #0

107a6: bb5a cbnz r2, 10800 <malloc@@GLIBC\_2.4+0x9c>

107a8: 441d add r5, r3

107aa: 9201 str r2, [sp, #4]

107ac: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

107b0: 4604 mov r4, r0

107b2: 9300 str r3, [sp, #0]

107b4: 4610 mov r0, r2

107b6: 2322 movs r3, #34 ; 0x22

107b8: 4629 mov r1, r5

107ba: 2203 movs r2, #3

107bc: f001 fada bl 11d74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x100c>

107c0: 1c43 adds r3, r0, #1

107c2: d01d beq.n 10800 <malloc@@GLIBC\_2.4+0x9c>

107c4: 4b13 ldr r3, [pc, #76] ; (10814 <malloc@@GLIBC\_2.4+0xb0>)

107c6: 447b add r3, pc

107c8: 681a ldr r2, [r3, #0]

107ca: 4282 cmp r2, r0

107cc: 4a12 ldr r2, [pc, #72] ; (10818 <malloc@@GLIBC\_2.4+0xb4>)

107ce: bf0c ite eq

107d0: 685b ldreq r3, [r3, #4]

107d2: 4603 movne r3, r0

107d4: 447a add r2, pc

107d6: 4428 add r0, r5

107d8: 441c add r4, r3

107da: 6010 str r0, [r2, #0]

107dc: 4a0f ldr r2, [pc, #60] ; (1081c <malloc@@GLIBC\_2.4+0xb8>)

107de: 4618 mov r0, r3

107e0: 447a add r2, pc

107e2: e9c2 4301 strd r4, r3, [r2, #4]

107e6: b003 add sp, #12

107e8: bd30 pop {r4, r5, pc}

107ea: 4a0d ldr r2, [pc, #52] ; (10820 <malloc@@GLIBC\_2.4+0xbc>)

107ec: 4b0d ldr r3, [pc, #52] ; (10824 <malloc@@GLIBC\_2.4+0xc0>)

107ee: 447a add r2, pc

107f0: 447b add r3, pc

107f2: 6914 ldr r4, [r2, #16]

107f4: 18e2 adds r2, r4, r3

107f6: 4264 negs r4, r4

107f8: 3a01 subs r2, #1

107fa: 4022 ands r2, r4

107fc: 600a str r2, [r1, #0]

107fe: e7b9 b.n 10774 <malloc@@GLIBC\_2.4+0x10>

10800: 2300 movs r3, #0

10802: 4618 mov r0, r3

10804: b003 add sp, #12

10806: bd30 pop {r4, r5, pc}

10808: 91b4 str r1, [sp, #720] ; 0x2d0

1080a: 0001 movs r1, r0

1080c: 919e str r1, [sp, #632] ; 0x278

1080e: 0001 movs r1, r0

10810: 81de strh r6, [r3, #14]

10812: 0001 movs r1, r0

10814: 9156 str r1, [sp, #344] ; 0x158

10816: 0001 movs r1, r0

10818: 9148 str r1, [sp, #288] ; 0x120

1081a: 0001 movs r1, r0

1081c: 913c str r1, [sp, #240] ; 0xf0

1081e: 0001 movs r1, r0

10820: 817e strh r6, [r7, #10]

10822: 0001 movs r1, r0

10824: 9180 str r1, [sp, #512] ; 0x200

10826: 0001 movs r1, r0

00010828 <calloc@@GLIBC\_2.4>:

10828: b538 push {r3, r4, r5, lr}

1082a: ea40 0301 orr.w r3, r0, r1

1082e: f5b3 3f80 cmp.w r3, #65536 ; 0x10000

10832: 460c mov r4, r1

10834: fb04 f400 mul.w r4, r4, r0

10838: bf34 ite cc

1083a: 2200 movcc r2, #0

1083c: 2201 movcs r2, #1

1083e: 2900 cmp r1, #0

10840: bf08 it eq

10842: 2200 moveq r2, #0

10844: b922 cbnz r2, 10850 <calloc@@GLIBC\_2.4+0x28>

10846: 4620 mov r0, r4

10848: e8bd 4038 ldmia.w sp!, {r3, r4, r5, lr}

1084c: f7f0 b8bc b.w 9c8 <malloc@plt>

10850: 4605 mov r5, r0

10852: 4620 mov r0, r4

10854: f002 ff08 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

10858: 42a8 cmp r0, r5

1085a: d0f4 beq.n 10846 <calloc@@GLIBC\_2.4+0x1e>

1085c: 2000 movs r0, #0

1085e: bd38 pop {r3, r4, r5, pc}

00010860 <free@@GLIBC\_2.4>:

10860: b538 push {r3, r4, r5, lr}

10862: 4c06 ldr r4, [pc, #24] ; (1087c <free@@GLIBC\_2.4+0x1c>)

10864: 447c add r4, pc

10866: 68a5 ldr r5, [r4, #8]

10868: 4285 cmp r5, r0

1086a: d000 beq.n 1086e <free@@GLIBC\_2.4+0xe>

1086c: bd38 pop {r3, r4, r5, pc}

1086e: 6862 ldr r2, [r4, #4]

10870: 2100 movs r1, #0

10872: 1b52 subs r2, r2, r5

10874: f002 ebec blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

10878: 6065 str r5, [r4, #4]

1087a: bd38 pop {r3, r4, r5, pc}

1087c: 90b8 str r0, [sp, #736] ; 0x2e0

1087e: 0001 movs r1, r0

10880: 4602 mov r2, r0

10882: 2300 movs r3, #0

10884: 4618 mov r0, r3

10886: f8c2 3100 str.w r3, [r2, #256] ; 0x100

1088a: 4770 bx lr

1088c: e92d 43f8 stmdb sp!, {r3, r4, r5, r6, r7, r8, r9, lr}

10890: 1e43 subs r3, r0, #1

10892: 2b15 cmp r3, #21

10894: d82a bhi.n 108ec <free@@GLIBC\_2.4+0x8c>

10896: e8df f003 tbb [pc, r3]

1089a: 1f1a subs r2, r3, #4

1089c: 2929 cmp r1, #41 ; 0x29

1089e: 2924 cmp r1, #36 ; 0x24

108a0: 2929 cmp r1, #41 ; 0x29

108a2: 2929 cmp r1, #41 ; 0x29

108a4: 0b29 lsrs r1, r5, #12

108a6: 2910 cmp r1, #16

108a8: 2929 cmp r1, #41 ; 0x29

108aa: 2929 cmp r1, #41 ; 0x29

108ac: 2929 cmp r1, #41 ; 0x29

108ae: 1529 asrs r1, r5, #20

108b0: 4b26 ldr r3, [pc, #152] ; (1094c <free@@GLIBC\_2.4+0xec>)

108b2: 447b add r3, pc

108b4: 4618 mov r0, r3

108b6: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

108ba: 4b25 ldr r3, [pc, #148] ; (10950 <free@@GLIBC\_2.4+0xf0>)

108bc: 447b add r3, pc

108be: 4618 mov r0, r3

108c0: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

108c4: 4b23 ldr r3, [pc, #140] ; (10954 <free@@GLIBC\_2.4+0xf4>)

108c6: 447b add r3, pc

108c8: 4618 mov r0, r3

108ca: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

108ce: 4b22 ldr r3, [pc, #136] ; (10958 <free@@GLIBC\_2.4+0xf8>)

108d0: 447b add r3, pc

108d2: 4618 mov r0, r3

108d4: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

108d8: 4b20 ldr r3, [pc, #128] ; (1095c <free@@GLIBC\_2.4+0xfc>)

108da: 447b add r3, pc

108dc: 4618 mov r0, r3

108de: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

108e2: 4b1f ldr r3, [pc, #124] ; (10960 <free@@GLIBC\_2.4+0x100>)

108e4: 447b add r3, pc

108e6: 4618 mov r0, r3

108e8: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

108ec: 3a01 subs r2, #1

108ee: f8df 9074 ldr.w r9, [pc, #116] ; 10964 <free@@GLIBC\_2.4+0x104>

108f2: 2300 movs r3, #0

108f4: 188e adds r6, r1, r2

108f6: 4604 mov r4, r0

108f8: 17c5 asrs r5, r0, #31

108fa: 548b strb r3, [r1, r2]

108fc: 44f9 add r9, pc

108fe: e000 b.n 10902 <free@@GLIBC\_2.4+0xa2>

10900: 4646 mov r6, r8

10902: 4620 mov r0, r4

10904: 4629 mov r1, r5

10906: 220a movs r2, #10

10908: 2300 movs r3, #0

1090a: f003 f941 bl 13b90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e28>

1090e: 4620 mov r0, r4

10910: 4629 mov r1, r5

10912: 2300 movs r3, #0

10914: f106 38ff add.w r8, r6, #4294967295 ; 0xffffffff

10918: f819 7002 ldrb.w r7, [r9, r2]

1091c: 220a movs r2, #10

1091e: f806 7c01 strb.w r7, [r6, #-1]

10922: f003 f935 bl 13b90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e28>

10926: 2d00 cmp r5, #0

10928: bf08 it eq

1092a: 2c0a cmpeq r4, #10

1092c: 460d mov r5, r1

1092e: 4604 mov r4, r0

10930: d2e6 bcs.n 10900 <free@@GLIBC\_2.4+0xa0>

10932: 4a0d ldr r2, [pc, #52] ; (10968 <free@@GLIBC\_2.4+0x108>)

10934: 1ff1 subs r1, r6, #7

10936: 447a add r2, pc

10938: 460b mov r3, r1

1093a: 6810 ldr r0, [r2, #0]

1093c: 8892 ldrh r2, [r2, #4]

1093e: f846 0c07 str.w r0, [r6, #-7]

10942: 4618 mov r0, r3

10944: f826 2c03 strh.w r2, [r6, #-3]

10948: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

1094c: 6f4e ldr r6, [r1, #116] ; 0x74

1094e: 0000 movs r0, r0

10950: 6fa4 ldr r4, [r4, #120] ; 0x78

10952: 0000 movs r0, r0

10954: 6fae ldr r6, [r5, #120] ; 0x78

10956: 0000 movs r0, r0

10958: 6f64 ldr r4, [r4, #116] ; 0x74

1095a: 0000 movs r0, r0

1095c: 6f3e ldr r6, [r7, #112] ; 0x70

1095e: 0000 movs r0, r0

10960: 6f68 ldr r0, [r5, #116] ; 0x74

10962: 0000 movs r0, r0

10964: 3c5c subs r4, #92 ; 0x5c

10966: 0000 movs r0, r0

10968: 6f52 ldr r2, [r2, #116] ; 0x74

1096a: 0000 movs r0, r0

1096c: 4904 ldr r1, [pc, #16] ; (10980 <free@@GLIBC\_2.4+0x120>)

1096e: 4602 mov r2, r0

10970: b508 push {r3, lr}

10972: 2002 movs r0, #2

10974: 4479 add r1, pc

10976: f7fb fa29 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

1097a: 207f movs r0, #127 ; 0x7f

1097c: f001 fa64 bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

10980: 58f0 ldr r0, [r6, r3]

10982: 0000 movs r0, r0

10984: b508 push {r3, lr}

10986: 207f movs r0, #127 ; 0x7f

10988: f001 fa5e bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

1098c: b500 push {lr}

1098e: b085 sub sp, #20

10990: b17b cbz r3, 109b2 <free@@GLIBC\_2.4+0x152>

10992: 4d0a ldr r5, [pc, #40] ; (109bc <free@@GLIBC\_2.4+0x15c>)

10994: 461c mov r4, r3

10996: 447d add r5, pc

10998: 4613 mov r3, r2

1099a: 460a mov r2, r1

1099c: 4908 ldr r1, [pc, #32] ; (109c0 <free@@GLIBC\_2.4+0x160>)

1099e: 9002 str r0, [sp, #8]

109a0: 2002 movs r0, #2

109a2: 9501 str r5, [sp, #4]

109a4: 4479 add r1, pc

109a6: 9400 str r4, [sp, #0]

109a8: f7fb fa10 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

109ac: 207f movs r0, #127 ; 0x7f

109ae: f001 fa4b bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

109b2: 4c04 ldr r4, [pc, #16] ; (109c4 <free@@GLIBC\_2.4+0x164>)

109b4: 447c add r4, pc

109b6: 4625 mov r5, r4

109b8: e7ee b.n 10998 <free@@GLIBC\_2.4+0x138>

109ba: bf00 nop

109bc: 6efa ldr r2, [r7, #108] ; 0x6c

109be: 0000 movs r0, r0

109c0: 6ef0 ldr r0, [r6, #108] ; 0x6c

109c2: 0000 movs r0, r0

109c4: 4b54 ldr r3, [pc, #336] ; (10b18 <realloc@@GLIBC\_2.4+0x150>)

...

000109c8 <realloc@@GLIBC\_2.4>:

109c8: b1a0 cbz r0, 109f4 <realloc@@GLIBC\_2.4+0x2c>

109ca: 4a10 ldr r2, [pc, #64] ; (10a0c <realloc@@GLIBC\_2.4+0x44>)

109cc: b538 push {r3, r4, r5, lr}

109ce: 447a add r2, pc

109d0: 4603 mov r3, r0

109d2: 4608 mov r0, r1

109d4: 6894 ldr r4, [r2, #8]

109d6: 429c cmp r4, r3

109d8: d10f bne.n 109fa <realloc@@GLIBC\_2.4+0x32>

109da: 6855 ldr r5, [r2, #4]

109dc: 6054 str r4, [r2, #4]

109de: f7ef eff6 blx 9cc <malloc@plt+0x4>

109e2: 4284 cmp r4, r0

109e4: d005 beq.n 109f2 <realloc@@GLIBC\_2.4+0x2a>

109e6: 1b2a subs r2, r5, r4

109e8: 4621 mov r1, r4

109ea: e8bd 4038 ldmia.w sp!, {r3, r4, r5, lr}

109ee: f003 b9bf b.w 13d70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x3008>

109f2: bd38 pop {r3, r4, r5, pc}

109f4: 4608 mov r0, r1

109f6: f7ef bfe7 b.w 9c8 <malloc@plt>

109fa: 4b05 ldr r3, [pc, #20] ; (10a10 <realloc@@GLIBC\_2.4+0x48>)

109fc: 227e movs r2, #126 ; 0x7e

109fe: 4905 ldr r1, [pc, #20] ; (10a14 <realloc@@GLIBC\_2.4+0x4c>)

10a00: 4805 ldr r0, [pc, #20] ; (10a18 <realloc@@GLIBC\_2.4+0x50>)

10a02: 447b add r3, pc

10a04: 4479 add r1, pc

10a06: 4478 add r0, pc

10a08: f7ff ffc0 bl 1098c <free@@GLIBC\_2.4+0x12c>

10a0c: 8f4e ldrh r6, [r1, #58] ; 0x3a

10a0e: 0001 movs r1, r0

10a10: 6f7e ldr r6, [r7, #116] ; 0x74

10a12: 0000 movs r0, r0

10a14: 6ed8 ldr r0, [r3, #108] ; 0x6c

10a16: 0000 movs r0, r0

10a18: 6ee6 ldr r6, [r4, #108] ; 0x6c

10a1a: 0000 movs r0, r0

10a1c: b580 push {r7, lr}

10a1e: 460d mov r5, r1

10a20: b0e8 sub sp, #416 ; 0x1a0

10a22: 4616 mov r6, r2

10a24: b1a3 cbz r3, 10a50 <realloc@@GLIBC\_2.4+0x88>

10a26: 4f0c ldr r7, [pc, #48] ; (10a58 <realloc@@GLIBC\_2.4+0x90>)

10a28: 461c mov r4, r3

10a2a: 447f add r7, pc

10a2c: f44f 72c8 mov.w r2, #400 ; 0x190

10a30: a904 add r1, sp, #16

10a32: f7ff ff2b bl 1088c <free@@GLIBC\_2.4+0x2c>

10a36: 4909 ldr r1, [pc, #36] ; (10a5c <realloc@@GLIBC\_2.4+0x94>)

10a38: e9cd 4700 strd r4, r7, [sp]

10a3c: 4633 mov r3, r6

10a3e: 462a mov r2, r5

10a40: 4479 add r1, pc

10a42: 9002 str r0, [sp, #8]

10a44: 2002 movs r0, #2

10a46: f7fb f9c1 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10a4a: 207f movs r0, #127 ; 0x7f

10a4c: f001 f9fc bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

10a50: 4c03 ldr r4, [pc, #12] ; (10a60 <realloc@@GLIBC\_2.4+0x98>)

10a52: 447c add r4, pc

10a54: 4627 mov r7, r4

10a56: e7e9 b.n 10a2c <realloc@@GLIBC\_2.4+0x64>

10a58: 6e66 ldr r6, [r4, #100] ; 0x64

10a5a: 0000 movs r0, r0

10a5c: 6ec4 ldr r4, [r0, #108] ; 0x6c

10a5e: 0000 movs r0, r0

10a60: 4ab6 ldr r2, [pc, #728] ; (10d3c <\_dl\_catch\_exception@@GLIBC\_PRIVATE+0x34>)

10a62: 0000 movs r0, r0

10a64: e92d 43f8 stmdb sp!, {r3, r4, r5, r6, r7, r8, r9, lr}

10a68: 9f08 ldr r7, [sp, #32]

10a6a: b9df cbnz r7, 10aa4 <realloc@@GLIBC\_2.4+0xdc>

10a6c: f8df 9048 ldr.w r9, [pc, #72] ; 10ab8 <realloc@@GLIBC\_2.4+0xf0>

10a70: 4604 mov r4, r0

10a72: 460d mov r5, r1

10a74: 4690 mov r8, r2

10a76: 461e mov r6, r3

10a78: 2700 movs r7, #0

10a7a: 44f9 add r9, pc

10a7c: e001 b.n 10a82 <realloc@@GLIBC\_2.4+0xba>

10a7e: 4604 mov r4, r0

10a80: 460d mov r5, r1

10a82: 4632 mov r2, r6

10a84: 463b mov r3, r7

10a86: 4620 mov r0, r4

10a88: 4629 mov r1, r5

10a8a: f003 f881 bl 13b90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e28>

10a8e: 42bd cmp r5, r7

10a90: bf08 it eq

10a92: 42b4 cmpeq r4, r6

10a94: f819 3002 ldrb.w r3, [r9, r2]

10a98: f808 3d01 strb.w r3, [r8, #-1]!

10a9c: d2ef bcs.n 10a7e <realloc@@GLIBC\_2.4+0xb6>

10a9e: 4640 mov r0, r8

10aa0: e8bd 83f8 ldmia.w sp!, {r3, r4, r5, r6, r7, r8, r9, pc}

10aa4: 4b05 ldr r3, [pc, #20] ; (10abc <realloc@@GLIBC\_2.4+0xf4>)

10aa6: 22f1 movs r2, #241 ; 0xf1

10aa8: 4905 ldr r1, [pc, #20] ; (10ac0 <realloc@@GLIBC\_2.4+0xf8>)

10aaa: 4806 ldr r0, [pc, #24] ; (10ac4 <realloc@@GLIBC\_2.4+0xfc>)

10aac: 447b add r3, pc

10aae: 4479 add r1, pc

10ab0: 4478 add r0, pc

10ab2: f7ff ff6b bl 1098c <free@@GLIBC\_2.4+0x12c>

10ab6: bf00 nop

10ab8: 3ade subs r2, #222 ; 0xde

10aba: 0000 movs r0, r0

10abc: 6ecc ldr r4, [r1, #108] ; 0x6c

10abe: 0000 movs r0, r0

10ac0: 6e2e ldr r6, [r5, #96] ; 0x60

10ac2: 0000 movs r0, r0

10ac4: 6e98 ldr r0, [r3, #104] ; 0x68

10ac6: 0000 movs r0, r0

10ac8: b5f8 push {r3, r4, r5, r6, r7, lr}

10aca: 780e ldrb r6, [r1, #0]

10acc: b1c6 cbz r6, 10b00 <realloc@@GLIBC\_2.4+0x138>

10ace: 4607 mov r7, r0

10ad0: 6800 ldr r0, [r0, #0]

10ad2: b170 cbz r0, 10af2 <realloc@@GLIBC\_2.4+0x12a>

10ad4: 7804 ldrb r4, [r0, #0]

10ad6: b18c cbz r4, 10afc <realloc@@GLIBC\_2.4+0x134>

10ad8: 4605 mov r5, r0

10ada: 4633 mov r3, r6

10adc: 460a mov r2, r1

10ade: e002 b.n 10ae6 <realloc@@GLIBC\_2.4+0x11e>

10ae0: f812 3f01 ldrb.w r3, [r2, #1]!

10ae4: b133 cbz r3, 10af4 <realloc@@GLIBC\_2.4+0x12c>

10ae6: 42a3 cmp r3, r4

10ae8: d1fa bne.n 10ae0 <realloc@@GLIBC\_2.4+0x118>

10aea: 2300 movs r3, #0

10aec: f805 3b01 strb.w r3, [r5], #1

10af0: 603d str r5, [r7, #0]

10af2: bdf8 pop {r3, r4, r5, r6, r7, pc}

10af4: f815 4f01 ldrb.w r4, [r5, #1]!

10af8: 2c00 cmp r4, #0

10afa: d1ee bne.n 10ada <realloc@@GLIBC\_2.4+0x112>

10afc: 4625 mov r5, r4

10afe: e7f7 b.n 10af0 <realloc@@GLIBC\_2.4+0x128>

10b00: 4b04 ldr r3, [pc, #16] ; (10b14 <realloc@@GLIBC\_2.4+0x14c>)

10b02: f240 1209 movw r2, #265 ; 0x109

10b06: 4904 ldr r1, [pc, #16] ; (10b18 <realloc@@GLIBC\_2.4+0x150>)

10b08: 4804 ldr r0, [pc, #16] ; (10b1c <realloc@@GLIBC\_2.4+0x154>)

10b0a: 447b add r3, pc

10b0c: 4479 add r1, pc

10b0e: 4478 add r0, pc

10b10: f7ff ff3c bl 1098c <free@@GLIBC\_2.4+0x12c>

10b14: 6e62 ldr r2, [r4, #100] ; 0x64

10b16: 0000 movs r0, r0

10b18: 6dd0 ldr r0, [r2, #92] ; 0x5c

10b1a: 0000 movs r0, r0

10b1c: 6e4a ldr r2, [r1, #100] ; 0x64

10b1e: 0000 movs r0, r0

10b20: 4c1b ldr r4, [pc, #108] ; (10b90 <realloc@@GLIBC\_2.4+0x1c8>)

10b22: 460e mov r6, r1

10b24: b580 push {r7, lr}

10b26: 447c add r4, pc

10b28: 461f mov r7, r3

10b2a: f5ad 6d83 sub.w sp, sp, #1048 ; 0x418

10b2e: 6823 ldr r3, [r4, #0]

10b30: 4615 mov r5, r2

10b32: 681c ldr r4, [r3, #0]

10b34: b324 cbz r4, 10b80 <realloc@@GLIBC\_2.4+0x1b8>

10b36: b33d cbz r5, 10b88 <realloc@@GLIBC\_2.4+0x1c0>

10b38: 7833 ldrb r3, [r6, #0]

10b3a: b9eb cbnz r3, 10b78 <realloc@@GLIBC\_2.4+0x1b0>

10b3c: f8df 8054 ldr.w r8, [pc, #84] ; 10b94 <realloc@@GLIBC\_2.4+0x1cc>

10b40: 44f8 add r8, pc

10b42: b1a8 cbz r0, 10b70 <realloc@@GLIBC\_2.4+0x1a8>

10b44: f44f 6280 mov.w r2, #1024 ; 0x400

10b48: a906 add r1, sp, #24

10b4a: f7ff fe9f bl 1088c <free@@GLIBC\_2.4+0x2c>

10b4e: 4a12 ldr r2, [pc, #72] ; (10b98 <realloc@@GLIBC\_2.4+0x1d0>)

10b50: 447a add r2, pc

10b52: 4912 ldr r1, [pc, #72] ; (10b9c <realloc@@GLIBC\_2.4+0x1d4>)

10b54: 462b mov r3, r5

10b56: 9004 str r0, [sp, #16]

10b58: 2002 movs r0, #2

10b5a: 9203 str r2, [sp, #12]

10b5c: 4479 add r1, pc

10b5e: 4622 mov r2, r4

10b60: 9702 str r7, [sp, #8]

10b62: e9cd 6800 strd r6, r8, [sp]

10b66: f7fb f931 bl bdcc <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x3ec>

10b6a: 207f movs r0, #127 ; 0x7f

10b6c: f001 f96c bl 11e48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10e0>

10b70: 4a0b ldr r2, [pc, #44] ; (10ba0 <realloc@@GLIBC\_2.4+0x1d8>)

10b72: 447a add r2, pc

10b74: 4610 mov r0, r2

10b76: e7ec b.n 10b52 <realloc@@GLIBC\_2.4+0x18a>

10b78: f8df 8028 ldr.w r8, [pc, #40] ; 10ba4 <realloc@@GLIBC\_2.4+0x1dc>

10b7c: 44f8 add r8, pc

10b7e: e7e0 b.n 10b42 <realloc@@GLIBC\_2.4+0x17a>

10b80: 4c09 ldr r4, [pc, #36] ; (10ba8 <realloc@@GLIBC\_2.4+0x1e0>)

10b82: 447c add r4, pc

10b84: 2d00 cmp r5, #0

10b86: d1d7 bne.n 10b38 <realloc@@GLIBC\_2.4+0x170>

10b88: 4d08 ldr r5, [pc, #32] ; (10bac <realloc@@GLIBC\_2.4+0x1e4>)

10b8a: 447d add r5, pc

10b8c: e7d4 b.n 10b38 <realloc@@GLIBC\_2.4+0x170>

10b8e: bf00 nop

10b90: 8d72 ldrh r2, [r6, #42] ; 0x2a

10b92: 0001 movs r1, r0

10b94: 49c8 ldr r1, [pc, #800] ; (10eb8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x150>)

10b96: 0000 movs r0, r0

10b98: 6d40 ldr r0, [r0, #84] ; 0x54

10b9a: 0000 movs r0, r0

10b9c: 6e54 ldr r4, [r2, #100] ; 0x64

10b9e: 0000 movs r0, r0

10ba0: 4996 ldr r1, [pc, #600] ; (10dfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x94>)

10ba2: 0000 movs r0, r0

10ba4: 6d14 ldr r4, [r2, #80] ; 0x50

10ba6: 0000 movs r0, r0

10ba8: 3a66 subs r2, #102 ; 0x66

10baa: 0000 movs r0, r0

10bac: 6dfe ldr r6, [r7, #92] ; 0x5c

...

00010bb0 <\_dl\_signal\_exception@@GLIBC\_PRIVATE>:

10bb0: b508 push {r3, lr}

10bb2: 4b0a ldr r3, [pc, #40] ; (10bdc <\_dl\_signal\_exception@@GLIBC\_PRIVATE+0x2c>)

10bb4: 447b add r3, pc

10bb6: 681b ldr r3, [r3, #0]

10bb8: b15b cbz r3, 10bd2 <\_dl\_signal\_exception@@GLIBC\_PRIVATE+0x22>

10bba: 4605 mov r5, r0

10bbc: e9d3 4600 ldrd r4, r6, [r3]

10bc0: c907 ldmia r1, {r0, r1, r2}

10bc2: e884 0007 stmia.w r4, {r0, r1, r2}

10bc6: f103 0008 add.w r0, r3, #8

10bca: 6035 str r5, [r6, #0]

10bcc: 2101 movs r1, #1

10bce: f001 f997 bl 11f00 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1198>

10bd2: e9d1 1300 ldrd r1, r3, [r1]

10bd6: f7ff ffa3 bl 10b20 <realloc@@GLIBC\_2.4+0x158>

10bda: bf00 nop

10bdc: 8d74 ldrh r4, [r6, #42] ; 0x2a

10bde: 0001 movs r1, r0

00010be0 <\_dl\_signal\_error@@GLIBC\_PRIVATE>:

10be0: 4c0c ldr r4, [pc, #48] ; (10c14 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x34>)

10be2: 4605 mov r5, r0

10be4: b508 push {r3, lr}

10be6: 447c add r4, pc

10be8: 6824 ldr r4, [r4, #0]

10bea: b15b cbz r3, 10c04 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x24>

10bec: b174 cbz r4, 10c0c <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x2c>

10bee: 461a mov r2, r3

10bf0: 6820 ldr r0, [r4, #0]

10bf2: f7fe fa9d bl f130 <\_dl\_exception\_create@@GLIBC\_PRIVATE>

10bf6: 6863 ldr r3, [r4, #4]

10bf8: f104 0008 add.w r0, r4, #8

10bfc: 2101 movs r1, #1

10bfe: 601d str r5, [r3, #0]

10c00: f001 f97e bl 11f00 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1198>

10c04: 4b04 ldr r3, [pc, #16] ; (10c18 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x38>)

10c06: 447b add r3, pc

10c08: 2c00 cmp r4, #0

10c0a: d1f0 bne.n 10bee <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xe>

10c0c: 4628 mov r0, r5

10c0e: f7ff ff87 bl 10b20 <realloc@@GLIBC\_2.4+0x158>

10c12: bf00 nop

10c14: 8d42 ldrh r2, [r0, #42] ; 0x2a

10c16: 0001 movs r1, r0

10c18: 6dbe ldr r6, [r7, #88] ; 0x58

10c1a: 0000 movs r0, r0

10c1c: b5f0 push {r4, r5, r6, r7, lr}

10c1e: 4617 mov r7, r2

10c20: 4c15 ldr r4, [pc, #84] ; (10c78 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x98>)

10c22: b083 sub sp, #12

10c24: 4b15 ldr r3, [pc, #84] ; (10c7c <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x9c>)

10c26: 4606 mov r6, r0

10c28: 447c add r4, pc

10c2a: 460d mov r5, r1

10c2c: 447b add r3, pc

10c2e: 6822 ldr r2, [r4, #0]

10c30: 685c ldr r4, [r3, #4]

10c32: f432 6308 bics.w r3, r2, #2176 ; 0x880

10c36: d108 bne.n 10c4a <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x6a>

10c38: b1cc cbz r4, 10c6e <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x8e>

10c3a: 4630 mov r0, r6

10c3c: e9d5 1200 ldrd r1, r2, [r5]

10c40: 4623 mov r3, r4

10c42: b003 add sp, #12

10c44: e8bd 40f0 ldmia.w sp!, {r4, r5, r6, r7, lr}

10c48: 4718 bx r3

10c4a: e9d1 1300 ldrd r1, r3, [r1]

10c4e: b15c cbz r4, 10c68 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x88>

10c50: 4a0b ldr r2, [pc, #44] ; (10c80 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xa0>)

10c52: 447a add r2, pc

10c54: 480b ldr r0, [pc, #44] ; (10c84 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xa4>)

10c56: 9200 str r2, [sp, #0]

10c58: 463a mov r2, r7

10c5a: 4478 add r0, pc

10c5c: f7fb f88a bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

10c60: 4b09 ldr r3, [pc, #36] ; (10c88 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xa8>)

10c62: 447b add r3, pc

10c64: 685c ldr r4, [r3, #4]

10c66: e7e7 b.n 10c38 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x58>

10c68: 4a08 ldr r2, [pc, #32] ; (10c8c <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xac>)

10c6a: 447a add r2, pc

10c6c: e7f2 b.n 10c54 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x74>

10c6e: 463a mov r2, r7

10c70: 4629 mov r1, r5

10c72: 4630 mov r0, r6

10c74: f7ef eeb0 blx 9d8 <\_dl\_signal\_exception@plt>

10c78: 7d44 ldrb r4, [r0, #21]

10c7a: 0001 movs r1, r0

10c7c: 8cfc ldrh r4, [r7, #38] ; 0x26

10c7e: 0001 movs r1, r0

10c80: 6d92 ldr r2, [r2, #88] ; 0x58

10c82: 0000 movs r0, r0

10c84: 6d96 ldr r6, [r2, #88] ; 0x58

10c86: 0000 movs r0, r0

10c88: 8cc6 ldrh r6, [r0, #38] ; 0x26

10c8a: 0001 movs r1, r0

10c8c: 6d72 ldr r2, [r6, #84] ; 0x54

10c8e: 0000 movs r0, r0

10c90: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

10c94: 4607 mov r7, r0

10c96: 4c16 ldr r4, [pc, #88] ; (10cf0 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x110>)

10c98: 4690 mov r8, r2

10c9a: 4816 ldr r0, [pc, #88] ; (10cf4 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x114>)

10c9c: 461e mov r6, r3

10c9e: 447c add r4, pc

10ca0: b082 sub sp, #8

10ca2: 4478 add r0, pc

10ca4: 460d mov r5, r1

10ca6: 6822 ldr r2, [r4, #0]

10ca8: 6843 ldr r3, [r0, #4]

10caa: f432 6208 bics.w r2, r2, #2176 ; 0x880

10cae: d107 bne.n 10cc0 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xe0>

10cb0: b1c3 cbz r3, 10ce4 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x104>

10cb2: 4632 mov r2, r6

10cb4: 4629 mov r1, r5

10cb6: 4638 mov r0, r7

10cb8: b002 add sp, #8

10cba: e8bd 41f0 ldmia.w sp!, {r4, r5, r6, r7, r8, lr}

10cbe: 4718 bx r3

10cc0: b16b cbz r3, 10cde <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xfe>

10cc2: 4b0d ldr r3, [pc, #52] ; (10cf8 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x118>)

10cc4: 447b add r3, pc

10cc6: 480d ldr r0, [pc, #52] ; (10cfc <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x11c>)

10cc8: 4642 mov r2, r8

10cca: 9300 str r3, [sp, #0]

10ccc: 4629 mov r1, r5

10cce: 4633 mov r3, r6

10cd0: 4478 add r0, pc

10cd2: f7fb f84f bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

10cd6: 4b0a ldr r3, [pc, #40] ; (10d00 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x120>)

10cd8: 447b add r3, pc

10cda: 685b ldr r3, [r3, #4]

10cdc: e7e8 b.n 10cb0 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xd0>

10cde: 4b09 ldr r3, [pc, #36] ; (10d04 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0x124>)

10ce0: 447b add r3, pc

10ce2: e7f0 b.n 10cc6 <\_dl\_signal\_error@@GLIBC\_PRIVATE+0xe6>

10ce4: 4633 mov r3, r6

10ce6: 4642 mov r2, r8

10ce8: 4629 mov r1, r5

10cea: 4638 mov r0, r7

10cec: f7ef ee8c blx a08 <\_dl\_signal\_error@plt>

10cf0: 7cce ldrb r6, [r1, #19]

10cf2: 0001 movs r1, r0

10cf4: 8c86 ldrh r6, [r0, #36] ; 0x24

10cf6: 0001 movs r1, r0

10cf8: 6d20 ldr r0, [r4, #80] ; 0x50

10cfa: 0000 movs r0, r0

10cfc: 6d20 ldr r0, [r4, #80] ; 0x50

10cfe: 0000 movs r0, r0

10d00: 8c50 ldrh r0, [r2, #34] ; 0x22

10d02: 0001 movs r1, r0

10d04: 6cfc ldr r4, [r7, #76] ; 0x4c

...

00010d08 <\_dl\_catch\_exception@@GLIBC\_PRIVATE>:

10d08: 4b14 ldr r3, [pc, #80] ; (10d5c <\_dl\_catch\_exception@@GLIBC\_PRIVATE+0x54>)

10d0a: b510 push {r4, lr}

10d0c: 447b add r3, pc

10d0e: b0ea sub sp, #424 ; 0x1a8

10d10: 4604 mov r4, r0

10d12: a808 add r0, sp, #32

10d14: 9203 str r2, [sp, #12]

10d16: 461a mov r2, r3

10d18: 681b ldr r3, [r3, #0]

10d1a: 9102 str r1, [sp, #8]

10d1c: 2100 movs r1, #0

10d1e: 9401 str r4, [sp, #4]

10d20: 9300 str r3, [sp, #0]

10d22: ab06 add r3, sp, #24

10d24: 9406 str r4, [sp, #24]

10d26: 6013 str r3, [r2, #0]

10d28: aa05 add r2, sp, #20

10d2a: 9207 str r2, [sp, #28]

10d2c: f001 f8b8 bl 11ea0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1138>

10d30: b970 cbnz r0, 10d50 <\_dl\_catch\_exception@@GLIBC\_PRIVATE+0x48>

10d32: 4604 mov r4, r0

10d34: e9dd 1002 ldrd r1, r0, [sp, #8]

10d38: 4788 blx r1

10d3a: 9a01 ldr r2, [sp, #4]

10d3c: 4b08 ldr r3, [pc, #32] ; (10d60 <\_dl\_catch\_exception@@GLIBC\_PRIVATE+0x58>)

10d3e: e9c2 4400 strd r4, r4, [r2]

10d42: 447b add r3, pc

10d44: 6094 str r4, [r2, #8]

10d46: 9a00 ldr r2, [sp, #0]

10d48: 601a str r2, [r3, #0]

10d4a: 4620 mov r0, r4

10d4c: b06a add sp, #424 ; 0x1a8

10d4e: bd10 pop {r4, pc}

10d50: 4b04 ldr r3, [pc, #16] ; (10d64 <\_dl\_catch\_exception@@GLIBC\_PRIVATE+0x5c>)

10d52: 9a00 ldr r2, [sp, #0]

10d54: 447b add r3, pc

10d56: 9c05 ldr r4, [sp, #20]

10d58: 601a str r2, [r3, #0]

10d5a: e7f6 b.n 10d4a <\_dl\_catch\_exception@@GLIBC\_PRIVATE+0x42>

10d5c: 8c1c ldrh r4, [r3, #32]

10d5e: 0001 movs r1, r0

10d60: 8be6 ldrh r6, [r4, #30]

10d62: 0001 movs r1, r0

10d64: 8bd4 ldrh r4, [r2, #30]

10d66: 0001 movs r1, r0

00010d68 <\_dl\_catch\_error@@GLIBC\_PRIVATE>:

10d68: b570 push {r4, r5, r6, lr}

10d6a: b084 sub sp, #16

10d6c: 4606 mov r6, r0

10d6e: 460d mov r5, r1

10d70: 4614 mov r4, r2

10d72: 4619 mov r1, r3

10d74: 9a08 ldr r2, [sp, #32]

10d76: a801 add r0, sp, #4

10d78: f7ef ee20 blx 9bc <\_dl\_catch\_exception@plt>

10d7c: e9dd 1201 ldrd r1, r2, [sp, #4]

10d80: 9b03 ldr r3, [sp, #12]

10d82: 1a9b subs r3, r3, r2

10d84: 6031 str r1, [r6, #0]

10d86: fab3 f383 clz r3, r3

10d8a: 602a str r2, [r5, #0]

10d8c: 095b lsrs r3, r3, #5

10d8e: 7023 strb r3, [r4, #0]

10d90: b004 add sp, #16

10d92: bd70 pop {r4, r5, r6, pc}

10d94: b570 push {r4, r5, r6, lr}

10d96: 2300 movs r3, #0

10d98: 4c05 ldr r4, [pc, #20] ; (10db0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x48>)

10d9a: 447c add r4, pc

10d9c: e9d4 6500 ldrd r6, r5, [r4]

10da0: 6060 str r0, [r4, #4]

10da2: 4610 mov r0, r2

10da4: 6023 str r3, [r4, #0]

10da6: 4788 blx r1

10da8: e9c4 6500 strd r6, r5, [r4]

10dac: bd70 pop {r4, r5, r6, pc}

10dae: bf00 nop

10db0: 8b8e ldrh r6, [r1, #28]

10db2: 0001 movs r1, r0

10db4: 4b6b ldr r3, [pc, #428] ; (10f64 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1fc>)

10db6: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

10dba: 447b add r3, pc

10dbc: 4616 mov r6, r2

10dbe: b083 sub sp, #12

10dc0: 681b ldr r3, [r3, #0]

10dc2: 4607 mov r7, r0

10dc4: 460c mov r4, r1

10dc6: 069a lsls r2, r3, #26

10dc8: f100 80a5 bmi.w 10f16 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ae>

10dcc: 69bb ldr r3, [r7, #24]

10dce: 2b00 cmp r3, #0

10dd0: f040 80b8 bne.w 10f44 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1dc>

10dd4: 4a64 ldr r2, [pc, #400] ; (10f68 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x200>)

10dd6: 1b33 subs r3, r6, r4

10dd8: f64a 21ab movw r1, #43691 ; 0xaaab

10ddc: f6ca 21aa movt r1, #43690 ; 0xaaaa

10de0: 447a add r2, pc

10de2: 109b asrs r3, r3, #2

10de4: 42a6 cmp r6, r4

10de6: f8d2 050c ldr.w r0, [r2, #1292] ; 0x50c

10dea: fb01 0303 mla r3, r1, r3, r0

10dee: f8c2 350c str.w r3, [r2, #1292] ; 0x50c

10df2: d966 bls.n 10ec2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15a>

10df4: f8df 8174 ldr.w r8, [pc, #372] ; 10f6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x204>

10df8: f24f 0a04 movw sl, #61444 ; 0xf004

10dfc: f8df 9170 ldr.w r9, [pc, #368] ; 10f70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x208>

10e00: f2ce 5a1f movt sl, #58655 ; 0xe51f

10e04: 44f8 add r8, pc

10e06: 44f9 add r9, pc

10e08: e03c b.n 10e84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11c>

10e0a: 2902 cmp r1, #2

10e0c: d05c beq.n 10ec8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x160>

10e0e: 2911 cmp r1, #17

10e10: d035 beq.n 10e7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x116>

10e12: 2901 cmp r1, #1

10e14: d145 bne.n 10ea2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13a>

10e16: 68a3 ldr r3, [r4, #8]

10e18: eba3 0b05 sub.w fp, r3, r5

10e1c: f00b 437e and.w r3, fp, #4261412864 ; 0xfe000000

10e20: f1b3 4f7e cmp.w r3, #4261412864 ; 0xfe000000

10e24: bf18 it ne

10e26: 2b00 cmpne r3, #0

10e28: d021 beq.n 10e6e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x106>

10e2a: f8d9 2000 ldr.w r2, [r9]

10e2e: 2a00 cmp r2, #0

10e30: d052 beq.n 10ed8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x170>

10e32: f8d9 3004 ldr.w r3, [r9, #4]

10e36: 494f ldr r1, [pc, #316] ; (10f74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x20c>)

10e38: 18d0 adds r0, r2, r3

10e3a: f8df c13c ldr.w ip, [pc, #316] ; 10f78 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x210>

10e3e: f04f 0e00 mov.w lr, #0

10e42: 4479 add r1, pc

10e44: f842 a003 str.w sl, [r2, r3]

10e48: 44fc add ip, pc

10e4a: f8c0 e004 str.w lr, [r0, #4]

10e4e: 690a ldr r2, [r1, #16]

10e50: 3308 adds r3, #8

10e52: 4483 add fp, r0

10e54: f8cc 3004 str.w r3, [ip, #4]

10e58: 4293 cmp r3, r2

10e5a: f00b 437e and.w r3, fp, #4261412864 ; 0xfe000000

10e5e: bf28 it cs

10e60: e9cc ee00 strdcs lr, lr, [ip]

10e64: f1b3 4f7e cmp.w r3, #4261412864 ; 0xfe000000

10e68: bf18 it ne

10e6a: 2b00 cmpne r3, #0

10e6c: d173 bne.n 10f56 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ee>

10e6e: 682b ldr r3, [r5, #0]

10e70: f3cb 0b97 ubfx fp, fp, #2, #24

10e74: f003 437f and.w r3, r3, #4278190080 ; 0xff000000

10e78: ea43 030b orr.w r3, r3, fp

10e7c: 602b str r3, [r5, #0]

10e7e: 340c adds r4, #12

10e80: 42a6 cmp r6, r4

10e82: d91e bls.n 10ec2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15a>

10e84: 7921 ldrb r1, [r4, #4]

10e86: 6825 ldr r5, [r4, #0]

10e88: 2917 cmp r1, #23

10e8a: d020 beq.n 10ece <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x166>

10e8c: 2900 cmp r1, #0

10e8e: d0f6 beq.n 10e7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x116>

10e90: 2912 cmp r1, #18

10e92: d019 beq.n 10ec8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x160>

10e94: d9b9 bls.n 10e0a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa2>

10e96: 2916 cmp r1, #22

10e98: d807 bhi.n 10eaa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x142>

10e9a: 2915 cmp r1, #21

10e9c: d214 bcs.n 10ec8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x160>

10e9e: 2913 cmp r1, #19

10ea0: d0ed beq.n 10e7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x116>

10ea2: 4638 mov r0, r7

10ea4: 2200 movs r2, #0

10ea6: f7f7 fcdb bl 8860 <\_dl\_rtld\_di\_serinfo@@GLIBC\_PRIVATE+0x1bcc>

10eaa: 29a0 cmp r1, #160 ; 0xa0

10eac: d1f9 bne.n 10ea2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13a>

10eae: 68a2 ldr r2, [r4, #8]

10eb0: 340c adds r4, #12

10eb2: 683b ldr r3, [r7, #0]

10eb4: f8d8 0040 ldr.w r0, [r8, #64] ; 0x40

10eb8: 4413 add r3, r2

10eba: 4798 blx r3

10ebc: 42a6 cmp r6, r4

10ebe: 6028 str r0, [r5, #0]

10ec0: d8e0 bhi.n 10e84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11c>

10ec2: b003 add sp, #12

10ec4: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

10ec8: 68a3 ldr r3, [r4, #8]

10eca: 602b str r3, [r5, #0]

10ecc: e7d7 b.n 10e7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x116>

10ece: 683b ldr r3, [r7, #0]

10ed0: 68a2 ldr r2, [r4, #8]

10ed2: 4413 add r3, r2

10ed4: 602b str r3, [r5, #0]

10ed6: e7d2 b.n 10e7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x116>

10ed8: 4928 ldr r1, [pc, #160] ; (10f7c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x214>)

10eda: 4610 mov r0, r2

10edc: 9201 str r2, [sp, #4]

10ede: 2322 movs r3, #34 ; 0x22

10ee0: 4479 add r1, pc

10ee2: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

10ee6: 9200 str r2, [sp, #0]

10ee8: 2207 movs r2, #7

10eea: 6909 ldr r1, [r1, #16]

10eec: f000 ff42 bl 11d74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x100c>

10ef0: 1c43 adds r3, r0, #1

10ef2: 4602 mov r2, r0

10ef4: d01f beq.n 10f36 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ce>

10ef6: 4922 ldr r1, [pc, #136] ; (10f80 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x218>)

10ef8: 4479 add r1, pc

10efa: 684b ldr r3, [r1, #4]

10efc: 6008 str r0, [r1, #0]

10efe: 2b00 cmp r3, #0

10f00: d099 beq.n 10e36 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xce>

10f02: 4b20 ldr r3, [pc, #128] ; (10f84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21c>)

10f04: f240 124f movw r2, #335 ; 0x14f

10f08: 491f ldr r1, [pc, #124] ; (10f88 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x220>)

10f0a: 4820 ldr r0, [pc, #128] ; (10f8c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x224>)

10f0c: 447b add r3, pc

10f0e: 4479 add r1, pc

10f10: 4478 add r0, pc

10f12: f7ff fd3b bl 1098c <free@@GLIBC\_2.4+0x12c>

10f16: 6841 ldr r1, [r0, #4]

10f18: 780b ldrb r3, [r1, #0]

10f1a: b923 cbnz r3, 10f26 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1be>

10f1c: 4b1c ldr r3, [pc, #112] ; (10f90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x228>)

10f1e: 447b add r3, pc

10f20: 681b ldr r3, [r3, #0]

10f22: 6819 ldr r1, [r3, #0]

10f24: b121 cbz r1, 10f30 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c8>

10f26: 481b ldr r0, [pc, #108] ; (10f94 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22c>)

10f28: 4478 add r0, pc

10f2a: f7fa ff23 bl bd74 <\_dl\_debug\_state@@GLIBC\_PRIVATE+0x394>

10f2e: e74d b.n 10dcc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x64>

10f30: 4919 ldr r1, [pc, #100] ; (10f98 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x230>)

10f32: 4479 add r1, pc

10f34: e7f7 b.n 10f26 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1be>

10f36: 4b19 ldr r3, [pc, #100] ; (10f9c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x234>)

10f38: 2200 movs r2, #0

10f3a: 6879 ldr r1, [r7, #4]

10f3c: 4610 mov r0, r2

10f3e: 447b add r3, pc

10f40: f7ef ed62 blx a08 <\_dl\_signal\_error@plt>

10f44: 4b16 ldr r3, [pc, #88] ; (10fa0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x238>)

10f46: 2237 movs r2, #55 ; 0x37

10f48: 4916 ldr r1, [pc, #88] ; (10fa4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x23c>)

10f4a: 4817 ldr r0, [pc, #92] ; (10fa8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x240>)

10f4c: 447b add r3, pc

10f4e: 4479 add r1, pc

10f50: 4478 add r0, pc

10f52: f7ff fd1b bl 1098c <free@@GLIBC\_2.4+0x12c>

10f56: 4b15 ldr r3, [pc, #84] ; (10fac <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x244>)

10f58: 2200 movs r2, #0

10f5a: 6879 ldr r1, [r7, #4]

10f5c: 4610 mov r0, r2

10f5e: 447b add r3, pc

10f60: f7ef ed52 blx a08 <\_dl\_signal\_error@plt>

10f64: 7bb2 ldrb r2, [r6, #14]

10f66: 0001 movs r1, r0

10f68: 827c strh r4, [r7, #18]

10f6a: 0001 movs r1, r0

10f6c: 7b68 ldrb r0, [r5, #13]

10f6e: 0001 movs r1, r0

10f70: 8b2a ldrh r2, [r5, #24]

10f72: 0001 movs r1, r0

10f74: 7b2a ldrb r2, [r5, #12]

10f76: 0001 movs r1, r0

10f78: 8ae8 ldrh r0, [r5, #22]

10f7a: 0001 movs r1, r0

10f7c: 7a8c ldrb r4, [r1, #10]

10f7e: 0001 movs r1, r0

10f80: 8a38 ldrh r0, [r7, #16]

10f82: 0001 movs r1, r0

10f84: 48c8 ldr r0, [pc, #800] ; (112a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x540>)

10f86: 0000 movs r0, r0

10f88: 3b6a subs r3, #106 ; 0x6a

10f8a: 0000 movs r0, r0

10f8c: 3b84 subs r3, #132 ; 0x84

10f8e: 0000 movs r0, r0

10f90: 897a ldrh r2, [r7, #10]

10f92: 0001 movs r1, r0

10f94: 6ae0 ldr r0, [r4, #44] ; 0x2c

10f96: 0000 movs r0, r0

10f98: 3ba6 subs r3, #166 ; 0xa6

10f9a: 0000 movs r0, r0

10f9c: 3b1a subs r3, #26

10f9e: 0000 movs r0, r0

10fa0: 6b00 ldr r0, [r0, #48] ; 0x30

10fa2: 0000 movs r0, r0

10fa4: 6ad6 ldr r6, [r2, #44] ; 0x2c

10fa6: 0000 movs r0, r0

10fa8: 6ae4 ldr r4, [r4, #44] ; 0x2c

10faa: 0000 movs r0, r0

10fac: 3b46 subs r3, #70 ; 0x46

10fae: 0000 movs r0, r0

10fb0: ee1d 0f70 mrc 15, 0, r0, cr13, cr0, {3}

10fb4: 4770 bx lr

10fb6: bf00 nop

...

10fc0: b580 push {r7, lr}

10fc2: 4667 mov r7, ip

10fc4: df00 svc 0

10fc6: bd80 pop {r7, pc}

10fc8: b510 push {r4, lr}

10fca: 4603 mov r3, r0

10fcc: f04f 0c2d mov.w ip, #45 ; 0x2d

10fd0: f7ff fff6 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

10fd4: f510 5f80 cmn.w r0, #4096 ; 0x1000

10fd8: d807 bhi.n 10fea <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x282>

10fda: 4a0d ldr r2, [pc, #52] ; (11010 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a8>)

10fdc: 4283 cmp r3, r0

10fde: 447a add r2, pc

10fe0: 6010 str r0, [r2, #0]

10fe2: bf98 it ls

10fe4: 2000 movls r0, #0

10fe6: d80b bhi.n 11000 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x298>

10fe8: bd10 pop {r4, pc}

10fea: 4c0a ldr r4, [pc, #40] ; (11014 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ac>)

10fec: 4243 negs r3, r0

10fee: 4a0a ldr r2, [pc, #40] ; (11018 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b0>)

10ff0: f04f 31ff mov.w r1, #4294967295 ; 0xffffffff

10ff4: 447c add r4, pc

10ff6: 2000 movs r0, #0

10ff8: 447a add r2, pc

10ffa: 6023 str r3, [r4, #0]

10ffc: 6011 str r1, [r2, #0]

10ffe: bd10 pop {r4, pc}

11000: 4b06 ldr r3, [pc, #24] ; (1101c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b4>)

11002: 220c movs r2, #12

11004: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11008: 447b add r3, pc

1100a: 601a str r2, [r3, #0]

1100c: bd10 pop {r4, pc}

1100e: bf00 nop

11010: 895a ldrh r2, [r3, #10]

11012: 0001 movs r1, r0

11014: 8960 ldrh r0, [r4, #10]

11016: 0001 movs r1, r0

11018: 8940 ldrh r0, [r0, #10]

1101a: 0001 movs r1, r0

1101c: 894c ldrh r4, [r1, #10]

1101e: 0001 movs r1, r0

11020: b538 push {r3, r4, r5, lr}

11022: 4605 mov r5, r0

11024: 4b18 ldr r3, [pc, #96] ; (11088 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x320>)

11026: 447b add r3, pc

11028: 681c ldr r4, [r3, #0]

1102a: b19c cbz r4, 11054 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ec>

1102c: 4b17 ldr r3, [pc, #92] ; (1108c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x324>)

1102e: 447b add r3, pc

11030: 681b ldr r3, [r3, #0]

11032: b97b cbnz r3, 11054 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ec>

11034: 2d00 cmp r5, #0

11036: d00b beq.n 11050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e8>

11038: dd15 ble.n 11066 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2fe>

1103a: 42ec cmn r4, r5

1103c: bf2c ite cs

1103e: 2301 movcs r3, #1

11040: 2300 movcc r3, #0

11042: b1bb cbz r3, 11074 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x30c>

11044: 4b12 ldr r3, [pc, #72] ; (11090 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x328>)

11046: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

1104a: 220c movs r2, #12

1104c: 447b add r3, pc

1104e: 601a str r2, [r3, #0]

11050: 4620 mov r0, r4

11052: bd38 pop {r3, r4, r5, pc}

11054: 2000 movs r0, #0

11056: f7ff ffb7 bl 10fc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x260>

1105a: 2800 cmp r0, #0

1105c: db0f blt.n 1107e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x316>

1105e: 4b0d ldr r3, [pc, #52] ; (11094 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x32c>)

11060: 447b add r3, pc

11062: 681c ldr r4, [r3, #0]

11064: e7e6 b.n 11034 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2cc>

11066: 426b negs r3, r5

11068: 429c cmp r4, r3

1106a: bf2c ite cs

1106c: 2300 movcs r3, #0

1106e: 2301 movcc r3, #1

11070: 2b00 cmp r3, #0

11072: d1e7 bne.n 11044 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2dc>

11074: 1960 adds r0, r4, r5

11076: f7ff ffa7 bl 10fc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x260>

1107a: 2800 cmp r0, #0

1107c: dae8 bge.n 11050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e8>

1107e: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

11082: 4620 mov r0, r4

11084: bd38 pop {r3, r4, r5, pc}

11086: bf00 nop

11088: 8912 ldrh r2, [r2, #8]

1108a: 0001 movs r1, r0

1108c: 88ea ldrh r2, [r5, #6]

1108e: 0001 movs r1, r0

11090: 8908 ldrh r0, [r1, #8]

11092: 0001 movs r1, r0

11094: 88d8 ldrh r0, [r3, #6]

11096: 0001 movs r1, r0

11098: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

1109c: b0af sub sp, #188 ; 0xbc

1109e: 4681 mov r9, r0

110a0: f04f 0cb7 mov.w ip, #183 ; 0xb7

110a4: 9103 str r1, [sp, #12]

110a6: f7ff ff8b bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

110aa: f510 5f80 cmn.w r0, #4096 ; 0x1000

110ae: f200 814c bhi.w 1134a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5e2>

110b2: 2800 cmp r0, #0

110b4: f340 8121 ble.w 112fa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x592>

110b8: f899 3000 ldrb.w r3, [r9]

110bc: 2b2f cmp r3, #47 ; 0x2f

110be: bf08 it eq

110c0: 4648 moveq r0, r9

110c2: f000 8117 beq.w 112f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x58c>

110c6: 4bc2 ldr r3, [pc, #776] ; (113d0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x668>)

110c8: 447b add r3, pc

110ca: 681f ldr r7, [r3, #0]

110cc: 9b03 ldr r3, [sp, #12]

110ce: ae14 add r6, sp, #80 ; 0x50

110d0: 49c0 ldr r1, [pc, #768] ; (113d4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x66c>)

110d2: f04f 0800 mov.w r8, #0

110d6: eb09 0403 add.w r4, r9, r3

110da: 4632 mov r2, r6

110dc: 4479 add r1, pc

110de: 2003 movs r0, #3

110e0: f804 8c01 strb.w r8, [r4, #-1]

110e4: 940a str r4, [sp, #40] ; 0x28

110e6: f000 fc7f bl 119e8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc80>

110ea: 4540 cmp r0, r8

110ec: f2c0 8132 blt.w 11354 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5ec>

110f0: e9dd ab14 ldrd sl, fp, [sp, #80] ; 0x50

110f4: 4632 mov r2, r6

110f6: 49b8 ldr r1, [pc, #736] ; (113d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x670>)

110f8: 2003 movs r0, #3

110fa: 9d2c ldr r5, [sp, #176] ; 0xb0

110fc: 4479 add r1, pc

110fe: e9cd ab04 strd sl, fp, [sp, #16]

11102: f000 fc71 bl 119e8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc80>

11106: 4540 cmp r0, r8

11108: f2c0 8138 blt.w 1137c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x614>

1110c: e9dd 1214 ldrd r1, r2, [sp, #80] ; 0x50

11110: 1e63 subs r3, r4, #1

11112: 982c ldr r0, [sp, #176] ; 0xb0

11114: 4593 cmp fp, r2

11116: 930b str r3, [sp, #44] ; 0x2c

11118: bf08 it eq

1111a: 458a cmpeq sl, r1

1111c: 4653 mov r3, sl

1111e: e9cd 120e strd r1, r2, [sp, #56] ; 0x38

11122: bf14 ite ne

11124: 2301 movne r3, #1

11126: 4643 moveq r3, r8

11128: 4285 cmp r5, r0

1112a: bf18 it ne

1112c: f043 0301 orrne.w r3, r3, #1

11130: 9010 str r0, [sp, #64] ; 0x40

11132: 9309 str r3, [sp, #36] ; 0x24

11134: 2b00 cmp r3, #0

11136: f000 80c3 beq.w 112c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x558>

1113a: f8df a2a0 ldr.w sl, [pc, #672] ; 113dc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x674>

1113e: 46c3 mov fp, r8

11140: 4ba7 ldr r3, [pc, #668] ; (113e0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x678>)

11142: f06f 0863 mvn.w r8, #99 ; 0x63

11146: 44fa add sl, pc

11148: e9cd 9711 strd r9, r7, [sp, #68] ; 0x44

1114c: 46a9 mov r9, r5

1114e: 447b add r3, pc

11150: 9308 str r3, [sp, #32]

11152: 4640 mov r0, r8

11154: f44f 2200 mov.w r2, #524288 ; 0x80000

11158: 9908 ldr r1, [sp, #32]

1115a: f000 fd7b bl 11c54 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xeec>

1115e: f1b0 0800 subs.w r8, r0, #0

11162: f2c0 810f blt.w 11384 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x61c>

11166: 4632 mov r2, r6

11168: 4641 mov r1, r8

1116a: 2003 movs r0, #3

1116c: f000 fc26 bl 119bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc54>

11170: 2800 cmp r0, #0

11172: f2c0 810e blt.w 11392 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x62a>

11176: f1bb 0f00 cmp.w fp, #0

1117a: d005 beq.n 11188 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x420>

1117c: 4658 mov r0, fp

1117e: f000 fa71 bl 11664 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8fc>

11182: 2800 cmp r0, #0

11184: f040 80ea bne.w 1135c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5f4>

11188: e9dd 3414 ldrd r3, r4, [sp, #80] ; 0x50

1118c: 4640 mov r0, r8

1118e: e9cd 3406 strd r3, r4, [sp, #24]

11192: 9b2c ldr r3, [sp, #176] ; 0xb0

11194: 9313 str r3, [sp, #76] ; 0x4c

11196: f000 fb39 bl 1180c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xaa4>

1119a: 4683 mov fp, r0

1119c: 2800 cmp r0, #0

1119e: f000 80dd beq.w 1135c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5f4>

111a2: 9d09 ldr r5, [sp, #36] ; 0x24

111a4: 2700 movs r7, #0

111a6: 464b mov r3, r9

111a8: 2400 movs r4, #0

111aa: e9cd 340c strd r3, r4, [sp, #48] ; 0x30

111ae: 4658 mov r0, fp

111b0: f8ca 7000 str.w r7, [sl]

111b4: f000 fa6a bl 1168c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x924>

111b8: b1f0 cbz r0, 111f8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x490>

111ba: 7a83 ldrb r3, [r0, #10]

111bc: f013 0ffb tst.w r3, #251 ; 0xfb

111c0: d1f5 bne.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

111c2: 7ac3 ldrb r3, [r0, #11]

111c4: 2b2e cmp r3, #46 ; 0x2e

111c6: f000 80aa beq.w 1131e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5b6>

111ca: e9dd 3406 ldrd r3, r4, [sp, #24]

111ce: e9dd 1204 ldrd r1, r2, [sp, #16]

111d2: 4294 cmp r4, r2

111d4: bf08 it eq

111d6: 428b cmpeq r3, r1

111d8: f005 0301 and.w r3, r5, #1

111dc: bf18 it ne

111de: 2300 movne r3, #0

111e0: b1bb cbz r3, 11212 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x4aa>

111e2: 6802 ldr r2, [r0, #0]

111e4: 454a cmp r2, r9

111e6: d014 beq.n 11212 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x4aa>

111e8: 4658 mov r0, fp

111ea: 461d mov r5, r3

111ec: f8ca 7000 str.w r7, [sl]

111f0: f000 fa4c bl 1168c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x924>

111f4: 2800 cmp r0, #0

111f6: d1e0 bne.n 111ba <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x452>

111f8: f8da 4000 ldr.w r4, [sl]

111fc: 2c00 cmp r4, #0

111fe: f040 809a bne.w 11336 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5ce>

11202: 2d00 cmp r5, #0

11204: f000 8094 beq.w 11330 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5c8>

11208: 4658 mov r0, fp

1120a: 4625 mov r5, r4

1120c: f000 fa74 bl 116f8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x990>

11210: e7cd b.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

11212: f100 040b add.w r4, r0, #11

11216: f44f 7280 mov.w r2, #256 ; 0x100

1121a: 4633 mov r3, r6

1121c: 9200 str r2, [sp, #0]

1121e: 4641 mov r1, r8

11220: 4622 mov r2, r4

11222: 2003 movs r0, #3

11224: f000 f9a4 bl 11570 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x808>

11228: 2800 cmp r0, #0

1122a: dbc0 blt.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

1122c: 9b18 ldr r3, [sp, #96] ; 0x60

1122e: f403 4370 and.w r3, r3, #61440 ; 0xf000

11232: f5b3 4f80 cmp.w r3, #16384 ; 0x4000

11236: d1ba bne.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

11238: e9dd 2314 ldrd r2, r3, [sp, #80] ; 0x50

1123c: e9dd 0104 ldrd r0, r1, [sp, #16]

11240: 428b cmp r3, r1

11242: bf08 it eq

11244: 4282 cmpeq r2, r0

11246: d1b2 bne.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

11248: e9dd 232c ldrd r2, r3, [sp, #176] ; 0xb0

1124c: e9dd 010c ldrd r0, r1, [sp, #48] ; 0x30

11250: 428b cmp r3, r1

11252: bf08 it eq

11254: 4282 cmpeq r2, r0

11256: d1aa bne.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

11258: 4620 mov r0, r4

1125a: f001 fa71 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

1125e: 9b0b ldr r3, [sp, #44] ; 0x2c

11260: 9911 ldr r1, [sp, #68] ; 0x44

11262: 1a5b subs r3, r3, r1

11264: 4298 cmp r0, r3

11266: 4602 mov r2, r0

11268: f080 80ac bcs.w 113c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x65c>

1126c: 9b0b ldr r3, [sp, #44] ; 0x2c

1126e: 4621 mov r1, r4

11270: 1a1b subs r3, r3, r0

11272: 1e58 subs r0, r3, #1

11274: 900b str r0, [sp, #44] ; 0x2c

11276: 4618 mov r0, r3

11278: f001 ef42 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

1127c: e9dd 450e ldrd r4, r5, [sp, #56] ; 0x38

11280: 4603 mov r3, r0

11282: e9dd 0106 ldrd r0, r1, [sp, #24]

11286: 428d cmp r5, r1

11288: e9cd 0104 strd r0, r1, [sp, #16]

1128c: bf08 it eq

1128e: 4284 cmpeq r4, r0

11290: 9910 ldr r1, [sp, #64] ; 0x40

11292: 9813 ldr r0, [sp, #76] ; 0x4c

11294: bf14 ite ne

11296: 2201 movne r2, #1

11298: 2200 moveq r2, #0

1129a: 460c mov r4, r1

1129c: 4284 cmp r4, r0

1129e: bf18 it ne

112a0: f042 0201 orrne.w r2, r2, #1

112a4: 212f movs r1, #47 ; 0x2f

112a6: 4681 mov r9, r0

112a8: f803 1c01 strb.w r1, [r3, #-1]

112ac: 2a00 cmp r2, #0

112ae: f47f af50 bne.w 11152 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x3ea>

112b2: 4658 mov r0, fp

112b4: e9dd 9711 ldrd r9, r7, [sp, #68] ; 0x44

112b8: f000 f9d4 bl 11664 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8fc>

112bc: 2800 cmp r0, #0

112be: d17d bne.n 113bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x654>

112c0: 9b03 ldr r3, [sp, #12]

112c2: 4648 mov r0, r9

112c4: 9a0b ldr r2, [sp, #44] ; 0x2c

112c6: 3b01 subs r3, #1

112c8: 444b add r3, r9

112ca: 429a cmp r2, r3

112cc: bf01 itttt eq

112ce: 232f moveq r3, #47 ; 0x2f

112d0: f802 3c01 strbeq.w r3, [r2, #-1]

112d4: f102 33ff addeq.w r3, r2, #4294967295 ; 0xffffffff

112d8: 930b streq r3, [sp, #44] ; 0x2c

112da: e9dd 310a ldrd r3, r1, [sp, #40] ; 0x28

112de: 1a5c subs r4, r3, r1

112e0: 4622 mov r2, r4

112e2: f001 ed16 blx 12d10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1fa8>

112e6: 9b03 ldr r3, [sp, #12]

112e8: 2b00 cmp r3, #0

112ea: d05c beq.n 113a6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x63e>

112ec: 4b3d ldr r3, [pc, #244] ; (113e4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x67c>)

112ee: 4648 mov r0, r9

112f0: 447b add r3, pc

112f2: 601f str r7, [r3, #0]

112f4: b02f add sp, #188 ; 0xbc

112f6: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

112fa: 4b3b ldr r3, [pc, #236] ; (113e8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x680>)

112fc: 447b add r3, pc

112fe: 681f ldr r7, [r3, #0]

11300: f43f aee4 beq.w 110cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x364>

11304: 2f24 cmp r7, #36 ; 0x24

11306: f43f aee1 beq.w 110cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x364>

1130a: 2f22 cmp r7, #34 ; 0x22

1130c: d103 bne.n 11316 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5ae>

1130e: 9b03 ldr r3, [sp, #12]

11310: ea59 0303 orrs.w r3, r9, r3

11314: d029 beq.n 1136a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x602>

11316: 2000 movs r0, #0

11318: b02f add sp, #188 ; 0xbc

1131a: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1131e: 7b03 ldrb r3, [r0, #12]

11320: 2b00 cmp r3, #0

11322: f43f af44 beq.w 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

11326: 8983 ldrh r3, [r0, #12]

11328: 2b2e cmp r3, #46 ; 0x2e

1132a: f47f af4e bne.w 111ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x462>

1132e: e73e b.n 111ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x446>

11330: 2402 movs r4, #2

11332: f8ca 4000 str.w r4, [sl]

11336: 4658 mov r0, fp

11338: f000 f994 bl 11664 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8fc>

1133c: 4b2b ldr r3, [pc, #172] ; (113ec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x684>)

1133e: 2000 movs r0, #0

11340: 447b add r3, pc

11342: 601c str r4, [r3, #0]

11344: b02f add sp, #188 ; 0xbc

11346: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

1134a: 4b29 ldr r3, [pc, #164] ; (113f0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x688>)

1134c: 4247 negs r7, r0

1134e: 447b add r3, pc

11350: 601f str r7, [r3, #0]

11352: e7d7 b.n 11304 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x59c>

11354: 4b27 ldr r3, [pc, #156] ; (113f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x68c>)

11356: 447b add r3, pc

11358: 681c ldr r4, [r3, #0]

1135a: e7ef b.n 1133c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5d4>

1135c: 4b26 ldr r3, [pc, #152] ; (113f8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x690>)

1135e: 447b add r3, pc

11360: 681c ldr r4, [r3, #0]

11362: 4640 mov r0, r8

11364: f000 fbc8 bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

11368: e7e8 b.n 1133c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5d4>

1136a: 4b24 ldr r3, [pc, #144] ; (113fc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x694>)

1136c: 2279 movs r2, #121 ; 0x79

1136e: 4924 ldr r1, [pc, #144] ; (11400 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x698>)

11370: 4824 ldr r0, [pc, #144] ; (11404 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x69c>)

11372: 447b add r3, pc

11374: 4479 add r1, pc

11376: 4478 add r0, pc

11378: f7ff fb08 bl 1098c <free@@GLIBC\_2.4+0x12c>

1137c: 4b22 ldr r3, [pc, #136] ; (11408 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6a0>)

1137e: 447b add r3, pc

11380: 681c ldr r4, [r3, #0]

11382: e7db b.n 1133c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5d4>

11384: 4b21 ldr r3, [pc, #132] ; (1140c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6a4>)

11386: 447b add r3, pc

11388: 681c ldr r4, [r3, #0]

1138a: f1bb 0f00 cmp.w fp, #0

1138e: d0d5 beq.n 1133c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5d4>

11390: e7d1 b.n 11336 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5ce>

11392: 4b1f ldr r3, [pc, #124] ; (11410 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6a8>)

11394: 447b add r3, pc

11396: 681c ldr r4, [r3, #0]

11398: f1bb 0f00 cmp.w fp, #0

1139c: d0e1 beq.n 11362 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5fa>

1139e: 4658 mov r0, fp

113a0: f000 f960 bl 11664 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8fc>

113a4: e7dd b.n 11362 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5fa>

113a6: 4621 mov r1, r4

113a8: 9c0a ldr r4, [sp, #40] ; 0x28

113aa: 4620 mov r0, r4

113ac: f7ef eb26 blx 9fc <realloc@plt>

113b0: 4623 mov r3, r4

113b2: 2800 cmp r0, #0

113b4: bf18 it ne

113b6: 4603 movne r3, r0

113b8: 4699 mov r9, r3

113ba: e797 b.n 112ec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x584>

113bc: 4b15 ldr r3, [pc, #84] ; (11414 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6ac>)

113be: 447b add r3, pc

113c0: 681c ldr r4, [r3, #0]

113c2: e7bb b.n 1133c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5d4>

113c4: 4b14 ldr r3, [pc, #80] ; (11418 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6b0>)

113c6: 2222 movs r2, #34 ; 0x22

113c8: 4614 mov r4, r2

113ca: 447b add r3, pc

113cc: 601a str r2, [r3, #0]

113ce: e7b2 b.n 11336 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x5ce>

113d0: 888c ldrh r4, [r1, #4]

113d2: 0001 movs r1, r0

113d4: 6988 ldr r0, [r1, #24]

113d6: 0000 movs r0, r0

113d8: 696c ldr r4, [r5, #20]

113da: 0000 movs r0, r0

113dc: 880e ldrh r6, [r1, #0]

113de: 0001 movs r1, r0

113e0: 691e ldr r6, [r3, #16]

113e2: 0000 movs r0, r0

113e4: 8664 strh r4, [r4, #50] ; 0x32

113e6: 0001 movs r1, r0

113e8: 8658 strh r0, [r3, #50] ; 0x32

113ea: 0001 movs r1, r0

113ec: 8614 strh r4, [r2, #48] ; 0x30

113ee: 0001 movs r1, r0

113f0: 8606 strh r6, [r0, #48] ; 0x30

113f2: 0001 movs r1, r0

113f4: 85fe strh r6, [r7, #46] ; 0x2e

113f6: 0001 movs r1, r0

113f8: 85f6 strh r6, [r6, #46] ; 0x2e

113fa: 0001 movs r1, r0

113fc: 674e str r6, [r1, #116] ; 0x74

113fe: 0000 movs r0, r0

11400: 66fc str r4, [r7, #108] ; 0x6c

11402: 0000 movs r0, r0

11404: 671e str r6, [r3, #112] ; 0x70

11406: 0000 movs r0, r0

11408: 85d6 strh r6, [r2, #46] ; 0x2e

1140a: 0001 movs r1, r0

1140c: 85ce strh r6, [r1, #46] ; 0x2e

1140e: 0001 movs r1, r0

11410: 85c0 strh r0, [r0, #46] ; 0x2e

11412: 0001 movs r1, r0

11414: 8596 strh r6, [r2, #44] ; 0x2c

11416: 0001 movs r1, r0

11418: 858a strh r2, [r1, #44] ; 0x2c

1141a: 0001 movs r1, r0

1141c: b40c push {r2, r3}

1141e: b508 push {r3, lr}

11420: 9a02 ldr r2, [sp, #8]

11422: 0653 lsls r3, r2, #25

11424: d419 bmi.n 1145a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6f2>

11426: f44f 4380 mov.w r3, #16384 ; 0x4000

1142a: f2c0 0340 movt r3, #64 ; 0x40

1142e: 4393 bics r3, r2

11430: d013 beq.n 1145a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6f2>

11432: f442 3200 orr.w r2, r2, #131072 ; 0x20000

11436: f44f 7ca1 mov.w ip, #322 ; 0x142

1143a: f7ff fdc1 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

1143e: f510 5f80 cmn.w r0, #4096 ; 0x1000

11442: d803 bhi.n 1144c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6e4>

11444: e8bd 4008 ldmia.w sp!, {r3, lr}

11448: b002 add sp, #8

1144a: 4770 bx lr

1144c: 4b07 ldr r3, [pc, #28] ; (1146c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x704>)

1144e: 4242 negs r2, r0

11450: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11454: 447b add r3, pc

11456: 601a str r2, [r3, #0]

11458: e7f4 b.n 11444 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x6dc>

1145a: 4b05 ldr r3, [pc, #20] ; (11470 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x708>)

1145c: 221c movs r2, #28

1145e: 4905 ldr r1, [pc, #20] ; (11474 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x70c>)

11460: 4805 ldr r0, [pc, #20] ; (11478 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x710>)

11462: 447b add r3, pc

11464: 4479 add r1, pc

11466: 4478 add r0, pc

11468: f7ff fa90 bl 1098c <free@@GLIBC\_2.4+0x12c>

1146c: 8500 strh r0, [r0, #40] ; 0x28

1146e: 0001 movs r1, r0

11470: 66b2 str r2, [r6, #104] ; 0x68

11472: 0000 movs r0, r0

11474: 6668 str r0, [r5, #100] ; 0x64

11476: 0000 movs r0, r0

11478: 6692 str r2, [r2, #104] ; 0x68

1147a: 0000 movs r0, r0

1147c: b5f8 push {r3, r4, r5, r6, r7, lr}

1147e: 461c mov r4, r3

11480: 4607 mov r7, r0

11482: 460e mov r6, r1

11484: b1f1 cbz r1, 114c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x75c>

11486: f248 0030 movw r0, #32816 ; 0x8030

1148a: f44f 4500 mov.w r5, #32768 ; 0x8000

1148e: b164 cbz r4, 114aa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x742>

11490: 6ba5 ldr r5, [r4, #56] ; 0x38

11492: f5b5 4f00 cmp.w r5, #32768 ; 0x8000

11496: bf38 it cc

11498: f44f 4500 movcc.w r5, #32768 ; 0x8000

1149c: f5b5 1f80 cmp.w r5, #1048576 ; 0x100000

114a0: bf28 it cs

114a2: f44f 1580 movcs.w r5, #1048576 ; 0x100000

114a6: f105 0030 add.w r0, r5, #48 ; 0x30

114aa: f7ef ea90 blx 9cc <malloc@plt+0x4>

114ae: 4604 mov r4, r0

114b0: b180 cbz r0, 114d4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x76c>

114b2: 2300 movs r3, #0

114b4: 6027 str r7, [r4, #0]

114b6: e9c4 5307 strd r5, r3, [r4, #28]

114ba: e9c4 3309 strd r3, r3, [r4, #36] ; 0x24

114be: 62e3 str r3, [r4, #44] ; 0x2c

114c0: 4620 mov r0, r4

114c2: bdf8 pop {r3, r4, r5, r6, r7, pc}

114c4: 2201 movs r2, #1

114c6: 2102 movs r1, #2

114c8: f000 fb5e bl 11b88 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe20>

114cc: 2800 cmp r0, #0

114ce: dada bge.n 11486 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x71e>

114d0: 2400 movs r4, #0

114d2: e7f5 b.n 114c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x758>

114d4: f242 0030 movw r0, #8240 ; 0x2030

114d8: f44f 5500 mov.w r5, #8192 ; 0x2000

114dc: f7ef ea76 blx 9cc <malloc@plt+0x4>

114e0: 4604 mov r4, r0

114e2: 2800 cmp r0, #0

114e4: d1e5 bne.n 114b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x74a>

114e6: 2e00 cmp r6, #0

114e8: d0f2 beq.n 114d0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x768>

114ea: 4d04 ldr r5, [pc, #16] ; (114fc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x794>)

114ec: 4638 mov r0, r7

114ee: 447d add r5, pc

114f0: 682e ldr r6, [r5, #0]

114f2: f000 fb01 bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

114f6: 602e str r6, [r5, #0]

114f8: e7e2 b.n 114c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x758>

114fa: bf00 nop

114fc: 8466 strh r6, [r4, #34] ; 0x22

114fe: 0001 movs r1, r0

11500: 7803 ldrb r3, [r0, #0]

11502: b34b cbz r3, 11558 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x7f0>

11504: b530 push {r4, r5, lr}

11506: f44f 4190 mov.w r1, #18432 ; 0x4800

1150a: b09b sub sp, #108 ; 0x6c

1150c: f2c0 010a movt r1, #10

11510: f000 fb4a bl 11ba8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe40>

11514: 1e04 subs r4, r0, #0

11516: db25 blt.n 11564 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x7fc>

11518: 466d mov r5, sp

1151a: 4621 mov r1, r4

1151c: 2003 movs r0, #3

1151e: 462a mov r2, r5

11520: f000 fa4c bl 119bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc54>

11524: 2800 cmp r0, #0

11526: db11 blt.n 1154c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x7e4>

11528: 9a04 ldr r2, [sp, #16]

1152a: f402 4270 and.w r2, r2, #61440 ; 0xf000

1152e: f5b2 4f80 cmp.w r2, #16384 ; 0x4000

11532: d107 bne.n 11544 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x7dc>

11534: 462b mov r3, r5

11536: 4620 mov r0, r4

11538: 2200 movs r2, #0

1153a: 2101 movs r1, #1

1153c: f7ff ff9e bl 1147c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x714>

11540: b01b add sp, #108 ; 0x6c

11542: bd30 pop {r4, r5, pc}

11544: 4b08 ldr r3, [pc, #32] ; (11568 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x800>)

11546: 2214 movs r2, #20

11548: 447b add r3, pc

1154a: 601a str r2, [r3, #0]

1154c: 4620 mov r0, r4

1154e: f000 fad3 bl 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

11552: 2000 movs r0, #0

11554: b01b add sp, #108 ; 0x6c

11556: bd30 pop {r4, r5, pc}

11558: 4a04 ldr r2, [pc, #16] ; (1156c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x804>)

1155a: 2102 movs r1, #2

1155c: 4618 mov r0, r3

1155e: 447a add r2, pc

11560: 6011 str r1, [r2, #0]

11562: 4770 bx lr

11564: 2000 movs r0, #0

11566: e7eb b.n 11540 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x7d8>

11568: 840c strh r4, [r1, #32]

1156a: 0001 movs r1, r0

1156c: 83f6 strh r6, [r6, #30]

1156e: 0001 movs r1, r0

11570: 2803 cmp r0, #3

11572: d113 bne.n 1159c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x834>

11574: b500 push {lr}

11576: 4608 mov r0, r1

11578: f240 1c47 movw ip, #327 ; 0x147

1157c: 4611 mov r1, r2

1157e: 461a mov r2, r3

11580: 9b01 ldr r3, [sp, #4]

11582: f7ff fd1d bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11586: f510 5f80 cmn.w r0, #4096 ; 0x1000

1158a: d90e bls.n 115aa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x842>

1158c: 4a08 ldr r2, [pc, #32] ; (115b0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x848>)

1158e: 4243 negs r3, r0

11590: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11594: 447a add r2, pc

11596: 6013 str r3, [r2, #0]

11598: f85d fb04 ldr.w pc, [sp], #4

1159c: 4b05 ldr r3, [pc, #20] ; (115b4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x84c>)

1159e: 2216 movs r2, #22

115a0: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

115a4: 447b add r3, pc

115a6: 601a str r2, [r3, #0]

115a8: 4770 bx lr

115aa: 2000 movs r0, #0

115ac: e7f4 b.n 11598 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x830>

115ae: bf00 nop

115b0: 83c0 strh r0, [r0, #30]

115b2: 0001 movs r1, r0

115b4: 83b0 strh r0, [r6, #28]

115b6: 0001 movs r1, r0

115b8: 4770 bx lr

115ba: bf00 nop

115bc: 4770 bx lr

115be: bf00 nop

115c0: 4770 bx lr

115c2: bf00 nop

115c4: b5f0 push {r4, r5, r6, r7, lr}

115c6: 460c mov r4, r1

115c8: b09b sub sp, #108 ; 0x6c

115ca: 2101 movs r1, #1

115cc: 4605 mov r5, r0

115ce: f000 fadb bl 11b88 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe20>

115d2: 3001 adds r0, #1

115d4: d001 beq.n 115da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x872>

115d6: b01b add sp, #108 ; 0x6c

115d8: bdf0 pop {r4, r5, r6, r7, pc}

115da: 4b16 ldr r3, [pc, #88] ; (11634 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8cc>)

115dc: 447b add r3, pc

115de: 681b ldr r3, [r3, #0]

115e0: 2b09 cmp r3, #9

115e2: d1f8 bne.n 115d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x86e>

115e4: f5b4 4f00 cmp.w r4, #32768 ; 0x8000

115e8: d11d bne.n 11626 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8be>

115ea: 4813 ldr r0, [pc, #76] ; (11638 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8d0>)

115ec: f240 1603 movw r6, #259 ; 0x103

115f0: 2700 movs r7, #0

115f2: 4478 add r0, pc

115f4: 4621 mov r1, r4

115f6: 2200 movs r2, #0

115f8: f000 fad6 bl 11ba8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe40>

115fc: 4285 cmp r5, r0

115fe: d111 bne.n 11624 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8bc>

11600: 4629 mov r1, r5

11602: 466a mov r2, sp

11604: 2003 movs r0, #3

11606: f000 f9d9 bl 119bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc54>

1160a: b958 cbnz r0, 11624 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8bc>

1160c: 9b04 ldr r3, [sp, #16]

1160e: f403 4370 and.w r3, r3, #61440 ; 0xf000

11612: f5b3 5f00 cmp.w r3, #8192 ; 0x2000

11616: d105 bne.n 11624 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8bc>

11618: e9dd 2308 ldrd r2, r3, [sp, #32]

1161c: 42bb cmp r3, r7

1161e: bf08 it eq

11620: 42b2 cmpeq r2, r6

11622: d0d8 beq.n 115d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x86e>

11624: deff udf #255 ; 0xff

11626: 4805 ldr r0, [pc, #20] ; (1163c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x8d4>)

11628: f240 1607 movw r6, #263 ; 0x107

1162c: 2700 movs r7, #0

1162e: 4478 add r0, pc

11630: e7e0 b.n 115f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x88c>

11632: bf00 nop

11634: 8378 strh r0, [r7, #26]

11636: 0001 movs r1, r0

11638: 653a str r2, [r7, #80] ; 0x50

1163a: 0000 movs r0, r0

1163c: 64f2 str r2, [r6, #76] ; 0x4c

1163e: 0000 movs r0, r0

11640: b508 push {r3, lr}

11642: f248 0101 movw r1, #32769 ; 0x8001

11646: 2000 movs r0, #0

11648: f7ff ffbc bl 115c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x85c>

1164c: f44f 4100 mov.w r1, #32768 ; 0x8000

11650: 2001 movs r0, #1

11652: f7ff ffb7 bl 115c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x85c>

11656: f44f 4100 mov.w r1, #32768 ; 0x8000

1165a: 2002 movs r0, #2

1165c: e8bd 4008 ldmia.w sp!, {r3, lr}

11660: e7b0 b.n 115c4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x85c>

11662: bf00 nop

11664: b140 cbz r0, 11678 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x910>

11666: b510 push {r4, lr}

11668: 6804 ldr r4, [r0, #0]

1166a: f7ef e9dc blx a24 <free@plt+0x4>

1166e: 4620 mov r0, r4

11670: e8bd 4010 ldmia.w sp!, {r4, lr}

11674: f000 ba40 b.w 11af8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd90>

11678: 4b03 ldr r3, [pc, #12] ; (11688 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x920>)

1167a: 2216 movs r2, #22

1167c: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11680: 447b add r3, pc

11682: 601a str r2, [r3, #0]

11684: 4770 bx lr

11686: bf00 nop

11688: 82d4 strh r4, [r2, #22]

1168a: 0001 movs r1, r0

1168c: 4a17 ldr r2, [pc, #92] ; (116ec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x984>)

1168e: b5f8 push {r3, r4, r5, r6, r7, lr}

11690: 447a add r2, pc

11692: 4604 mov r4, r0

11694: 6a43 ldr r3, [r0, #36] ; 0x24

11696: f100 0530 add.w r5, r0, #48 ; 0x30

1169a: 6816 ldr r6, [r2, #0]

1169c: e007 b.n 116ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x946>

1169e: 18a0 adds r0, r4, r2

116a0: 58a2 ldr r2, [r4, r2]

116a2: 6847 ldr r7, [r0, #4]

116a4: 8901 ldrh r1, [r0, #8]

116a6: 62a7 str r7, [r4, #40] ; 0x28

116a8: 440b add r3, r1

116aa: 6263 str r3, [r4, #36] ; 0x24

116ac: b9a2 cbnz r2, 116d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x970>

116ae: 6a21 ldr r1, [r4, #32]

116b0: f103 0230 add.w r2, r3, #48 ; 0x30

116b4: 4299 cmp r1, r3

116b6: d8f2 bhi.n 1169e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x936>

116b8: 69e2 ldr r2, [r4, #28]

116ba: 4629 mov r1, r5

116bc: 6820 ldr r0, [r4, #0]

116be: f000 f829 bl 11714 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x9ac>

116c2: 2230 movs r2, #48 ; 0x30

116c4: 2300 movs r3, #0

116c6: 2800 cmp r0, #0

116c8: dd01 ble.n 116ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x966>

116ca: 6220 str r0, [r4, #32]

116cc: e7e7 b.n 1169e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x936>

116ce: d104 bne.n 116da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x972>

116d0: 4b07 ldr r3, [pc, #28] ; (116f0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x988>)

116d2: 2000 movs r0, #0

116d4: 447b add r3, pc

116d6: 601e str r6, [r3, #0]

116d8: bdf8 pop {r3, r4, r5, r6, r7, pc}

116da: 4b06 ldr r3, [pc, #24] ; (116f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x98c>)

116dc: 447b add r3, pc

116de: 681b ldr r3, [r3, #0]

116e0: 2b02 cmp r3, #2

116e2: bf18 it ne

116e4: 2000 movne r0, #0

116e6: d0f3 beq.n 116d0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x968>

116e8: bdf8 pop {r3, r4, r5, r6, r7, pc}

116ea: bf00 nop

116ec: 82c4 strh r4, [r0, #22]

116ee: 0001 movs r1, r0

116f0: 8280 strh r0, [r0, #20]

116f2: 0001 movs r1, r0

116f4: 8278 strh r0, [r7, #18]

116f6: 0001 movs r1, r0

116f8: 2200 movs r2, #0

116fa: b510 push {r4, lr}

116fc: 4611 mov r1, r2

116fe: 4604 mov r4, r0

11700: 6800 ldr r0, [r0, #0]

11702: f000 f99b bl 11a3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xcd4>

11706: 2300 movs r3, #0

11708: e9c4 3309 strd r3, r3, [r4, #36] ; 0x24

1170c: 6223 str r3, [r4, #32]

1170e: 62e3 str r3, [r4, #44] ; 0x2c

11710: bd10 pop {r4, pc}

11712: bf00 nop

11714: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

11718: f5b2 7f86 cmp.w r2, #268 ; 0x10c

1171c: b0d1 sub sp, #324 ; 0x144

1171e: 460d mov r5, r1

11720: bf94 ite ls

11722: ac0b addls r4, sp, #44 ; 0x2c

11724: 460c movhi r4, r1

11726: 4603 mov r3, r0

11728: f04f 0cd9 mov.w ip, #217 ; 0xd9

1172c: 4621 mov r1, r4

1172e: f7ff fc47 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11732: f510 5f80 cmn.w r0, #4096 ; 0x1000

11736: d854 bhi.n 117e2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa7a>

11738: e9cd 5308 strd r5, r3, [sp, #32]

1173c: 1823 adds r3, r4, r0

1173e: 429c cmp r4, r3

11740: 9307 str r3, [sp, #28]

11742: d249 bcs.n 117d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa70>

11744: f04f 32ff mov.w r2, #4294967295 ; 0xffffffff

11748: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

1174c: 9d08 ldr r5, [sp, #32]

1174e: e9cd 2304 strd r2, r3, [sp, #16]

11752: e00c b.n 1176e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa06>

11754: 4549 cmp r1, r9

11756: bf08 it eq

11758: 45c0 cmpeq r8, r8

1175a: d12a bne.n 117b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa4a>

1175c: 9a06 ldr r2, [sp, #24]

1175e: 812b strh r3, [r5, #8]

11760: e9cd 0104 strd r0, r1, [sp, #16]

11764: 72aa strb r2, [r5, #10]

11766: 441d add r5, r3

11768: 9b07 ldr r3, [sp, #28]

1176a: 429c cmp r4, r3

1176c: d234 bcs.n 117d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa70>

1176e: f8b4 c010 ldrh.w ip, [r4, #16]

11772: f104 0113 add.w r1, r4, #19

11776: e9d4 6700 ldrd r6, r7, [r4]

1177a: f105 000b add.w r0, r5, #11

1177e: f1ac 0305 sub.w r3, ip, #5

11782: e9d4 8902 ldrd r8, r9, [r4, #8]

11786: f023 0303 bic.w r3, r3, #3

1178a: 9303 str r3, [sp, #12]

1178c: 7ca3 ldrb r3, [r4, #18]

1178e: f1ac 0213 sub.w r2, ip, #19

11792: f04f 0b00 mov.w fp, #0

11796: 4464 add r4, ip

11798: 9306 str r3, [sp, #24]

1179a: f001 eaba blx 12d10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1fa8>

1179e: 455f cmp r7, fp

117a0: 4640 mov r0, r8

117a2: bf08 it eq

117a4: 42b6 cmpeq r6, r6

117a6: ea4f 71e0 mov.w r1, r0, asr #31

117aa: e9c5 6800 strd r6, r8, [r5]

117ae: 9b03 ldr r3, [sp, #12]

117b0: d0d0 beq.n 11754 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x9ec>

117b2: e9dd 3404 ldrd r3, r4, [sp, #16]

117b6: 3401 adds r4, #1

117b8: bf08 it eq

117ba: f1b3 3fff cmpeq.w r3, #4294967295 ; 0xffffffff

117be: d019 beq.n 117f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa8c>

117c0: e9dd 2304 ldrd r2, r3, [sp, #16]

117c4: 2100 movs r1, #0

117c6: 9809 ldr r0, [sp, #36] ; 0x24

117c8: 9100 str r1, [sp, #0]

117ca: f000 f963 bl 11a94 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd2c>

117ce: 9b08 ldr r3, [sp, #32]

117d0: 1ae8 subs r0, r5, r3

117d2: b051 add sp, #324 ; 0x144

117d4: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

117d8: 9b08 ldr r3, [sp, #32]

117da: 1ae8 subs r0, r5, r3

117dc: b051 add sp, #324 ; 0x144

117de: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

117e2: 4a08 ldr r2, [pc, #32] ; (11804 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa9c>)

117e4: 4243 negs r3, r0

117e6: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

117ea: 447a add r2, pc

117ec: 6013 str r3, [r2, #0]

117ee: b051 add sp, #324 ; 0x144

117f0: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

117f4: 4b04 ldr r3, [pc, #16] ; (11808 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xaa0>)

117f6: 224b movs r2, #75 ; 0x4b

117f8: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

117fc: 447b add r3, pc

117fe: 601a str r2, [r3, #0]

11800: e7e7 b.n 117d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xa6a>

11802: bf00 nop

11804: 816a strh r2, [r5, #10]

11806: 0001 movs r1, r0

11808: 8158 strh r0, [r3, #10]

1180a: 0001 movs r1, r0

1180c: b530 push {r4, r5, lr}

1180e: b09b sub sp, #108 ; 0x6c

11810: 4601 mov r1, r0

11812: 4604 mov r4, r0

11814: 466d mov r5, sp

11816: 2003 movs r0, #3

11818: 462a mov r2, r5

1181a: f000 f8cf bl 119bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc54>

1181e: 2800 cmp r0, #0

11820: db17 blt.n 11852 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xaea>

11822: 9b04 ldr r3, [sp, #16]

11824: f403 4370 and.w r3, r3, #61440 ; 0xf000

11828: f5b3 4f80 cmp.w r3, #16384 ; 0x4000

1182c: d114 bne.n 11858 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xaf0>

1182e: 2103 movs r1, #3

11830: 4620 mov r0, r4

11832: f000 f9a9 bl 11b88 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe20>

11836: 1c43 adds r3, r0, #1

11838: d00b beq.n 11852 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xaea>

1183a: f000 0303 and.w r3, r0, #3

1183e: 2b01 cmp r3, #1

11840: d010 beq.n 11864 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xafc>

11842: 4602 mov r2, r0

11844: 462b mov r3, r5

11846: 4620 mov r0, r4

11848: 2100 movs r1, #0

1184a: f7ff fe17 bl 1147c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x714>

1184e: b01b add sp, #108 ; 0x6c

11850: bd30 pop {r4, r5, pc}

11852: 2000 movs r0, #0

11854: b01b add sp, #108 ; 0x6c

11856: bd30 pop {r4, r5, pc}

11858: 4b05 ldr r3, [pc, #20] ; (11870 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb08>)

1185a: 2214 movs r2, #20

1185c: 2000 movs r0, #0

1185e: 447b add r3, pc

11860: 601a str r2, [r3, #0]

11862: e7f4 b.n 1184e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xae6>

11864: 4b03 ldr r3, [pc, #12] ; (11874 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb0c>)

11866: 2216 movs r2, #22

11868: 2000 movs r0, #0

1186a: 447b add r3, pc

1186c: 601a str r2, [r3, #0]

1186e: e7ee b.n 1184e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xae6>

11870: 80f6 strh r6, [r6, #6]

11872: 0001 movs r1, r0

11874: 80ea strh r2, [r5, #6]

11876: 0001 movs r1, r0

11878: f8b0 315c ldrh.w r3, [r0, #348] ; 0x15c

1187c: 6802 ldr r2, [r0, #0]

1187e: 3b01 subs r3, #1

11880: 1a89 subs r1, r1, r2

11882: 1c5a adds r2, r3, #1

11884: ea4f 1243 mov.w r2, r3, lsl #5

11888: d017 beq.n 118ba <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb52>

1188a: b430 push {r4, r5}

1188c: e004 b.n 11898 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb30>

1188e: 3b01 subs r3, #1

11890: 1c5c adds r4, r3, #1

11892: ea4f 1243 mov.w r2, r3, lsl #5

11896: d00d beq.n 118b4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb4c>

11898: f8d0 4154 ldr.w r4, [r0, #340] ; 0x154

1189c: 18a5 adds r5, r4, r2

1189e: 58a2 ldr r2, [r4, r2]

118a0: 2a01 cmp r2, #1

118a2: d1f4 bne.n 1188e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb26>

118a4: 68ac ldr r4, [r5, #8]

118a6: 696d ldr r5, [r5, #20]

118a8: 1b0c subs r4, r1, r4

118aa: 42ac cmp r4, r5

118ac: d2ef bcs.n 1188e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb26>

118ae: 4610 mov r0, r2

118b0: bc30 pop {r4, r5}

118b2: 4770 bx lr

118b4: 2000 movs r0, #0

118b6: bc30 pop {r4, r5}

118b8: 4770 bx lr

118ba: 2000 movs r0, #0

118bc: 4770 bx lr

118be: bf00 nop

118c0: 490d ldr r1, [pc, #52] ; (118f8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xb90>)

118c2: b410 push {r4}

118c4: 4479 add r1, pc

118c6: 6dd3 ldr r3, [r2, #92] ; 0x5c

118c8: e9d1 2400 ldrd r2, r4, [r1]

118cc: 6888 ldr r0, [r1, #8]

118ce: 1a9b subs r3, r3, r2

118d0: eb03 73d3 add.w r3, r3, r3, lsr #31

118d4: 105a asrs r2, r3, #1

118d6: fba2 2304 umull r2, r3, r2, r4

118da: 0c14 lsrs r4, r2, #16

118dc: ea44 4403 orr.w r4, r4, r3, lsl #16

118e0: 4284 cmp r4, r0

118e2: bf3f itttt cc

118e4: 68c9 ldrcc r1, [r1, #12]

118e6: f831 3014 ldrhcc.w r3, [r1, r4, lsl #1]

118ea: 3301 addcc r3, #1

118ec: f821 3014 strhcc.w r3, [r1, r4, lsl #1]

118f0: f85d 4b04 ldr.w r4, [sp], #4

118f4: 4770 bx lr

118f6: bf00 nop

118f8: 8078 strh r0, [r7, #2]

118fa: 0001 movs r1, r0

118fc: b570 push {r4, r5, r6, lr}

118fe: 0849 lsrs r1, r1, #1

11900: 4c1d ldr r4, [pc, #116] ; (11978 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc10>)

11902: b0a8 sub sp, #160 ; 0xa0

11904: 4e1d ldr r6, [pc, #116] ; (1197c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc14>)

11906: 4d1e ldr r5, [pc, #120] ; (11980 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc18>)

11908: 447c add r4, pc

1190a: 447e add r6, pc

1190c: e9c4 1002 strd r1, r0, [r4, #8]

11910: a926 add r1, sp, #152 ; 0x98

11912: 6022 str r2, [r4, #0]

11914: aa06 add r2, sp, #24

11916: 6063 str r3, [r4, #4]

11918: f04f 33ff mov.w r3, #4294967295 ; 0xffffffff

1191c: 5970 ldr r0, [r6, r5]

1191e: 9005 str r0, [sp, #20]

11920: f841 3d04 str.w r3, [r1, #-4]!

11924: 4291 cmp r1, r2

11926: d1fb bne.n 11920 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xbb8>

11928: 2200 movs r2, #0

1192a: a905 add r1, sp, #20

1192c: 201b movs r0, #27

1192e: 2304 movs r3, #4

11930: f2c1 0300 movt r3, #4096 ; 0x1000

11934: 9326 str r3, [sp, #152] ; 0x98

11936: f000 fb6f bl 12018 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12b0>

1193a: 2800 cmp r0, #0

1193c: db18 blt.n 11970 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc08>

1193e: 2400 movs r4, #0

11940: 9403 str r4, [sp, #12]

11942: f000 f81f bl 11984 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc1c>

11946: 4601 mov r1, r0

11948: f244 2040 movw r0, #16960 ; 0x4240

1194c: f2c0 000f movt r0, #15

11950: f001 ffc6 bl 138e0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b78>

11954: ab03 add r3, sp, #12

11956: 4622 mov r2, r4

11958: 9004 str r0, [sp, #16]

1195a: e893 0003 ldmia.w r3, {r0, r1}

1195e: ab01 add r3, sp, #4

11960: e883 0003 stmia.w r3, {r0, r1}

11964: 4619 mov r1, r3

11966: 2002 movs r0, #2

11968: f001 fe6a bl 13640 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28d8>

1196c: b028 add sp, #160 ; 0xa0

1196e: bd70 pop {r4, r5, r6, pc}

11970: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11974: e7fa b.n 1196c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc04>

11976: bf00 nop

11978: 8034 strh r4, [r6, #0]

1197a: 0001 movs r1, r0

1197c: 76f2 strb r2, [r6, #27]

1197e: 0001 movs r1, r0

11980: 0034 movs r4, r6

11982: 0000 movs r0, r0

11984: 4b01 ldr r3, [pc, #4] ; (1198c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc24>)

11986: 447b add r3, pc

11988: 6a18 ldr r0, [r3, #32]

1198a: 4770 bx lr

1198c: 6fe6 ldr r6, [r4, #124] ; 0x7c

1198e: 0001 movs r1, r0

11990: 4608 mov r0, r1

11992: f04f 0cc3 mov.w ip, #195 ; 0xc3

11996: b500 push {lr}

11998: 4611 mov r1, r2

1199a: f7ff fb11 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

1199e: f510 5f80 cmn.w r0, #4096 ; 0x1000

119a2: d801 bhi.n 119a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc40>

119a4: f85d fb04 ldr.w pc, [sp], #4

119a8: 4b03 ldr r3, [pc, #12] ; (119b8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc50>)

119aa: 4242 negs r2, r0

119ac: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

119b0: 447b add r3, pc

119b2: 601a str r2, [r3, #0]

119b4: e7f6 b.n 119a4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc3c>

119b6: bf00 nop

119b8: 7fa4 ldrb r4, [r4, #30]

119ba: 0001 movs r1, r0

119bc: 4608 mov r0, r1

119be: f04f 0cc5 mov.w ip, #197 ; 0xc5

119c2: b500 push {lr}

119c4: 4611 mov r1, r2

119c6: f7ff fafb bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

119ca: f510 5f80 cmn.w r0, #4096 ; 0x1000

119ce: d801 bhi.n 119d4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc6c>

119d0: f85d fb04 ldr.w pc, [sp], #4

119d4: 4b03 ldr r3, [pc, #12] ; (119e4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc7c>)

119d6: 4242 negs r2, r0

119d8: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

119dc: 447b add r3, pc

119de: 601a str r2, [r3, #0]

119e0: e7f6 b.n 119d0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc68>

119e2: bf00 nop

119e4: 7f78 ldrb r0, [r7, #29]

119e6: 0001 movs r1, r0

119e8: 4608 mov r0, r1

119ea: f04f 0cc4 mov.w ip, #196 ; 0xc4

119ee: b500 push {lr}

119f0: 4611 mov r1, r2

119f2: f7ff fae5 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

119f6: f510 5f80 cmn.w r0, #4096 ; 0x1000

119fa: d801 bhi.n 11a00 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc98>

119fc: f85d fb04 ldr.w pc, [sp], #4

11a00: 4b03 ldr r3, [pc, #12] ; (11a10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xca8>)

11a02: 4242 negs r2, r0

11a04: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11a08: 447b add r3, pc

11a0a: 601a str r2, [r3, #0]

11a0c: e7f6 b.n 119fc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xc94>

11a0e: bf00 nop

11a10: 7f4c ldrb r4, [r1, #29]

11a12: 0001 movs r1, r0

11a14: b500 push {lr}

11a16: f04f 0c03 mov.w ip, #3

11a1a: f7ff fad1 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11a1e: f510 5f80 cmn.w r0, #4096 ; 0x1000

11a22: d801 bhi.n 11a28 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xcc0>

11a24: f85d fb04 ldr.w pc, [sp], #4

11a28: 4b03 ldr r3, [pc, #12] ; (11a38 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xcd0>)

11a2a: 4242 negs r2, r0

11a2c: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11a30: 447b add r3, pc

11a32: 601a str r2, [r3, #0]

11a34: e7f6 b.n 11a24 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xcbc>

11a36: bf00 nop

11a38: 7f24 ldrb r4, [r4, #28]

11a3a: 0001 movs r1, r0

11a3c: b5f0 push {r4, r5, r6, r7, lr}

11a3e: 17cf asrs r7, r1, #31

11a40: b083 sub sp, #12

11a42: 460d mov r5, r1

11a44: 4614 mov r4, r2

11a46: 4639 mov r1, r7

11a48: 466b mov r3, sp

11a4a: 462a mov r2, r5

11a4c: f04f 0c8c mov.w ip, #140 ; 0x8c

11a50: f7ff fab6 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11a54: f510 5f80 cmn.w r0, #4096 ; 0x1000

11a58: d80a bhi.n 11a70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd08>

11a5a: b938 cbnz r0, 11a6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd04>

11a5c: e9dd 2300 ldrd r2, r3, [sp]

11a60: 17d5 asrs r5, r2, #31

11a62: 4610 mov r0, r2

11a64: 42ab cmp r3, r5

11a66: bf08 it eq

11a68: 4292 cmpeq r2, r2

11a6a: d108 bne.n 11a7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd16>

11a6c: b003 add sp, #12

11a6e: bdf0 pop {r4, r5, r6, r7, pc}

11a70: 4b06 ldr r3, [pc, #24] ; (11a8c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd24>)

11a72: 4242 negs r2, r0

11a74: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11a78: 447b add r3, pc

11a7a: 601a str r2, [r3, #0]

11a7c: e7f6 b.n 11a6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd04>

11a7e: 4b04 ldr r3, [pc, #16] ; (11a90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd28>)

11a80: 224b movs r2, #75 ; 0x4b

11a82: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11a86: 447b add r3, pc

11a88: 601a str r2, [r3, #0]

11a8a: e7ef b.n 11a6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd04>

11a8c: 7edc ldrb r4, [r3, #27]

11a8e: 0001 movs r1, r0

11a90: 7ece ldrb r6, [r1, #27]

11a92: 0001 movs r1, r0

11a94: b510 push {r4, lr}

11a96: b082 sub sp, #8

11a98: 4619 mov r1, r3

11a9a: f04f 0c8c mov.w ip, #140 ; 0x8c

11a9e: 9c04 ldr r4, [sp, #16]

11aa0: 466b mov r3, sp

11aa2: f7ff fa8d bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11aa6: f510 5f80 cmn.w r0, #4096 ; 0x1000

11aaa: d805 bhi.n 11ab8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd50>

11aac: 17c1 asrs r1, r0, #31

11aae: b908 cbnz r0, 11ab4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd4c>

11ab0: e9dd 0100 ldrd r0, r1, [sp]

11ab4: b002 add sp, #8

11ab6: bd10 pop {r4, pc}

11ab8: 4b04 ldr r3, [pc, #16] ; (11acc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd64>)

11aba: 4242 negs r2, r0

11abc: f04f 31ff mov.w r1, #4294967295 ; 0xffffffff

11ac0: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11ac4: 447b add r3, pc

11ac6: 601a str r2, [r3, #0]

11ac8: e7f4 b.n 11ab4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd4c>

11aca: bf00 nop

11acc: 7e90 ldrb r0, [r2, #26]

11ace: 0001 movs r1, r0

11ad0: b500 push {lr}

11ad2: f04f 0c21 mov.w ip, #33 ; 0x21

11ad6: f7ff fa73 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11ada: f510 5f80 cmn.w r0, #4096 ; 0x1000

11ade: d801 bhi.n 11ae4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd7c>

11ae0: f85d fb04 ldr.w pc, [sp], #4

11ae4: 4b03 ldr r3, [pc, #12] ; (11af4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd8c>)

11ae6: 4242 negs r2, r0

11ae8: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11aec: 447b add r3, pc

11aee: 601a str r2, [r3, #0]

11af0: e7f6 b.n 11ae0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xd78>

11af2: bf00 nop

11af4: 7e68 ldrb r0, [r5, #25]

11af6: 0001 movs r1, r0

11af8: b500 push {lr}

11afa: f04f 0c06 mov.w ip, #6

11afe: f7ff fa5f bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11b02: f510 5f80 cmn.w r0, #4096 ; 0x1000

11b06: d801 bhi.n 11b0c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xda4>

11b08: f85d fb04 ldr.w pc, [sp], #4

11b0c: 4b03 ldr r3, [pc, #12] ; (11b1c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdb4>)

11b0e: 4242 negs r2, r0

11b10: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11b14: 447b add r3, pc

11b16: 601a str r2, [r3, #0]

11b18: e7f6 b.n 11b08 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xda0>

11b1a: bf00 nop

11b1c: 7e40 ldrb r0, [r0, #25]

11b1e: 0001 movs r1, r0

11b20: 2909 cmp r1, #9

11b22: b500 push {lr}

11b24: b083 sub sp, #12

11b26: d009 beq.n 11b3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdd4>

11b28: f04f 0cdd mov.w ip, #221 ; 0xdd

11b2c: f7ff fa48 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11b30: f510 5f80 cmn.w r0, #4096 ; 0x1000

11b34: d81c bhi.n 11b70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe08>

11b36: b003 add sp, #12

11b38: f85d fb04 ldr.w pc, [sp], #4

11b3c: 2110 movs r1, #16

11b3e: 466a mov r2, sp

11b40: f04f 0cdd mov.w ip, #221 ; 0xdd

11b44: f7ff fa3c bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11b48: f510 5f80 cmn.w r0, #4096 ; 0x1000

11b4c: d807 bhi.n 11b5e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdf6>

11b4e: e9dd 3000 ldrd r3, r0, [sp]

11b52: 2b02 cmp r3, #2

11b54: bf08 it eq

11b56: 4240 negeq r0, r0

11b58: b003 add sp, #12

11b5a: f85d fb04 ldr.w pc, [sp], #4

11b5e: 4a08 ldr r2, [pc, #32] ; (11b80 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe18>)

11b60: 4243 negs r3, r0

11b62: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11b66: 447a add r2, pc

11b68: 6013 str r3, [r2, #0]

11b6a: b003 add sp, #12

11b6c: f85d fb04 ldr.w pc, [sp], #4

11b70: 4b04 ldr r3, [pc, #16] ; (11b84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe1c>)

11b72: 4242 negs r2, r0

11b74: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11b78: 447b add r3, pc

11b7a: 601a str r2, [r3, #0]

11b7c: e7db b.n 11b36 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdce>

11b7e: bf00 nop

11b80: 7dee ldrb r6, [r5, #23]

11b82: 0001 movs r1, r0

11b84: 7ddc ldrb r4, [r3, #23]

11b86: 0001 movs r1, r0

11b88: b40e push {r1, r2, r3}

11b8a: b500 push {lr}

11b8c: b082 sub sp, #8

11b8e: ab03 add r3, sp, #12

11b90: 9a04 ldr r2, [sp, #16]

11b92: f853 1b04 ldr.w r1, [r3], #4

11b96: 9301 str r3, [sp, #4]

11b98: f7ff ffc2 bl 11b20 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xdb8>

11b9c: b002 add sp, #8

11b9e: f85d eb04 ldr.w lr, [sp], #4

11ba2: b003 add sp, #12

11ba4: 4770 bx lr

11ba6: bf00 nop

11ba8: b40e push {r1, r2, r3}

11baa: 4601 mov r1, r0

11bac: b510 push {r4, lr}

11bae: b083 sub sp, #12

11bb0: 9a05 ldr r2, [sp, #20]

11bb2: f012 0340 ands.w r3, r2, #64 ; 0x40

11bb6: d114 bne.n 11be2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe7a>

11bb8: f44f 4480 mov.w r4, #16384 ; 0x4000

11bbc: f2c0 0440 movt r4, #64 ; 0x40

11bc0: ea34 0002 bics.w r0, r4, r2

11bc4: d00d beq.n 11be2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe7a>

11bc6: f06f 0063 mvn.w r0, #99 ; 0x63

11bca: f44f 7ca1 mov.w ip, #322 ; 0x142

11bce: f7ff f9f7 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11bd2: f510 5f80 cmn.w r0, #4096 ; 0x1000

11bd6: d808 bhi.n 11bea <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe82>

11bd8: b003 add sp, #12

11bda: e8bd 4010 ldmia.w sp!, {r4, lr}

11bde: b003 add sp, #12

11be0: 4770 bx lr

11be2: a806 add r0, sp, #24

11be4: 9b06 ldr r3, [sp, #24]

11be6: 9001 str r0, [sp, #4]

11be8: e7ed b.n 11bc6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe5e>

11bea: 4b03 ldr r3, [pc, #12] ; (11bf8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe90>)

11bec: 4242 negs r2, r0

11bee: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11bf2: 447b add r3, pc

11bf4: 601a str r2, [r3, #0]

11bf6: e7ef b.n 11bd8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xe70>

11bf8: 7d62 ldrb r2, [r4, #21]

11bfa: 0001 movs r1, r0

11bfc: b40e push {r1, r2, r3}

11bfe: 4601 mov r1, r0

11c00: b510 push {r4, lr}

11c02: b083 sub sp, #12

11c04: 9a05 ldr r2, [sp, #20]

11c06: f012 0340 ands.w r3, r2, #64 ; 0x40

11c0a: d116 bne.n 11c3a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xed2>

11c0c: f44f 4480 mov.w r4, #16384 ; 0x4000

11c10: f2c0 0440 movt r4, #64 ; 0x40

11c14: ea34 0002 bics.w r0, r4, r2

11c18: d00f beq.n 11c3a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xed2>

11c1a: f442 3200 orr.w r2, r2, #131072 ; 0x20000

11c1e: f06f 0063 mvn.w r0, #99 ; 0x63

11c22: f44f 7ca1 mov.w ip, #322 ; 0x142

11c26: f7ff f9cb bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11c2a: f510 5f80 cmn.w r0, #4096 ; 0x1000

11c2e: d808 bhi.n 11c42 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xeda>

11c30: b003 add sp, #12

11c32: e8bd 4010 ldmia.w sp!, {r4, lr}

11c36: b003 add sp, #12

11c38: 4770 bx lr

11c3a: a806 add r0, sp, #24

11c3c: 9b06 ldr r3, [sp, #24]

11c3e: 9001 str r0, [sp, #4]

11c40: e7eb b.n 11c1a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xeb2>

11c42: 4b03 ldr r3, [pc, #12] ; (11c50 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xee8>)

11c44: 4242 negs r2, r0

11c46: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11c4a: 447b add r3, pc

11c4c: 601a str r2, [r3, #0]

11c4e: e7ef b.n 11c30 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xec8>

11c50: 7d0a ldrb r2, [r1, #20]

11c52: 0001 movs r1, r0

11c54: b40c push {r2, r3}

11c56: b510 push {r4, lr}

11c58: b082 sub sp, #8

11c5a: 9a04 ldr r2, [sp, #16]

11c5c: f012 0340 ands.w r3, r2, #64 ; 0x40

11c60: d113 bne.n 11c8a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf22>

11c62: f44f 4480 mov.w r4, #16384 ; 0x4000

11c66: f2c0 0440 movt r4, #64 ; 0x40

11c6a: 4394 bics r4, r2

11c6c: d00d beq.n 11c8a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf22>

11c6e: f442 3200 orr.w r2, r2, #131072 ; 0x20000

11c72: f44f 7ca1 mov.w ip, #322 ; 0x142

11c76: f7ff f9a3 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11c7a: f510 5f80 cmn.w r0, #4096 ; 0x1000

11c7e: d808 bhi.n 11c92 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf2a>

11c80: b002 add sp, #8

11c82: e8bd 4010 ldmia.w sp!, {r4, lr}

11c86: b002 add sp, #8

11c88: 4770 bx lr

11c8a: ac05 add r4, sp, #20

11c8c: 9b05 ldr r3, [sp, #20]

11c8e: 9401 str r4, [sp, #4]

11c90: e7ed b.n 11c6e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf06>

11c92: 4b03 ldr r3, [pc, #12] ; (11ca0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf38>)

11c94: 4242 negs r2, r0

11c96: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11c9a: 447b add r3, pc

11c9c: 601a str r2, [r3, #0]

11c9e: e7ef b.n 11c80 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf18>

11ca0: 7cba ldrb r2, [r7, #18]

11ca2: 0001 movs r1, r0

11ca4: b500 push {lr}

11ca6: f04f 0c03 mov.w ip, #3

11caa: f7ff f989 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11cae: f510 5f80 cmn.w r0, #4096 ; 0x1000

11cb2: d801 bhi.n 11cb8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf50>

11cb4: f85d fb04 ldr.w pc, [sp], #4

11cb8: 4b03 ldr r3, [pc, #12] ; (11cc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf60>)

11cba: 4242 negs r2, r0

11cbc: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11cc0: 447b add r3, pc

11cc2: 601a str r2, [r3, #0]

11cc4: e7f6 b.n 11cb4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf4c>

11cc6: bf00 nop

11cc8: 7c94 ldrb r4, [r2, #18]

11cca: 0001 movs r1, r0

11ccc: b500 push {lr}

11cce: f04f 0c04 mov.w ip, #4

11cd2: f7ff f975 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11cd6: f510 5f80 cmn.w r0, #4096 ; 0x1000

11cda: d801 bhi.n 11ce0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf78>

11cdc: f85d fb04 ldr.w pc, [sp], #4

11ce0: 4b03 ldr r3, [pc, #12] ; (11cf0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf88>)

11ce2: 4242 negs r2, r0

11ce4: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11ce8: 447b add r3, pc

11cea: 601a str r2, [r3, #0]

11cec: e7f6 b.n 11cdc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xf74>

11cee: bf00 nop

11cf0: 7c6c ldrb r4, [r5, #17]

11cf2: 0001 movs r1, r0

11cf4: b570 push {r4, r5, r6, lr}

11cf6: 4616 mov r6, r2

11cf8: 430a orrs r2, r1

11cfa: 4605 mov r5, r0

11cfc: 0c13 lsrs r3, r2, #16

11cfe: 460c mov r4, r1

11d00: fb06 f404 mul.w r4, r6, r4

11d04: bf14 ite ne

11d06: 2001 movne r0, #1

11d08: 2000 moveq r0, #0

11d0a: 2900 cmp r1, #0

11d0c: bf08 it eq

11d0e: 2000 moveq r0, #0

11d10: b998 cbnz r0, 11d3a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfd2>

11d12: 686b ldr r3, [r5, #4]

11d14: 42a3 cmp r3, r4

11d16: bf28 it cs

11d18: 2001 movcs r0, #1

11d1a: d20d bcs.n 11d38 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfd0>

11d1c: 462e mov r6, r5

11d1e: f856 0b08 ldr.w r0, [r6], #8

11d22: 42b0 cmp r0, r6

11d24: d001 beq.n 11d2a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfc2>

11d26: f7ee ee7e blx a24 <free@plt+0x4>

11d2a: 4620 mov r0, r4

11d2c: f7ee ee4e blx 9cc <malloc@plt+0x4>

11d30: b1c8 cbz r0, 11d66 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xffe>

11d32: 6028 str r0, [r5, #0]

11d34: 2001 movs r0, #1

11d36: 606c str r4, [r5, #4]

11d38: bd70 pop {r4, r5, r6, pc}

11d3a: 4620 mov r0, r4

11d3c: f001 fc94 bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

11d40: 42b0 cmp r0, r6

11d42: d0e6 beq.n 11d12 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfaa>

11d44: 462c mov r4, r5

11d46: f854 0b08 ldr.w r0, [r4], #8

11d4a: 42a0 cmp r0, r4

11d4c: d001 beq.n 11d52 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0xfea>

11d4e: f7ee ee6a blx a24 <free@plt+0x4>

11d52: 4b07 ldr r3, [pc, #28] ; (11d70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1008>)

11d54: 220c movs r2, #12

11d56: 602c str r4, [r5, #0]

11d58: f44f 6180 mov.w r1, #1024 ; 0x400

11d5c: 447b add r3, pc

11d5e: 2000 movs r0, #0

11d60: 6069 str r1, [r5, #4]

11d62: 601a str r2, [r3, #0]

11d64: bd70 pop {r4, r5, r6, pc}

11d66: f44f 6380 mov.w r3, #1024 ; 0x400

11d6a: 602e str r6, [r5, #0]

11d6c: 606b str r3, [r5, #4]

11d6e: bd70 pop {r4, r5, r6, pc}

11d70: 7bf8 ldrb r0, [r7, #15]

11d72: 0001 movs r1, r0

11d74: b530 push {r4, r5, lr}

11d76: 9d04 ldr r5, [sp, #16]

11d78: f3c5 040b ubfx r4, r5, #0, #12

11d7c: b954 cbnz r4, 11d94 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x102c>

11d7e: 0b2d lsrs r5, r5, #12

11d80: 9c03 ldr r4, [sp, #12]

11d82: f04f 0cc0 mov.w ip, #192 ; 0xc0

11d86: f7ff f91b bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11d8a: f510 5f80 cmn.w r0, #4096 ; 0x1000

11d8e: 4603 mov r3, r0

11d90: d807 bhi.n 11da2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x103a>

11d92: bd30 pop {r4, r5, pc}

11d94: 4b06 ldr r3, [pc, #24] ; (11db0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1048>)

11d96: 2216 movs r2, #22

11d98: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11d9c: 447b add r3, pc

11d9e: 601a str r2, [r3, #0]

11da0: bd30 pop {r4, r5, pc}

11da2: 4a04 ldr r2, [pc, #16] ; (11db4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x104c>)

11da4: 425b negs r3, r3

11da6: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

11daa: 447a add r2, pc

11dac: 6013 str r3, [r2, #0]

11dae: bd30 pop {r4, r5, pc}

11db0: 7bb8 ldrb r0, [r7, #14]

11db2: 0001 movs r1, r0

11db4: 7baa ldrb r2, [r5, #14]

11db6: 0001 movs r1, r0

...

11dc0: b480 push {r7}

11dc2: f04f 075b mov.w r7, #91 ; 0x5b

11dc6: df00 svc 0

11dc8: bc80 pop {r7}

11dca: f510 5f80 cmn.w r0, #4096 ; 0x1000

11dce: bf38 it cc

11dd0: 4770 bxcc lr

11dd2: e7ff b.n 11dd4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x106c>

11dd4: f1c0 0000 rsb r0, r0, #0

11dd8: 4902 ldr r1, [pc, #8] ; (11de4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x107c>)

11dda: 4479 add r1, pc

11ddc: 6008 str r0, [r1, #0]

11dde: f06f 0000 mvn.w r0, #0

11de2: 4770 bx lr

11de4: 7b7a ldrb r2, [r7, #13]

11de6: 0001 movs r1, r0

...

11df0: b480 push {r7}

11df2: f04f 077d mov.w r7, #125 ; 0x7d

11df6: df00 svc 0

11df8: bc80 pop {r7}

11dfa: f510 5f80 cmn.w r0, #4096 ; 0x1000

11dfe: bf38 it cc

11e00: 4770 bxcc lr

11e02: e7ff b.n 11e04 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x109c>

11e04: f1c0 0000 rsb r0, r0, #0

11e08: 4902 ldr r1, [pc, #8] ; (11e14 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10ac>)

11e0a: 4479 add r1, pc

11e0c: 6008 str r0, [r1, #0]

11e0e: f06f 0000 mvn.w r0, #0

11e12: 4770 bx lr

11e14: 7b4a ldrb r2, [r1, #13]

11e16: 0001 movs r1, r0

...

11e20: b480 push {r7}

11e22: f04f 077a mov.w r7, #122 ; 0x7a

11e26: df00 svc 0

11e28: bc80 pop {r7}

11e2a: f510 5f80 cmn.w r0, #4096 ; 0x1000

11e2e: bf38 it cc

11e30: 4770 bxcc lr

11e32: e7ff b.n 11e34 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10cc>

11e34: f1c0 0000 rsb r0, r0, #0

11e38: 4902 ldr r1, [pc, #8] ; (11e44 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10dc>)

11e3a: 4479 add r1, pc

11e3c: 6008 str r0, [r1, #0]

11e3e: f06f 0000 mvn.w r0, #0

11e42: 4770 bx lr

11e44: 7b1a ldrb r2, [r3, #12]

11e46: 0001 movs r1, r0

11e48: b500 push {lr}

11e4a: 4603 mov r3, r0

11e4c: f04f 0cf8 mov.w ip, #248 ; 0xf8

11e50: f7ff f8b6 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11e54: f510 5f80 cmn.w r0, #4096 ; 0x1000

11e58: d80c bhi.n 11e74 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x110c>

11e5a: 4618 mov r0, r3

11e5c: f04f 0c01 mov.w ip, #1

11e60: f7ff f8ae bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11e64: f510 5f80 cmn.w r0, #4096 ; 0x1000

11e68: d903 bls.n 11e72 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x110a>

11e6a: 4b05 ldr r3, [pc, #20] ; (11e80 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1118>)

11e6c: 4240 negs r0, r0

11e6e: 447b add r3, pc

11e70: 6018 str r0, [r3, #0]

11e72: deff udf #255 ; 0xff

11e74: 4a03 ldr r2, [pc, #12] ; (11e84 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x111c>)

11e76: 4240 negs r0, r0

11e78: 447a add r2, pc

11e7a: 6010 str r0, [r2, #0]

11e7c: e7ed b.n 11e5a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x10f2>

11e7e: bf00 nop

11e80: 7ae6 ldrb r6, [r4, #11]

11e82: 0001 movs r1, r0

11e84: 7adc ldrb r4, [r3, #11]

11e86: 0001 movs r1, r0

...

11e90: b480 push {r7}

11e92: f04f 0714 mov.w r7, #20

11e96: df00 svc 0

11e98: bc80 pop {r7}

11e9a: 4770 bx lr

11e9c: 0000 movs r0, r0

11e9e: 0000 movs r0, r0

11ea0: f8df c04c ldr.w ip, [pc, #76] ; 11ef0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1188>

11ea4: 44fc add ip, pc

11ea6: f8dc 2000 ldr.w r2, [ip]

11eaa: 4684 mov ip, r0

11eac: 466b mov r3, sp

11eae: ea83 0302 eor.w r3, r3, r2

11eb2: f84c 3b04 str.w r3, [ip], #4

11eb6: ea8e 0302 eor.w r3, lr, r2

11eba: f84c 3b04 str.w r3, [ip], #4

11ebe: e8ac 0ff0 stmia.w ip!, {r4, r5, r6, r7, r8, r9, sl, fp}

11ec2: f8df 3030 ldr.w r3, [pc, #48] ; 11ef4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x118c>

11ec6: 447b add r3, pc

11ec8: 681a ldr r2, [r3, #0]

11eca: ecac 8b10 vstmia ip!, {d8-d15}

11ece: f412 7f00 tst.w r2, #512 ; 0x200

11ed2: d00b beq.n 11eec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1184>

11ed4: ecec a102 stfp f2, [ip], #8

11ed8: ecec b102 stfp f3, [ip], #8

11edc: ecec c102 stfp f4, [ip], #8

11ee0: ecec d102 stfp f5, [ip], #8

11ee4: ecec e102 stfp f6, [ip], #8

11ee8: ecec f102 stfp f7, [ip], #8

11eec: f7fe bcc8 b.w 10880 <free@@GLIBC\_2.4+0x20>

11ef0: 6abc ldr r4, [r7, #40] ; 0x28

11ef2: 0001 movs r1, r0

11ef4: 6ae6 ldr r6, [r4, #44] ; 0x2c

11ef6: 0001 movs r1, r0

...

11f00: 4684 mov ip, r0

11f02: f85c 3b04 ldr.w r3, [ip], #4

11f06: 4c12 ldr r4, [pc, #72] ; (11f50 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11e8>)

11f08: 447c add r4, pc

11f0a: 6822 ldr r2, [r4, #0]

11f0c: ea83 0302 eor.w r3, r3, r2

11f10: f85c 4b04 ldr.w r4, [ip], #4

11f14: ea84 0402 eor.w r4, r4, r2

11f18: 469d mov sp, r3

11f1a: 46a6 mov lr, r4

11f1c: e8bc 0ff0 ldmia.w ip!, {r4, r5, r6, r7, r8, r9, sl, fp}

11f20: 4a0c ldr r2, [pc, #48] ; (11f54 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11ec>)

11f22: 447a add r2, pc

11f24: 6813 ldr r3, [r2, #0]

11f26: ecbc 8b10 vldmia ip!, {d8-d15}

11f2a: f413 7f00 tst.w r3, #512 ; 0x200

11f2e: d00b beq.n 11f48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11e0>

11f30: ecfc a102 ldfp f2, [ip], #8

11f34: ecfc b102 ldfp f3, [ip], #8

11f38: ecfc c102 ldfp f4, [ip], #8

11f3c: ecfc d102 ldfp f5, [ip], #8

11f40: ecfc e102 ldfp f6, [ip], #8

11f44: ecfc f102 ldfp f7, [ip], #8

11f48: 0008 movs r0, r1

11f4a: bf08 it eq

11f4c: 2001 moveq r0, #1

11f4e: 4770 bx lr

11f50: 6a58 ldr r0, [r3, #36] ; 0x24

11f52: 0001 movs r1, r0

11f54: 6a8a ldr r2, [r1, #40] ; 0x28

11f56: 0001 movs r1, r0

11f58: e92d 41f0 stmdb sp!, {r4, r5, r6, r7, r8, lr}

11f5c: 4680 mov r8, r0

11f5e: 4f2a ldr r7, [pc, #168] ; (12008 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12a0>)

11f60: b0c6 sub sp, #280 ; 0x118

11f62: 4615 mov r5, r2

11f64: 460e mov r6, r1

11f66: 447f add r7, pc

11f68: 2900 cmp r1, #0

11f6a: d03f beq.n 11fec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1284>

11f6c: f851 3b04 ldr.w r3, [r1], #4

11f70: 2280 movs r2, #128 ; 0x80

11f72: 466c mov r4, sp

11f74: a803 add r0, sp, #12

11f76: 6023 str r3, [r4, #0]

11f78: f001 e8c2 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

11f7c: f8d6 3084 ldr.w r3, [r6, #132] ; 0x84

11f80: 075a lsls r2, r3, #29

11f82: d428 bmi.n 11fd6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x126e>

11f84: 4a21 ldr r2, [pc, #132] ; (1200c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12a4>)

11f86: f043 6380 orr.w r3, r3, #67108864 ; 0x4000000

11f8a: 58ba ldr r2, [r7, r2]

11f8c: e9c4 3201 strd r3, r2, [r4, #4]

11f90: aa23 add r2, sp, #140 ; 0x8c

11f92: b34d cbz r5, 11fe8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1280>

11f94: 4621 mov r1, r4

11f96: 4640 mov r0, r8

11f98: 2308 movs r3, #8

11f9a: f04f 0cae mov.w ip, #174 ; 0xae

11f9e: f7ff f80f bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

11fa2: f510 5f80 cmn.w r0, #4096 ; 0x1000

11fa6: 4604 mov r4, r0

11fa8: d826 bhi.n 11ff8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1290>

11faa: 43c3 mvns r3, r0

11fac: 0fdb lsrs r3, r3, #31

11fae: 2d00 cmp r5, #0

11fb0: bf08 it eq

11fb2: 2300 moveq r3, #0

11fb4: b15b cbz r3, 11fce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1266>

11fb6: 9b23 ldr r3, [sp, #140] ; 0x8c

11fb8: 4628 mov r0, r5

11fba: 2280 movs r2, #128 ; 0x80

11fbc: a926 add r1, sp, #152 ; 0x98

11fbe: f840 3b04 str.w r3, [r0], #4

11fc2: f001 e89e blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

11fc6: e9dd 2324 ldrd r2, r3, [sp, #144] ; 0x90

11fca: e9c5 2321 strd r2, r3, [r5, #132] ; 0x84

11fce: 4620 mov r0, r4

11fd0: b046 add sp, #280 ; 0x118

11fd2: e8bd 81f0 ldmia.w sp!, {r4, r5, r6, r7, r8, pc}

11fd6: 4a0e ldr r2, [pc, #56] ; (12010 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12a8>)

11fd8: f043 6380 orr.w r3, r3, #67108864 ; 0x4000000

11fdc: 58ba ldr r2, [r7, r2]

11fde: e9c4 3201 strd r3, r2, [r4, #4]

11fe2: aa23 add r2, sp, #140 ; 0x8c

11fe4: 2d00 cmp r5, #0

11fe6: d1d5 bne.n 11f94 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x122c>

11fe8: 462a mov r2, r5

11fea: e7d3 b.n 11f94 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x122c>

11fec: 4611 mov r1, r2

11fee: 2a00 cmp r2, #0

11ff0: d0d1 beq.n 11f96 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x122e>

11ff2: 4631 mov r1, r6

11ff4: aa23 add r2, sp, #140 ; 0x8c

11ff6: e7ce b.n 11f96 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x122e>

11ff8: 4b06 ldr r3, [pc, #24] ; (12014 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12ac>)

11ffa: 4242 negs r2, r0

11ffc: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

12000: 447b add r3, pc

12002: 601a str r2, [r3, #0]

12004: e7e3 b.n 11fce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1266>

12006: bf00 nop

12008: 7096 strb r6, [r2, #2]

1200a: 0001 movs r1, r0

1200c: 0048 lsls r0, r1, #1

1200e: 0000 movs r0, r0

12010: 004c lsls r4, r1, #1

12012: 0000 movs r0, r0

12014: 7954 ldrb r4, [r2, #5]

12016: 0001 movs r1, r0

12018: b410 push {r4}

1201a: 1e44 subs r4, r0, #1

1201c: 2c3f cmp r4, #63 ; 0x3f

1201e: d806 bhi.n 1202e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12c6>

12020: f1a0 0320 sub.w r3, r0, #32

12024: 2b01 cmp r3, #1

12026: d902 bls.n 1202e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12c6>

12028: f85d 4b04 ldr.w r4, [sp], #4

1202c: e794 b.n 11f58 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x11f0>

1202e: 4b04 ldr r3, [pc, #16] ; (12040 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x12d8>)

12030: 2216 movs r2, #22

12032: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

12036: f85d 4b04 ldr.w r4, [sp], #4

1203a: 447b add r3, pc

1203c: 601a str r2, [r3, #0]

1203e: 4770 bx lr

12040: 791a ldrb r2, [r3, #4]

12042: 0001 movs r1, r0

...

12050: bf00 nop

12052: bf00 nop

12054: f3af 8000 nop.w

12058: f3af 8000 nop.w

1205c: f3af 8000 nop.w

12060: f04f 0777 mov.w r7, #119 ; 0x77

12064: df00 svc 0

12066: bf00 nop

12068: f3af 8000 nop.w

1206c: f3af 8000 nop.w

12070: f04f 07ad mov.w r7, #173 ; 0xad

12074: df00 svc 0

12076: bf00 nop

...

12080: 7802 ldrb r2, [r0, #0]

12082: b2c9 uxtb r1, r1

12084: f000 0307 and.w r3, r0, #7

12088: f1c3 030f rsb r3, r3, #15

1208c: 428a cmp r2, r1

1208e: bf18 it ne

12090: 2a00 cmpne r2, #0

12092: d045 beq.n 12120 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13b8>

12094: f810 2f01 ldrb.w r2, [r0, #1]!

12098: 3b01 subs r3, #1

1209a: bf18 it ne

1209c: 428a cmpne r2, r1

1209e: bf18 it ne

120a0: 2a00 cmpne r2, #0

120a2: d1f7 bne.n 12094 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x132c>

120a4: 428a cmp r2, r1

120a6: bf18 it ne

120a8: 2a00 cmpne r2, #0

120aa: d039 beq.n 12120 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13b8>

120ac: f100 0001 add.w r0, r0, #1

120b0: b4f0 push {r4, r5, r6, r7}

120b2: e8f0 2302 ldrd r2, r3, [r0], #8

120b6: ea41 2101 orr.w r1, r1, r1, lsl #8

120ba: f240 1c01 movw ip, #257 ; 0x101

120be: f890 f040 pld [r0, #64] ; 0x40

120c2: f2c0 1c01 movt ip, #257 ; 0x101

120c6: ea41 4101 orr.w r1, r1, r1, lsl #16

120ca: facc f452 uqsub8 r4, ip, r2

120ce: ea82 0601 eor.w r6, r2, r1

120d2: facc f553 uqsub8 r5, ip, r3

120d6: ea83 0701 eor.w r7, r3, r1

120da: facc f656 uqsub8 r6, ip, r6

120de: f890 f080 pld [r0, #128] ; 0x80

120e2: facc f757 uqsub8 r7, ip, r7

120e6: ea44 0406 orr.w r4, r4, r6

120ea: ea45 0507 orr.w r5, r5, r7

120ee: ea54 0605 orrs.w r6, r4, r5

120f2: bf08 it eq

120f4: e8f0 2302 ldrdeq r2, r3, [r0], #8

120f8: d0e7 beq.n 120ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1362>

120fa: 2c00 cmp r4, #0

120fc: f1a0 0004 sub.w r0, r0, #4

12100: bf06 itte eq

12102: 462c moveq r4, r5

12104: 461a moveq r2, r3

12106: 3804 subne r0, #4

12108: ba24 rev r4, r4

1210a: fab4 f384 clz r3, r4

1210e: f1a3 0307 sub.w r3, r3, #7

12112: fa22 f203 lsr.w r2, r2, r3

12116: b2c9 uxtb r1, r1

12118: b2d2 uxtb r2, r2

1211a: eb00 00d3 add.w r0, r0, r3, lsr #3

1211e: bcf0 pop {r4, r5, r6, r7}

12120: 428a cmp r2, r1

12122: bf18 it ne

12124: 2000 movne r0, #0

12126: 4770 bx lr

...

12140: eba2 0003 sub.w r0, r2, r3

12144: 4770 bx lr

12146: bf00 nop

12148: f3af 8000 nop.w

1214c: f3af 8000 nop.w

12150: 7802 ldrb r2, [r0, #0]

12152: 780b ldrb r3, [r1, #0]

12154: 2a01 cmp r2, #1

12156: bf28 it cs

12158: 429a cmpcs r2, r3

1215a: d1f1 bne.n 12140 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x13d8>

1215c: e96d 4504 strd r4, r5, [sp, #-16]!

12160: ea40 0401 orr.w r4, r0, r1

12164: e9cd 6702 strd r6, r7, [sp, #8]

12168: f06f 0c00 mvn.w ip, #0

1216c: ea4f 7244 mov.w r2, r4, lsl #29

12170: b332 cbz r2, 121c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1458>

12172: ea80 0401 eor.w r4, r0, r1

12176: f014 0f07 tst.w r4, #7

1217a: d16e bne.n 1225a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x14f2>

1217c: f000 0407 and.w r4, r0, #7

12180: f020 0007 bic.w r0, r0, #7

12184: f004 0503 and.w r5, r4, #3

12188: f021 0107 bic.w r1, r1, #7

1218c: ea4f 05c5 mov.w r5, r5, lsl #3

12190: e8f0 2304 ldrd r2, r3, [r0], #16

12194: f014 0f04 tst.w r4, #4

12198: e8f1 6704 ldrd r6, r7, [r1], #16

1219c: fa0c f405 lsl.w r4, ip, r5

121a0: ea62 0204 orn r2, r2, r4

121a4: ea66 0604 orn r6, r6, r4

121a8: d00e beq.n 121c8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1460>

121aa: ea63 0304 orn r3, r3, r4

121ae: 4662 mov r2, ip

121b0: ea67 0704 orn r7, r7, r4

121b4: 4666 mov r6, ip

121b6: e007 b.n 121c8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1460>

121b8: f3af 8000 nop.w

121bc: f3af 8000 nop.w

121c0: e8f0 2304 ldrd r2, r3, [r0], #16

121c4: e8f1 6704 ldrd r6, r7, [r1], #16

121c8: fa82 f54c uadd8 r5, r2, ip

121cc: ea82 0406 eor.w r4, r2, r6

121d0: faa4 f48c sel r4, r4, ip

121d4: bb6c cbnz r4, 12232 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x14ca>

121d6: fa83 f54c uadd8 r5, r3, ip

121da: ea83 0507 eor.w r5, r3, r7

121de: faa5 f58c sel r5, r5, ip

121e2: b995 cbnz r5, 1220a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x14a2>

121e4: e950 2302 ldrd r2, r3, [r0, #-8]

121e8: e951 6702 ldrd r6, r7, [r1, #-8]

121ec: fa82 f54c uadd8 r5, r2, ip

121f0: ea82 0406 eor.w r4, r2, r6

121f4: faa4 f48c sel r4, r4, ip

121f8: fa83 f54c uadd8 r5, r3, ip

121fc: ea83 0507 eor.w r5, r3, r7

12200: faa5 f58c sel r5, r5, ip

12204: 4325 orrs r5, r4

12206: d0db beq.n 121c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1458>

12208: b99c cbnz r4, 12232 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x14ca>

1220a: ba2d rev r5, r5

1220c: fab5 f485 clz r4, r5

12210: f024 0407 bic.w r4, r4, #7

12214: fa27 f104 lsr.w r1, r7, r4

12218: e9dd 6702 ldrd r6, r7, [sp, #8]

1221c: fa23 f304 lsr.w r3, r3, r4

12220: f003 00ff and.w r0, r3, #255 ; 0xff

12224: f001 01ff and.w r1, r1, #255 ; 0xff

12228: e8fd 4504 ldrd r4, r5, [sp], #16

1222c: eba0 0001 sub.w r0, r0, r1

12230: 4770 bx lr

12232: ba24 rev r4, r4

12234: fab4 f484 clz r4, r4

12238: f024 0407 bic.w r4, r4, #7

1223c: fa26 f104 lsr.w r1, r6, r4

12240: e9dd 6702 ldrd r6, r7, [sp, #8]

12244: fa22 f204 lsr.w r2, r2, r4

12248: f002 00ff and.w r0, r2, #255 ; 0xff

1224c: f001 01ff and.w r1, r1, #255 ; 0xff

12250: e8fd 4504 ldrd r4, r5, [sp], #16

12254: eba0 0001 sub.w r0, r0, r1

12258: 4770 bx lr

1225a: f014 0f03 tst.w r4, #3

1225e: d13c bne.n 122da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1572>

12260: f010 0403 ands.w r4, r0, #3

12264: d128 bne.n 122b8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1550>

12266: f850 2b08 ldr.w r2, [r0], #8

1226a: f851 3b08 ldr.w r3, [r1], #8

1226e: fa82 f54c uadd8 r5, r2, ip

12272: ea82 0503 eor.w r5, r2, r3

12276: faa5 f58c sel r5, r5, ip

1227a: b95d cbnz r5, 12294 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x152c>

1227c: f850 2c04 ldr.w r2, [r0, #-4]

12280: f851 3c04 ldr.w r3, [r1, #-4]

12284: fa82 f54c uadd8 r5, r2, ip

12288: ea82 0503 eor.w r5, r2, r3

1228c: faa5 f58c sel r5, r5, ip

12290: 2d00 cmp r5, #0

12292: d0e8 beq.n 12266 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x14fe>

12294: ba2d rev r5, r5

12296: fab5 f485 clz r4, r5

1229a: f024 0407 bic.w r4, r4, #7

1229e: fa23 f104 lsr.w r1, r3, r4

122a2: fa22 f204 lsr.w r2, r2, r4

122a6: f002 00ff and.w r0, r2, #255 ; 0xff

122aa: f001 01ff and.w r1, r1, #255 ; 0xff

122ae: e8fd 4504 ldrd r4, r5, [sp], #16

122b2: eba0 0001 sub.w r0, r0, r1

122b6: 4770 bx lr

122b8: ea4f 04c4 mov.w r4, r4, lsl #3

122bc: f020 0003 bic.w r0, r0, #3

122c0: f850 2b08 ldr.w r2, [r0], #8

122c4: f021 0103 bic.w r1, r1, #3

122c8: f851 3b08 ldr.w r3, [r1], #8

122cc: fa0c f404 lsl.w r4, ip, r4

122d0: ea62 0204 orn r2, r2, r4

122d4: ea63 0304 orn r3, r3, r4

122d8: e7c9 b.n 1226e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1506>

122da: f010 0403 ands.w r4, r0, #3

122de: d01a beq.n 12316 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15ae>

122e0: eba1 0104 sub.w r1, r1, r4

122e4: f020 0003 bic.w r0, r0, #3

122e8: 07e4 lsls r4, r4, #31

122ea: f850 2b04 ldr.w r2, [r0], #4

122ee: d006 beq.n 122fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1596>

122f0: d20f bcs.n 12312 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15aa>

122f2: 788b ldrb r3, [r1, #2]

122f4: fa5f f4a2 uxtb.w r4, r2, ror #16

122f8: 1ae4 subs r4, r4, r3

122fa: d106 bne.n 1230a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15a2>

122fc: b12b cbz r3, 1230a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15a2>

122fe: 78cb ldrb r3, [r1, #3]

12300: fa5f f4b2 uxtb.w r4, r2, ror #24

12304: 1ae4 subs r4, r4, r3

12306: d100 bne.n 1230a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15a2>

12308: b91b cbnz r3, 12312 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15aa>

1230a: 4620 mov r0, r4

1230c: f85d 4b10 ldr.w r4, [sp], #16

12310: 4770 bx lr

12312: f101 0104 add.w r1, r1, #4

12316: f850 2b04 ldr.w r2, [r0], #4

1231a: 07cc lsls r4, r1, #31

1231c: f021 0103 bic.w r1, r1, #3

12320: f851 3b04 ldr.w r3, [r1], #4

12324: d84c bhi.n 123c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1658>

12326: d224 bcs.n 12372 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x160a>

12328: f022 447f bic.w r4, r2, #4278190080 ; 0xff000000

1232c: fa82 f54c uadd8 r5, r2, ip

12330: ea94 2513 eors.w r5, r4, r3, lsr #8

12334: faa5 f58c sel r5, r5, ip

12338: d10a bne.n 12350 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15e8>

1233a: b965 cbnz r5, 12356 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15ee>

1233c: f851 3b04 ldr.w r3, [r1], #4

12340: ea84 0402 eor.w r4, r4, r2

12344: ebb4 6f03 cmp.w r4, r3, lsl #24

12348: d10e bne.n 12368 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1600>

1234a: f850 2b04 ldr.w r2, [r0], #4

1234e: e7eb b.n 12328 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x15c0>

12350: ea4f 2313 mov.w r3, r3, lsr #8

12354: e059 b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

12356: f035 457f bics.w r5, r5, #4278190080 ; 0xff000000

1235a: d151 bne.n 12400 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1698>

1235c: 7808 ldrb r0, [r1, #0]

1235e: e8fd 4504 ldrd r4, r5, [sp], #16

12362: f1c0 0000 rsb r0, r0, #0

12366: 4770 bx lr

12368: ea4f 6212 mov.w r2, r2, lsr #24

1236c: f003 03ff and.w r3, r3, #255 ; 0xff

12370: e04b b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

12372: ea02 441c and.w r4, r2, ip, lsr #16

12376: fa82 f54c uadd8 r5, r2, ip

1237a: ea94 4513 eors.w r5, r4, r3, lsr #16

1237e: faa5 f58c sel r5, r5, ip

12382: d10a bne.n 1239a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1632>

12384: b965 cbnz r5, 123a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1638>

12386: f851 3b04 ldr.w r3, [r1], #4

1238a: ea84 0402 eor.w r4, r4, r2

1238e: ebb4 4f03 cmp.w r4, r3, lsl #16

12392: d10c bne.n 123ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1646>

12394: f850 2b04 ldr.w r2, [r0], #4

12398: e7eb b.n 12372 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x160a>

1239a: ea4f 4313 mov.w r3, r3, lsr #16

1239e: e034 b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

123a0: ea15 451c ands.w r5, r5, ip, lsr #16

123a4: d12c bne.n 12400 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1698>

123a6: 880b ldrh r3, [r1, #0]

123a8: ea4f 4212 mov.w r2, r2, lsr #16

123ac: e02d b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

123ae: ea4f 4212 mov.w r2, r2, lsr #16

123b2: ea03 431c and.w r3, r3, ip, lsr #16

123b6: e028 b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

123b8: f3af 8000 nop.w

123bc: f3af 8000 nop.w

123c0: f002 04ff and.w r4, r2, #255 ; 0xff

123c4: fa82 f54c uadd8 r5, r2, ip

123c8: ea94 6513 eors.w r5, r4, r3, lsr #24

123cc: faa5 f58c sel r5, r5, ip

123d0: d10a bne.n 123e8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1680>

123d2: b965 cbnz r5, 123ee <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1686>

123d4: f851 3b04 ldr.w r3, [r1], #4

123d8: ea84 0402 eor.w r4, r4, r2

123dc: ebb4 2f03 cmp.w r4, r3, lsl #8

123e0: d109 bne.n 123f6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x168e>

123e2: f850 2b04 ldr.w r2, [r0], #4

123e6: e7eb b.n 123c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1658>

123e8: ea4f 6313 mov.w r3, r3, lsr #24

123ec: e00d b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

123ee: f015 0fff tst.w r5, #255 ; 0xff

123f2: d105 bne.n 12400 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1698>

123f4: 680b ldr r3, [r1, #0]

123f6: ea4f 2212 mov.w r2, r2, lsr #8

123fa: f023 437f bic.w r3, r3, #4278190080 ; 0xff000000

123fe: e004 b.n 1240a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16a2>

12400: f04f 0000 mov.w r0, #0

12404: e8fd 4504 ldrd r4, r5, [sp], #16

12408: 4770 bx lr

1240a: ba12 rev r2, r2

1240c: ba1b rev r3, r3

1240e: fa82 f44c uadd8 r4, r2, ip

12412: ea82 0403 eor.w r4, r2, r3

12416: faa4 f58c sel r5, r4, ip

1241a: fab5 f485 clz r4, r5

1241e: fa02 f204 lsl.w r2, r2, r4

12422: fa03 f304 lsl.w r3, r3, r4

12426: ea4f 6012 mov.w r0, r2, lsr #24

1242a: e8fd 4504 ldrd r4, r5, [sp], #16

1242e: eba0 6013 sub.w r0, r0, r3, lsr #24

12432: 4770 bx lr

...

12440: f04f 0c00 mov.w ip, #0

12444: e005 b.n 12452 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16ea>

12446: bf00 nop

12448: f3af 8000 nop.w

1244c: f3af 8000 nop.w

12450: 4684 mov ip, r0

12452: f890 f000 pld [r0]

12456: f891 f000 pld [r1]

1245a: f001 0307 and.w r3, r1, #7

1245e: f1c3 0310 rsb r3, r3, #16

12462: f811 2b01 ldrb.w r2, [r1], #1

12466: 3b01 subs r3, #1

12468: f800 2b01 strb.w r2, [r0], #1

1246c: bf18 it ne

1246e: 2a00 cmpne r2, #0

12470: d1f7 bne.n 12462 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x16fa>

12472: 2a00 cmp r2, #0

12474: d032 beq.n 124dc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1774>

12476: e8f1 2302 ldrd r2, r3, [r1], #8

1247a: f890 f040 pld [r0, #64] ; 0x40

1247e: f891 f040 pld [r1, #64] ; 0x40

12482: b4f0 push {r4, r5, r6, r7}

12484: f240 1701 movw r7, #257 ; 0x101

12488: f010 0f03 tst.w r0, #3

1248c: f2c0 1701 movt r7, #257 ; 0x101

12490: d12a bne.n 124e8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1780>

12492: bf00 nop

12494: f3af 8000 nop.w

12498: f3af 8000 nop.w

1249c: f3af 8000 nop.w

124a0: fac7 f452 uqsub8 r4, r7, r2

124a4: fac7 f553 uqsub8 r5, r7, r3

124a8: f891 f080 pld [r1, #128] ; 0x80

124ac: 2c00 cmp r4, #0

124ae: f890 f080 pld [r0, #128] ; 0x80

124b2: d108 bne.n 124c6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x175e>

124b4: f840 2b04 str.w r2, [r0], #4

124b8: 2d00 cmp r5, #0

124ba: d106 bne.n 124ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1762>

124bc: f840 3b04 str.w r3, [r0], #4

124c0: e8f1 2302 ldrd r2, r3, [r1], #8

124c4: e7ec b.n 124a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1738>

124c6: f1a1 0104 sub.w r1, r1, #4

124ca: f1a1 0104 sub.w r1, r1, #4

124ce: f811 2b01 ldrb.w r2, [r1], #1

124d2: 2a00 cmp r2, #0

124d4: f800 2b01 strb.w r2, [r0], #1

124d8: d1f9 bne.n 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

124da: bcf0 pop {r4, r5, r6, r7}

124dc: f1bc 0f00 cmp.w ip, #0

124e0: bf0c ite eq

124e2: 3801 subeq r0, #1

124e4: 4660 movne r0, ip

124e6: 4770 bx lr

124e8: f010 0f01 tst.w r0, #1

124ec: d03d beq.n 1256a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1802>

124ee: f010 0f02 tst.w r0, #2

124f2: d176 bne.n 125e2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x187a>

124f4: fac7 f452 uqsub8 r4, r7, r2

124f8: fac7 f553 uqsub8 r5, r7, r3

124fc: 2c00 cmp r4, #0

124fe: bf18 it ne

12500: 3908 subne r1, #8

12502: d1e4 bne.n 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

12504: f800 2b01 strb.w r2, [r0], #1

12508: ea4f 2212 mov.w r2, r2, lsr #8

1250c: f800 2b01 strb.w r2, [r0], #1

12510: ea4f 2212 mov.w r2, r2, lsr #8

12514: f800 2b01 strb.w r2, [r0], #1

12518: ea4f 2212 mov.w r2, r2, lsr #8

1251c: f3af 8000 nop.w

12520: 2d00 cmp r5, #0

12522: d11b bne.n 1255c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x17f4>

12524: ea42 2203 orr.w r2, r2, r3, lsl #8

12528: ea4f 6613 mov.w r6, r3, lsr #24

1252c: f840 2b04 str.w r2, [r0], #4

12530: e8f1 2302 ldrd r2, r3, [r1], #8

12534: fac7 f452 uqsub8 r4, r7, r2

12538: f891 f080 pld [r1, #128] ; 0x80

1253c: fac7 f553 uqsub8 r5, r7, r3

12540: f890 f080 pld [r0, #128] ; 0x80

12544: 2c00 cmp r4, #0

12546: d106 bne.n 12556 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x17ee>

12548: ea46 2602 orr.w r6, r6, r2, lsl #8

1254c: ea4f 6212 mov.w r2, r2, lsr #24

12550: f840 6b04 str.w r6, [r0], #4

12554: e7e4 b.n 12520 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x17b8>

12556: f1a1 0104 sub.w r1, r1, #4

1255a: 4632 mov r2, r6

1255c: f1a1 0104 sub.w r1, r1, #4

12560: f800 2b01 strb.w r2, [r0], #1

12564: ea4f 2212 mov.w r2, r2, lsr #8

12568: e7b1 b.n 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

1256a: fac7 f452 uqsub8 r4, r7, r2

1256e: fac7 f553 uqsub8 r5, r7, r3

12572: 2c00 cmp r4, #0

12574: bf18 it ne

12576: 3908 subne r1, #8

12578: d1a9 bne.n 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

1257a: f800 2b01 strb.w r2, [r0], #1

1257e: ea4f 2212 mov.w r2, r2, lsr #8

12582: f800 2b01 strb.w r2, [r0], #1

12586: ea4f 2212 mov.w r2, r2, lsr #8

1258a: bf00 nop

1258c: f3af 8000 nop.w

12590: 2d00 cmp r5, #0

12592: d11b bne.n 125cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1864>

12594: ea42 4203 orr.w r2, r2, r3, lsl #16

12598: ea4f 4613 mov.w r6, r3, lsr #16

1259c: f840 2b04 str.w r2, [r0], #4

125a0: e8f1 2302 ldrd r2, r3, [r1], #8

125a4: fac7 f452 uqsub8 r4, r7, r2

125a8: f891 f080 pld [r1, #128] ; 0x80

125ac: fac7 f553 uqsub8 r5, r7, r3

125b0: f890 f080 pld [r0, #128] ; 0x80

125b4: 2c00 cmp r4, #0

125b6: d106 bne.n 125c6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x185e>

125b8: ea46 4602 orr.w r6, r6, r2, lsl #16

125bc: ea4f 4212 mov.w r2, r2, lsr #16

125c0: f840 6b04 str.w r6, [r0], #4

125c4: e7e4 b.n 12590 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1828>

125c6: f1a1 0104 sub.w r1, r1, #4

125ca: 4632 mov r2, r6

125cc: f1a1 0104 sub.w r1, r1, #4

125d0: f800 2b01 strb.w r2, [r0], #1

125d4: ea4f 2212 mov.w r2, r2, lsr #8

125d8: f800 2b01 strb.w r2, [r0], #1

125dc: ea4f 2212 mov.w r2, r2, lsr #8

125e0: e775 b.n 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

125e2: fac7 f452 uqsub8 r4, r7, r2

125e6: fac7 f553 uqsub8 r5, r7, r3

125ea: 2c00 cmp r4, #0

125ec: bf18 it ne

125ee: 3908 subne r1, #8

125f0: f47f af6d bne.w 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

125f4: f800 2b01 strb.w r2, [r0], #1

125f8: ea4f 2212 mov.w r2, r2, lsr #8

125fc: f3af 8000 nop.w

12600: 2d00 cmp r5, #0

12602: d11b bne.n 1263c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x18d4>

12604: ea42 6203 orr.w r2, r2, r3, lsl #24

12608: ea4f 2613 mov.w r6, r3, lsr #8

1260c: f840 2b04 str.w r2, [r0], #4

12610: e8f1 2302 ldrd r2, r3, [r1], #8

12614: fac7 f452 uqsub8 r4, r7, r2

12618: f891 f080 pld [r1, #128] ; 0x80

1261c: fac7 f553 uqsub8 r5, r7, r3

12620: f890 f080 pld [r0, #128] ; 0x80

12624: 2c00 cmp r4, #0

12626: d106 bne.n 12636 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x18ce>

12628: ea46 6602 orr.w r6, r6, r2, lsl #24

1262c: ea4f 2212 mov.w r2, r2, lsr #8

12630: f840 6b04 str.w r6, [r0], #4

12634: e7e4 b.n 12600 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1898>

12636: f1a1 0104 sub.w r1, r1, #4

1263a: 4632 mov r2, r6

1263c: f1a1 0104 sub.w r1, r1, #4

12640: f800 2b01 strb.w r2, [r0], #1

12644: ea4f 2212 mov.w r2, r2, lsr #8

12648: f800 2b01 strb.w r2, [r0], #1

1264c: ea4f 2212 mov.w r2, r2, lsr #8

12650: f800 2b01 strb.w r2, [r0], #1

12654: ea4f 2212 mov.w r2, r2, lsr #8

12658: e739 b.n 124ce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1766>

1265a: bf00 nop

1265c: b5f0 push {r4, r5, r6, r7, lr}

1265e: 4607 mov r7, r0

12660: 780c ldrb r4, [r1, #0]

12662: b0c1 sub sp, #260 ; 0x104

12664: 2c00 cmp r4, #0

12666: d04b beq.n 12700 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1998>

12668: 784b ldrb r3, [r1, #1]

1266a: 460e mov r6, r1

1266c: 2b00 cmp r3, #0

1266e: d047 beq.n 12700 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1998>

12670: 2240 movs r2, #64 ; 0x40

12672: 2100 movs r1, #0

12674: 4668 mov r0, sp

12676: f000 ecec blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

1267a: 2240 movs r2, #64 ; 0x40

1267c: 2100 movs r1, #0

1267e: 4605 mov r5, r0

12680: 4410 add r0, r2

12682: f000 ece6 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

12686: 2240 movs r2, #64 ; 0x40

12688: 2100 movs r1, #0

1268a: f105 0080 add.w r0, r5, #128 ; 0x80

1268e: f000 ece0 blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

12692: 2240 movs r2, #64 ; 0x40

12694: 2100 movs r1, #0

12696: f105 00c0 add.w r0, r5, #192 ; 0xc0

1269a: f000 ecda blx 13050 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e8>

1269e: 4631 mov r1, r6

126a0: 2201 movs r2, #1

126a2: e001 b.n 126a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1940>

126a4: f811 4f01 ldrb.w r4, [r1, #1]!

126a8: 552a strb r2, [r5, r4]

126aa: 2c00 cmp r4, #0

126ac: d1fa bne.n 126a4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x193c>

126ae: 783b ldrb r3, [r7, #0]

126b0: 4620 mov r0, r4

126b2: 5ceb ldrb r3, [r5, r3]

126b4: b9f3 cbnz r3, 126f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x198c>

126b6: 787b ldrb r3, [r7, #1]

126b8: 2001 movs r0, #1

126ba: 5ceb ldrb r3, [r5, r3]

126bc: b9d3 cbnz r3, 126f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x198c>

126be: 78bb ldrb r3, [r7, #2]

126c0: 2002 movs r0, #2

126c2: 5ceb ldrb r3, [r5, r3]

126c4: b9b3 cbnz r3, 126f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x198c>

126c6: 78fb ldrb r3, [r7, #3]

126c8: 2003 movs r0, #3

126ca: 5ceb ldrb r3, [r5, r3]

126cc: b993 cbnz r3, 126f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x198c>

126ce: f027 0303 bic.w r3, r7, #3

126d2: f813 4f04 ldrb.w r4, [r3, #4]!

126d6: 7898 ldrb r0, [r3, #2]

126d8: 78d9 ldrb r1, [r3, #3]

126da: 785a ldrb r2, [r3, #1]

126dc: 5d2c ldrb r4, [r5, r4]

126de: 5c28 ldrb r0, [r5, r0]

126e0: 5c69 ldrb r1, [r5, r1]

126e2: 5caa ldrb r2, [r5, r2]

126e4: 4301 orrs r1, r0

126e6: 4322 orrs r2, r4

126e8: 4311 orrs r1, r2

126ea: d0f2 beq.n 126d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x196a>

126ec: 1bdb subs r3, r3, r7

126ee: b11a cbz r2, 126f8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1990>

126f0: 1b1b subs r3, r3, r4

126f2: 1c58 adds r0, r3, #1

126f4: b041 add sp, #260 ; 0x104

126f6: bdf0 pop {r4, r5, r6, r7, pc}

126f8: 1a1b subs r3, r3, r0

126fa: 1cd8 adds r0, r3, #3

126fc: b041 add sp, #260 ; 0x104

126fe: bdf0 pop {r4, r5, r6, r7, pc}

12700: 4621 mov r1, r4

12702: 4638 mov r0, r7

12704: f000 ff48 bl 13598 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2830>

12708: 1bc0 subs r0, r0, r7

1270a: b041 add sp, #260 ; 0x104

1270c: bdf0 pop {r4, r5, r6, r7, pc}

1270e: bf00 nop

12710: b538 push {r3, r4, r5, lr}

12712: 4605 mov r5, r0

12714: f000 f814 bl 12740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19d8>

12718: 1c44 adds r4, r0, #1

1271a: 4620 mov r0, r4

1271c: f7ee e956 blx 9cc <malloc@plt+0x4>

12720: b128 cbz r0, 1272e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19c6>

12722: 4622 mov r2, r4

12724: 4629 mov r1, r5

12726: e8bd 4038 ldmia.w sp!, {r3, r4, r5, lr}

1272a: f001 bb21 b.w 13d70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x3008>

1272e: bd38 pop {r3, r4, r5, pc}

...

12740: f890 f000 pld [r0]

12744: e96d 4502 strd r4, r5, [sp, #-8]!

12748: f020 0107 bic.w r1, r0, #7

1274c: f06f 0c00 mvn.w ip, #0

12750: f010 0407 ands.w r4, r0, #7

12754: f891 f020 pld [r1, #32]

12758: f040 8049 bne.w 127ee <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a86>

1275c: f04f 0400 mov.w r4, #0

12760: f06f 0007 mvn.w r0, #7

12764: e9d1 2300 ldrd r2, r3, [r1]

12768: f891 f040 pld [r1, #64] ; 0x40

1276c: f100 0008 add.w r0, r0, #8

12770: fa82 f24c uadd8 r2, r2, ip

12774: faa4 f28c sel r2, r4, ip

12778: fa83 f34c uadd8 r3, r3, ip

1277c: faa2 f38c sel r3, r2, ip

12780: bb4b cbnz r3, 127d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a6e>

12782: e9d1 2302 ldrd r2, r3, [r1, #8]

12786: fa82 f24c uadd8 r2, r2, ip

1278a: f100 0008 add.w r0, r0, #8

1278e: faa4 f28c sel r2, r4, ip

12792: fa83 f34c uadd8 r3, r3, ip

12796: faa2 f38c sel r3, r2, ip

1279a: b9e3 cbnz r3, 127d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a6e>

1279c: e9d1 2304 ldrd r2, r3, [r1, #16]

127a0: fa82 f24c uadd8 r2, r2, ip

127a4: f100 0008 add.w r0, r0, #8

127a8: faa4 f28c sel r2, r4, ip

127ac: fa83 f34c uadd8 r3, r3, ip

127b0: faa2 f38c sel r3, r2, ip

127b4: b97b cbnz r3, 127d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a6e>

127b6: e9d1 2306 ldrd r2, r3, [r1, #24]

127ba: f101 0120 add.w r1, r1, #32

127be: fa82 f24c uadd8 r2, r2, ip

127c2: f100 0008 add.w r0, r0, #8

127c6: faa4 f28c sel r2, r4, ip

127ca: fa83 f34c uadd8 r3, r3, ip

127ce: faa2 f38c sel r3, r2, ip

127d2: 2b00 cmp r3, #0

127d4: d0c6 beq.n 12764 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x19fc>

127d6: 2a00 cmp r2, #0

127d8: bf04 itt eq

127da: 3004 addeq r0, #4

127dc: 461a moveq r2, r3

127de: ba12 rev r2, r2

127e0: fab2 f282 clz r2, r2

127e4: e8fd 4502 ldrd r4, r5, [sp], #8

127e8: eb00 00d2 add.w r0, r0, r2, lsr #3

127ec: 4770 bx lr

127ee: e9d1 2300 ldrd r2, r3, [r1]

127f2: f004 0503 and.w r5, r4, #3

127f6: f1c4 0000 rsb r0, r4, #0

127fa: ea4f 05c5 mov.w r5, r5, lsl #3

127fe: f014 0f04 tst.w r4, #4

12802: f891 f040 pld [r1, #64] ; 0x40

12806: fa0c f505 lsl.w r5, ip, r5

1280a: ea62 0205 orn r2, r2, r5

1280e: bf1c itt ne

12810: ea63 0305 ornne r3, r3, r5

12814: 4662 movne r2, ip

12816: f04f 0400 mov.w r4, #0

1281a: e7a9 b.n 12770 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a08>

1281c: b379 cbz r1, 1287e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b16>

1281e: 1841 adds r1, r0, r1

12820: b410 push {r4}

12822: bf28 it cs

12824: f04f 31ff movcs.w r1, #4294967295 ; 0xffffffff

12828: 0783 lsls r3, r0, #30

1282a: d047 beq.n 128bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b54>

1282c: 7803 ldrb r3, [r0, #0]

1282e: b343 cbz r3, 12882 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b1a>

12830: 1c43 adds r3, r0, #1

12832: e001 b.n 12838 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ad0>

12834: 7814 ldrb r4, [r2, #0]

12836: b32c cbz r4, 12884 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b1c>

12838: f013 0f03 tst.w r3, #3

1283c: 461a mov r2, r3

1283e: f103 0301 add.w r3, r3, #1

12842: d1f7 bne.n 12834 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1acc>

12844: 4291 cmp r1, r2

12846: d926 bls.n 12896 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b2e>

12848: 4613 mov r3, r2

1284a: f853 4b04 ldr.w r4, [r3], #4

1284e: f1a4 3401 sub.w r4, r4, #16843009 ; 0x1010101

12852: f014 3f80 tst.w r4, #2155905152 ; 0x80808080

12856: d00b beq.n 12870 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b08>

12858: f813 4c04 ldrb.w r4, [r3, #-4]

1285c: b1dc cbz r4, 12896 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b2e>

1285e: f813 4c03 ldrb.w r4, [r3, #-3]

12862: b1e4 cbz r4, 1289e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b36>

12864: f813 4c02 ldrb.w r4, [r3, #-2]

12868: b1f4 cbz r4, 128a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b40>

1286a: f813 4c01 ldrb.w r4, [r3, #-1]

1286e: b304 cbz r4, 128b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b4a>

12870: 4299 cmp r1, r3

12872: 461a mov r2, r3

12874: d8e8 bhi.n 12848 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ae0>

12876: 1a08 subs r0, r1, r0

12878: f85d 4b04 ldr.w r4, [sp], #4

1287c: 4770 bx lr

1287e: 4608 mov r0, r1

12880: 4770 bx lr

12882: 4602 mov r2, r0

12884: 4291 cmp r1, r2

12886: bf94 ite ls

12888: ebc0 0001 rsbls r0, r0, r1

1288c: ebc0 0002 rsbhi r0, r0, r2

12890: f85d 4b04 ldr.w r4, [sp], #4

12894: 4770 bx lr

12896: 4291 cmp r1, r2

12898: bf28 it cs

1289a: 4611 movcs r1, r2

1289c: e7eb b.n 12876 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b0e>

1289e: 1c53 adds r3, r2, #1

128a0: 4299 cmp r1, r3

128a2: bf28 it cs

128a4: 4619 movcs r1, r3

128a6: e7e6 b.n 12876 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b0e>

128a8: 1c93 adds r3, r2, #2

128aa: 4299 cmp r1, r3

128ac: bf28 it cs

128ae: 4619 movcs r1, r3

128b0: e7e1 b.n 12876 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b0e>

128b2: 1cd3 adds r3, r2, #3

128b4: 4299 cmp r1, r3

128b6: bf28 it cs

128b8: 4619 movcs r1, r3

128ba: e7dc b.n 12876 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b0e>

128bc: 4602 mov r2, r0

128be: e7c1 b.n 12844 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1adc>

128c0: 2a03 cmp r2, #3

128c2: b4f0 push {r4, r5, r6, r7}

128c4: d944 bls.n 12950 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1be8>

128c6: 1d04 adds r4, r0, #4

128c8: f022 0703 bic.w r7, r2, #3

128cc: 1d0d adds r5, r1, #4

128ce: 4427 add r7, r4

128d0: e02b b.n 1292a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1bc2>

128d2: f814 3c03 ldrb.w r3, [r4, #-3]

128d6: f815 cc03 ldrb.w ip, [r5, #-3]

128da: ebb3 060c subs.w r6, r3, ip

128de: bf18 it ne

128e0: 2601 movne r6, #1

128e2: 2b00 cmp r3, #0

128e4: bf08 it eq

128e6: 2601 moveq r6, #1

128e8: bb76 cbnz r6, 12948 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1be0>

128ea: f814 3c02 ldrb.w r3, [r4, #-2]

128ee: f815 cc02 ldrb.w ip, [r5, #-2]

128f2: ebb3 060c subs.w r6, r3, ip

128f6: bf18 it ne

128f8: 2601 movne r6, #1

128fa: 2b00 cmp r3, #0

128fc: bf08 it eq

128fe: 2601 moveq r6, #1

12900: bb16 cbnz r6, 12948 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1be0>

12902: f814 3c01 ldrb.w r3, [r4, #-1]

12906: 3504 adds r5, #4

12908: f815 6c05 ldrb.w r6, [r5, #-5]

1290c: 3404 adds r4, #4

1290e: ebb3 0c06 subs.w ip, r3, r6

12912: bf18 it ne

12914: f04f 0c01 movne.w ip, #1

12918: 2b00 cmp r3, #0

1291a: bf08 it eq

1291c: f04f 0c01 moveq.w ip, #1

12920: f1bc 0f00 cmp.w ip, #0

12924: d132 bne.n 1298c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c24>

12926: 42bc cmp r4, r7

12928: d033 beq.n 12992 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c2a>

1292a: f814 3c04 ldrb.w r3, [r4, #-4]

1292e: 4620 mov r0, r4

12930: f815 cc04 ldrb.w ip, [r5, #-4]

12934: 4629 mov r1, r5

12936: ebb3 060c subs.w r6, r3, ip

1293a: bf18 it ne

1293c: 2601 movne r6, #1

1293e: 2b00 cmp r3, #0

12940: bf08 it eq

12942: 2601 moveq r6, #1

12944: 2e00 cmp r6, #0

12946: d0c4 beq.n 128d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b6a>

12948: eba3 000c sub.w r0, r3, ip

1294c: bcf0 pop {r4, r5, r6, r7}

1294e: 4770 bx lr

12950: 2600 movs r6, #0

12952: 4633 mov r3, r6

12954: b1d2 cbz r2, 1298c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c24>

12956: 4605 mov r5, r0

12958: f811 6b01 ldrb.w r6, [r1], #1

1295c: f815 3b01 ldrb.w r3, [r5], #1

12960: 4410 add r0, r2

12962: 1af4 subs r4, r6, r3

12964: bf18 it ne

12966: 2401 movne r4, #1

12968: 2b00 cmp r3, #0

1296a: bf08 it eq

1296c: 2401 moveq r4, #1

1296e: b15c cbz r4, 12988 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c20>

12970: e00c b.n 1298c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c24>

12972: f815 3b01 ldrb.w r3, [r5], #1

12976: f811 6b01 ldrb.w r6, [r1], #1

1297a: 1b9c subs r4, r3, r6

1297c: bf18 it ne

1297e: 2401 movne r4, #1

12980: 2b00 cmp r3, #0

12982: bf08 it eq

12984: 2401 moveq r4, #1

12986: b90c cbnz r4, 1298c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c24>

12988: 4285 cmp r5, r0

1298a: d1f2 bne.n 12972 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c0a>

1298c: 1b98 subs r0, r3, r6

1298e: bcf0 pop {r4, r5, r6, r7}

12990: 4770 bx lr

12992: f002 0203 and.w r2, r2, #3

12996: e7dd b.n 12954 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1bec>

...

129a0: f001 01ff and.w r1, r1, #255 ; 0xff

129a4: 2a10 cmp r2, #16

129a6: db2a blt.n 129fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c96>

129a8: f010 0f07 tst.w r0, #7

129ac: d007 beq.n 129be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c56>

129ae: f810 3b01 ldrb.w r3, [r0], #1

129b2: 3a01 subs r2, #1

129b4: 428b cmp r3, r1

129b6: d02c beq.n 12a12 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1caa>

129b8: f010 0f07 tst.w r0, #7

129bc: d1f7 bne.n 129ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c46>

129be: b4f0 push {r4, r5, r6, r7}

129c0: ea41 2101 orr.w r1, r1, r1, lsl #8

129c4: ea41 4101 orr.w r1, r1, r1, lsl #16

129c8: f022 0607 bic.w r6, r2, #7

129cc: f07f 0700 mvns.w r7, #0

129d0: 2300 movs r3, #0

129d2: e8f0 4502 ldrd r4, r5, [r0], #8

129d6: 3e08 subs r6, #8

129d8: ea84 0401 eor.w r4, r4, r1

129dc: ea85 0501 eor.w r5, r5, r1

129e0: fa84 f447 uadd8 r4, r4, r7

129e4: faa3 f487 sel r4, r3, r7

129e8: fa85 f547 uadd8 r5, r5, r7

129ec: faa4 f587 sel r5, r4, r7

129f0: b98d cbnz r5, 12a16 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cae>

129f2: d1ee bne.n 129d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c6a>

129f4: bcf0 pop {r4, r5, r6, r7}

129f6: f001 01ff and.w r1, r1, #255 ; 0xff

129fa: f002 0207 and.w r2, r2, #7

129fe: b132 cbz r2, 12a0e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ca6>

12a00: f810 3b01 ldrb.w r3, [r0], #1

12a04: 3a01 subs r2, #1

12a06: ea83 0301 eor.w r3, r3, r1

12a0a: b113 cbz r3, 12a12 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1caa>

12a0c: d1f8 bne.n 12a00 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c98>

12a0e: 2000 movs r0, #0

12a10: 4770 bx lr

12a12: 3801 subs r0, #1

12a14: 4770 bx lr

12a16: 2c00 cmp r4, #0

12a18: bf06 itte eq

12a1a: 462c moveq r4, r5

12a1c: 3803 subeq r0, #3

12a1e: 3807 subne r0, #7

12a20: f014 0f01 tst.w r4, #1

12a24: d107 bne.n 12a36 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cce>

12a26: 3001 adds r0, #1

12a28: f414 7f80 tst.w r4, #256 ; 0x100

12a2c: bf02 ittt eq

12a2e: 3001 addeq r0, #1

12a30: f414 3fc0 tsteq.w r4, #98304 ; 0x18000

12a34: 3001 addeq r0, #1

12a36: bcf0 pop {r4, r5, r6, r7}

12a38: 3801 subs r0, #1

12a3a: 4770 bx lr

12a3c: 2a0f cmp r2, #15

12a3e: 460b mov r3, r1

12a40: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

12a44: 4605 mov r5, r0

12a46: b083 sub sp, #12

12a48: d946 bls.n 12ad8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d70>

12a4a: 078c lsls r4, r1, #30

12a4c: f000 80af beq.w 12bae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e46>

12a50: 7800 ldrb r0, [r0, #0]

12a52: 3501 adds r5, #1

12a54: 780c ldrb r4, [r1, #0]

12a56: 3301 adds r3, #1

12a58: 1b00 subs r0, r0, r4

12a5a: d134 bne.n 12ac6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5e>

12a5c: 188c adds r4, r1, r2

12a5e: e005 b.n 12a6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d04>

12a60: 7832 ldrb r2, [r6, #0]

12a62: 3301 adds r3, #1

12a64: f815 0b01 ldrb.w r0, [r5], #1

12a68: 1a80 subs r0, r0, r2

12a6a: d12c bne.n 12ac6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5e>

12a6c: 079a lsls r2, r3, #30

12a6e: 461e mov r6, r3

12a70: eba4 0003 sub.w r0, r4, r3

12a74: 4619 mov r1, r3

12a76: d1f3 bne.n 12a60 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1cf8>

12a78: f015 0c03 ands.w ip, r5, #3

12a7c: ea4f 0490 mov.w r4, r0, lsr #2

12a80: 680f ldr r7, [r1, #0]

12a82: f004 0103 and.w r1, r4, #3

12a86: d13f bne.n 12b08 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1da0>

12a88: 2901 cmp r1, #1

12a8a: f8d5 c000 ldr.w ip, [r5]

12a8e: f000 80df beq.w 12c50 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ee8>

12a92: 2900 cmp r1, #0

12a94: f000 809a beq.w 12bcc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e64>

12a98: 2903 cmp r1, #3

12a9a: f000 80aa beq.w 12bf2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e8a>

12a9e: 3402 adds r4, #2

12aa0: f1a5 0208 sub.w r2, r5, #8

12aa4: f1a6 0308 sub.w r3, r6, #8

12aa8: 45bc cmp ip, r7

12aaa: f000 80f4 beq.w 12c96 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f2e>

12aae: f10d 0203 add.w r2, sp, #3

12ab2: 4669 mov r1, sp

12ab4: e9cd 7c00 strd r7, ip, [sp]

12ab8: f812 3f01 ldrb.w r3, [r2, #1]!

12abc: f811 0b01 ldrb.w r0, [r1], #1

12ac0: 4283 cmp r3, r0

12ac2: d0f9 beq.n 12ab8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d50>

12ac4: 1a18 subs r0, r3, r0

12ac6: b003 add sp, #12

12ac8: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

12acc: f020 0303 bic.w r3, r0, #3

12ad0: f000 0203 and.w r2, r0, #3

12ad4: 441d add r5, r3

12ad6: 4433 add r3, r6

12ad8: 2a00 cmp r2, #0

12ada: f000 80a1 beq.w 12c20 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1eb8>

12ade: 4629 mov r1, r5

12ae0: 781c ldrb r4, [r3, #0]

12ae2: f811 0b01 ldrb.w r0, [r1], #1

12ae6: 1b00 subs r0, r0, r4

12ae8: bf04 itt eq

12aea: 1954 addeq r4, r2, r5

12aec: 1b5b subeq r3, r3, r5

12aee: d005 beq.n 12afc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d94>

12af0: e7e9 b.n 12ac6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5e>

12af2: 7808 ldrb r0, [r1, #0]

12af4: 5c5a ldrb r2, [r3, r1]

12af6: 3101 adds r1, #1

12af8: 1a80 subs r0, r0, r2

12afa: d1e4 bne.n 12ac6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5e>

12afc: 428c cmp r4, r1

12afe: d1f8 bne.n 12af2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d8a>

12b00: 2000 movs r0, #0

12b02: b003 add sp, #12

12b04: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

12b08: f025 0e03 bic.w lr, r5, #3

12b0c: ea4f 0ccc mov.w ip, ip, lsl #3

12b10: f1cc 0920 rsb r9, ip, #32

12b14: 2901 cmp r1, #1

12b16: e9de 2800 ldrd r2, r8, [lr]

12b1a: f10e 0a04 add.w sl, lr, #4

12b1e: fa22 f20c lsr.w r2, r2, ip

12b22: fa08 fb09 lsl.w fp, r8, r9

12b26: ea42 020b orr.w r2, r2, fp

12b2a: d07b beq.n 12c24 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ebc>

12b2c: b1f1 cbz r1, 12b6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e04>

12b2e: 2903 cmp r1, #3

12b30: f000 80a2 beq.w 12c78 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f10>

12b34: f1ae 0e04 sub.w lr, lr, #4

12b38: 3402 adds r4, #2

12b3a: f1a6 0308 sub.w r3, r6, #8

12b3e: 4297 cmp r7, r2

12b40: f000 80c0 beq.w 12cc4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f5c>

12b44: f10d 0103 add.w r1, sp, #3

12b48: 466c mov r4, sp

12b4a: e9cd 7200 strd r7, r2, [sp]

12b4e: f811 3f01 ldrb.w r3, [r1, #1]!

12b52: f814 0b01 ldrb.w r0, [r4], #1

12b56: 4283 cmp r3, r0

12b58: d0f9 beq.n 12b4e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1de6>

12b5a: e7b3 b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12b5c: fa28 f80c lsr.w r8, r8, ip

12b60: fa01 f209 lsl.w r2, r1, r9

12b64: ea48 0202 orr.w r2, r8, r2

12b68: 4677 mov r7, lr

12b6a: 4688 mov r8, r1

12b6c: 4297 cmp r7, r2

12b6e: f8da 1004 ldr.w r1, [sl, #4]

12b72: f8d3 b004 ldr.w fp, [r3, #4]

12b76: d11d bne.n 12bb4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e4c>

12b78: fa28 f80c lsr.w r8, r8, ip

12b7c: fa01 f209 lsl.w r2, r1, r9

12b80: ea48 0202 orr.w r2, r8, r2

12b84: 46d6 mov lr, sl

12b86: 465f mov r7, fp

12b88: 4688 mov r8, r1

12b8a: 4297 cmp r7, r2

12b8c: f8de 1008 ldr.w r1, [lr, #8]

12b90: f8d3 a008 ldr.w sl, [r3, #8]

12b94: d073 beq.n 12c7e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f16>

12b96: f10d 0103 add.w r1, sp, #3

12b9a: 466c mov r4, sp

12b9c: e9cd 7200 strd r7, r2, [sp]

12ba0: f811 3f01 ldrb.w r3, [r1, #1]!

12ba4: f814 0b01 ldrb.w r0, [r4], #1

12ba8: 4283 cmp r3, r0

12baa: d0f9 beq.n 12ba0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e38>

12bac: e78a b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12bae: 4610 mov r0, r2

12bb0: 460e mov r6, r1

12bb2: e761 b.n 12a78 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d10>

12bb4: f10d 0103 add.w r1, sp, #3

12bb8: 466c mov r4, sp

12bba: e9cd 7200 strd r7, r2, [sp]

12bbe: f811 3f01 ldrb.w r3, [r1, #1]!

12bc2: f814 0b01 ldrb.w r0, [r4], #1

12bc6: 4283 cmp r3, r0

12bc8: d0f9 beq.n 12bbe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e56>

12bca: e77b b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12bcc: 462a mov r2, r5

12bce: 45bc cmp ip, r7

12bd0: f8d2 8004 ldr.w r8, [r2, #4]

12bd4: f8d3 e004 ldr.w lr, [r3, #4]

12bd8: d010 beq.n 12bfc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e94>

12bda: f10d 0203 add.w r2, sp, #3

12bde: 4669 mov r1, sp

12be0: e9cd 7c00 strd r7, ip, [sp]

12be4: f812 3f01 ldrb.w r3, [r2, #1]!

12be8: f811 0b01 ldrb.w r0, [r1], #1

12bec: 4283 cmp r3, r0

12bee: d0f9 beq.n 12be4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e7c>

12bf0: e768 b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12bf2: 3401 adds r4, #1

12bf4: 1f2a subs r2, r5, #4

12bf6: 1f33 subs r3, r6, #4

12bf8: 46be mov lr, r7

12bfa: 46e0 mov r8, ip

12bfc: 45f0 cmp r8, lr

12bfe: f8d2 c008 ldr.w ip, [r2, #8]

12c02: 689f ldr r7, [r3, #8]

12c04: f43f af50 beq.w 12aa8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d40>

12c08: f10d 0203 add.w r2, sp, #3

12c0c: 4669 mov r1, sp

12c0e: e9cd e800 strd lr, r8, [sp]

12c12: f812 3f01 ldrb.w r3, [r2, #1]!

12c16: f811 0b01 ldrb.w r0, [r1], #1

12c1a: 4283 cmp r3, r0

12c1c: d0f9 beq.n 12c12 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1eaa>

12c1e: e751 b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12c20: 4610 mov r0, r2

12c22: e750 b.n 12ac6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5e>

12c24: f10e 0a08 add.w sl, lr, #8

12c28: 3c01 subs r4, #1

12c2a: 1d33 adds r3, r6, #4

12c2c: 4297 cmp r7, r2

12c2e: f8da 1000 ldr.w r1, [sl]

12c32: f8d3 e000 ldr.w lr, [r3]

12c36: d091 beq.n 12b5c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1df4>

12c38: f10d 0103 add.w r1, sp, #3

12c3c: 466c mov r4, sp

12c3e: e9cd 7200 strd r7, r2, [sp]

12c42: f811 3f01 ldrb.w r3, [r1, #1]!

12c46: f814 0b01 ldrb.w r0, [r4], #1

12c4a: 4283 cmp r3, r0

12c4c: d0f9 beq.n 12c42 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1eda>

12c4e: e739 b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12c50: 3c01 subs r4, #1

12c52: 1d2a adds r2, r5, #4

12c54: 1d33 adds r3, r6, #4

12c56: 45bc cmp ip, r7

12c58: 6811 ldr r1, [r2, #0]

12c5a: f8d3 e000 ldr.w lr, [r3]

12c5e: d017 beq.n 12c90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f28>

12c60: f10d 0203 add.w r2, sp, #3

12c64: 4669 mov r1, sp

12c66: e9cd 7c00 strd r7, ip, [sp]

12c6a: f812 3f01 ldrb.w r3, [r2, #1]!

12c6e: f811 0b01 ldrb.w r0, [r1], #1

12c72: 4283 cmp r3, r0

12c74: d0f9 beq.n 12c6a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f02>

12c76: e725 b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12c78: 3401 adds r4, #1

12c7a: 1f33 subs r3, r6, #4

12c7c: e785 b.n 12b8a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e22>

12c7e: fa28 f80c lsr.w r8, r8, ip

12c82: fa01 f209 lsl.w r2, r1, r9

12c86: ea48 0202 orr.w r2, r8, r2

12c8a: 4657 mov r7, sl

12c8c: 4688 mov r8, r1

12c8e: e756 b.n 12b3e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1dd6>

12c90: 4677 mov r7, lr

12c92: 468c mov ip, r1

12c94: e79b b.n 12bce <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e66>

12c96: 3c04 subs r4, #4

12c98: f8d2 c00c ldr.w ip, [r2, #12]

12c9c: 68df ldr r7, [r3, #12]

12c9e: bf1c itt ne

12ca0: 3210 addne r2, #16

12ca2: 3310 addne r3, #16

12ca4: d1d7 bne.n 12c56 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1eee>

12ca6: 45bc cmp ip, r7

12ca8: f43f af10 beq.w 12acc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d64>

12cac: f10d 0203 add.w r2, sp, #3

12cb0: 4669 mov r1, sp

12cb2: e9cd 7c00 strd r7, ip, [sp]

12cb6: f812 3f01 ldrb.w r3, [r2, #1]!

12cba: f811 0b01 ldrb.w r0, [r1], #1

12cbe: 4283 cmp r3, r0

12cc0: d0f9 beq.n 12cb6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f4e>

12cc2: e6ff b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12cc4: f8de 100c ldr.w r1, [lr, #12]

12cc8: 3c04 subs r4, #4

12cca: fa28 f80c lsr.w r8, r8, ip

12cce: 68df ldr r7, [r3, #12]

12cd0: fa01 f209 lsl.w r2, r1, r9

12cd4: ea48 0202 orr.w r2, r8, r2

12cd8: d004 beq.n 12ce4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f7c>

12cda: f10e 0a10 add.w sl, lr, #16

12cde: 3310 adds r3, #16

12ce0: 4688 mov r8, r1

12ce2: e7a3 b.n 12c2c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ec4>

12ce4: 4297 cmp r7, r2

12ce6: f43f aef1 beq.w 12acc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d64>

12cea: f10d 0103 add.w r1, sp, #3

12cee: 466c mov r4, sp

12cf0: e9cd 7200 strd r7, r2, [sp]

12cf4: f811 3f01 ldrb.w r3, [r1, #1]!

12cf8: f814 0b01 ldrb.w r0, [r4], #1

12cfc: 4283 cmp r3, r0

12cfe: d0f9 beq.n 12cf4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f8c>

12d00: e6e0 b.n 12ac4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d5c>

12d02: bf00 nop

...

12d10: c001 stmia r0!, {r0}

12d12: e050 b.n 12db6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x204e>

12d14: 000c movs r4, r1

12d16: 8152 strh r2, [r2, #10]

12d18: 00f8 lsls r0, r7, #3

12d1a: 9a00 ldr r2, [sp, #0]

12d1c: 4011 ands r1, r2

12d1e: e92d 1002 stmdb sp!, {r1, ip}

12d22: e081 b.n 12e28 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x20c0>

12d24: 0002 movs r2, r0

12d26: e080 b.n 12e2a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x20c2>

12d28: 2004 movs r0, #4

12d2a: e252 b.n 131d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x246a>

12d2c: 002b movs r3, r5

12d2e: ba00 rev r0, r0

12d30: c003 stmia r0!, {r0, r1}

12d32: e210 b.n 13156 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x23ee>

12d34: f004 f551 bl 4177da <\_r\_debug@@GLIBC\_2.4+0x3ede7e>

12d38: 0030 movs r0, r6

12d3a: 1a00 subs r0, r0, r0

12d3c: c003 stmia r0!, {r0, r1}

12d3e: e211 b.n 13164 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x23fc>

12d40: 0039 movs r1, r7

12d42: 1a00 subs r0, r0, r0

12d44: 201c movs r0, #28

12d46: e252 b.n 131ee <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2486>

12d48: 01e0 lsls r0, r4, #7

12d4a: e92d 000c stmdb sp!, {r2, r3}

12d4e: ba00 rev r0, r0

12d50: f004 f551 bl 4177f6 <\_r\_debug@@GLIBC\_2.4+0x3ede9a>

12d54: 2060 movs r0, #96 ; 0x60

12d56: e252 b.n 131fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2496>

12d58: f020 f551 bl 4337fe <\_r\_debug@@GLIBC\_2.4+0x409ea2>

12d5c: 0002 movs r2, r0

12d5e: ba00 rev r0, r0

12d60: f040 f551 bl 453806 <\_r\_debug@@GLIBC\_2.4+0x429eaa>

12d64: f060 f551 bl 47380a <\_r\_debug@@GLIBC\_2.4+0x449eae>

12d68: f080 f551 bl 49380e <\_r\_debug@@GLIBC\_2.4+0x469eb2>

12d6c: 51f8 str r0, [r7, r7]

12d6e: e931 2020 ldmdb r1!, {r5, sp}

12d72: e252 b.n 1321a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24b2>

12d74: 51f8 str r0, [r7, r7]

12d76: e920 fffa stmdb r0!, {r1, r3, r4, r5, r6, r7, r8, r9, sl, fp, ip, sp, lr, pc}

12d7a: aaff add r2, sp, #1020 ; 0x3fc

12d7c: 0060 lsls r0, r4, #1

12d7e: e372 b.n 13466 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26fe>

12d80: fff9 aaff ; <UNDEFINED> instruction: 0xfff9aaff

12d84: c01c stmia r0!, {r2, r3, r4}

12d86: e212 b.n 131ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2446>

12d88: c020 stmia r0!, {r5}

12d8a: e26c b.n 13266 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24fe>

12d8c: f00c 108f and.w r0, ip, #9371791 ; 0x8f008f

12d90: 0011 movs r1, r2

12d92: ea00 f000 ; <UNDEFINED> instruction: 0xea00f000

12d96: e320 b.n 133da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2672>

12d98: 3004 adds r0, #4

12d9a: e531 b.n 12800 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a98>

12d9c: 4004 ands r4, r0

12d9e: e531 b.n 12804 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a9c>

12da0: 5004 str r4, [r0, r0]

12da2: e531 b.n 12808 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aa0>

12da4: 6004 str r4, [r0, #0]

12da6: e531 b.n 1280c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aa4>

12da8: 7004 strb r4, [r0, #0]

12daa: e531 b.n 12810 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aa8>

12dac: 8004 strh r4, [r0, #0]

12dae: e531 b.n 12814 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aac>

12db0: e004 b.n 12dbc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2054>

12db2: e531 b.n 12818 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ab0>

12db4: f00c e08f ; <UNDEFINED> instruction: 0xf00ce08f

12db8: f000 e320 blx 4133fc <\_r\_debug@@GLIBC\_2.4+0x3e9aa0>

12dbc: f000 e320 blx 413400 <\_r\_debug@@GLIBC\_2.4+0x3e9aa4>

12dc0: 3004 adds r0, #4

12dc2: e520 b.n 12806 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1a9e>

12dc4: 4004 ands r4, r0

12dc6: e520 b.n 1280a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aa2>

12dc8: 5004 str r4, [r0, r0]

12dca: e520 b.n 1280e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aa6>

12dcc: 6004 str r4, [r0, #0]

12dce: e520 b.n 12812 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aaa>

12dd0: 7004 strb r4, [r0, #0]

12dd2: e520 b.n 12816 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1aae>

12dd4: 8004 strh r4, [r0, #0]

12dd6: e520 b.n 1281a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ab2>

12dd8: e004 b.n 12de4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x207c>

12dda: e520 b.n 1281e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ab6>

12ddc: 01e0 lsls r0, r4, #7

12dde: e8bd 2f82 ldmia.w sp!, {r1, r7, r8, r9, sl, fp, sp}

12de2: e1b0 b.n 13146 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x23de>

12de4: 3001 adds r0, #1

12de6: 1571 asrs r1, r6, #21

12de8: 4001 ands r1, r0

12dea: 2571 movs r5, #113 ; 0x71

12dec: c001 stmia r0!, {r0}

12dee: 2551 movs r5, #81 ; 0x51

12df0: 3001 adds r0, #1

12df2: 1560 asrs r0, r4, #21

12df4: 4001 ands r1, r0

12df6: 2560 movs r5, #96 ; 0x60

12df8: c001 stmia r0!, {r0}

12dfa: 2540 movs r5, #64 ; 0x40

12dfc: 8011 strh r1, [r2, #0]

12dfe: e8bd 0002 ldmia.w sp!, {r1}

12e02: e35c b.n 134be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2756>

12e04: 3001 adds r0, #1

12e06: c571 stmia r5!, {r0, r4, r5, r6}

12e08: 4001 ands r1, r0

12e0a: a571 add r5, pc, #452 ; (adr r5, 12fd0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2268>)

12e0c: e001 b.n 12e12 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x20aa>

12e0e: e571 b.n 128f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b8c>

12e10: 3001 adds r0, #1

12e12: c560 stmia r5!, {r5, r6}

12e14: 4001 ands r1, r0

12e16: a560 add r5, pc, #384 ; (adr r5, 12f98 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2230>)

12e18: 200c movs r0, #12

12e1a: e052 b.n 12ec2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x215a>

12e1c: e001 b.n 12e22 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x20ba>

12e1e: e560 b.n 128e2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b7a>

12e20: ffee baff ; <UNDEFINED> instruction: 0xffeebaff

12e24: c003 stmia r0!, {r0, r1}

12e26: e211 b.n 1324c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24e4>

12e28: ffc5 0aff ; <UNDEFINED> instruction: 0xffc50aff

12e2c: 1003 asrs r3, r0, #32

12e2e: e3c1 b.n 135b4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x284c>

12e30: 0002 movs r2, r0

12e32: e35c b.n 134ee <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2786>

12e34: 3000 adds r0, #0

12e36: e591 b.n 1295c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1bf4>

12e38: 002c movs r4, r5

12e3a: 0a00 lsrs r0, r0, #8

12e3c: 0057 lsls r7, r2, #1

12e3e: ba00 rev r0, r0

12e40: 201c movs r0, #28

12e42: e252 b.n 132ea <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2582>

12e44: 001f movs r7, r3

12e46: ba00 rev r0, r0

12e48: 05e0 lsls r0, r4, #23

12e4a: e92d f004 stmdb sp!, {r2, ip, sp, lr, pc}

12e4e: f551 2060 adcs.w r0, r1, #917504 ; 0xe0000

12e52: e252 b.n 132fa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2592>

12e54: f020 f551 bl 4338fa <\_r\_debug@@GLIBC\_2.4+0x409f9e>

12e58: 0002 movs r2, r0

12e5a: ba00 rev r0, r0

12e5c: f040 f551 bl 453902 <\_r\_debug@@GLIBC\_2.4+0x429fa6>

12e60: f060 f551 bl 473906 <\_r\_debug@@GLIBC\_2.4+0x449faa>

12e64: f080 f551 bl 49390a <\_r\_debug@@GLIBC\_2.4+0x469fae>

12e68: 1580 asrs r0, r0, #22

12e6a: e931 e403 ldmdb r1!, {r0, r1, sl, sp, lr, pc}

12e6e: e1a0 b.n 131b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x244a>

12e70: 2020 movs r0, #32

12e72: e252 b.n 1331a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25b2>

12e74: 0078 lsls r0, r7, #1

12e76: e931 ec2c ldmdb r1!, {r2, r3, r5, sl, fp, sp, lr, pc}

12e7a: e18e b.n 1319a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2432>

12e7c: c40c stmia r4!, {r2, r3}

12e7e: e1a0 b.n 131c2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x245a>

12e80: cc2a ldmia r4!, {r1, r3, r5}

12e82: e18c b.n 1319e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2436>

12e84: a40a add r4, pc, #40 ; (adr r4, 12eb0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2148>)

12e86: e1a0 b.n 131ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2462>

12e88: ac28 add r4, sp, #160 ; 0xa0

12e8a: e18a b.n 131a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x243a>

12e8c: 8408 strh r0, [r1, #32]

12e8e: e1a0 b.n 131d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x246a>

12e90: 8c27 ldrh r7, [r4, #32]

12e92: e188 b.n 131a6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x243e>

12e94: 7407 strb r7, [r0, #16]

12e96: e1a0 b.n 131da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2472>

12e98: 7c26 ldrb r6, [r4, #16]

12e9a: e187 b.n 131ac <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2444>

12e9c: 6406 str r6, [r0, #64] ; 0x40

12e9e: e1a0 b.n 131e2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x247a>

12ea0: 6c25 ldr r5, [r4, #64] ; 0x40

12ea2: e186 b.n 131b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x244a>

12ea4: 5405 strb r5, [r0, r0]

12ea6: e1a0 b.n 131ea <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2482>

12ea8: 5c24 ldrb r4, [r4, r0]

12eaa: e185 b.n 131b8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2450>

12eac: 4404 add r4, r0

12eae: e1a0 b.n 131f2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x248a>

12eb0: 4c23 ldr r4, [pc, #140] ; (12f40 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21d8>)

12eb2: e184 b.n 131be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2456>

12eb4: 55f0 strb r0, [r6, r7]

12eb6: e920 ffe9 stmdb r0!, {r0, r3, r5, r6, r7, r8, r9, sl, fp, ip, sp, lr, pc}

12eba: aaff add r2, sp, #1020 ; 0x3fc

12ebc: 0060 lsls r0, r4, #1

12ebe: e372 b.n 135a6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x283e>

12ec0: ffe8 aaff ; <UNDEFINED> instruction: 0xffe8aaff

12ec4: 05e0 lsls r0, r4, #23

12ec6: e8bd c01c ldmia.w sp!, {r2, r3, r4, lr, pc}

12eca: e212 b.n 132f2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258a>

12ecc: 0005 movs r5, r0

12ece: 0a00 lsrs r0, r0, #8

12ed0: e403 b.n 126da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1972>

12ed2: e1a0 b.n 13216 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24ae>

12ed4: 3004 adds r0, #4

12ed6: e531 b.n 1293c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1bd4>

12ed8: c004 stmia r0!, {r2}

12eda: e25c b.n 13396 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x262e>

12edc: ec23 e18e stfd f6, [r3], #-568 ; 0xfffffdc8

12ee0: e004 b.n 12eec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2184>

12ee2: e520 b.n 12926 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1bbe>

12ee4: fff9 caff ; <UNDEFINED> instruction: 0xfff9caff

12ee8: 1003 asrs r3, r0, #32

12eea: e281 b.n 133f0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2688>

12eec: ffbb eaff ; <UNDEFINED> instruction: 0xffbbeaff

12ef0: 201c movs r0, #28

12ef2: e252 b.n 1339a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2632>

12ef4: 001f movs r7, r3

12ef6: ba00 rev r0, r0

12ef8: 05e0 lsls r0, r4, #23

12efa: e92d f004 stmdb sp!, {r2, ip, sp, lr, pc}

12efe: f551 2060 adcs.w r0, r1, #917504 ; 0xe0000

12f02: e252 b.n 133aa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2642>

12f04: f020 f551 bl 4339aa <\_r\_debug@@GLIBC\_2.4+0x40a04e>

12f08: 0002 movs r2, r0

12f0a: ba00 rev r0, r0

12f0c: f040 f551 bl 4539b2 <\_r\_debug@@GLIBC\_2.4+0x42a056>

12f10: f060 f551 bl 4739b6 <\_r\_debug@@GLIBC\_2.4+0x44a05a>

12f14: f080 f551 bl 4939ba <\_r\_debug@@GLIBC\_2.4+0x46a05e>

12f18: 1580 asrs r0, r0, #22

12f1a: e931 e803 ldmdb r1!, {r0, r1, fp, sp, lr, pc}

12f1e: e1a0 b.n 13262 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24fa>

12f20: 2020 movs r0, #32

12f22: e252 b.n 133ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2662>

12f24: 0078 lsls r0, r7, #1

12f26: e931 e82c ldmdb r1!, {r2, r3, r5, fp, sp, lr, pc}

12f2a: e18e b.n 1324a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24e2>

12f2c: c80c ldmia r0!, {r2, r3}

12f2e: e1a0 b.n 13272 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x250a>

12f30: c82a ldmia r0!, {r1, r3, r5}

12f32: e18c b.n 1324e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24e6>

12f34: a80a add r0, sp, #40 ; 0x28

12f36: e1a0 b.n 1327a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2512>

12f38: a828 add r0, sp, #160 ; 0xa0

12f3a: e18a b.n 13252 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24ea>

12f3c: 8808 ldrh r0, [r1, #0]

12f3e: e1a0 b.n 13282 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x251a>

12f40: 8827 ldrh r7, [r4, #0]

12f42: e188 b.n 13256 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24ee>

12f44: 7807 ldrb r7, [r0, #0]

12f46: e1a0 b.n 1328a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2522>

12f48: 7826 ldrb r6, [r4, #0]

12f4a: e187 b.n 1325c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24f4>

12f4c: 6806 ldr r6, [r0, #0]

12f4e: e1a0 b.n 13292 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x252a>

12f50: 6825 ldr r5, [r4, #0]

12f52: e186 b.n 13262 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24fa>

12f54: 5805 ldr r5, [r0, r0]

12f56: e1a0 b.n 1329a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2532>

12f58: 5824 ldr r4, [r4, r0]

12f5a: e185 b.n 13268 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2500>

12f5c: 4804 ldr r0, [pc, #16] ; (12f70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2208>)

12f5e: e1a0 b.n 132a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x253a>

12f60: 4823 ldr r0, [pc, #140] ; (12ff0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2288>)

12f62: e184 b.n 1326e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2506>

12f64: 55f0 strb r0, [r6, r7]

12f66: e920 ffe9 stmdb r0!, {r0, r3, r5, r6, r7, r8, r9, sl, fp, ip, sp, lr, pc}

12f6a: aaff add r2, sp, #1020 ; 0x3fc

12f6c: 0060 lsls r0, r4, #1

12f6e: e372 b.n 13656 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28ee>

12f70: ffe8 aaff ; <UNDEFINED> instruction: 0xffe8aaff

12f74: 05e0 lsls r0, r4, #23

12f76: e8bd c01c ldmia.w sp!, {r2, r3, r4, lr, pc}

12f7a: e212 b.n 133a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x263a>

12f7c: 0005 movs r5, r0

12f7e: 0a00 lsrs r0, r0, #8

12f80: e803 e1a0 ; <UNDEFINED> instruction: 0xe803e1a0

12f84: 3004 adds r0, #4

12f86: e531 b.n 129ec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c84>

12f88: c004 stmia r0!, {r2}

12f8a: e25c b.n 13446 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26de>

12f8c: e823 e18e ; <UNDEFINED> instruction: 0xe823e18e

12f90: e004 b.n 12f9c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2234>

12f92: e520 b.n 129d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1c6e>

12f94: fff9 caff ; <UNDEFINED> instruction: 0xfff9caff

12f98: 1002 asrs r2, r0, #32

12f9a: e281 b.n 134a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2738>

12f9c: ff8f eaff ; <UNDEFINED> instruction: 0xff8feaff

12fa0: 201c movs r0, #28

12fa2: e252 b.n 1344a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26e2>

12fa4: 001f movs r7, r3

12fa6: ba00 rev r0, r0

12fa8: 05e0 lsls r0, r4, #23

12faa: e92d f004 stmdb sp!, {r2, ip, sp, lr, pc}

12fae: f551 2060 adcs.w r0, r1, #917504 ; 0xe0000

12fb2: e252 b.n 1345a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26f2>

12fb4: f020 f551 bl 433a5a <\_r\_debug@@GLIBC\_2.4+0x40a0fe>

12fb8: 0002 movs r2, r0

12fba: ba00 rev r0, r0

12fbc: f040 f551 bl 453a62 <\_r\_debug@@GLIBC\_2.4+0x42a106>

12fc0: f060 f551 bl 473a66 <\_r\_debug@@GLIBC\_2.4+0x44a10a>

12fc4: f080 f551 bl 493a6a <\_r\_debug@@GLIBC\_2.4+0x46a10e>

12fc8: 1580 asrs r0, r0, #22

12fca: e931 ec03 ldmdb r1!, {r0, r1, sl, fp, sp, lr, pc}

12fce: e1a0 b.n 13312 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25aa>

12fd0: 2020 movs r0, #32

12fd2: e252 b.n 1347a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2712>

12fd4: 0078 lsls r0, r7, #1

12fd6: e931 e42c ldmdb r1!, {r2, r3, r5, sl, sp, lr, pc}

12fda: e18e b.n 132fa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2592>

12fdc: cc0c ldmia r4!, {r2, r3}

12fde: e1a0 b.n 13322 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25ba>

12fe0: c42a stmia r4!, {r1, r3, r5}

12fe2: e18c b.n 132fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2596>

12fe4: ac0a add r4, sp, #40 ; 0x28

12fe6: e1a0 b.n 1332a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25c2>

12fe8: a428 add r4, pc, #160 ; (adr r4, 1308c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2324>)

12fea: e18a b.n 13302 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x259a>

12fec: 8c08 ldrh r0, [r1, #32]

12fee: e1a0 b.n 13332 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25ca>

12ff0: 8427 strh r7, [r4, #32]

12ff2: e188 b.n 13306 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x259e>

12ff4: 7c07 ldrb r7, [r0, #16]

12ff6: e1a0 b.n 1333a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25d2>

12ff8: 7426 strb r6, [r4, #16]

12ffa: e187 b.n 1330c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25a4>

12ffc: 6c06 ldr r6, [r0, #64] ; 0x40

12ffe: e1a0 b.n 13342 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25da>

13000: 6425 str r5, [r4, #64] ; 0x40

13002: e186 b.n 13312 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25aa>

13004: 5c05 ldrb r5, [r0, r0]

13006: e1a0 b.n 1334a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25e2>

13008: 5424 strb r4, [r4, r0]

1300a: e185 b.n 13318 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25b0>

1300c: 4c04 ldr r4, [pc, #16] ; (13020 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22b8>)

1300e: e1a0 b.n 13352 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25ea>

13010: 4423 add r3, r4

13012: e184 b.n 1331e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25b6>

13014: 55f0 strb r0, [r6, r7]

13016: e920 ffe9 stmdb r0!, {r0, r3, r5, r6, r7, r8, r9, sl, fp, ip, sp, lr, pc}

1301a: aaff add r2, sp, #1020 ; 0x3fc

1301c: 0060 lsls r0, r4, #1

1301e: e372 b.n 13706 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x299e>

13020: ffe8 aaff ; <UNDEFINED> instruction: 0xffe8aaff

13024: 05e0 lsls r0, r4, #23

13026: e8bd c01c ldmia.w sp!, {r2, r3, r4, lr, pc}

1302a: e212 b.n 13452 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26ea>

1302c: 0005 movs r5, r0

1302e: 0a00 lsrs r0, r0, #8

13030: ec03 e1a0 stfd f6, [r3], {160} ; 0xa0

13034: 3004 adds r0, #4

13036: e531 b.n 12a9c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d34>

13038: c004 stmia r0!, {r2}

1303a: e25c b.n 134f6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x278e>

1303c: e423 b.n 12886 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1b1e>

1303e: e18e b.n 1335e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x25f6>

13040: e004 b.n 1304c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22e4>

13042: e520 b.n 12a86 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1d1e>

13044: fff9 caff ; <UNDEFINED> instruction: 0xfff9caff

13048: 1001 asrs r1, r0, #32

1304a: e281 b.n 13550 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27e8>

1304c: ff63 eaff vpmin.u32 q15, <illegal reg q9.5>, <illegal reg q15.5>

13050: 3000 adds r0, #0

13052: e1a0 b.n 13396 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x262e>

13054: 0008 movs r0, r1

13056: e352 b.n 136fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2996>

13058: 0011 movs r1, r2

1305a: 3a00 subs r2, #0

1305c: 0003 movs r3, r0

1305e: e313 b.n 13688 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2920>

13060: 1001 asrs r1, r0, #32

13062: 14c3 asrs r3, r0, #19

13064: 2001 movs r0, #1

13066: 1242 asrs r2, r0, #9

13068: fffb 1aff ; <UNDEFINED> instruction: 0xfffb1aff

1306c: 10ff asrs r7, r7, #3

1306e: e201 b.n 13474 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x270c>

13070: 1401 asrs r1, r0, #16

13072: e181 b.n 13378 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2610>

13074: 1801 adds r1, r0, r0

13076: e181 b.n 1337c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2614>

13078: c001 stmia r0!, {r0}

1307a: e1a0 b.n 133be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2656>

1307c: 2008 movs r0, #8

1307e: e252 b.n 13526 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27be>

13080: 1002 asrs r2, r0, #32

13082: 28a3 cmp r0, #163 ; 0xa3

13084: 2008 movs r0, #8

13086: 2252 movs r2, #82 ; 0x52

13088: 1002 asrs r2, r0, #32

1308a: 28a3 cmp r0, #163 ; 0xa3

1308c: 2008 movs r0, #8

1308e: 2252 movs r2, #82 ; 0x52

13090: 1002 asrs r2, r0, #32

13092: 28a3 cmp r0, #163 ; 0xa3

13094: 2008 movs r0, #8

13096: 2252 movs r2, #82 ; 0x52

13098: 1002 asrs r2, r0, #32

1309a: 28a3 cmp r0, #163 ; 0xa3

1309c: fff6 2aff ; <UNDEFINED> instruction: 0xfff62aff

130a0: 2007 movs r0, #7

130a2: e202 b.n 134aa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2742>

130a4: 2001 movs r0, #1

130a6: e252 b.n 1354e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27e6>

130a8: 1001 asrs r1, r0, #32

130aa: 24c3 movs r4, #195 ; 0xc3

130ac: 2001 movs r0, #1

130ae: 2252 movs r2, #82 ; 0x52

130b0: 1001 asrs r1, r0, #32

130b2: 24c3 movs r4, #195 ; 0xc3

130b4: 2001 movs r0, #1

130b6: 2252 movs r2, #82 ; 0x52

130b8: 1001 asrs r1, r0, #32

130ba: 24c3 movs r4, #195 ; 0xc3

130bc: 2001 movs r0, #1

130be: 2252 movs r2, #82 ; 0x52

130c0: 1001 asrs r1, r0, #32

130c2: 24c3 movs r4, #195 ; 0xc3

130c4: fff6 2aff ; <UNDEFINED> instruction: 0xfff62aff

130c8: ff1e e12f vrhadd.u16 d14, d14, d31

130cc: b510 push {r4, lr}

130ce: 4614 mov r4, r2

130d0: f000 e816 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

130d4: 4420 add r0, r4

130d6: bd10 pop {r4, pc}

...

13100: c000 stmia r0!, {}

13102: e1a0 b.n 13446 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26de>

13104: 0040 lsls r0, r0, #1

13106: e352 b.n 137ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a46>

13108: 0028 movs r0, r5

1310a: aa00 add r2, sp, #0

1310c: 303c adds r0, #60 ; 0x3c

1310e: e202 b.n 13516 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27ae>

13110: c003 stmia r0!, {r0, r1}

13112: e08c b.n 1322e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24c6>

13114: 1003 asrs r3, r0, #32

13116: e081 b.n 1321c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24b4>

13118: 303a adds r0, #58 ; 0x3a

1311a: e263 b.n 135e4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x287c>

1311c: f083 e08f ; <UNDEFINED> instruction: 0xf083e08f

13120: 303c adds r0, #60 ; 0x3c

13122: e511 b.n 12b48 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1de0>

13124: 303c adds r0, #60 ; 0x3c

13126: e50c b.n 12b42 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1dda>

13128: 3038 adds r0, #56 ; 0x38

1312a: e511 b.n 12b50 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1de8>

1312c: 3038 adds r0, #56 ; 0x38

1312e: e50c b.n 12b4a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1de2>

13130: 3034 adds r0, #52 ; 0x34

13132: e511 b.n 12b58 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1df0>

13134: 3034 adds r0, #52 ; 0x34

13136: e50c b.n 12b52 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1dea>

13138: 3030 adds r0, #48 ; 0x30

1313a: e511 b.n 12b60 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1df8>

1313c: 3030 adds r0, #48 ; 0x30

1313e: e50c b.n 12b5a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1df2>

13140: 302c adds r0, #44 ; 0x2c

13142: e511 b.n 12b68 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e00>

13144: 302c adds r0, #44 ; 0x2c

13146: e50c b.n 12b62 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1dfa>

13148: 3028 adds r0, #40 ; 0x28

1314a: e511 b.n 12b70 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e08>

1314c: 3028 adds r0, #40 ; 0x28

1314e: e50c b.n 12b6a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e02>

13150: 3024 adds r0, #36 ; 0x24

13152: e511 b.n 12b78 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e10>

13154: 3024 adds r0, #36 ; 0x24

13156: e50c b.n 12b72 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e0a>

13158: 3020 adds r0, #32

1315a: e511 b.n 12b80 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e18>

1315c: 3020 adds r0, #32

1315e: e50c b.n 12b7a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e12>

13160: 301c adds r0, #28

13162: e511 b.n 12b88 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e20>

13164: 301c adds r0, #28

13166: e50c b.n 12b82 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e1a>

13168: 3018 adds r0, #24

1316a: e511 b.n 12b90 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e28>

1316c: 3018 adds r0, #24

1316e: e50c b.n 12b8a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e22>

13170: 3014 adds r0, #20

13172: e511 b.n 12b98 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e30>

13174: 3014 adds r0, #20

13176: e50c b.n 12b92 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e2a>

13178: 3010 adds r0, #16

1317a: e511 b.n 12ba0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e38>

1317c: 3010 adds r0, #16

1317e: e50c b.n 12b9a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e32>

13180: 300c adds r0, #12

13182: e511 b.n 12ba8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e40>

13184: 300c adds r0, #12

13186: e50c b.n 12ba2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e3a>

13188: 3008 adds r0, #8

1318a: e511 b.n 12bb0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e48>

1318c: 3008 adds r0, #8

1318e: e50c b.n 12baa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e42>

13190: 3004 adds r0, #4

13192: e511 b.n 12bb8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e50>

13194: 3004 adds r0, #4

13196: e50c b.n 12bb2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e4a>

13198: 2f82 cmp r7, #130 ; 0x82

1319a: e1b0 b.n 134fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2796>

1319c: 30b2 adds r0, #178 ; 0xb2

1319e: 20d1 movs r0, #209 ; 0xd1

131a0: 1000 asrs r0, r0, #32

131a2: 15d1 asrs r1, r2, #23

131a4: 30b2 adds r0, #178 ; 0xb2

131a6: 20cc movs r0, #204 ; 0xcc

131a8: 1000 asrs r0, r0, #32

131aa: 15cc asrs r4, r1, #23

131ac: ff1e e12f vrhadd.u16 d14, d14, d31

131b0: 8020 strh r0, [r4, #0]

131b2: e52d b.n 12c10 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1ea8>

131b4: 8007 strh r7, [r0, #0]

131b6: e201 b.n 135bc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2854>

131b8: 3007 adds r0, #7

131ba: e20c b.n 135d6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x286e>

131bc: 0008 movs r0, r1

131be: e153 b.n 13468 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2700>

131c0: 007e lsls r6, r7, #1

131c2: 1a00 subs r0, r0, r0

131c4: 8e8c ldrh r4, [r1, #52] ; 0x34

131c6: e1b0 b.n 1352a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27c2>

131c8: 0008 movs r0, r1

131ca: 0a00 lsrs r0, r0, #8

131cc: 8000 strh r0, [r0, #0]

131ce: e278 b.n 136c2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x295a>

131d0: 2ea8 cmp r6, #168 ; 0xa8

131d2: e042 b.n 1325a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x24f2>

131d4: 3004 adds r0, #4

131d6: 4491 add r9, r2

131d8: 3004 adds r0, #4

131da: 448c add ip, r1

131dc: 8108 strh r0, [r1, #8]

131de: e1b0 b.n 13542 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27da>

131e0: 30b2 adds r0, #178 ; 0xb2

131e2: 20d1 movs r0, #209 ; 0xd1

131e4: 8001 strh r1, [r0, #0]

131e6: 14d1 asrs r1, r2, #19

131e8: 30b2 adds r0, #178 ; 0xb2

131ea: 20cc movs r0, #204 ; 0xcc

131ec: 8001 strh r1, [r0, #0]

131ee: 14cc asrs r4, r1, #19

131f0: 8040 strh r0, [r0, #2]

131f2: e252 b.n 1369a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2932>

131f4: 001b movs r3, r3

131f6: ba00 rev r0, r0

131f8: 0c02 lsrs r2, r0, #16

131fa: e358 b.n 138ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b46>

131fc: 0036 movs r6, r6

131fe: aa00 add r2, sp, #0

13200: 1008 asrs r0, r1, #32

13202: e241 b.n 13688 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2920>

13204: c008 stmia r0!, {r3}

13206: e24c b.n 136a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x293a>

13208: 20d8 movs r0, #216 ; 0xd8

1320a: e1c1 b.n 13590 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2828>

1320c: 20f8 movs r0, #248 ; 0xf8

1320e: e1cc b.n 135aa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2842>

13210: 21d0 movs r1, #208 ; 0xd0

13212: e1c1 b.n 13598 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2830>

13214: 21f0 movs r1, #240 ; 0xf0

13216: e1cc b.n 135b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x284a>

13218: 21d8 movs r1, #216 ; 0xd8

1321a: e1c1 b.n 135a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2838>

1321c: 21f8 movs r1, #248 ; 0xf8

1321e: e1cc b.n 135ba <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2852>

13220: 22d0 movs r2, #208 ; 0xd0

13222: e1c1 b.n 135a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2840>

13224: 22f0 movs r2, #240 ; 0xf0

13226: e1cc b.n 135c2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x285a>

13228: 22d8 movs r2, #216 ; 0xd8

1322a: e1c1 b.n 135b0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2848>

1322c: 22f8 movs r2, #248 ; 0xf8

1322e: e1cc b.n 135ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2862>

13230: 23d0 movs r3, #208 ; 0xd0

13232: e1c1 b.n 135b8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2850>

13234: 23f0 movs r3, #240 ; 0xf0

13236: e1cc b.n 135d2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x286a>

13238: 23d8 movs r3, #216 ; 0xd8

1323a: e1c1 b.n 135c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2858>

1323c: 23f8 movs r3, #248 ; 0xf8

1323e: e1cc b.n 135da <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2872>

13240: 24d0 movs r4, #208 ; 0xd0

13242: e1e1 b.n 13608 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28a0>

13244: 24f0 movs r4, #240 ; 0xf0

13246: e1ec b.n 13622 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28ba>

13248: 8040 strh r0, [r0, #2]

1324a: e258 b.n 136fe <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2996>

1324c: ffed aaff ; <UNDEFINED> instruction: 0xffedaaff

13250: 003f movs r7, r7

13252: e318 b.n 13886 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b1e>

13254: 0001 movs r1, r0

13256: 1a00 subs r0, r0, r0

13258: 8020 strh r0, [r4, #0]

1325a: e49d b.n 12b98 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1e30>

1325c: ff1e e12f vrhadd.u16 d14, d14, d31

13260: 1008 asrs r0, r1, #32

13262: e281 b.n 13768 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a00>

13264: c008 stmia r0!, {r3}

13266: e28c b.n 13782 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a1a>

13268: 3038 adds r0, #56 ; 0x38

1326a: e208 b.n 1367e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2916>

1326c: c003 stmia r0!, {r0, r1}

1326e: e08c b.n 1338a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2622>

13270: 1003 asrs r3, r0, #32

13272: e081 b.n 13378 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2610>

13274: 3034 adds r0, #52 ; 0x34

13276: e263 b.n 13740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29d8>

13278: f003 e08f ; <UNDEFINED> instruction: 0xf003e08f

1327c: 23d8 movs r3, #216 ; 0xd8

1327e: e141 b.n 13504 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x279c>

13280: 23f8 movs r3, #248 ; 0xf8

13282: e14c b.n 1351e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27b6>

13284: 23d0 movs r3, #208 ; 0xd0

13286: e141 b.n 1350c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27a4>

13288: 23f0 movs r3, #240 ; 0xf0

1328a: e14c b.n 13526 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27be>

1328c: 22d8 movs r2, #216 ; 0xd8

1328e: e141 b.n 13514 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27ac>

13290: 22f8 movs r2, #248 ; 0xf8

13292: e14c b.n 1352e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27c6>

13294: 22d0 movs r2, #208 ; 0xd0

13296: e141 b.n 1351c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27b4>

13298: 22f0 movs r2, #240 ; 0xf0

1329a: e14c b.n 13536 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27ce>

1329c: 21d8 movs r1, #216 ; 0xd8

1329e: e141 b.n 13524 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27bc>

132a0: 21f8 movs r1, #248 ; 0xf8

132a2: e14c b.n 1353e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27d6>

132a4: 21d0 movs r1, #208 ; 0xd0

132a6: e141 b.n 1352c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27c4>

132a8: 21f0 movs r1, #240 ; 0xf0

132aa: e14c b.n 13546 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27de>

132ac: 20d8 movs r0, #216 ; 0xd8

132ae: e141 b.n 13534 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27cc>

132b0: 20f8 movs r0, #248 ; 0xf8

132b2: e14c b.n 1354e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27e6>

132b4: 0004 movs r4, r0

132b6: e318 b.n 138ea <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b82>

132b8: 3004 adds r0, #4

132ba: 1491 asrs r1, r2, #18

132bc: 3004 adds r0, #4

132be: 148c asrs r4, r1, #18

132c0: 8f88 ldrh r0, [r1, #60] ; 0x3c

132c2: e1b0 b.n 13626 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28be>

132c4: 30b2 adds r0, #178 ; 0xb2

132c6: 20d1 movs r0, #209 ; 0xd1

132c8: 8000 strh r0, [r0, #0]

132ca: 15d1 asrs r1, r2, #23

132cc: 30b2 adds r0, #178 ; 0xb2

132ce: 20cc movs r0, #204 ; 0xcc

132d0: 8000 strh r0, [r0, #0]

132d2: 15cc asrs r4, r1, #23

132d4: 8020 strh r0, [r4, #0]

132d6: e49d b.n 12c14 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1eac>

132d8: ff1e e12f vrhadd.u16 d14, d14, d31

132dc: 1008 asrs r0, r1, #32

132de: e241 b.n 13764 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29fc>

132e0: c008 stmia r0!, {r3}

132e2: e24c b.n 1377e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a16>

132e4: f008 f5d1 bl 41be8a <\_r\_debug@@GLIBC\_2.4+0x3f252e>

132e8: f048 f5d1 bl 45be8e <\_r\_debug@@GLIBC\_2.4+0x432532>

132ec: 8040 strh r0, [r0, #2]

132ee: e258 b.n 137a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a3a>

132f0: f088 f5d1 bl 49be96 <\_r\_debug@@GLIBC\_2.4+0x47253a>

132f4: 20d8 movs r0, #216 ; 0xd8

132f6: e1c1 b.n 1367c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2914>

132f8: 40f8 lsrs r0, r7

132fa: e1cd b.n 13698 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2930>

132fc: 41d0 rors r0, r2

132fe: e1c1 b.n 13684 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x291c>

13300: 61f0 str r0, [r6, #28]

13302: e1cd b.n 136a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2938>

13304: 61d8 str r0, [r3, #28]

13306: e1c1 b.n 1368c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2924>

13308: a1f8 add r1, pc, #992 ; (adr r1, 136ec <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2984>)

1330a: e1cd b.n 136a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2940>

1330c: f0c8 f5d1 bl 4dbeb2 <\_r\_debug@@GLIBC\_2.4+0x4b2556>

13310: a2d0 add r2, pc, #832 ; (adr r2, 13654 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28ec>)

13312: e1e1 b.n 136d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2970>

13314: 0013 movs r3, r2

13316: ea00 0000 and.w r0, r0, r0

1331a: e1a0 b.n 1365e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28f6>

1331c: 0000 movs r0, r0

1331e: e1a0 b.n 13662 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28fa>

13320: 0000 movs r0, r0

13322: e1a0 b.n 13666 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28fe>

13324: 0000 movs r0, r0

13326: e1a0 b.n 1366a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2902>

13328: 0000 movs r0, r0

1332a: e1a0 b.n 1366e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2906>

1332c: 0000 movs r0, r0

1332e: e1a0 b.n 13672 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x290a>

13330: 0000 movs r0, r0

13332: e1a0 b.n 13676 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x290e>

13334: 0000 movs r0, r0

13336: e1a0 b.n 1367a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2912>

13338: 0000 movs r0, r0

1333a: e1a0 b.n 1367e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2916>

1333c: 0000 movs r0, r0

1333e: e1a0 b.n 13682 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x291a>

13340: f0e8 f5d1 bl 4fbee6 <\_r\_debug@@GLIBC\_2.4+0x4d258a>

13344: 22f8 movs r2, #248 ; 0xf8

13346: e1cc b.n 136e2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x297a>

13348: 22d8 movs r2, #216 ; 0xd8

1334a: e1c1 b.n 136d0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2968>

1334c: 43f0 mvns r0, r6

1334e: e1cc b.n 136ea <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2982>

13350: 43d0 mvns r0, r2

13352: e1c1 b.n 136d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2970>

13354: 63f8 str r0, [r7, #60] ; 0x3c

13356: e1cc b.n 136f2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x298a>

13358: 63d8 str r0, [r3, #60] ; 0x3c

1335a: e1c1 b.n 136e0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2978>

1335c: a4f0 add r4, pc, #960 ; (adr r4, 13720 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29b8>)

1335e: e1ec b.n 1373a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29d2>

13360: a4d0 add r4, pc, #832 ; (adr r4, 136a4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x293c>)

13362: e1e1 b.n 13728 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29c0>

13364: 8040 strh r0, [r0, #2]

13366: e258 b.n 1381a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ab2>

13368: 20f8 movs r0, #248 ; 0xf8

1336a: e1cc b.n 13706 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x299e>

1336c: 20d8 movs r0, #216 ; 0xd8

1336e: e1c1 b.n 136f4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x298c>

13370: 41f0 rors r0, r6

13372: e1cc b.n 1370e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29a6>

13374: 41d0 rors r0, r2

13376: e1c1 b.n 136fc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2994>

13378: 61f8 str r0, [r7, #28]

1337a: e1cc b.n 13716 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29ae>

1337c: 61d8 str r0, [r3, #28]

1337e: e1c1 b.n 13704 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x299c>

13380: a2f0 add r2, pc, #960 ; (adr r2, 13744 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29dc>)

13382: e1cc b.n 1371e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29b6>

13384: a2d0 add r2, pc, #832 ; (adr r2, 136c8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2960>)

13386: e1c1 b.n 1370c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29a4>

13388: ffec 2aff ; <UNDEFINED> instruction: 0xffec2aff

1338c: 22f8 movs r2, #248 ; 0xf8

1338e: e1cc b.n 1372a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29c2>

13390: 1028 asrs r0, r5, #32

13392: e281 b.n 13898 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b30>

13394: 43f0 mvns r0, r6

13396: e1cc b.n 13732 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29ca>

13398: 40d8 lsrs r0, r3

1339a: e1cd b.n 13738 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29d0>

1339c: 63f8 str r0, [r7, #60] ; 0x3c

1339e: e1cc b.n 1373a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29d2>

133a0: 61d0 str r0, [r2, #28]

133a2: e1cd b.n 13740 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29d8>

133a4: a4f0 add r4, pc, #960 ; (adr r4, 13768 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a00>)

133a6: e1cc b.n 13742 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29da>

133a8: a1d8 add r1, pc, #864 ; (adr r1, 1370c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29a4>)

133aa: e1cd b.n 13748 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29e0>

133ac: c048 stmia r0!, {r3, r6}

133ae: e28c b.n 138ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b62>

133b0: 003f movs r7, r7

133b2: e318 b.n 139e6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2c7e>

133b4: ffab 1aff ; <UNDEFINED> instruction: 0xffab1aff

133b8: 8020 strh r0, [r4, #0]

133ba: e49d b.n 12cf8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x1f90>

133bc: ff1e e12f vrhadd.u16 d14, d14, d31

133c0: f000 f5d1 bl 413f66 <\_r\_debug@@GLIBC\_2.4+0x3ea60a>

133c4: f040 f5d1 bl 453f6a <\_r\_debug@@GLIBC\_2.4+0x42a60e>

133c8: 8e8c ldrh r4, [r1, #52] ; 0x34

133ca: e1b0 b.n 1372e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29c6>

133cc: f080 f5d1 bl 493f72 <\_r\_debug@@GLIBC\_2.4+0x46a616>

133d0: 0008 movs r0, r1

133d2: 0a00 lsrs r0, r0, #8

133d4: 8000 strh r0, [r0, #0]

133d6: e278 b.n 138ca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b62>

133d8: 2ea8 cmp r6, #168 ; 0xa8

133da: e042 b.n 13462 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x26fa>

133dc: 3004 adds r0, #4

133de: 4491 add r9, r2

133e0: 3004 adds r0, #4

133e2: 448c add ip, r1

133e4: 8108 strh r0, [r1, #8]

133e6: e1b0 b.n 1374a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x29e2>

133e8: 3001 adds r0, #1

133ea: 14d1 asrs r1, r2, #19

133ec: 80b2 strh r2, [r6, #4]

133ee: 20d1 movs r0, #209 ; 0xd1

133f0: 3001 adds r0, #1

133f2: 14cc asrs r4, r1, #19

133f4: 80b2 strh r2, [r6, #4]

133f6: 20cc movs r0, #204 ; 0xcc

133f8: f0c0 f5d1 bl 4d3f9e <\_r\_debug@@GLIBC\_2.4+0x4aa642>

133fc: 2040 movs r0, #64 ; 0x40

133fe: e252 b.n 138a6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b3e>

13400: 8020 strh r0, [r4, #0]

13402: 449d add sp, r3

13404: ff40 4aff vpmin.u8 q10, q8, <illegal reg q15.5>

13408: f100 f5d1 bl 513fae <\_r\_debug@@GLIBC\_2.4+0x4ea652>

1340c: 1004 asrs r4, r0, #32

1340e: e241 b.n 13894 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b2c>

13410: c008 stmia r0!, {r3}

13412: e24c b.n 138ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b46>

13414: 8040 strh r0, [r0, #2]

13416: e252 b.n 138be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b56>

13418: 2004 movs r0, #4

1341a: e591 b.n 12f40 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21d8>

1341c: 3008 adds r0, #8

1341e: e591 b.n 12f44 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21dc>

13420: 40f8 lsrs r0, r7

13422: e1cd b.n 137c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a58>

13424: 400c ands r4, r1

13426: e591 b.n 12f4c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21e4>

13428: 5010 str r0, [r2, r0]

1342a: e591 b.n 12f50 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21e8>

1342c: 61f0 str r0, [r6, #28]

1342e: e1cd b.n 137cc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a64>

13430: 6014 str r4, [r2, #0]

13432: e591 b.n 12f58 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21f0>

13434: 7018 strb r0, [r3, #0]

13436: e591 b.n 12f5c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21f4>

13438: a1f8 add r1, pc, #992 ; (adr r1, 1381c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ab4>)

1343a: e1cd b.n 137d8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a70>

1343c: a01c add r0, pc, #112 ; (adr r0, 134b0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2748>)

1343e: e591 b.n 12f64 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x21fc>

13440: b020 add sp, #128 ; 0x80

13442: e5b1 b.n 12fa8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2240>

13444: 001b movs r3, r3

13446: ea00 0000 and.w r0, r0, r0

1344a: e1a0 b.n 1378e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a26>

1344c: 0000 movs r0, r0

1344e: e1a0 b.n 13792 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a2a>

13450: 0000 movs r0, r0

13452: e1a0 b.n 13796 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a2e>

13454: 0000 movs r0, r0

13456: e1a0 b.n 1379a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a32>

13458: 0000 movs r0, r0

1345a: e1a0 b.n 1379e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a36>

1345c: 0000 movs r0, r0

1345e: e1a0 b.n 137a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a3a>

13460: 0000 movs r0, r0

13462: e1a0 b.n 137a6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a3e>

13464: 0000 movs r0, r0

13466: e1a0 b.n 137aa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a42>

13468: 0000 movs r0, r0

1346a: e1a0 b.n 137ae <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a46>

1346c: 0000 movs r0, r0

1346e: e1a0 b.n 137b2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a4a>

13470: 0000 movs r0, r0

13472: e1a0 b.n 137b6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a4e>

13474: 0000 movs r0, r0

13476: e1a0 b.n 137ba <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a52>

13478: 0000 movs r0, r0

1347a: e1a0 b.n 137be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a56>

1347c: 0000 movs r0, r0

1347e: e1a0 b.n 137c2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2a5a>

13480: f124 f5d1 bl 538026 <\_r\_debug@@GLIBC\_2.4+0x50e6ca>

13484: 22f8 movs r2, #248 ; 0xf8

13486: e1cc b.n 13822 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2aba>

13488: 2024 movs r0, #36 ; 0x24

1348a: e591 b.n 12fb0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2248>

1348c: 3028 adds r0, #40 ; 0x28

1348e: e591 b.n 12fb4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x224c>

13490: 43f0 mvns r0, r6

13492: e1cc b.n 1382e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ac6>

13494: 402c ands r4, r5

13496: e591 b.n 12fbc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2254>

13498: 5030 str r0, [r6, r0]

1349a: e591 b.n 12fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2258>

1349c: 63f8 str r0, [r7, #60] ; 0x3c

1349e: e1cc b.n 1383a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ad2>

134a0: 6034 str r4, [r6, #0]

134a2: e591 b.n 12fc8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2260>

134a4: 7038 strb r0, [r7, #0]

134a6: e591 b.n 12fcc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2264>

134a8: a4f0 add r4, pc, #960 ; (adr r4, 1386c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b04>)

134aa: e1ec b.n 13886 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b1e>

134ac: a03c add r0, pc, #240 ; (adr r0, 135a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2838>)

134ae: e591 b.n 12fd4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x226c>

134b0: b040 add sp, #256 ; 0x100

134b2: e5b1 b.n 13018 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22b0>

134b4: 8040 strh r0, [r0, #2]

134b6: e258 b.n 1396a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2c02>

134b8: 20f8 movs r0, #248 ; 0xf8

134ba: e1cc b.n 13856 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2aee>

134bc: 2004 movs r0, #4

134be: e591 b.n 12fe4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x227c>

134c0: 3008 adds r0, #8

134c2: e591 b.n 12fe8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2280>

134c4: 41f0 rors r0, r6

134c6: e1cc b.n 13862 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2afa>

134c8: 400c ands r4, r1

134ca: e591 b.n 12ff0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2288>

134cc: 5010 str r0, [r2, r0]

134ce: e591 b.n 12ff4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x228c>

134d0: 61f8 str r0, [r7, #28]

134d2: e1cc b.n 1386e <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b06>

134d4: 6014 str r4, [r2, #0]

134d6: e591 b.n 12ffc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2294>

134d8: 7018 strb r0, [r3, #0]

134da: e591 b.n 13000 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2298>

134dc: a2f0 add r2, pc, #960 ; (adr r2, 138a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b38>)

134de: e1cc b.n 1387a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b12>

134e0: a01c add r0, pc, #112 ; (adr r0, 13554 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27ec>)

134e2: e591 b.n 13008 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22a0>

134e4: b020 add sp, #128 ; 0x80

134e6: e591 b.n 1300c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x22a4>

134e8: ffe4 2aff ; <UNDEFINED> instruction: 0xffe42aff

134ec: 22f8 movs r2, #248 ; 0xf8

134ee: e1cc b.n 1388a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b22>

134f0: 1024 asrs r4, r4, #32

134f2: e281 b.n 139f8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2c90>

134f4: 43f0 mvns r0, r6

134f6: e1cc b.n 13892 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b2a>

134f8: 40d8 lsrs r0, r3

134fa: e1cd b.n 13898 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b30>

134fc: 63f8 str r0, [r7, #60] ; 0x3c

134fe: e1cc b.n 1389a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b32>

13500: 61d0 str r0, [r2, #28]

13502: e1cd b.n 138a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b38>

13504: a4f0 add r4, pc, #960 ; (adr r4, 138c8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b60>)

13506: e1cc b.n 138a2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b3a>

13508: a1d8 add r1, pc, #864 ; (adr r1, 1386c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b04>)

1350a: e1cd b.n 138a8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b40>

1350c: c048 stmia r0!, {r3, r6}

1350e: e28c b.n 13a2a <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2cc2>

13510: 203f movs r0, #63 ; 0x3f

13512: e218 b.n 13946 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2bde>

13514: 8020 strh r0, [r4, #0]

13516: e49d b.n 12e54 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x20ec>

13518: fefb 1aff mrc2 10, 7, r1, cr11, cr15, {7} ; <UNPREDICTABLE>

1351c: ff1e e12f vrhadd.u16 d14, d14, d31

13520: 7802 ldrb r2, [r0, #0]

13522: f000 0307 and.w r3, r0, #7

13526: b2c9 uxtb r1, r1

13528: f1c3 030f rsb r3, r3, #15

1352c: 428a cmp r2, r1

1352e: bf08 it eq

13530: 4770 bxeq lr

13532: f810 2f01 ldrb.w r2, [r0, #1]!

13536: 3b01 subs r3, #1

13538: bf18 it ne

1353a: 428a cmpne r2, r1

1353c: d1f9 bne.n 13532 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27ca>

1353e: 428a cmp r2, r1

13540: bf08 it eq

13542: 4770 bxeq lr

13544: f100 0001 add.w r0, r0, #1

13548: e8f0 2302 ldrd r2, r3, [r0], #8

1354c: ea41 2101 orr.w r1, r1, r1, lsl #8

13550: f240 1c01 movw ip, #257 ; 0x101

13554: f890 f040 pld [r0, #64] ; 0x40

13558: f2c0 1c01 movt ip, #257 ; 0x101

1355c: ea41 4101 orr.w r1, r1, r1, lsl #16

13560: ea82 0201 eor.w r2, r2, r1

13564: ea83 0301 eor.w r3, r3, r1

13568: facc f252 uqsub8 r2, ip, r2

1356c: facc f353 uqsub8 r3, ip, r3

13570: f890 f080 pld [r0, #128] ; 0x80

13574: 4313 orrs r3, r2

13576: bf08 it eq

13578: e8f0 2302 ldrdeq r2, r3, [r0], #8

1357c: d0f0 beq.n 13560 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x27f8>

1357e: 2a00 cmp r2, #0

13580: f1a0 0004 sub.w r0, r0, #4

13584: bf0c ite eq

13586: 461a moveq r2, r3

13588: 3804 subne r0, #4

1358a: ba12 rev r2, r2

1358c: fab2 f282 clz r2, r2

13590: eb00 00d2 add.w r0, r0, r2, lsr #3

13594: 4770 bx lr

13596: bf00 nop

13598: b4f0 push {r4, r5, r6, r7}

1359a: 0785 lsls r5, r0, #30

1359c: b2cc uxtb r4, r1

1359e: d010 beq.n 135c2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x285a>

135a0: 7803 ldrb r3, [r0, #0]

135a2: 429c cmp r4, r3

135a4: bf18 it ne

135a6: 2b00 cmpne r3, #0

135a8: d009 beq.n 135be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2856>

135aa: 1c43 adds r3, r0, #1

135ac: 079a lsls r2, r3, #30

135ae: 4618 mov r0, r3

135b0: d007 beq.n 135c2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x285a>

135b2: f813 2b01 ldrb.w r2, [r3], #1

135b6: 42a2 cmp r2, r4

135b8: bf18 it ne

135ba: 2a00 cmpne r2, #0

135bc: d1f6 bne.n 135ac <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2844>

135be: bcf0 pop {r4, r5, r6, r7}

135c0: 4770 bx lr

135c2: 020f lsls r7, r1, #8

135c4: f64f 6cff movw ip, #65279 ; 0xfeff

135c8: f6c7 6cfe movt ip, #32510 ; 0x7efe

135cc: b2bf uxth r7, r7

135ce: 4327 orrs r7, r4

135d0: ea47 4707 orr.w r7, r7, r7, lsl #16

135d4: 4601 mov r1, r0

135d6: f851 3b04 ldr.w r3, [r1], #4

135da: ea87 0603 eor.w r6, r7, r3

135de: f1a3 3201 sub.w r2, r3, #16843009 ; 0x1010101

135e2: f1a6 3501 sub.w r5, r6, #16843009 ; 0x1010101

135e6: f102 4200 add.w r2, r2, #2147483648 ; 0x80000000

135ea: f105 4500 add.w r5, r5, #2147483648 ; 0x80000000

135ee: 405a eors r2, r3

135f0: 4075 eors r5, r6

135f2: 402a ands r2, r5

135f4: ea42 020c orr.w r2, r2, ip

135f8: 3201 adds r2, #1

135fa: d017 beq.n 1362c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28c4>

135fc: f811 3c04 ldrb.w r3, [r1, #-4]

13600: 2b00 cmp r3, #0

13602: bf18 it ne

13604: 42a3 cmpne r3, r4

13606: d0da beq.n 135be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2856>

13608: f811 3c03 ldrb.w r3, [r1, #-3]

1360c: 42a3 cmp r3, r4

1360e: bf18 it ne

13610: 2b00 cmpne r3, #0

13612: d00f beq.n 13634 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28cc>

13614: f811 3c02 ldrb.w r3, [r1, #-2]

13618: 2b00 cmp r3, #0

1361a: bf18 it ne

1361c: 42a3 cmpne r3, r4

1361e: d00b beq.n 13638 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28d0>

13620: f811 3c01 ldrb.w r3, [r1, #-1]

13624: 42a3 cmp r3, r4

13626: bf18 it ne

13628: 2b00 cmpne r3, #0

1362a: d001 beq.n 13630 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28c8>

1362c: 4608 mov r0, r1

1362e: e7d1 b.n 135d4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x286c>

13630: 3003 adds r0, #3

13632: e7c4 b.n 135be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2856>

13634: 3001 adds r0, #1

13636: e7c2 b.n 135be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2856>

13638: 3002 adds r0, #2

1363a: e7c0 b.n 135be <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2856>

1363c: 0000 movs r0, r0

1363e: 0000 movs r0, r0

13640: b480 push {r7}

13642: f04f 0768 mov.w r7, #104 ; 0x68

13646: df00 svc 0

13648: bc80 pop {r7}

1364a: f510 5f80 cmn.w r0, #4096 ; 0x1000

1364e: bf38 it cc

13650: 4770 bxcc lr

13652: e7ff b.n 13654 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28ec>

13654: f1c0 0000 rsb r0, r0, #0

13658: 4902 ldr r1, [pc, #8] ; (13664 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x28fc>)

1365a: 4479 add r1, pc

1365c: 6008 str r0, [r1, #0]

1365e: f06f 0000 mvn.w r0, #0

13662: 4770 bx lr

13664: 62fa str r2, [r7, #44] ; 0x2c

13666: 0001 movs r1, r0

13668: 1e4a subs r2, r1, #1

1366a: bf08 it eq

1366c: 4770 bxeq lr

1366e: f0c0 8124 bcc.w 138ba <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b52>

13672: 4288 cmp r0, r1

13674: f240 8116 bls.w 138a4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b3c>

13678: 4211 tst r1, r2

1367a: f000 8117 beq.w 138ac <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b44>

1367e: fab0 f380 clz r3, r0

13682: fab1 f281 clz r2, r1

13686: eba2 0303 sub.w r3, r2, r3

1368a: f1c3 031f rsb r3, r3, #31

1368e: a204 add r2, pc, #16 ; (adr r2, 136a0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2938>)

13690: eb02 1303 add.w r3, r2, r3, lsl #4

13694: f04f 0200 mov.w r2, #0

13698: 469f mov pc, r3

1369a: bf00 nop

1369c: f3af 8000 nop.w

136a0: ebb0 7fc1 cmp.w r0, r1, lsl #31

136a4: bf00 nop

136a6: eb42 0202 adc.w r2, r2, r2

136aa: bf28 it cs

136ac: eba0 70c1 subcs.w r0, r0, r1, lsl #31

136b0: ebb0 7f81 cmp.w r0, r1, lsl #30

136b4: bf00 nop

136b6: eb42 0202 adc.w r2, r2, r2

136ba: bf28 it cs

136bc: eba0 7081 subcs.w r0, r0, r1, lsl #30

136c0: ebb0 7f41 cmp.w r0, r1, lsl #29

136c4: bf00 nop

136c6: eb42 0202 adc.w r2, r2, r2

136ca: bf28 it cs

136cc: eba0 7041 subcs.w r0, r0, r1, lsl #29

136d0: ebb0 7f01 cmp.w r0, r1, lsl #28

136d4: bf00 nop

136d6: eb42 0202 adc.w r2, r2, r2

136da: bf28 it cs

136dc: eba0 7001 subcs.w r0, r0, r1, lsl #28

136e0: ebb0 6fc1 cmp.w r0, r1, lsl #27

136e4: bf00 nop

136e6: eb42 0202 adc.w r2, r2, r2

136ea: bf28 it cs

136ec: eba0 60c1 subcs.w r0, r0, r1, lsl #27

136f0: ebb0 6f81 cmp.w r0, r1, lsl #26

136f4: bf00 nop

136f6: eb42 0202 adc.w r2, r2, r2

136fa: bf28 it cs

136fc: eba0 6081 subcs.w r0, r0, r1, lsl #26

13700: ebb0 6f41 cmp.w r0, r1, lsl #25

13704: bf00 nop

13706: eb42 0202 adc.w r2, r2, r2

1370a: bf28 it cs

1370c: eba0 6041 subcs.w r0, r0, r1, lsl #25

13710: ebb0 6f01 cmp.w r0, r1, lsl #24

13714: bf00 nop

13716: eb42 0202 adc.w r2, r2, r2

1371a: bf28 it cs

1371c: eba0 6001 subcs.w r0, r0, r1, lsl #24

13720: ebb0 5fc1 cmp.w r0, r1, lsl #23

13724: bf00 nop

13726: eb42 0202 adc.w r2, r2, r2

1372a: bf28 it cs

1372c: eba0 50c1 subcs.w r0, r0, r1, lsl #23

13730: ebb0 5f81 cmp.w r0, r1, lsl #22

13734: bf00 nop

13736: eb42 0202 adc.w r2, r2, r2

1373a: bf28 it cs

1373c: eba0 5081 subcs.w r0, r0, r1, lsl #22

13740: ebb0 5f41 cmp.w r0, r1, lsl #21

13744: bf00 nop

13746: eb42 0202 adc.w r2, r2, r2

1374a: bf28 it cs

1374c: eba0 5041 subcs.w r0, r0, r1, lsl #21

13750: ebb0 5f01 cmp.w r0, r1, lsl #20

13754: bf00 nop

13756: eb42 0202 adc.w r2, r2, r2

1375a: bf28 it cs

1375c: eba0 5001 subcs.w r0, r0, r1, lsl #20

13760: ebb0 4fc1 cmp.w r0, r1, lsl #19

13764: bf00 nop

13766: eb42 0202 adc.w r2, r2, r2

1376a: bf28 it cs

1376c: eba0 40c1 subcs.w r0, r0, r1, lsl #19

13770: ebb0 4f81 cmp.w r0, r1, lsl #18

13774: bf00 nop

13776: eb42 0202 adc.w r2, r2, r2

1377a: bf28 it cs

1377c: eba0 4081 subcs.w r0, r0, r1, lsl #18

13780: ebb0 4f41 cmp.w r0, r1, lsl #17

13784: bf00 nop

13786: eb42 0202 adc.w r2, r2, r2

1378a: bf28 it cs

1378c: eba0 4041 subcs.w r0, r0, r1, lsl #17

13790: ebb0 4f01 cmp.w r0, r1, lsl #16

13794: bf00 nop

13796: eb42 0202 adc.w r2, r2, r2

1379a: bf28 it cs

1379c: eba0 4001 subcs.w r0, r0, r1, lsl #16

137a0: ebb0 3fc1 cmp.w r0, r1, lsl #15

137a4: bf00 nop

137a6: eb42 0202 adc.w r2, r2, r2

137aa: bf28 it cs

137ac: eba0 30c1 subcs.w r0, r0, r1, lsl #15

137b0: ebb0 3f81 cmp.w r0, r1, lsl #14

137b4: bf00 nop

137b6: eb42 0202 adc.w r2, r2, r2

137ba: bf28 it cs

137bc: eba0 3081 subcs.w r0, r0, r1, lsl #14

137c0: ebb0 3f41 cmp.w r0, r1, lsl #13

137c4: bf00 nop

137c6: eb42 0202 adc.w r2, r2, r2

137ca: bf28 it cs

137cc: eba0 3041 subcs.w r0, r0, r1, lsl #13

137d0: ebb0 3f01 cmp.w r0, r1, lsl #12

137d4: bf00 nop

137d6: eb42 0202 adc.w r2, r2, r2

137da: bf28 it cs

137dc: eba0 3001 subcs.w r0, r0, r1, lsl #12

137e0: ebb0 2fc1 cmp.w r0, r1, lsl #11

137e4: bf00 nop

137e6: eb42 0202 adc.w r2, r2, r2

137ea: bf28 it cs

137ec: eba0 20c1 subcs.w r0, r0, r1, lsl #11

137f0: ebb0 2f81 cmp.w r0, r1, lsl #10

137f4: bf00 nop

137f6: eb42 0202 adc.w r2, r2, r2

137fa: bf28 it cs

137fc: eba0 2081 subcs.w r0, r0, r1, lsl #10

13800: ebb0 2f41 cmp.w r0, r1, lsl #9

13804: bf00 nop

13806: eb42 0202 adc.w r2, r2, r2

1380a: bf28 it cs

1380c: eba0 2041 subcs.w r0, r0, r1, lsl #9

13810: ebb0 2f01 cmp.w r0, r1, lsl #8

13814: bf00 nop

13816: eb42 0202 adc.w r2, r2, r2

1381a: bf28 it cs

1381c: eba0 2001 subcs.w r0, r0, r1, lsl #8

13820: ebb0 1fc1 cmp.w r0, r1, lsl #7

13824: bf00 nop

13826: eb42 0202 adc.w r2, r2, r2

1382a: bf28 it cs

1382c: eba0 10c1 subcs.w r0, r0, r1, lsl #7

13830: ebb0 1f81 cmp.w r0, r1, lsl #6

13834: bf00 nop

13836: eb42 0202 adc.w r2, r2, r2

1383a: bf28 it cs

1383c: eba0 1081 subcs.w r0, r0, r1, lsl #6

13840: ebb0 1f41 cmp.w r0, r1, lsl #5

13844: bf00 nop

13846: eb42 0202 adc.w r2, r2, r2

1384a: bf28 it cs

1384c: eba0 1041 subcs.w r0, r0, r1, lsl #5

13850: ebb0 1f01 cmp.w r0, r1, lsl #4

13854: bf00 nop

13856: eb42 0202 adc.w r2, r2, r2

1385a: bf28 it cs

1385c: eba0 1001 subcs.w r0, r0, r1, lsl #4

13860: ebb0 0fc1 cmp.w r0, r1, lsl #3

13864: bf00 nop

13866: eb42 0202 adc.w r2, r2, r2

1386a: bf28 it cs

1386c: eba0 00c1 subcs.w r0, r0, r1, lsl #3

13870: ebb0 0f81 cmp.w r0, r1, lsl #2

13874: bf00 nop

13876: eb42 0202 adc.w r2, r2, r2

1387a: bf28 it cs

1387c: eba0 0081 subcs.w r0, r0, r1, lsl #2

13880: ebb0 0f41 cmp.w r0, r1, lsl #1

13884: bf00 nop

13886: eb42 0202 adc.w r2, r2, r2

1388a: bf28 it cs

1388c: eba0 0041 subcs.w r0, r0, r1, lsl #1

13890: ebb0 0f01 cmp.w r0, r1

13894: bf00 nop

13896: eb42 0202 adc.w r2, r2, r2

1389a: bf28 it cs

1389c: eba0 0001 subcs.w r0, r0, r1

138a0: 4610 mov r0, r2

138a2: 4770 bx lr

138a4: bf0c ite eq

138a6: 2001 moveq r0, #1

138a8: 2000 movne r0, #0

138aa: 4770 bx lr

138ac: fab1 f281 clz r2, r1

138b0: f1c2 021f rsb r2, r2, #31

138b4: fa20 f002 lsr.w r0, r0, r2

138b8: 4770 bx lr

138ba: b108 cbz r0, 138c0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b58>

138bc: f04f 30ff mov.w r0, #4294967295 ; 0xffffffff

138c0: f000 b97e b.w 13bc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e58>

138c4: 2900 cmp r1, #0

138c6: d0f8 beq.n 138ba <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b52>

138c8: e92d 4003 stmdb sp!, {r0, r1, lr}

138cc: f7ff fecc bl 13668 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2900>

138d0: e8bd 4006 ldmia.w sp!, {r1, r2, lr}

138d4: fb02 f300 mul.w r3, r2, r0

138d8: eba1 0103 sub.w r1, r1, r3

138dc: 4770 bx lr

138de: bf00 nop

138e0: 2900 cmp r1, #0

138e2: f000 813e beq.w 13b62 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2dfa>

138e6: ea80 0c01 eor.w ip, r0, r1

138ea: bf48 it mi

138ec: 4249 negmi r1, r1

138ee: 1e4a subs r2, r1, #1

138f0: f000 811f beq.w 13b32 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2dca>

138f4: 0003 movs r3, r0

138f6: bf48 it mi

138f8: 4243 negmi r3, r0

138fa: 428b cmp r3, r1

138fc: f240 811e bls.w 13b3c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2dd4>

13900: 4211 tst r1, r2

13902: f000 8123 beq.w 13b4c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2de4>

13906: fab3 f283 clz r2, r3

1390a: fab1 f081 clz r0, r1

1390e: eba0 0202 sub.w r2, r0, r2

13912: f1c2 021f rsb r2, r2, #31

13916: a004 add r0, pc, #16 ; (adr r0, 13928 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2bc0>)

13918: eb00 1202 add.w r2, r0, r2, lsl #4

1391c: f04f 0000 mov.w r0, #0

13920: 4697 mov pc, r2

13922: bf00 nop

13924: f3af 8000 nop.w

13928: ebb3 7fc1 cmp.w r3, r1, lsl #31

1392c: bf00 nop

1392e: eb40 0000 adc.w r0, r0, r0

13932: bf28 it cs

13934: eba3 73c1 subcs.w r3, r3, r1, lsl #31

13938: ebb3 7f81 cmp.w r3, r1, lsl #30

1393c: bf00 nop

1393e: eb40 0000 adc.w r0, r0, r0

13942: bf28 it cs

13944: eba3 7381 subcs.w r3, r3, r1, lsl #30

13948: ebb3 7f41 cmp.w r3, r1, lsl #29

1394c: bf00 nop

1394e: eb40 0000 adc.w r0, r0, r0

13952: bf28 it cs

13954: eba3 7341 subcs.w r3, r3, r1, lsl #29

13958: ebb3 7f01 cmp.w r3, r1, lsl #28

1395c: bf00 nop

1395e: eb40 0000 adc.w r0, r0, r0

13962: bf28 it cs

13964: eba3 7301 subcs.w r3, r3, r1, lsl #28

13968: ebb3 6fc1 cmp.w r3, r1, lsl #27

1396c: bf00 nop

1396e: eb40 0000 adc.w r0, r0, r0

13972: bf28 it cs

13974: eba3 63c1 subcs.w r3, r3, r1, lsl #27

13978: ebb3 6f81 cmp.w r3, r1, lsl #26

1397c: bf00 nop

1397e: eb40 0000 adc.w r0, r0, r0

13982: bf28 it cs

13984: eba3 6381 subcs.w r3, r3, r1, lsl #26

13988: ebb3 6f41 cmp.w r3, r1, lsl #25

1398c: bf00 nop

1398e: eb40 0000 adc.w r0, r0, r0

13992: bf28 it cs

13994: eba3 6341 subcs.w r3, r3, r1, lsl #25

13998: ebb3 6f01 cmp.w r3, r1, lsl #24

1399c: bf00 nop

1399e: eb40 0000 adc.w r0, r0, r0

139a2: bf28 it cs

139a4: eba3 6301 subcs.w r3, r3, r1, lsl #24

139a8: ebb3 5fc1 cmp.w r3, r1, lsl #23

139ac: bf00 nop

139ae: eb40 0000 adc.w r0, r0, r0

139b2: bf28 it cs

139b4: eba3 53c1 subcs.w r3, r3, r1, lsl #23

139b8: ebb3 5f81 cmp.w r3, r1, lsl #22

139bc: bf00 nop

139be: eb40 0000 adc.w r0, r0, r0

139c2: bf28 it cs

139c4: eba3 5381 subcs.w r3, r3, r1, lsl #22

139c8: ebb3 5f41 cmp.w r3, r1, lsl #21

139cc: bf00 nop

139ce: eb40 0000 adc.w r0, r0, r0

139d2: bf28 it cs

139d4: eba3 5341 subcs.w r3, r3, r1, lsl #21

139d8: ebb3 5f01 cmp.w r3, r1, lsl #20

139dc: bf00 nop

139de: eb40 0000 adc.w r0, r0, r0

139e2: bf28 it cs

139e4: eba3 5301 subcs.w r3, r3, r1, lsl #20

139e8: ebb3 4fc1 cmp.w r3, r1, lsl #19

139ec: bf00 nop

139ee: eb40 0000 adc.w r0, r0, r0

139f2: bf28 it cs

139f4: eba3 43c1 subcs.w r3, r3, r1, lsl #19

139f8: ebb3 4f81 cmp.w r3, r1, lsl #18

139fc: bf00 nop

139fe: eb40 0000 adc.w r0, r0, r0

13a02: bf28 it cs

13a04: eba3 4381 subcs.w r3, r3, r1, lsl #18

13a08: ebb3 4f41 cmp.w r3, r1, lsl #17

13a0c: bf00 nop

13a0e: eb40 0000 adc.w r0, r0, r0

13a12: bf28 it cs

13a14: eba3 4341 subcs.w r3, r3, r1, lsl #17

13a18: ebb3 4f01 cmp.w r3, r1, lsl #16

13a1c: bf00 nop

13a1e: eb40 0000 adc.w r0, r0, r0

13a22: bf28 it cs

13a24: eba3 4301 subcs.w r3, r3, r1, lsl #16

13a28: ebb3 3fc1 cmp.w r3, r1, lsl #15

13a2c: bf00 nop

13a2e: eb40 0000 adc.w r0, r0, r0

13a32: bf28 it cs

13a34: eba3 33c1 subcs.w r3, r3, r1, lsl #15

13a38: ebb3 3f81 cmp.w r3, r1, lsl #14

13a3c: bf00 nop

13a3e: eb40 0000 adc.w r0, r0, r0

13a42: bf28 it cs

13a44: eba3 3381 subcs.w r3, r3, r1, lsl #14

13a48: ebb3 3f41 cmp.w r3, r1, lsl #13

13a4c: bf00 nop

13a4e: eb40 0000 adc.w r0, r0, r0

13a52: bf28 it cs

13a54: eba3 3341 subcs.w r3, r3, r1, lsl #13

13a58: ebb3 3f01 cmp.w r3, r1, lsl #12

13a5c: bf00 nop

13a5e: eb40 0000 adc.w r0, r0, r0

13a62: bf28 it cs

13a64: eba3 3301 subcs.w r3, r3, r1, lsl #12

13a68: ebb3 2fc1 cmp.w r3, r1, lsl #11

13a6c: bf00 nop

13a6e: eb40 0000 adc.w r0, r0, r0

13a72: bf28 it cs

13a74: eba3 23c1 subcs.w r3, r3, r1, lsl #11

13a78: ebb3 2f81 cmp.w r3, r1, lsl #10

13a7c: bf00 nop

13a7e: eb40 0000 adc.w r0, r0, r0

13a82: bf28 it cs

13a84: eba3 2381 subcs.w r3, r3, r1, lsl #10

13a88: ebb3 2f41 cmp.w r3, r1, lsl #9

13a8c: bf00 nop

13a8e: eb40 0000 adc.w r0, r0, r0

13a92: bf28 it cs

13a94: eba3 2341 subcs.w r3, r3, r1, lsl #9

13a98: ebb3 2f01 cmp.w r3, r1, lsl #8

13a9c: bf00 nop

13a9e: eb40 0000 adc.w r0, r0, r0

13aa2: bf28 it cs

13aa4: eba3 2301 subcs.w r3, r3, r1, lsl #8

13aa8: ebb3 1fc1 cmp.w r3, r1, lsl #7

13aac: bf00 nop

13aae: eb40 0000 adc.w r0, r0, r0

13ab2: bf28 it cs

13ab4: eba3 13c1 subcs.w r3, r3, r1, lsl #7

13ab8: ebb3 1f81 cmp.w r3, r1, lsl #6

13abc: bf00 nop

13abe: eb40 0000 adc.w r0, r0, r0

13ac2: bf28 it cs

13ac4: eba3 1381 subcs.w r3, r3, r1, lsl #6

13ac8: ebb3 1f41 cmp.w r3, r1, lsl #5

13acc: bf00 nop

13ace: eb40 0000 adc.w r0, r0, r0

13ad2: bf28 it cs

13ad4: eba3 1341 subcs.w r3, r3, r1, lsl #5

13ad8: ebb3 1f01 cmp.w r3, r1, lsl #4

13adc: bf00 nop

13ade: eb40 0000 adc.w r0, r0, r0

13ae2: bf28 it cs

13ae4: eba3 1301 subcs.w r3, r3, r1, lsl #4

13ae8: ebb3 0fc1 cmp.w r3, r1, lsl #3

13aec: bf00 nop

13aee: eb40 0000 adc.w r0, r0, r0

13af2: bf28 it cs

13af4: eba3 03c1 subcs.w r3, r3, r1, lsl #3

13af8: ebb3 0f81 cmp.w r3, r1, lsl #2

13afc: bf00 nop

13afe: eb40 0000 adc.w r0, r0, r0

13b02: bf28 it cs

13b04: eba3 0381 subcs.w r3, r3, r1, lsl #2

13b08: ebb3 0f41 cmp.w r3, r1, lsl #1

13b0c: bf00 nop

13b0e: eb40 0000 adc.w r0, r0, r0

13b12: bf28 it cs

13b14: eba3 0341 subcs.w r3, r3, r1, lsl #1

13b18: ebb3 0f01 cmp.w r3, r1

13b1c: bf00 nop

13b1e: eb40 0000 adc.w r0, r0, r0

13b22: bf28 it cs

13b24: eba3 0301 subcs.w r3, r3, r1

13b28: f1bc 0f00 cmp.w ip, #0

13b2c: bf48 it mi

13b2e: 4240 negmi r0, r0

13b30: 4770 bx lr

13b32: ea9c 0f00 teq ip, r0

13b36: bf48 it mi

13b38: 4240 negmi r0, r0

13b3a: 4770 bx lr

13b3c: bf38 it cc

13b3e: 2000 movcc r0, #0

13b40: bf04 itt eq

13b42: ea4f 70ec moveq.w r0, ip, asr #31

13b46: f040 0001 orreq.w r0, r0, #1

13b4a: 4770 bx lr

13b4c: fab1 f281 clz r2, r1

13b50: f1c2 021f rsb r2, r2, #31

13b54: f1bc 0f00 cmp.w ip, #0

13b58: fa23 f002 lsr.w r0, r3, r2

13b5c: bf48 it mi

13b5e: 4240 negmi r0, r0

13b60: 4770 bx lr

13b62: 2800 cmp r0, #0

13b64: bfc8 it gt

13b66: f06f 4000 mvngt.w r0, #2147483648 ; 0x80000000

13b6a: bfb8 it lt

13b6c: f04f 4000 movlt.w r0, #2147483648 ; 0x80000000

13b70: f000 b826 b.w 13bc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e58>

13b74: 2900 cmp r1, #0

13b76: d0f4 beq.n 13b62 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2dfa>

13b78: e92d 4003 stmdb sp!, {r0, r1, lr}

13b7c: f7ff feb3 bl 138e6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b7e>

13b80: e8bd 4006 ldmia.w sp!, {r1, r2, lr}

13b84: fb02 f300 mul.w r3, r2, r0

13b88: eba1 0103 sub.w r1, r1, r3

13b8c: 4770 bx lr

13b8e: bf00 nop

13b90: b953 cbnz r3, 13ba8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e40>

13b92: b94a cbnz r2, 13ba8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e40>

13b94: 2900 cmp r1, #0

13b96: bf08 it eq

13b98: 2800 cmpeq r0, #0

13b9a: bf1c itt ne

13b9c: f04f 31ff movne.w r1, #4294967295 ; 0xffffffff

13ba0: f04f 30ff movne.w r0, #4294967295 ; 0xffffffff

13ba4: f000 b80c b.w 13bc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e58>

13ba8: f1ad 0c08 sub.w ip, sp, #8

13bac: e96d ce04 strd ip, lr, [sp, #-16]!

13bb0: f000 f80c bl 13bcc <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e64>

13bb4: f8dd e004 ldr.w lr, [sp, #4]

13bb8: e9dd 2302 ldrd r2, r3, [sp, #8]

13bbc: b004 add sp, #16

13bbe: 4770 bx lr

13bc0: b502 push {r1, lr}

13bc2: f04f 0008 mov.w r0, #8

13bc6: f000 f88d bl 13ce4 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f7c>

13bca: bd02 pop {r1, pc}

13bcc: 4299 cmp r1, r3

13bce: bf08 it eq

13bd0: 4290 cmpeq r0, r2

13bd2: e92d 4ff0 stmdb sp!, {r4, r5, r6, r7, r8, r9, sl, fp, lr}

13bd6: 4604 mov r4, r0

13bd8: bf38 it cc

13bda: 2000 movcc r0, #0

13bdc: 460d mov r5, r1

13bde: 9f09 ldr r7, [sp, #36] ; 0x24

13be0: bf38 it cc

13be2: 4601 movcc r1, r0

13be4: d36c bcc.n 13cc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f58>

13be6: 4690 mov r8, r2

13be8: 4699 mov r9, r3

13bea: fab3 f683 clz r6, r3

13bee: 2b00 cmp r3, #0

13bf0: d06f beq.n 13cd2 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f6a>

13bf2: fab5 f185 clz r1, r5

13bf6: 2d00 cmp r5, #0

13bf8: d067 beq.n 13cca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f62>

13bfa: 1a76 subs r6, r6, r1

13bfc: f1a6 0c20 sub.w ip, r6, #32

13c00: fa09 fb06 lsl.w fp, r9, r6

13c04: fa08 f30c lsl.w r3, r8, ip

13c08: f1c6 0e20 rsb lr, r6, #32

13c0c: ea4b 0b03 orr.w fp, fp, r3

13c10: fa28 f30e lsr.w r3, r8, lr

13c14: ea4b 0b03 orr.w fp, fp, r3

13c18: fa08 fa06 lsl.w sl, r8, r6

13c1c: 455d cmp r5, fp

13c1e: bf08 it eq

13c20: 4554 cmpeq r4, sl

13c22: bf3c itt cc

13c24: 2000 movcc r0, #0

13c26: 4601 movcc r1, r0

13c28: d30a bcc.n 13c40 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ed8>

13c2a: 2001 movs r0, #1

13c2c: ebb4 040a subs.w r4, r4, sl

13c30: fa00 f10c lsl.w r1, r0, ip

13c34: fa20 f30e lsr.w r3, r0, lr

13c38: eb65 050b sbc.w r5, r5, fp

13c3c: 4319 orrs r1, r3

13c3e: 40b0 lsls r0, r6

13c40: 2e00 cmp r6, #0

13c42: d03d beq.n 13cc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f58>

13c44: ea4f 085a mov.w r8, sl, lsr #1

13c48: 4632 mov r2, r6

13c4a: ea48 78cb orr.w r8, r8, fp, lsl #31

13c4e: ea4f 095b mov.w r9, fp, lsr #1

13c52: e00b b.n 13c6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f04>

13c54: ebb4 0308 subs.w r3, r4, r8

13c58: eb65 0a09 sbc.w sl, r5, r9

13c5c: 18db adds r3, r3, r3

13c5e: eb4a 0a0a adc.w sl, sl, sl

13c62: 1c5c adds r4, r3, #1

13c64: f14a 0500 adc.w r5, sl, #0

13c68: 3a01 subs r2, #1

13c6a: d007 beq.n 13c7c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f14>

13c6c: 454d cmp r5, r9

13c6e: bf08 it eq

13c70: 4544 cmpeq r4, r8

13c72: d2ef bcs.n 13c54 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2eec>

13c74: 1924 adds r4, r4, r4

13c76: 416d adcs r5, r5

13c78: 3a01 subs r2, #1

13c7a: d1f7 bne.n 13c6c <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f04>

13c7c: fa05 fe0e lsl.w lr, r5, lr

13c80: fa24 f306 lsr.w r3, r4, r6

13c84: fa25 fc0c lsr.w ip, r5, ip

13c88: ea43 030e orr.w r3, r3, lr

13c8c: 1900 adds r0, r0, r4

13c8e: fa25 fe06 lsr.w lr, r5, r6

13c92: ea43 040c orr.w r4, r3, ip

13c96: f1a6 0c20 sub.w ip, r6, #32

13c9a: f1c6 0220 rsb r2, r6, #32

13c9e: fa0e f306 lsl.w r3, lr, r6

13ca2: fa04 fc0c lsl.w ip, r4, ip

13ca6: fa04 f606 lsl.w r6, r4, r6

13caa: ea43 030c orr.w r3, r3, ip

13cae: fa24 f202 lsr.w r2, r4, r2

13cb2: eb45 0101 adc.w r1, r5, r1

13cb6: 4313 orrs r3, r2

13cb8: 1b80 subs r0, r0, r6

13cba: 4675 mov r5, lr

13cbc: eb61 0103 sbc.w r1, r1, r3

13cc0: b10f cbz r7, 13cc6 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f5e>

13cc2: e9c7 4500 strd r4, r5, [r7]

13cc6: e8bd 8ff0 ldmia.w sp!, {r4, r5, r6, r7, r8, r9, sl, fp, pc}

13cca: fab4 f184 clz r1, r4

13cce: 3120 adds r1, #32

13cd0: e793 b.n 13bfa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e92>

13cd2: fab2 f682 clz r6, r2

13cd6: fab5 f185 clz r1, r5

13cda: 3620 adds r6, #32

13cdc: 2d00 cmp r5, #0

13cde: d18c bne.n 13bfa <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2e92>

13ce0: e7f3 b.n 13cca <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2f62>

13ce2: bf00 nop

13ce4: b530 push {r4, r5, lr}

13ce6: b0c1 sub sp, #260 ; 0x104

13ce8: 2280 movs r2, #128 ; 0x80

13cea: 491e ldr r1, [pc, #120] ; (13d64 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2ffc>)

13cec: eb0d 0302 add.w r3, sp, r2

13cf0: 4604 mov r4, r0

13cf2: 4479 add r1, pc

13cf4: 466d mov r5, sp

13cf6: 4618 mov r0, r3

13cf8: f7ff ea02 blx 13100 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2398>

13cfc: f06f 4300 mvn.w r3, #2147483648 ; 0x80000000

13d00: 462a mov r2, r5

13d02: 9320 str r3, [sp, #128] ; 0x80

13d04: f04f 0caf mov.w ip, #175 ; 0xaf

13d08: f06f 0301 mvn.w r3, #1

13d0c: 9321 str r3, [sp, #132] ; 0x84

13d0e: 2308 movs r3, #8

13d10: 4601 mov r1, r0

13d12: 2000 movs r0, #0

13d14: f7fd f954 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

13d18: f04f 0c14 mov.w ip, #20

13d1c: f7fd f950 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

13d20: f04f 0ce0 mov.w ip, #224 ; 0xe0

13d24: 4603 mov r3, r0

13d26: f7fd f94b bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

13d2a: f44f 7c86 mov.w ip, #268 ; 0x10c

13d2e: 4601 mov r1, r0

13d30: 4622 mov r2, r4

13d32: 4618 mov r0, r3

13d34: f7fd f944 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

13d38: f510 5f80 cmn.w r0, #4096 ; 0x1000

13d3c: 4604 mov r4, r0

13d3e: d80a bhi.n 13d56 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2fee>

13d40: 4629 mov r1, r5

13d42: 2002 movs r0, #2

13d44: 2200 movs r2, #0

13d46: 2308 movs r3, #8

13d48: f04f 0caf mov.w ip, #175 ; 0xaf

13d4c: f7fd f938 bl 10fc0 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x258>

13d50: 4620 mov r0, r4

13d52: b041 add sp, #260 ; 0x104

13d54: bd30 pop {r4, r5, pc}

13d56: 4b04 ldr r3, [pc, #16] ; (13d68 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x3000>)

13d58: 4242 negs r2, r0

13d5a: f04f 34ff mov.w r4, #4294967295 ; 0xffffffff

13d5e: 447b add r3, pc

13d60: 601a str r2, [r3, #0]

13d62: e7ed b.n 13d40 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2fd8>

13d64: 0876 lsrs r6, r6, #1

13d66: 0000 movs r0, r0

13d68: 5bf6 ldrh r6, [r6, r7]

13d6a: 0001 movs r1, r0

13d6c: 0000 movs r0, r0

13d6e: 0000 movs r0, r0

13d70: 4778 bx pc

13d72: 46c0 nop ; (mov r8, r8)

13d74: c000 stmia r0!, {}

13d76: e59f b.n 138b8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b50>

13d78: f00f e08c blx 422e94 <\_r\_debug@@GLIBC\_2.4+0x3f9538>

13d7c: f380 ffff bl 394d7e <\_r\_debug@@GLIBC\_2.4+0x36b422>

13d80: 4778 bx pc

13d82: 46c0 nop ; (mov r8, r8)

13d84: c000 stmia r0!, {}

13d86: e59f b.n 138c8 <\_dl\_catch\_error@@GLIBC\_PRIVATE+0x2b60>

13d88: f00f e08c blx 422ea4 <\_r\_debug@@GLIBC\_2.4+0x3f9548>

13d8c: f2c0 ffff bl 2d4d8e <\_r\_debug@@GLIBC\_2.4+0x2ab432>