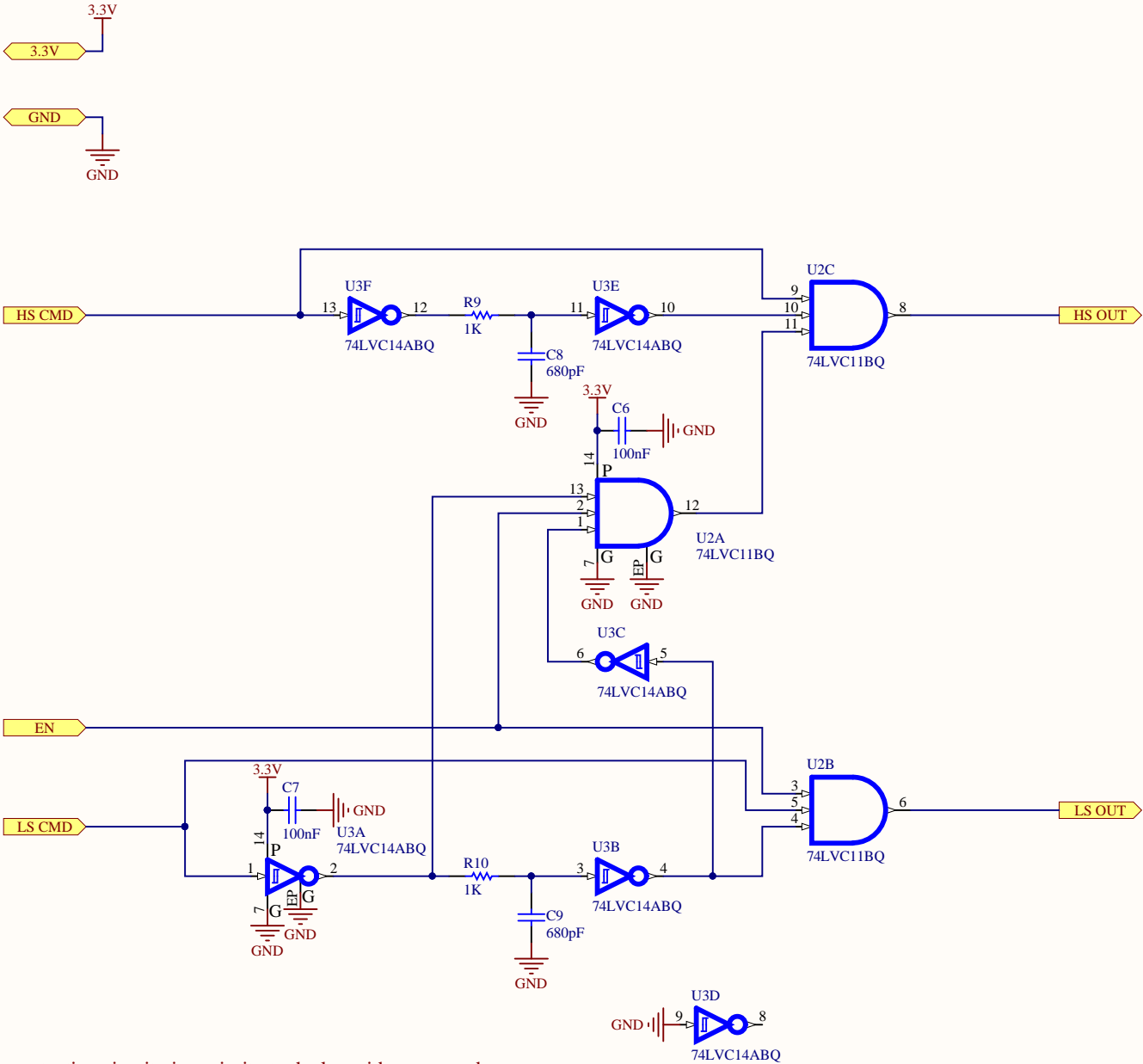


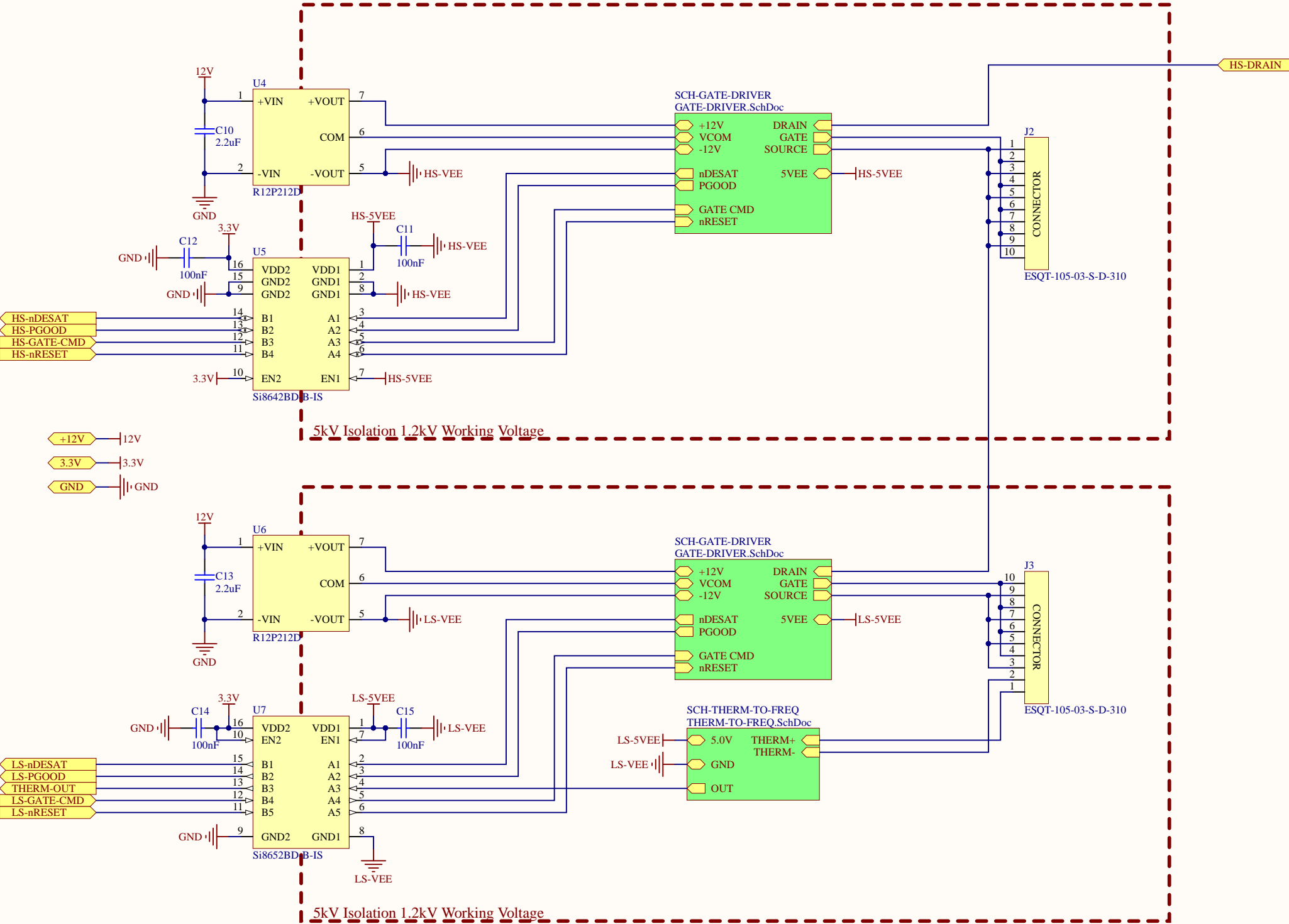
**Makani Project**  
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USA



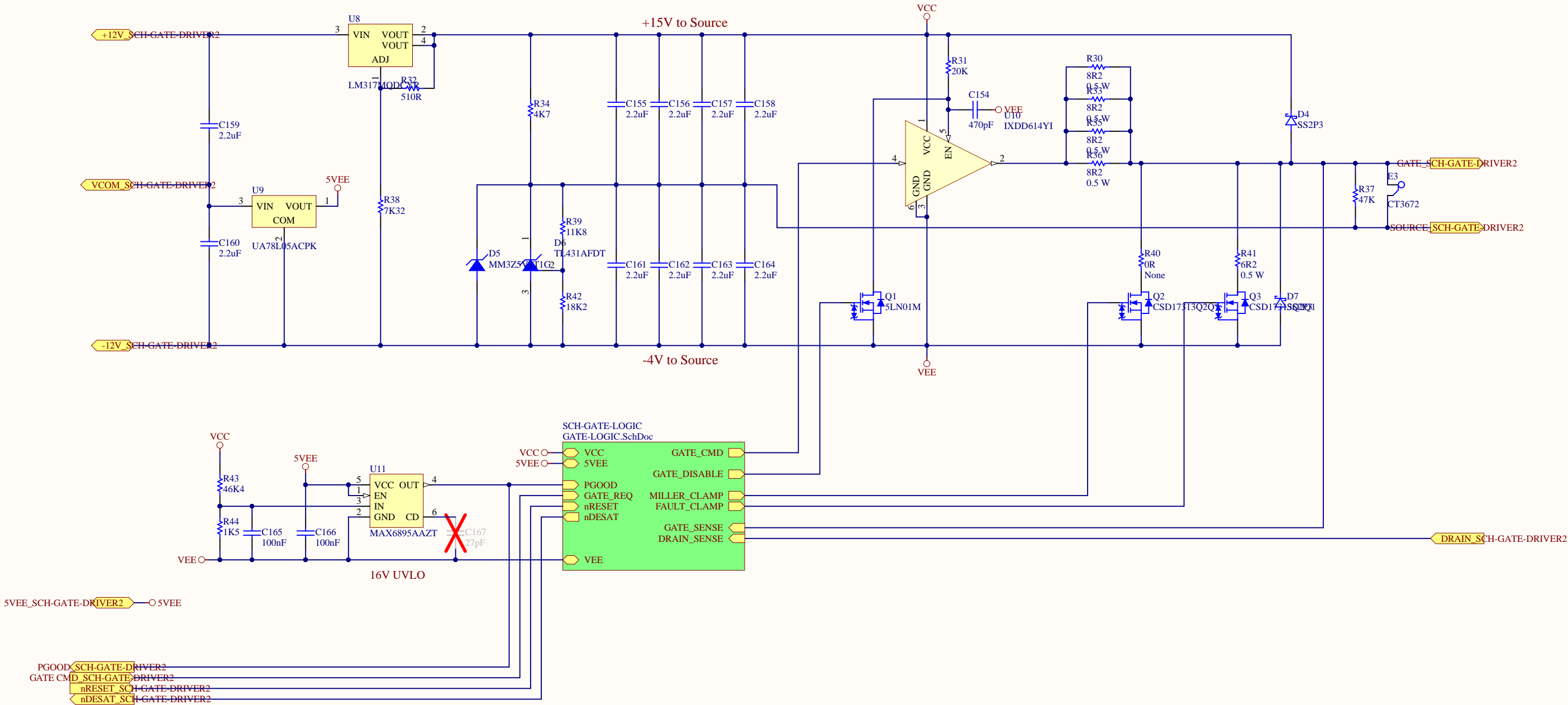


This protection circuit give priority to the low-side command and will prevent simultaneous high-side and low-side commands.

The dead-time is set nominally to 300ns or 1% of a 30kHz switching period.

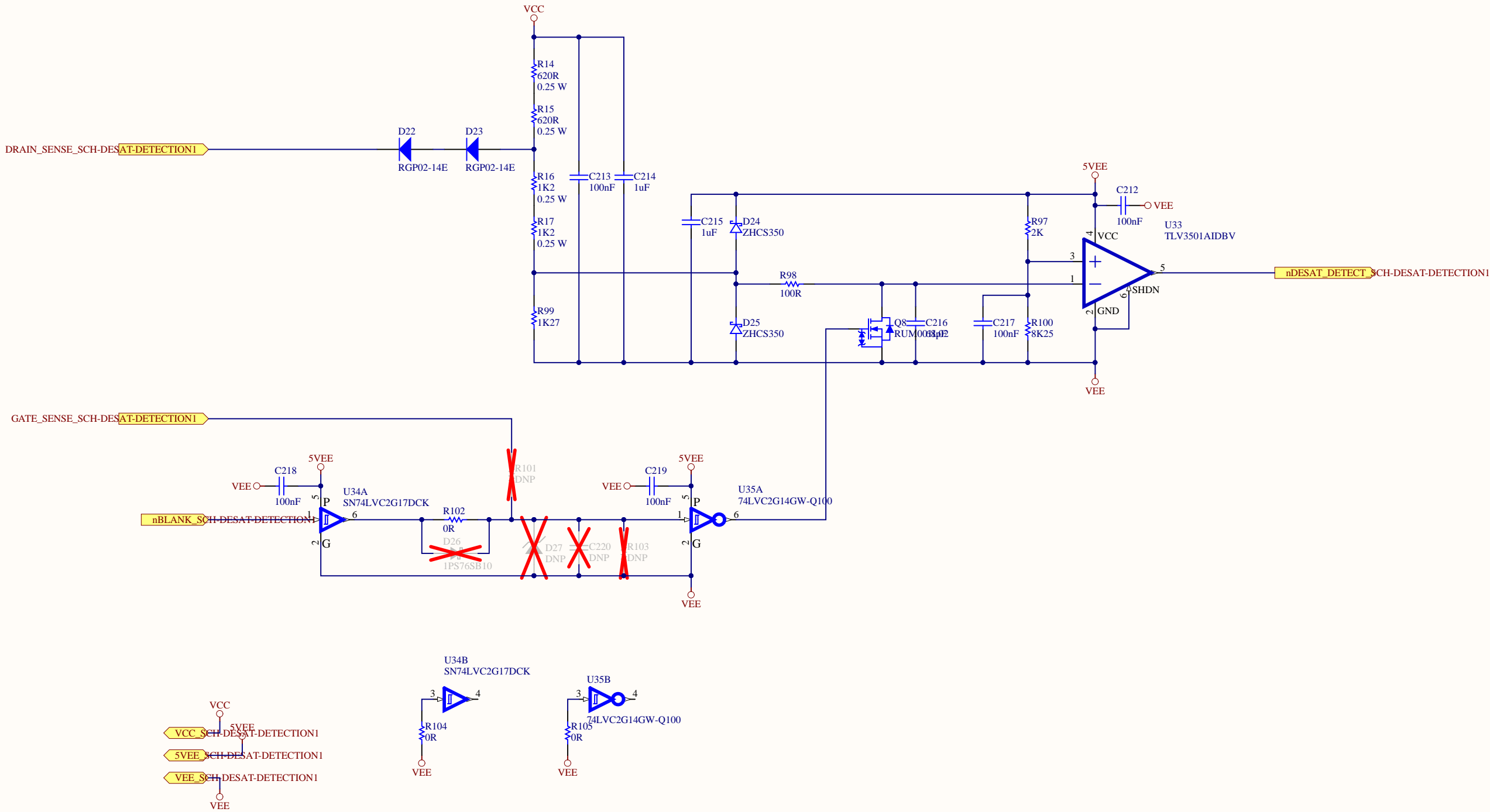




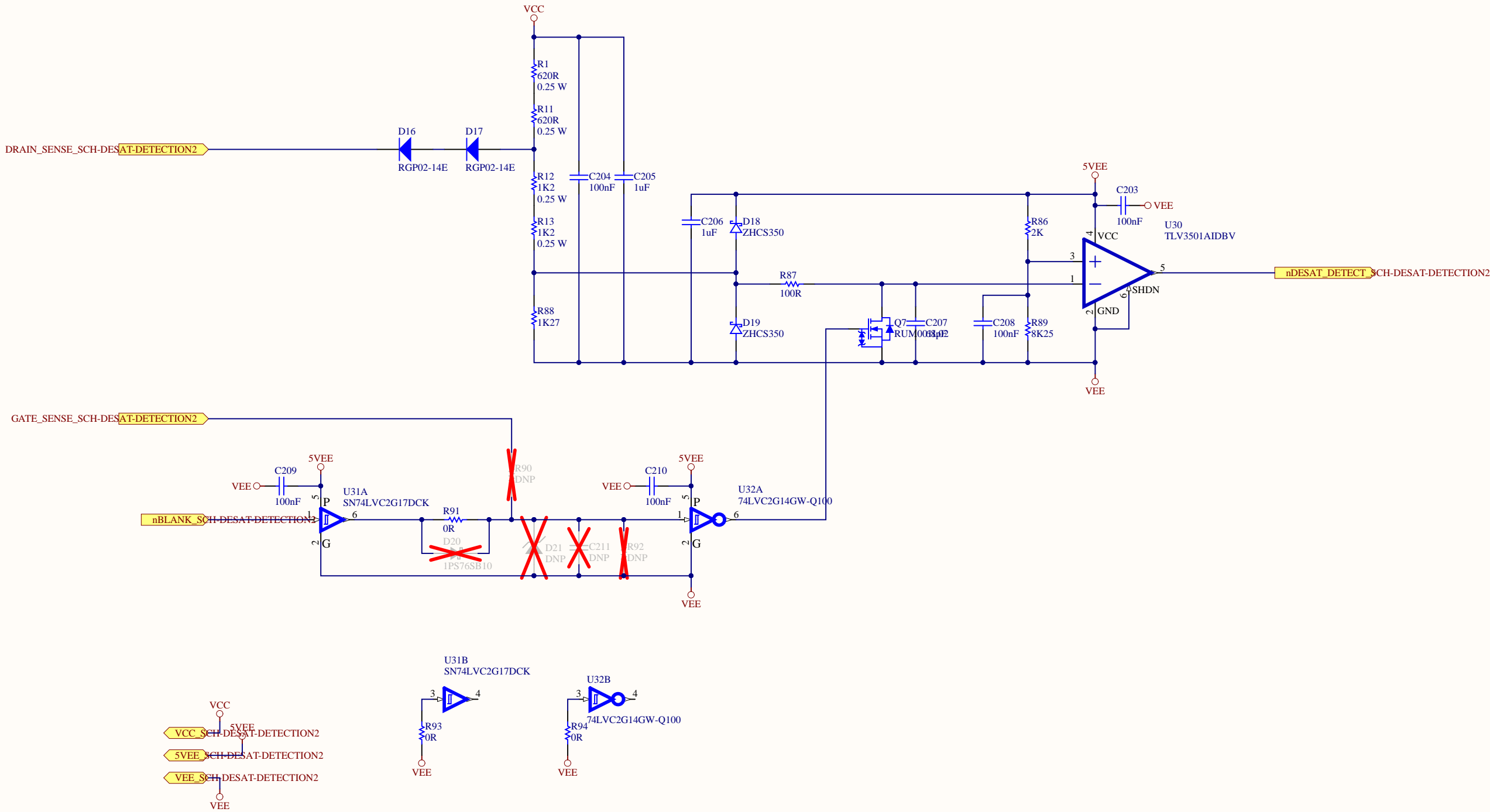


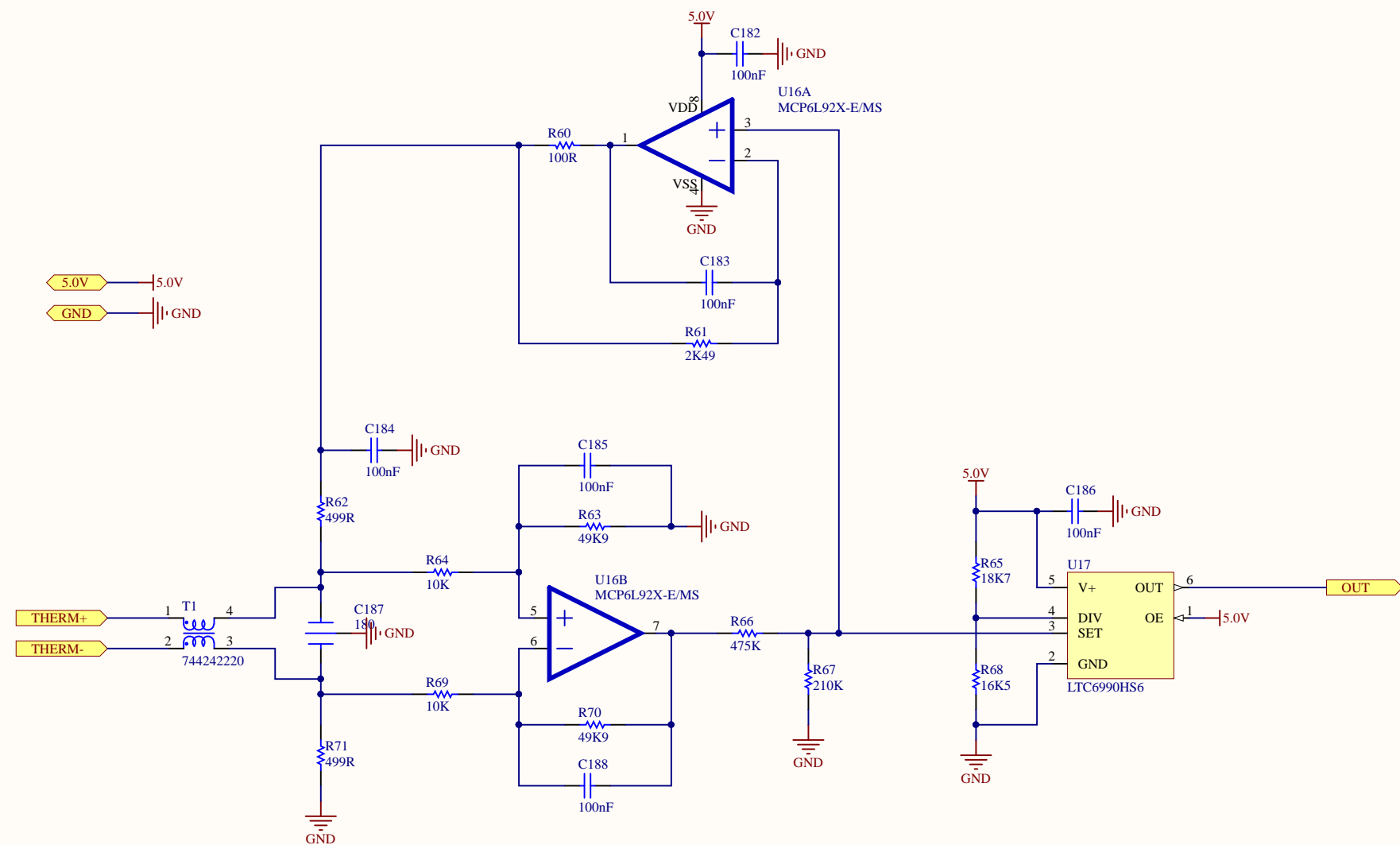










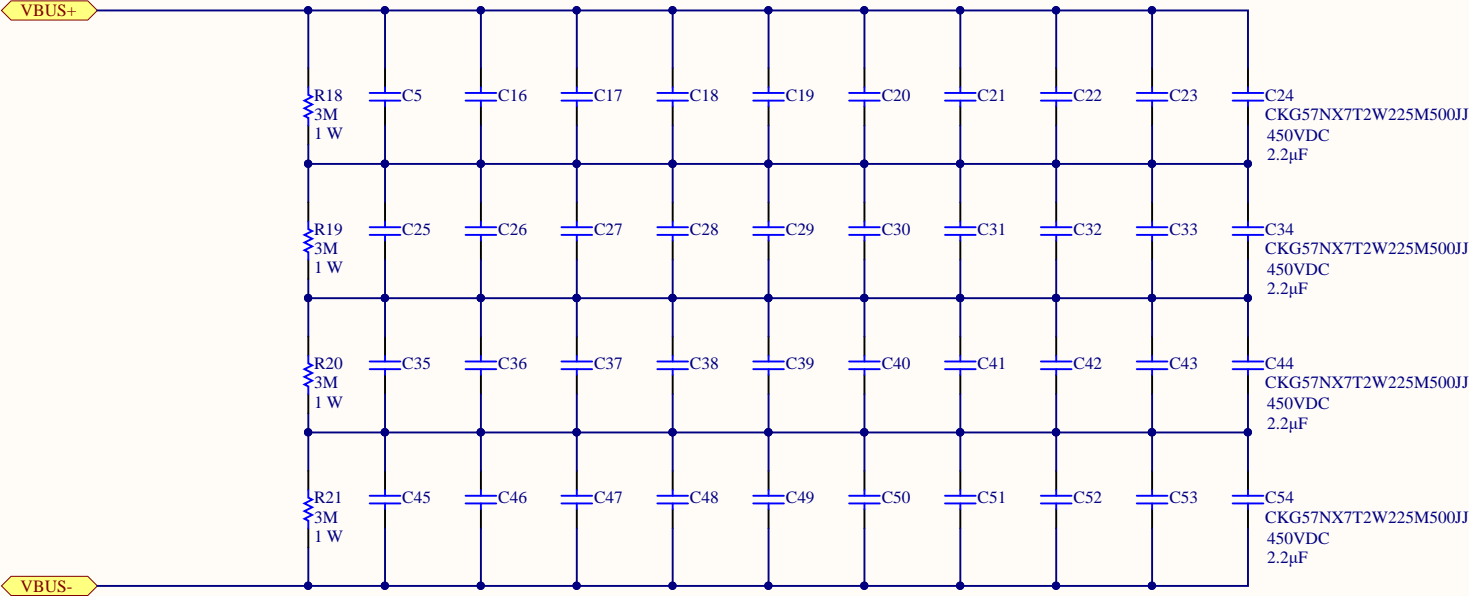


For platinum thermistor P0K5.232.5FC.B.M:  
-20C (460Ohms) to 250C (970Ohms) => 1.4kHz to 700Hz.

Frequency output is not linear with thermistor resistance and must be compensated for best accuracy.

DIV pin is set to provide a division factor of 128 and no HI-Z output.

Quiescent current: 2.5mA



The connection from DC busbar to DC capacitors is made within this schematic.  
The layout should include six holes (three positive, three negative) to match the M4 bolt pattern on the HT-3000 module. The bottom of the PCB should also include an exposed copper area to match the spacer / standoff from busbar to PCB.