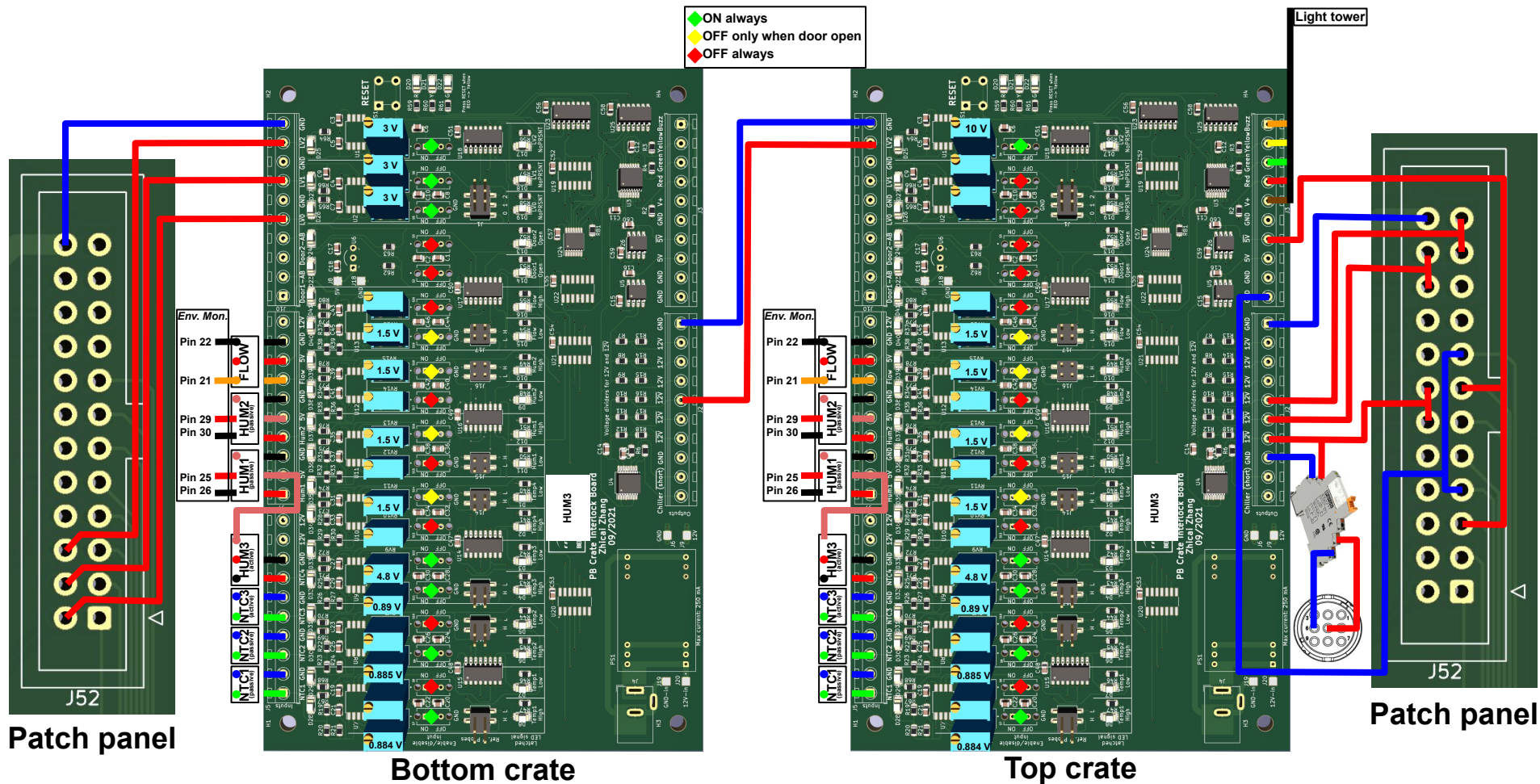


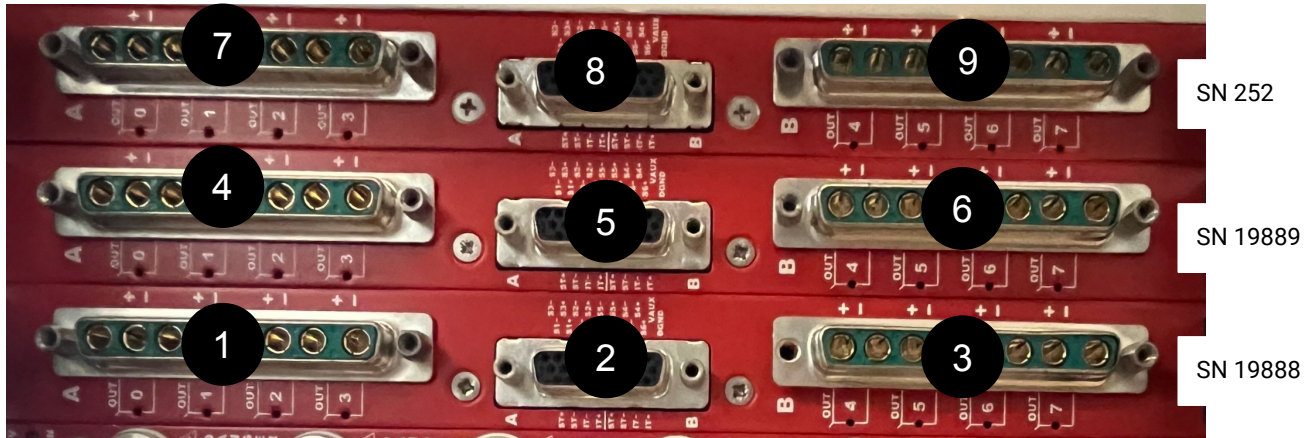
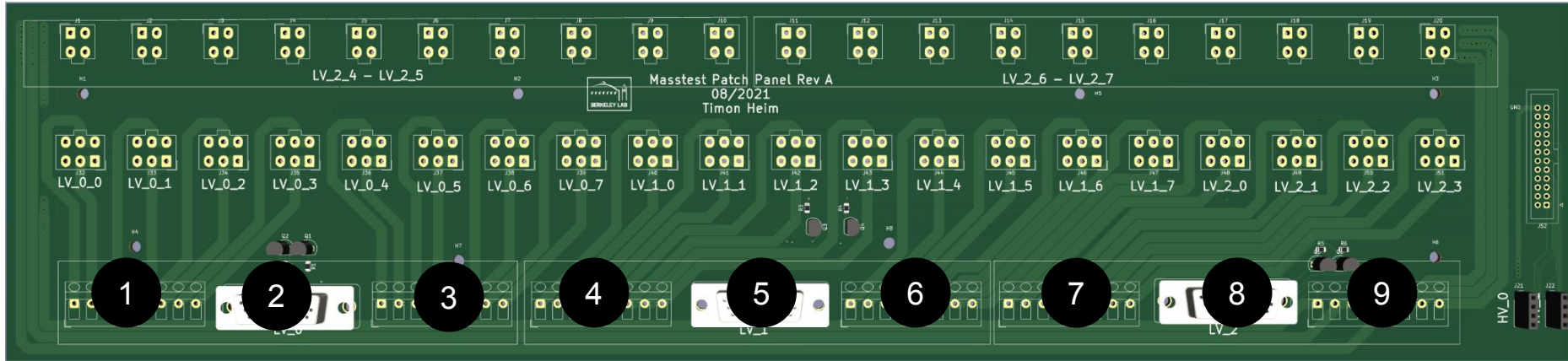
Powerboard Massive Test Crate Connections

Samantha, Timon, Zhicai

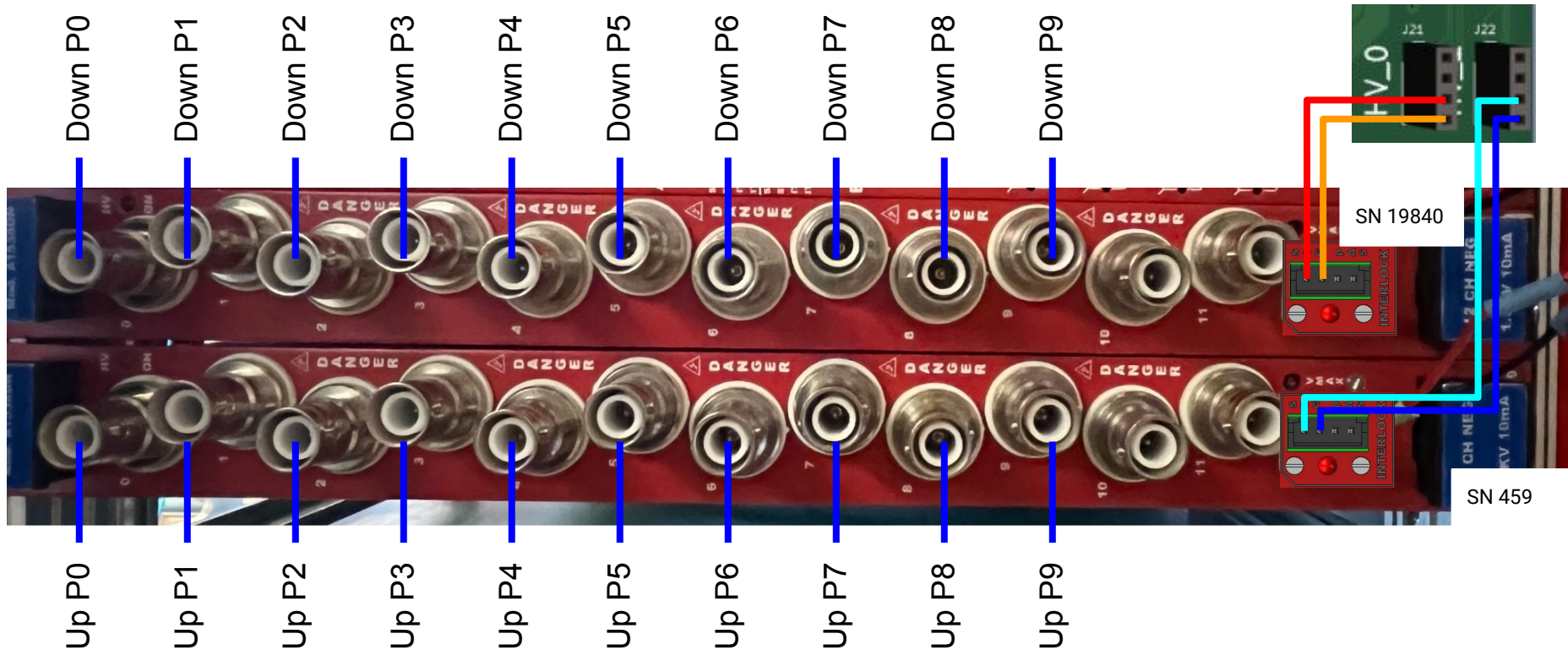
Interlock board connections



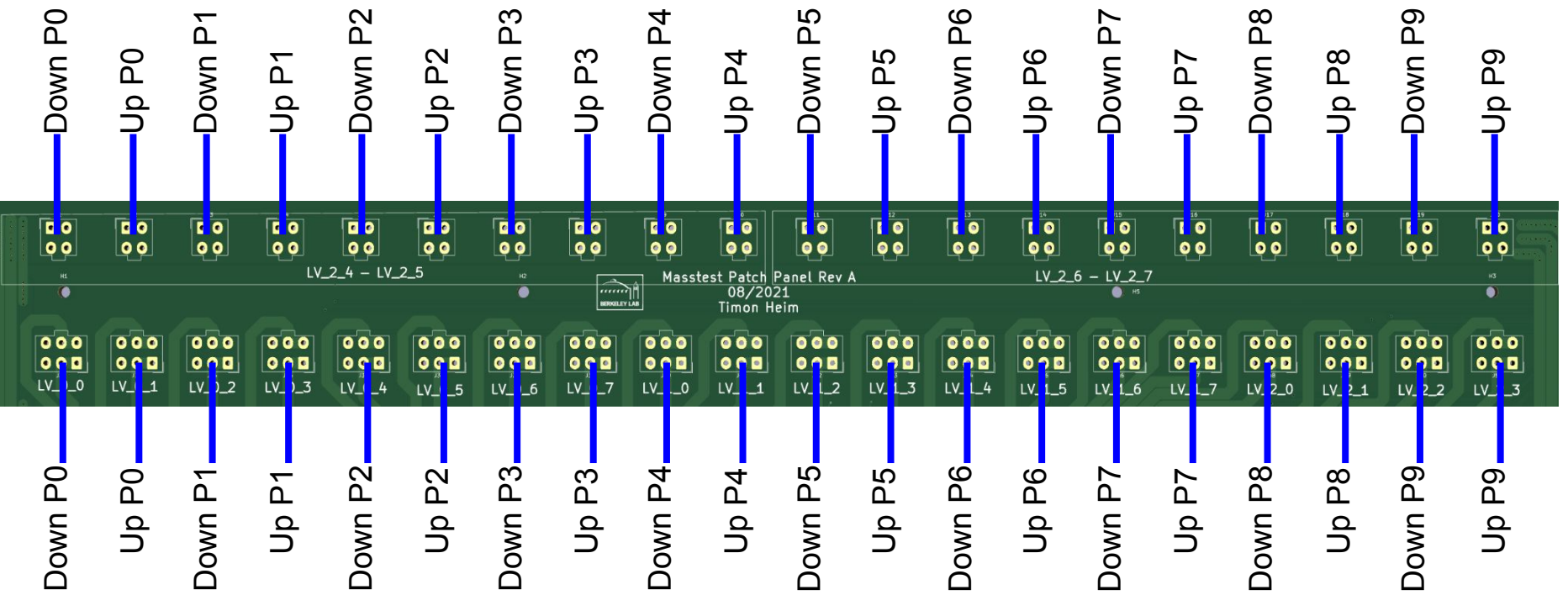
Patch panel - LV connections



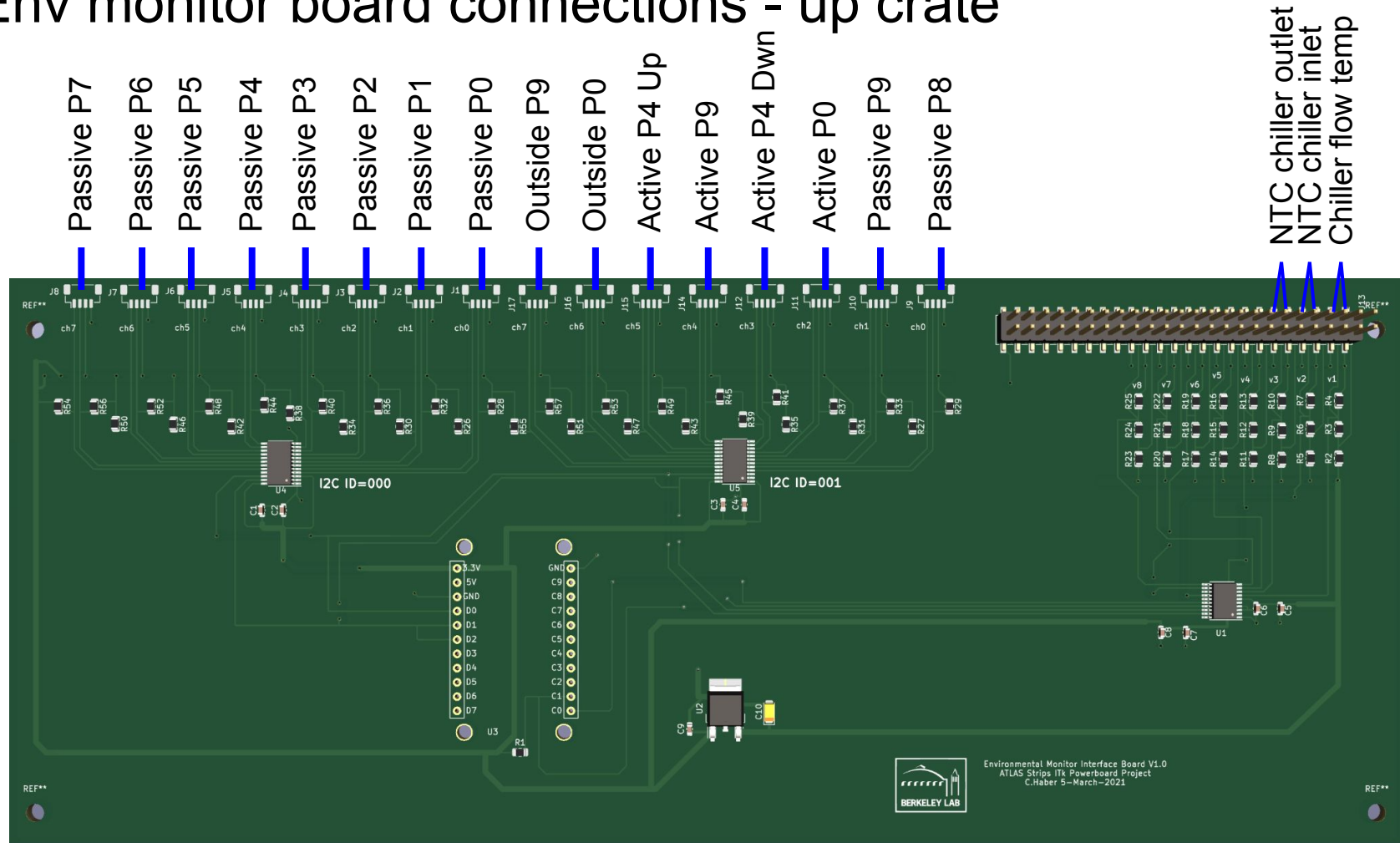
HV - active boards connection



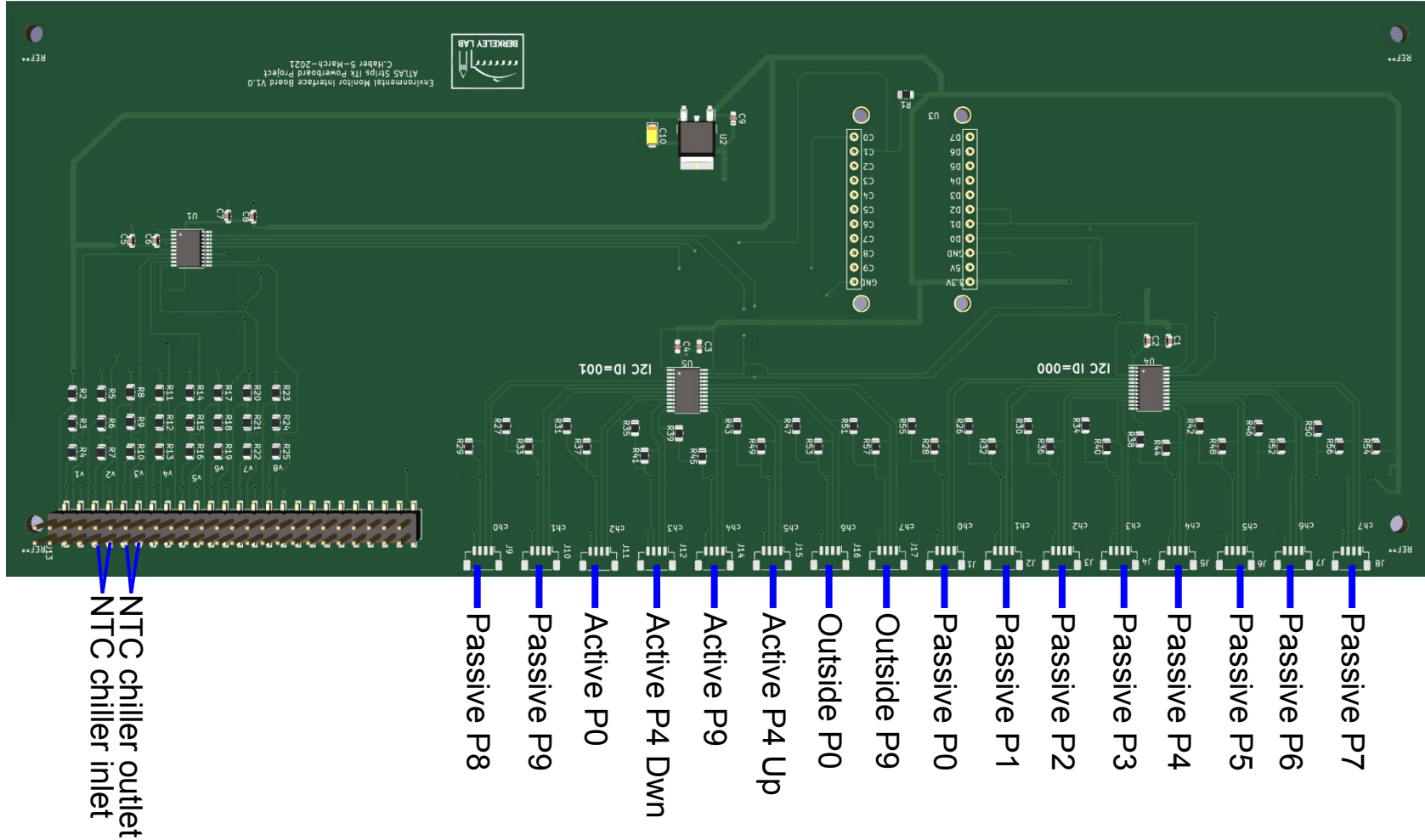
Patch panel - active boards connection



Env monitor board connections - up crate



Env monitor board connections - down crate



P0-P9 definitions

