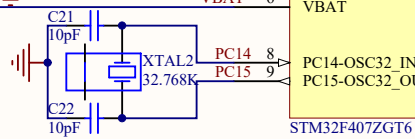
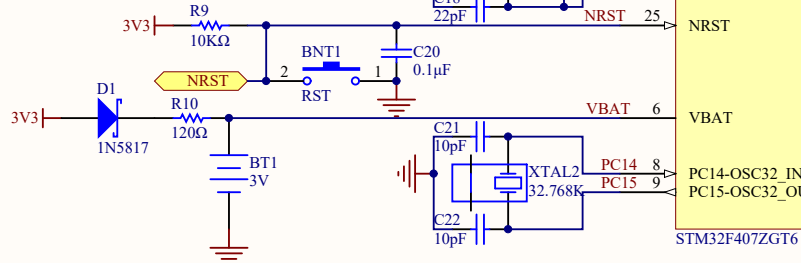


USB\_CONNECT PA8  
USB\_OTG\_FS\_DM PA11  
USB\_OTG\_FS\_DP PA12  
SYS\_JTMS-SWDIO PA13  
SYS\_JTCK-SWCLK PA14  
SYS\_JTDI PA15

BOOT1 R4 10KΩ  
SYS\_JTDO-SWO PB3  
SYS\_JTRST PB4

BOOT0 R6 10KΩ  
C12 22pF  
XTAL1 8M  
C18 22pF  
NRST  
BOOT0 PDR\_ON



U1A

PA0	34	PA0-WKUP
PA1	35	PA1
PA2	36	PA2
PA3	37	PA3
PA4	40	PA4
PA5	41	PA5
PA6	42	PA6
PA7	43	PA7
PA8	100	PA8
PA9	101	PA9
PA10	102	PA10
PA11	103	PA11
PA12	104	PA12
PA13	105	PA13
PA14	106	PA14
PA15	110	PA15
PB0	46	PB0
PB1	47	PB1
PB2	48	PB2/BOOT1
PB3	133	PB3
PB4	134	PB4
PB5	135	PB5
PB6	136	PB6
PB7	137	PB7
PB8	139	PB8
PB9	140	PB9
PB10	69	PB10
PB11	70	PB11
PB12	73	PB12
PB13	74	PB13
PB14	75	PB14
PB15	76	PB15

PC0	26	PC0
PC1	27	PC1
PC2	28	PC2
PC3	29	PC3
PC4	44	PC4
PC5	45	PC5
PC6	96	PC6
PC7	97	PC7
PC8	98	PC8
PC9	99	PC9
PC10	111	PC10
PC11	112	PC11
PC12	113	PC12
PC13	114	PC13-ANTI_TAMP
PC14	138	PC14-OSC32_IN
PC15	143	PC15-OSC32_OUT

STM32F407ZGT6

PD0	114	PD0
PD1	115	PD1
PD2	116	PD2
PD3	117	PD3
PD4	118	PD4
PD5	119	PD5
PD6	122	PD6
PD7	123	PD7
PD8	77	PD8
PD9	78	PD9
PD10	79	PD10
PD11	80	PD11
PD12	81	PD12
PD13	82	PD13
PD14	85	PD14
PD15	86	PD15
PE0	141	PE0
PE1	142	PE1
PE2	143	PE2
PE3	144	PE3
PE4	145	PE4
PE5	146	PE5
PE6	147	PE6
PE7	148	PE7
PE8	149	PE8
PE9	150	PE9
PE10	151	PE10
PE11	152	PE11
PE12	153	PE12
PE13	154	PE13
PE14	155	PE14
PE15	156	PE15

PF0	10	PF0
PF1	11	PF1
PF2	12	PF2
PF3	13	PF3
PF4	14	PF4
PF5	15	PF5
PF6	16	PF6
PF7	17	PF7
PF8	18	PF8
PF9	19	PF9
PF10	20	PF10
PF11	21	PF11
PF12	22	PF12
PF13	23	PF13
PF14	24	PF14
PF15	25	PF15
PG0	56	PG0
PG1	57	PG1
PG2	58	PG2
PG3	59	PG3
PG4	60	PG4
PG5	61	PG5
PG6	62	PG6
PG7	63	PG7
PG8	64	PG8
PG9	65	PG9
PG10	66	PG10
PG11	67	PG11
PG12	68	PG12
PG13	69	PG13
PG14	70	PG14
PG15	71	PG15

P1

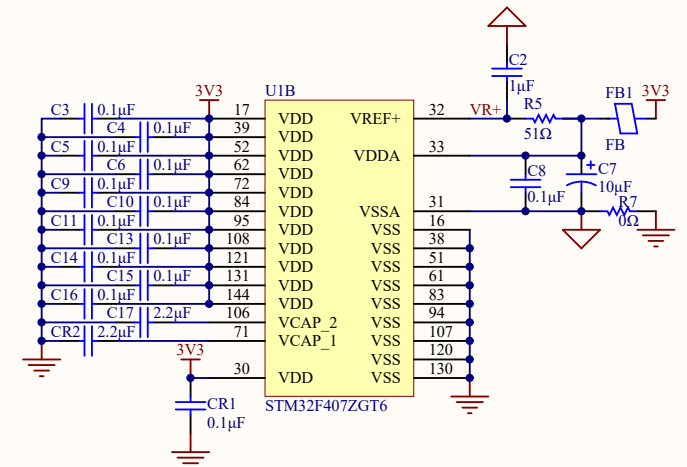
PF6	1	2	PF7
PF8	3	4	PF9
PF10	5	6	NRST
PC0	7	8	PC1
PC2	9	10	PC3
VR+	11	12	PA0
PA1	13	14	PA2
PA3	15	16	PA4
PA5	17	18	PA6
PA7	19	20	PC4
PC5	21	22	PC8
PB1	23	24	PB2
PF11	25	26	PF12
PF13	27	28	PF14
PF15	29	30	PG0
PG1	31	32	PG7
PE8	33	34	PE9
PE10	35	36	PE11
PE12	37	38	PE13
PE14	39	40	PE15
PB10	41	42	PB11
PB13	43	44	PB12
PB15	45	46	PB14
PD9	47	48	PD8
PD11	49	50	PD10
PD13	51	52	PD12
PD15	53	54	PD14
PG3	55	56	PG2
	57	58	PG4

Header 29 X 2

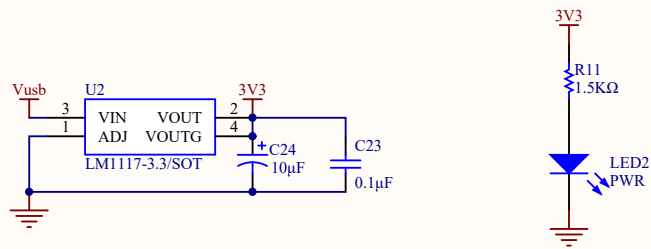
P2

PF4	1	2	PF5
PF2	3	4	PF3
PF0	5	6	PF1
VBAT	7	8	PC13
PE5	9	10	PE6
PE3	11	12	PE4
PE1	13	14	PE2
PB9	15	16	PE0
PB7	17	18	PB8
PB5	19	20	PB6
PB3	21	22	PB4
PG14	23	24	PG15
PG12	25	26	PG13
PG10	27	28	PG11
PD7	29	30	PG9
PD5	31	32	PD6
PD3	33	34	PD4
PD1	35	36	PD2
PC12	37	38	PD0
PC10	39	40	PC11
PA14	41	42	PA15
PA12	43	44	PA13
PA10	45	46	PA11
PA8	47	48	PA9
PC8	49	50	PC9
PC6	51	52	PC7
PG7	53	54	PG8
PG5	55	56	PG6
	57	58	

Header 29 X 2



Title		
Size	Number	Revision
A4		
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File:	D:\	\\STM32F407ZGT6\CORE\BOARD S MCU.SchDoc



Title		
Size A4	Number	Revision
Date:	2017/3/21	Sheet of
File:	D:\	\\.\STM32F407ZGD-CORE: BOARD S POWER.SchDoc

