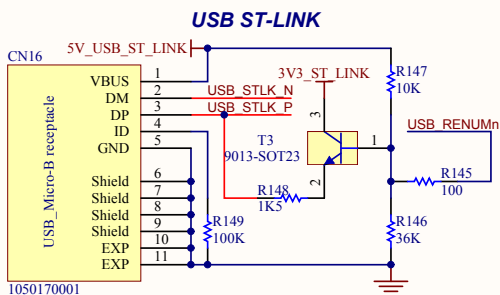
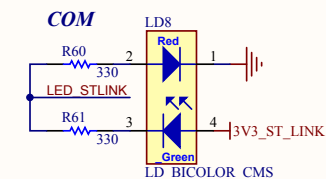
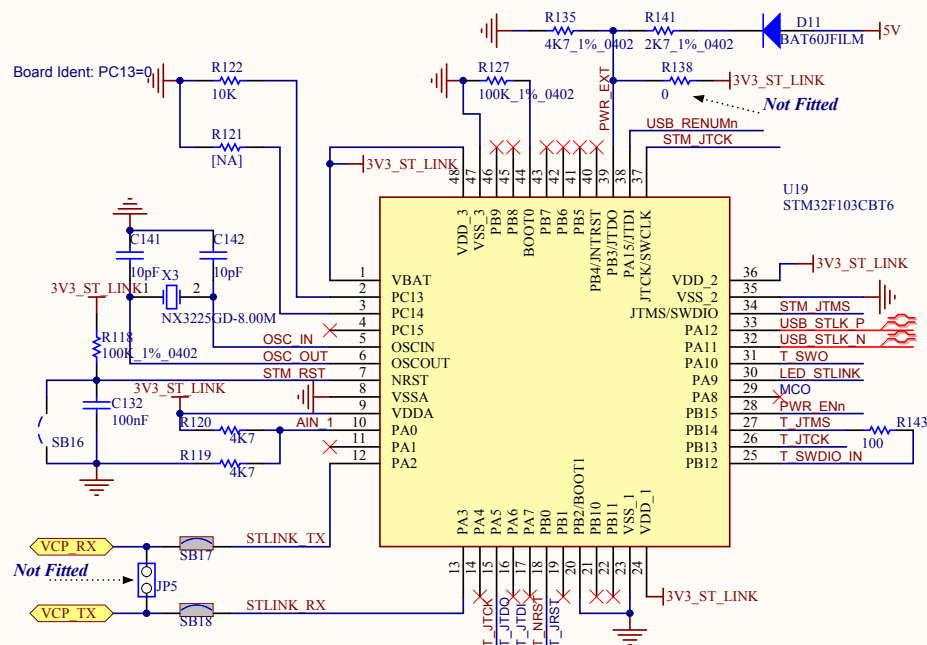
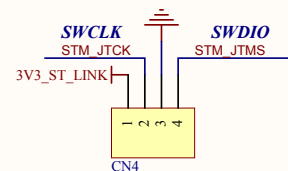


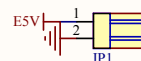
Rev B-01 --> MB1225 B-01, VDD\_VREGDSI of U13 is connected to VDD instead of DSI\_VDDI2, EXT\_I2C for CN2 is built by GPIOs. Add DFSDM\_CKOUT/DATIN1/DATIN5, Remove L1  
Rev A-01 --> MB1225 A-01



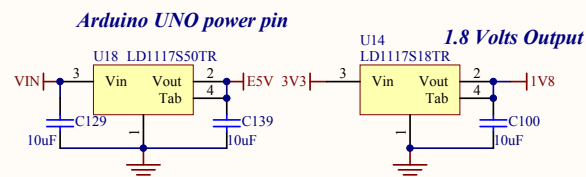
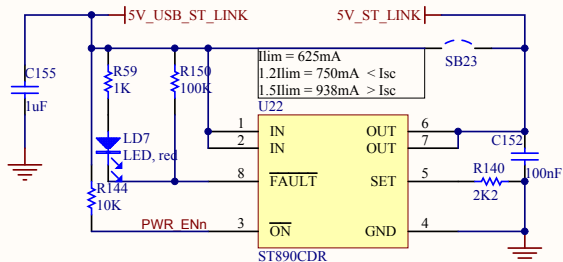
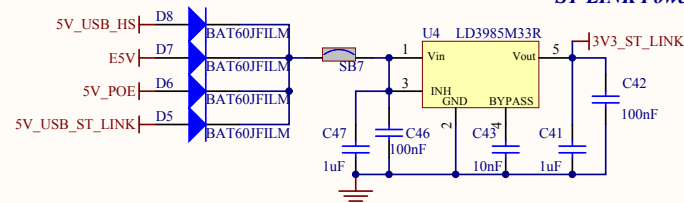
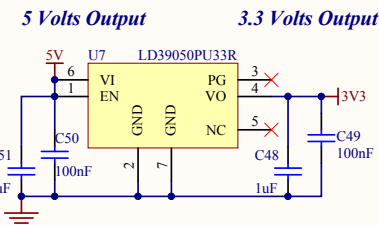
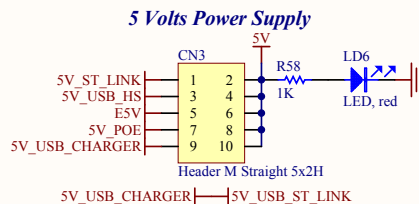
1050170001



Not Fitted, must be on a border or the PCB.

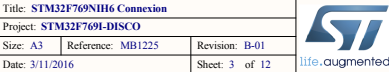


Must be on a border or the PCB.

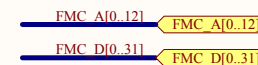


Title: ST-LINK/V2-1 with support of SWD only			
Project: STM32F769I-DISCO			
Size: A4	Reference: MB1225	Revision: B-01	
Date: 3/10/2016	Sheet: 2 of 12		life.augmented

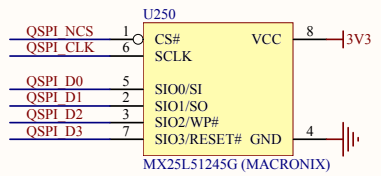
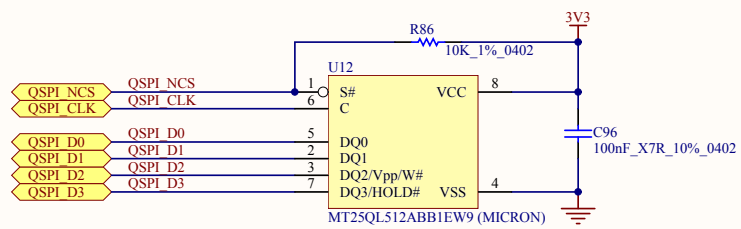








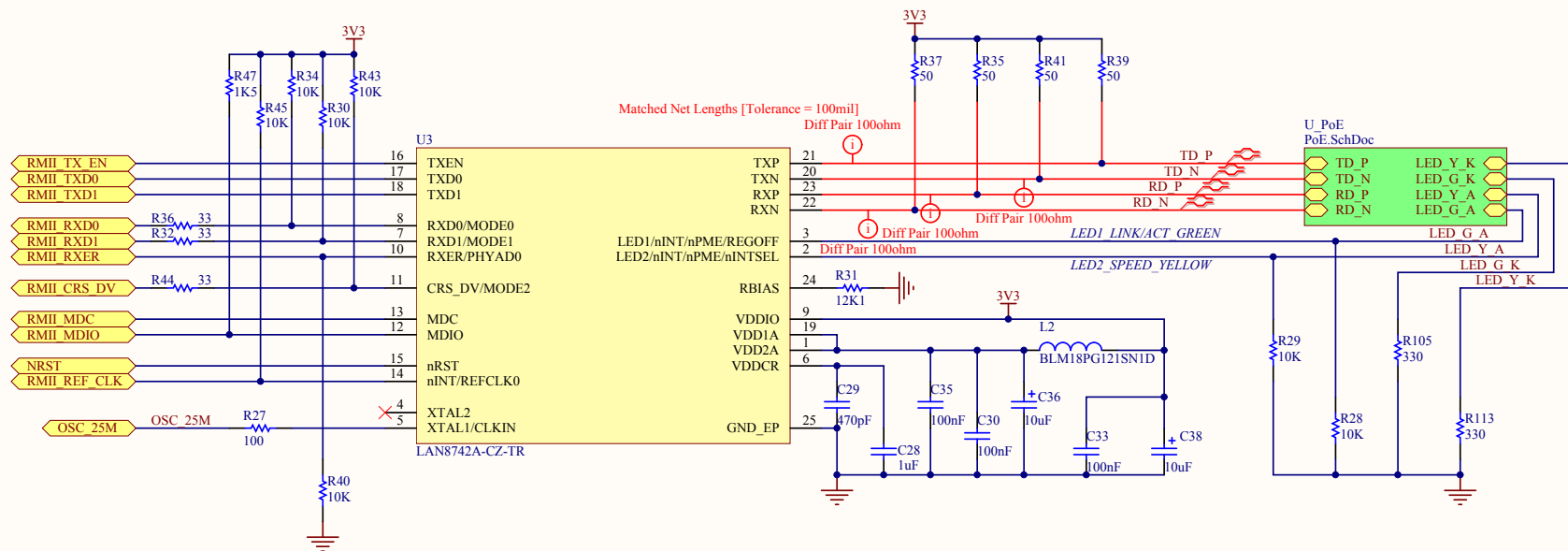
Quad SPI Flash Memory









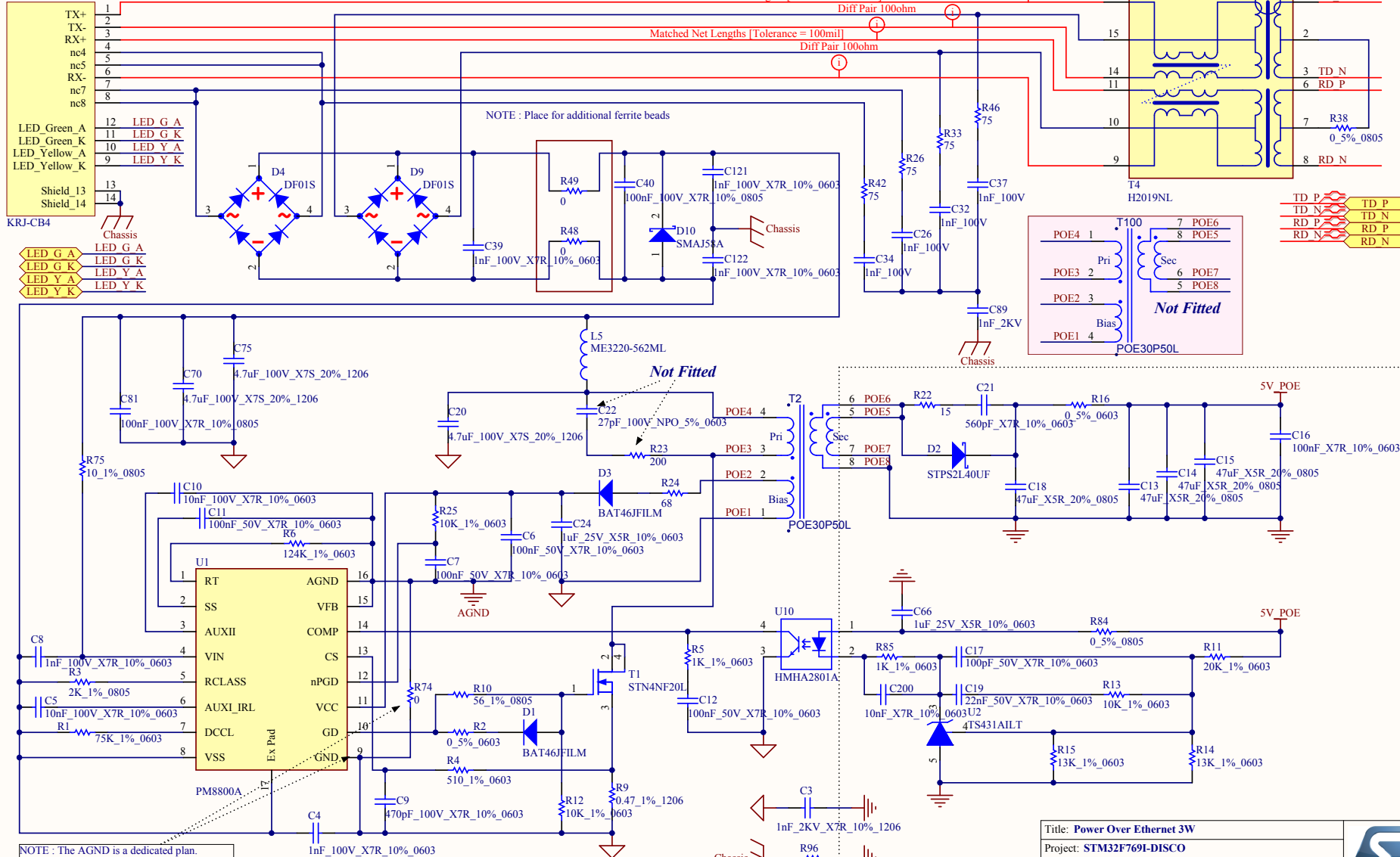


INPUT

## Power Over Ethernet

Matched Net Lengths [Tolerance = 100mil]  
Diff Pair 100ohmMatched Net Lengths [Tolerance = 100mil]  
Diff Pair 100ohmMatched Net Lengths [Tolerance = 100mil]  
Diff Pair 100ohmMatched Net Lengths [Tolerance = 100mil]  
Diff Pair 100ohm

CN10



Title: Power Over Ethernet 3W

Project: STM32F769I-DISCO

Size: A4 Reference: MB1225

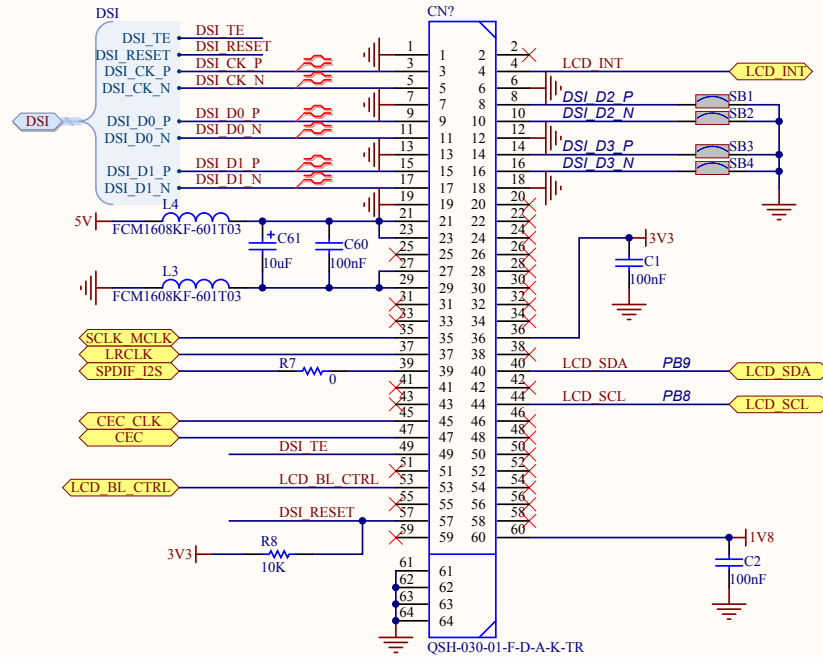
Date: 3/10/2016

Revision: B-01

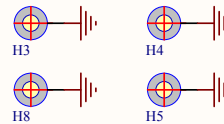
Sheet: 10 of 12



## DSI LCD



MB1166 I2C Address : 0101 0100



**MICRO SD (TF) Card**

uSD D0  
uSD D1  
uSD D2  
uSD D3  
uSD CLK  
uSD CMD

3V3

R82 47K\_1%\_0402  
R81 47K\_1%\_0402  
R83 47K\_1%\_0402  
R79 47K\_1%\_0402  
R80 47K\_1%\_0402

3V3

3V3

CN5 PJS008-2003

SMS064FF or SMS128FF

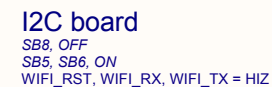
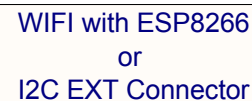
SW2 SW1

10

R76 [NA]  
R73 0\_5%\_0402

uSD Detect

Input pin with pull-up



life.augmented