Release Notes for MPLAB® Code Configurator PIC24/dsPIC33/PIC32MM library v1.54

1 What is MPLAB Code Configurator PIC24/dsPIC33/PIC32MM library

The PIC24-dsPIC33-PIC32MM library is the library for MCC 16 bit and PIC32MM device support.

2 System Requirements

- MPLAB® X IDE 4.05 or later
- XC16 Compiler v1.33 or later
- XC32 Compiler v1.44 or later
- MCC Plugin Version 3.45.1

3 Installing MPLAB® Code Configurator pic24-dsPIC33-pic32mm_v1.54

Basic steps for installing MPLAB® Code Configurator needs to be installed as below.

To install the MPLAB® Code Configurator V3.45.1 Plugin:

- 1. In the MPLAB® X IDE, select **Plugins** from the Tools menu
- 2. Select the Available Plugins tab
- 3. Check the box for the MPLAB® Code Configurator v3, and click on Install

To install the pic24-dsPIC33-pic32mm_v1.54

- 1. In the MPLAB® X IDE, select **Options** from the **Tools** menu
- 2. Select Plugins tab
- 3. Click on Install Library
- 4. Add pic24-dspic33-pic32mm_v1.54.jar
- 5. Restart MPLAB® X IDE

To load different peripheral library version

- 1. Open MPLAB® Code Configurator
- 2. In Versions tab under PIC24/dsPIC33/PIC32MM MCUs will find the multiple library version (loaded version is indicated by the green dot)
- 3. Right Click on the required version of the library and select Mark for load
- 4. Click on Load Selected Libraries button to load the library

4. What's New

- dsPIC33EP512GM710 device family.
- New devices dsPIC33EV256GM103 and dsPIC33EV256GM003 added in dsPIC33EV256GM106 device family.

5. Repairs and Enhancements

#	ID	Description	Device(s)
1	MCCV3XX-7339	ADC – Interrupt generated only for one channel when more than one shared ADC channels enabled. Only one channel generate interrupt whereas according to datasheet each channel can generate interrupt individually.	dsPIC33EP64GS506 device family
2	MCCV3XX-7350	System module – wrong pin selected when CLKO functionality selected from System module settings	PIC32MM0064GPL036
3	MCCV3XX-7202	Some config registers not available to set "FBSLIM" in "Registers"	dsPIC33EV128GM106
4	MCCV3XX-6732	ADC - In dsPIC33EP32GS202 device, only 8 channel ADC can be configured but has 12 channels.	dsPIC33EP32GS202
5	MCCV3XX-7479	ADC - removed the invalid option in CH123SA bit	PIC24 and dsPIC33EP512MC202, dsPIC33EP512GP502, dsPIC33EP512GP202, dsPIC33EP512MC502, dsPIC33EV128GM106 device family
6	MCCV3XX-7123	UART: "UEN" setting missing in xml description	PIC24FJ64GA106 PIC24FJ64GA108 PIC24FJ64GA110

6. Known Issues

#	ID	Description	Device(s)
		SPI - For dsPIC devices, SPI Clock PPS pin i.e. SCK has to be made as both Input and Output during Master mode. MCC generates the SCK PPS setting as Output for Master code.	
		Workaround: Manually add a line of code in Pin manager file to set the SCK PPS pin as Input as well.	
		Ex: RPINR20bits.SCK1INR = 63;	SCK issue applicable for all dsPIC devices.
1	MCCV3XX-6274	Also good to take care of dsPIC33EP64GS506 Errata#28.	Errata applicable for
		Errata - When SPI is enabled for the first time, there may be a spurious clock on the SCK. This may result in one bit of data shifted out on the data line, resulting in a mismatch between the clock and data lines.	dsPIC33EP64GS506 device family
		Please refer http://ww1.microchip.com/downloads/en/DeviceDoc/800006 56d.pdf	
		IOC PIN - Making changes to the IOC settings in the UI have no effect on the generated code.	
2	MCCV3XX-6324	Workaround: Deselect the "RB2" pin selection as input in the pin-manager grid view and again configure "RB2" as GPIO input in the pin-manager grid view. Now change the IOC settings and try to generate the code.	PIC24FJ1024GB610
3	MCCV3XX-4173	PTG, CAN: Undo/Redo functionality not supported for settings done in Table	dsPIC devices
4	MCCV3XX-4961	TMR: In 32bit mode, ISR generated in driver code does not match with Interrupt bit set in Register view. Generated code is correct but wrong interrupt bit enabled in UI.	All 16bit devices
5	MCCV3XX-5293	REFCLKO PPS pin - Currently in MCC, some GA110 devices have both REFCLKO non-PPS and PPS pin in them. PPS pins needs to be removed as it is not available.	PIC24FJ128GA110 device family
6	MCCV3XX-5406	I2C1 - module pins not displayed in the Pin Manager when CMP is also used on reload	PIC24F128GA204 device family
7	MCCV3XX-6596	CAN: SJW option is not available, fixed to 1	dsPIC devices

8	MCCV3XX-6670	CAN: UI should allow message ID of both type SID, EID to be added in Filter table. Currently the UI blocks one message id. Ex: 0x123 and 0x123x both should be allowed to add in table.	dsPIC devices
9	MCCV3XX-6650	External Interrupt: previously generated code disappears when regenerating after close/open MCC Workaround - After reopen MCC, click on the EXT_INT module in "Project Resource" and Generate, the generated code will be ok, nothing will be deleted.	All 16bit devices
10	MCCV3XX-7195	Exception when changing the FNOSC settings between PLL and NON-PLL modes in register view	All 16bit devices

Frequently Asked Questions

For frequently asked questions, please refer to the FAQ post on the MCC Forum (http://www.microchip.com/forums/f293.aspx)

7 Supported Families

The MCC PIC24-dsPIC33-PIC32MM Library v1.54 supports the following families.

- 1 PIC24F16KM204 (http://www.microchip.com/PIC24F16KM204)
- 2 PIC24FV32KA302 (http://www.microchip.com/PIC24F32KA302)
- 3 PIC24FJ128GA010 (http://www.microchip.com/PIC24FJ128GA010)
- 4 PIC24FJ64GA004 (http://www.microchip.com/PIC24FJ64GA004)
- 5 PIC24FJ64GB004 (http://www.microchip.com/PIC24FJ64GB004)
- 6 PIC24FJ64GA104 (http://www.microchip.com/PIC24FJ64GA104)
- 7 PIC24FJ256GB110 (http://www.microchip.com/PIC24FJ256GB110)
- 8 PIC24FJ256GA110 (http://www.microchip.com/PIC24FJ256GA110)
- 9 PIC24FJ256DA210 (http://www.microchip.com/PIC24FJ256DA210)
- 10 PIC24FJ256GB210 (http://www.microchip.com/PIC24FJ256DA210)
- 11 PIC24FJ128GA310 (http://www.microchip.com/PIC24FJ128GA310)
- 12 PIC24FJ128GB204 (http://www.microchip.com/PIC24FJ128GB204)
- 13 PIC24FJ128GA204 (http://www.microchip.com/PIC24FJ128GA204)
- 14 PIC24FJ128GC010 (http://www.microchip.com/PIC24FJ128GC010)
- 15 PIC24FJ256GB412 (http://www.microchip.com/PIC24FJ256GB412)
- 16 PIC24FJ256GA412 (http://www.microchip.com/PIC24FJ256GA412)
- 17 PIC24FJ1024GB610 (http://www.microchip.com/PIC24FJ1024GB610)
- 18 PIC24FJ1024GA610 (http://www.microchip.com/PIC24FJ1024GA610)
- 19 dsPIC33EP512GP506 (http://www.microchip.com/dsPIC33EP512GP506)
- 20 dsPIC33EV256GM106(http://www.microchip.com/dsPIC33EV256GM106)
- 21 dsPIC33EV256GM006(http://www.microchip.com/dsPIC33EV256GM006)
- 22 PIC24FJ256GA705 (http://www.microchip.com/PIC24FJ256GA705)

- 23 PIC32MM0064GPL036 (http://www.microchip.com/PIC32MM0064GPL036)
- 24 PIC32MM0256GPM064 (http://www.microchip.com/PIC32MM0064GPM064)
- 25 dsPIC33EP128GS806 (http://www.microchip.com/dsPIC33EP128GS806)
- 26 dsPIC33EP64GS506 (http://www.microchip.com/dsPIC33EP64GS506)
- 27 dsPIC33EP32GS202 (http://www.microchip.com/dsPIC33EP32GS202)
- 28 dsPIC33EP512GM710(http://www.microchip.com/dsPIC33EP512GM710)

Note: New devices dsPIC33EV256GM103 and dsPIC33EV256GM003 added in dsPIC33EV256GM106 device family.

8 Software License Information

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 Additional Support

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- Technical Support

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Technical support is available through the web site at: http://support.microchip.com

10 Appendix: Device Peripheral List

Note:

Peripheral Supported in MCC

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Peripheral Presently not supported in MCC Peripheral does not exist in the Device

Waveform Clocks and Safety & System User Device **Integrated Analog Wired Communications** Control Timers Monitoring Flexibility Interface PIC24/dsPIC33/PIC32MM PIC24EP128GP202 PIC24EP128GP204 PIC24EP128GP206 PIC24EP128MC202 PIC24EP128MC204 PIC24EP128MC206 PIC24EP256GP202 PIC24EP256GP204 PIC24EP256GP206 PIC24EP256MC202 PIC24EP256MC204 \checkmark PIC24EP256MC206 PIC24EP32GP202 PIC24EP32GP203 PIC24EP32GP204 PIC24EP32MC202 PIC24EP32MC203 PIC24EP32MC204 PIC24EP512GP202 PIC24EP512GP204 PIC24EP512GP206 PIC24EP512MC202 PIC24EP512MC204 PIC24EP512MC206 PIC24EP64GP202 PIC24EP64GP203 PIC24EP64GP204 PIC24EP64GP206 PIC24EP64MC202 PIC24EP64MC203 PIC24EP64MC204 PIC24EP64MC206 PIC24F08KA101 PIC24F08KA102 PIC24F08KM101 PIC24F08KM102 PIC24F08KM202 PIC24F08KM204 PIC24F16KA101 PIC24F16KA102 PIC24F16KA301 PIC24F16KA302 PIC24F16KA304 PIC24F16KM102 PIC24F16KM104 PIC24F16KM202 PIC24F16KM204 PIC24F32KA301 PIC24F32KA302 PIC24F32KA304 PIC24FJ1024GA606 PIC24FJ1024GA610 PIC24FJ1024GB606 PIC24FJ1024GB610 PIC24FJ128DA106 PIC24FJ128DA110 PIC24FJ128DA206 PIC24FJ128DA210 PIC24FJ128GA006 PIC24FJ128GA008 PIC24FJ128GA010

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Note:

- 1. This table intends to list the peripheral modules supported by MCC for all the supported devices.
- 2. Some peripherals that are not yet supported by MCC (but are available in the device) may not be listed in this table.