

Politecnico di Torino

Sheet: /  
File: EFES\_project.sch

**Title: FPGA Based Digital Audio Synthesizer**

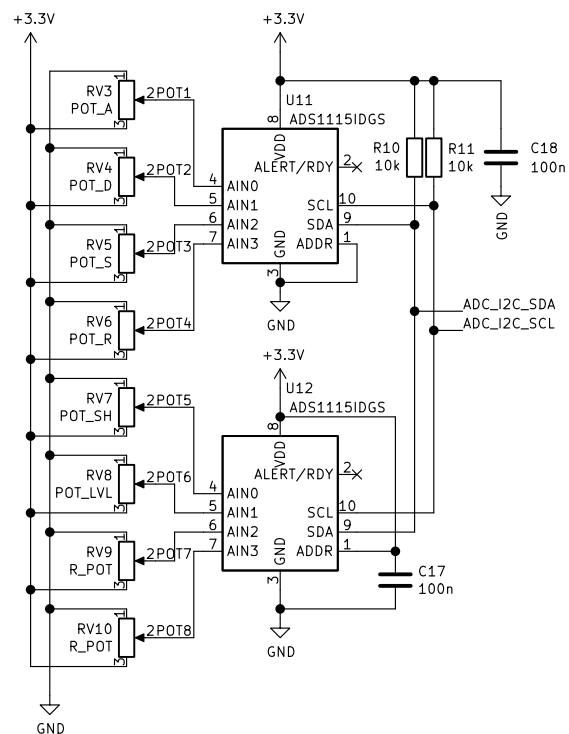
Size: A4 Date: 2025-02-23

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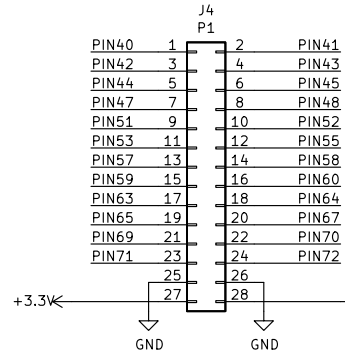
Rev: 1.0

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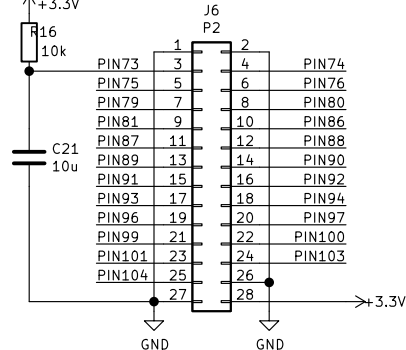
## ANALOG TO DIGITAL CONVERSION



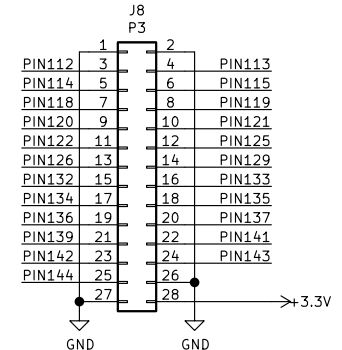
## CONNECTOR P1



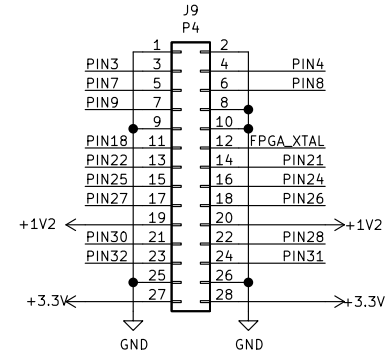
## CONNECTOR P2



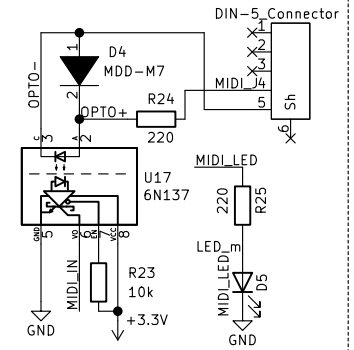
## CONNECTOR P3



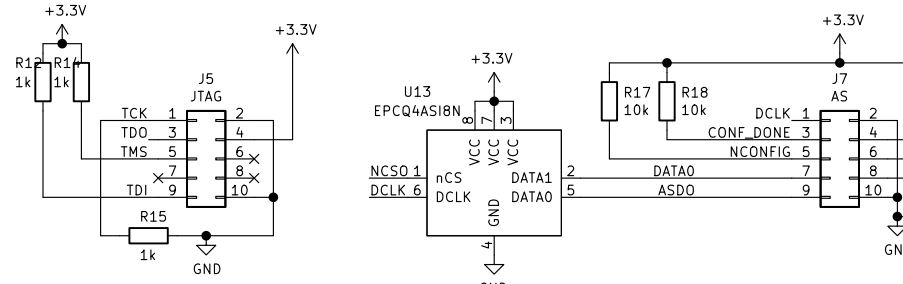
## CONNECTOR P4



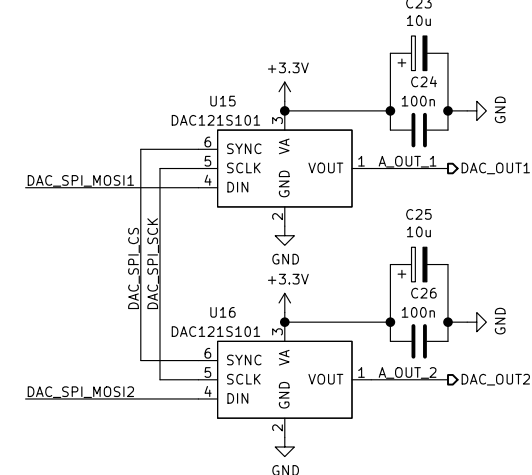
## MIDI INPUT



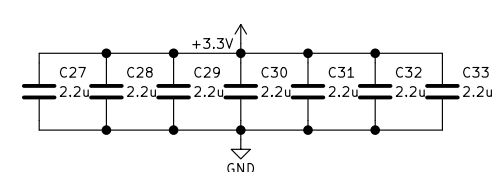
## PROGRAMMING / CONFIGURATION



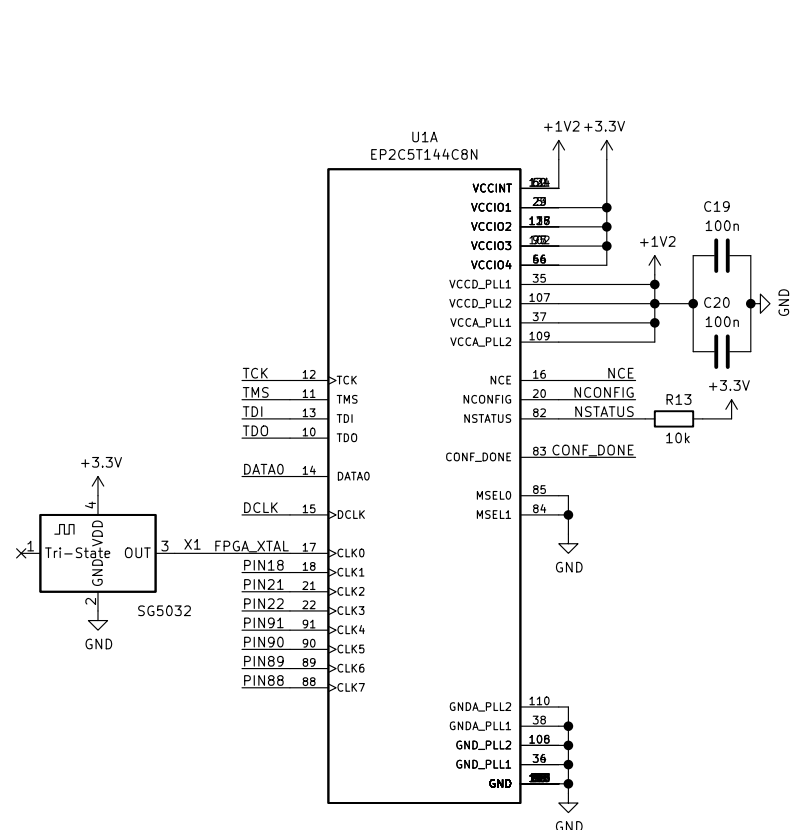
## DIGITAL TO ANALOG CONVERSION



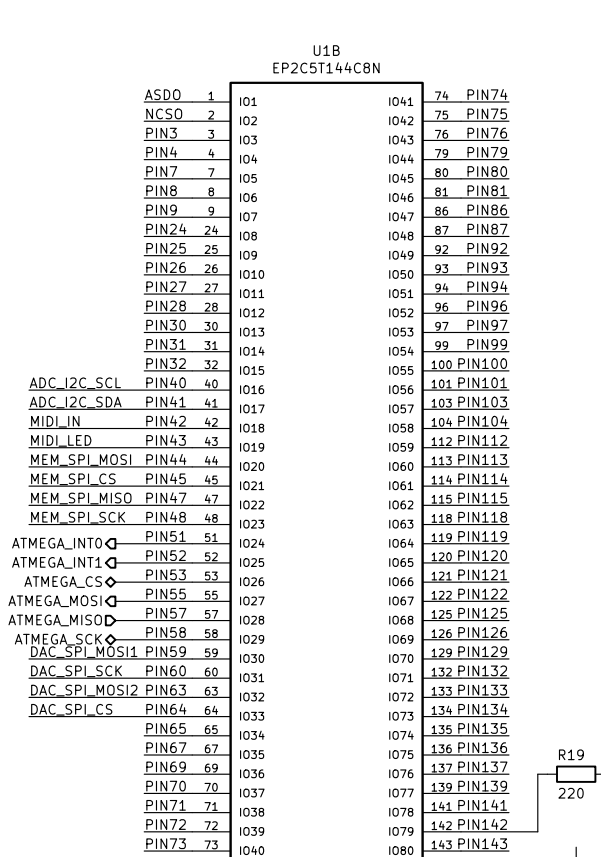
## BYPASS CAPS



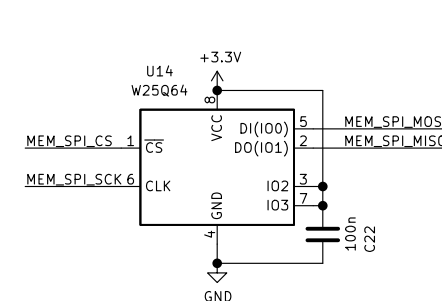
## POWER / CLK / PLL



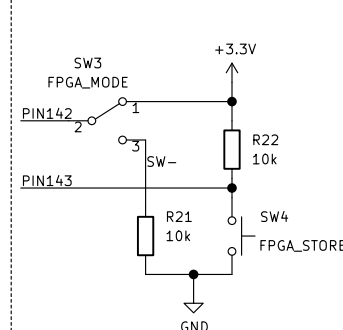
## FPGA GPIO PINS



## FLASH MEMORY



## SW / BTN



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File: subsheet\_1.sch

Title: FPGA Based Digital Audio Synthesizer

Size: A3 Date: 2025-02-23

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