ESC201A Quiz2 Set B

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TOTAL POINTS

8/11

QUESTION 1

Q1 6 pts

1.1 Q1(a) 4/4

- √ + 4 pts Completely Correct
 - + 0 pts Completely Incorrect
 - + 0 pts Not Attempted
 - + 0 pts Copied
 - + 1 pts VBB eq correct
 - + 1 pts VBB calculation correct
 - + 1 pts VCC eq correct
 - + 1 pts VCC calculation correct

1.2 Q1(b) 1 / 2

- + 2 pts Completely Correct
- + 0 pts Completely Incorrect
- + 0 pts Not Attempted
- + 0 pts Copied
- + 1 pts Vs = +2V case analyzed correctly
- √ + 1 pts Vs = -2V case analyzed correctly

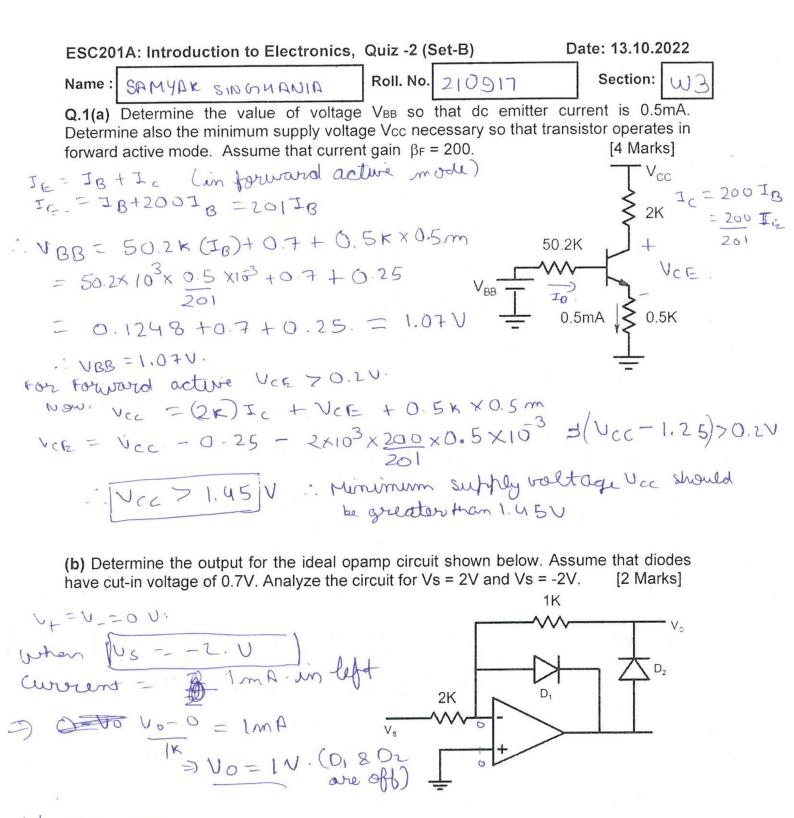
QUESTION 2

2 Q2 3 / 5

- + 5 pts Correct
- + 0 pts Completely Incorrect
- + 0 pts Not Attempted
- + 0 pts Copied
- + 2 pts Small signal model of circuit drawn

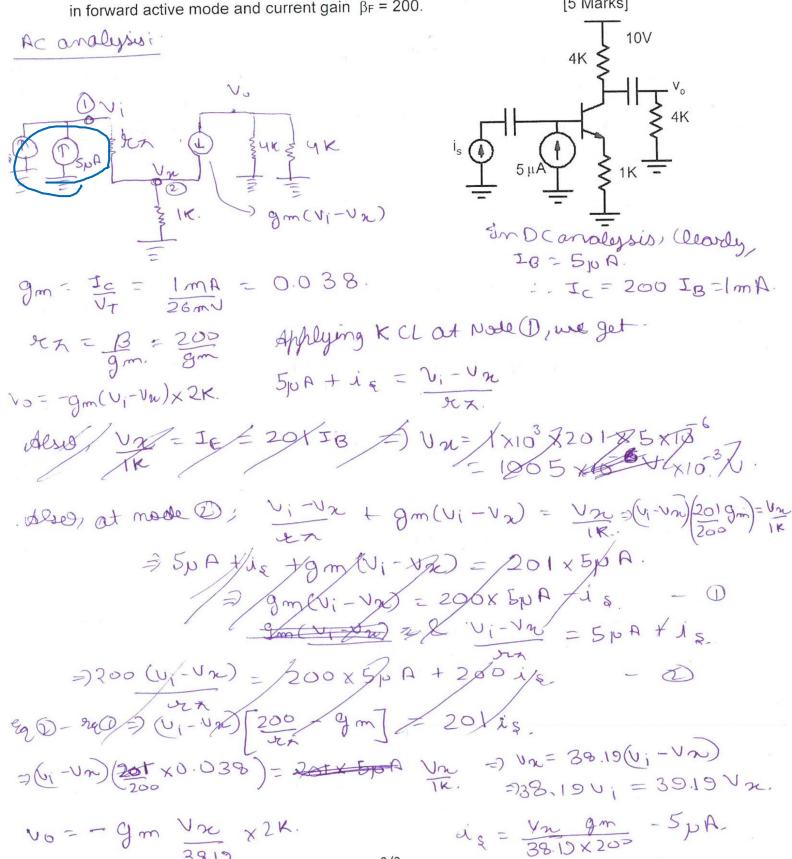
correctly

- \checkmark + 1 pts Expressions for gm and r_pi correct
 - + 1 pts vo/is expression correct
 - + 1 pts vo/is value correct
- + 2 Point adjustment
 - small signal model only partially correct, partial marks for correct approach



When $V_5 = 20$ Governt = 2-0 = ImA in right. $= 0 - V_0 = ImA = V_0 = -IV \cdot (0, 80 are off)$ **Q.2** For the circuit shown below, carry out ac analysis to determine the ratio $\frac{v_O}{r}$, where

 \mathbf{v}_{o} is ac output voltage and \mathbf{i}_{s} is ac sinusoidal current . Assume that transistor is biased [5 Marks] in forward active mode and current gain $\beta_F = 200$.



2/2

is - Un - 50 A

10 = - 9m m x2k 15 Un - 5pA.

Vo = - gm Vx x2K.