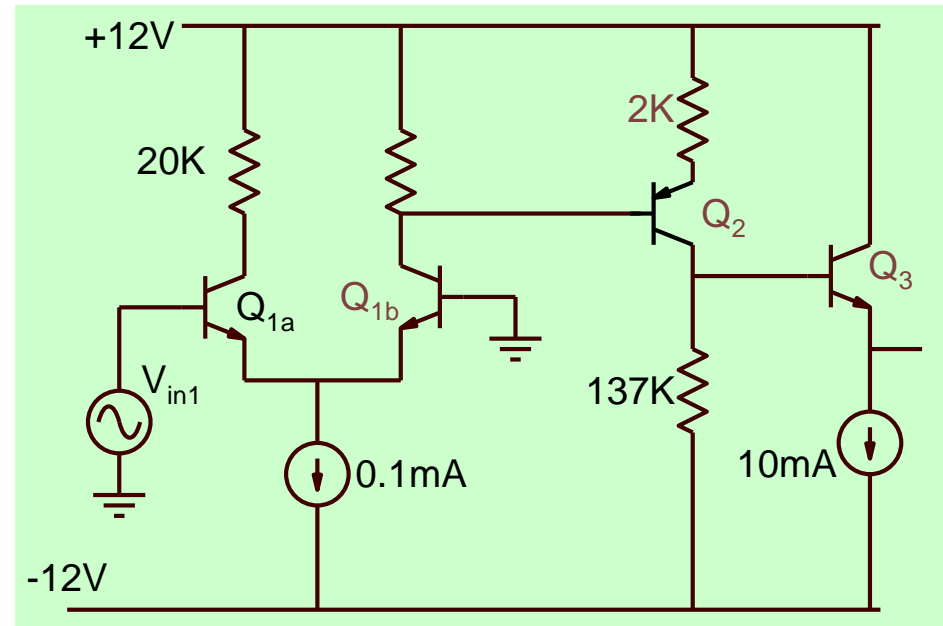
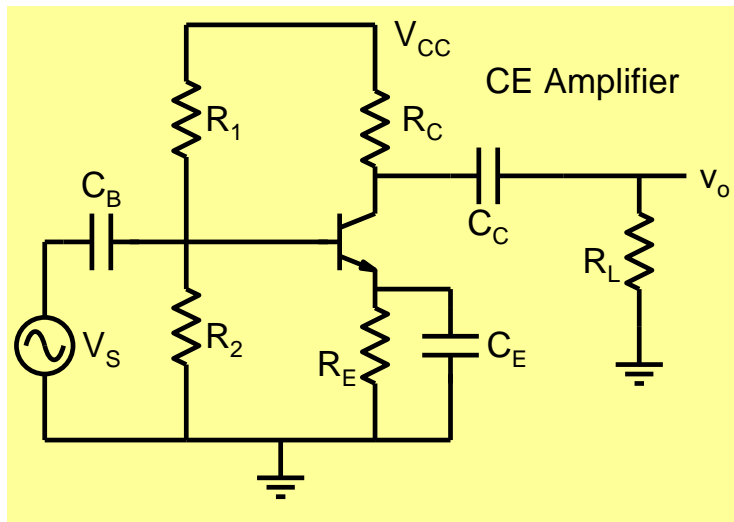


ESc201 : Introduction to Electronics

Operational Amplifier Part -1

Amit Verma
Dept. of Electrical Engineering
IIT Kanpur

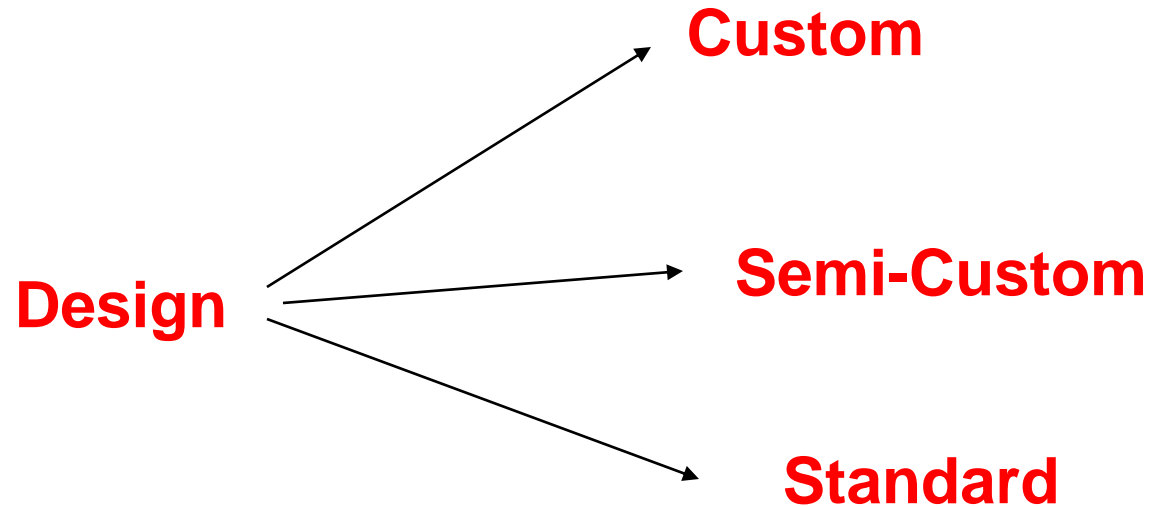
Amplifier Design requires specialized knowledge



It is not possible for every user to design his/her own amplifier !

Why can't we have experts design and implement amplifiers and make it available to everybody else !

Although this is done, it does not satisfy all the users due to diverse requirements

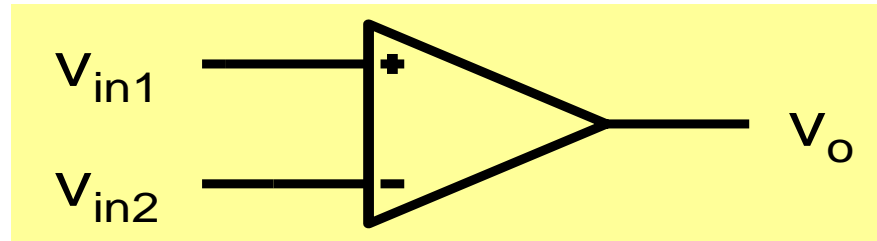


Semi-custom: partially completed design which is customized by the user

Opamp is a good illustration of the advantages of semi-custom approach

Difference Amplifier

-An amplifier that is sensitive to difference in input voltages and insensitive to what is common.



$$v_{id} = v_{in1} - v_{in2}$$

$$v_{ic} = \frac{v_{in1} + v_{in2}}{2}$$

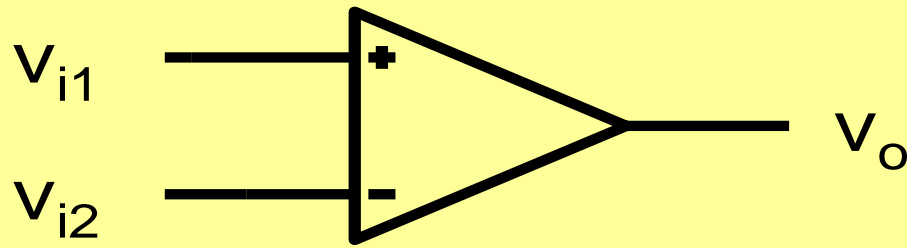
$$v_o = A_d v_{id} + A_{cm} v_{ic}$$

A_d : Differential mode gain

A_{cm} : Common mode gain

$$A_d \gg A_{cm}$$

$$\text{Common Mode Rejection Ratio: } CMRR = \frac{A_d}{A_{cm}}$$



$$A_d = 100; \quad A_{cm} = 0.01$$

$$v_{i1} = 1V + 5mV \times \sin(\omega t);$$

$$v_{i2} = 1V - 5mV \times \sin(\omega t)$$

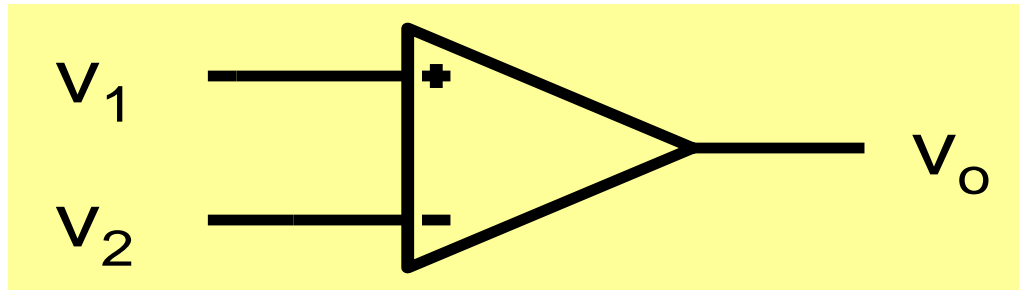
$$v_{id} = v_{in1} - v_{in2} = 10mV \times \sin(\omega t)$$

$$v_{ic} = \frac{v_{in1} + v_{in2}}{2} = 1V$$

$$\begin{aligned} v_o &= A_d v_{id} + A_{cm} v_{ic} \\ &= 1V \times \sin(\omega t) + 10mV \end{aligned}$$

Whatever is common is rejected and whatever is different is amplified !

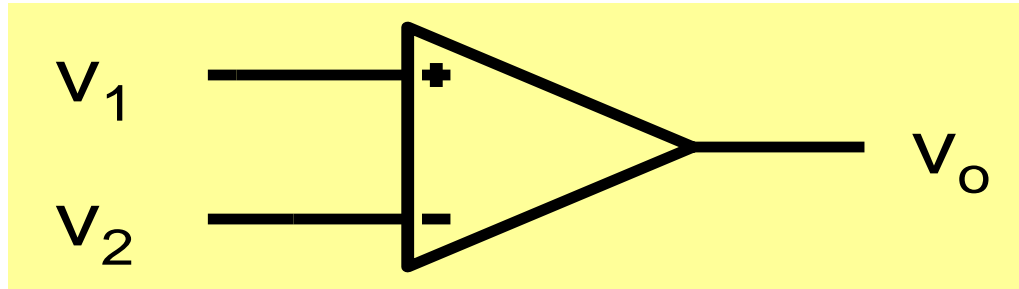
Operational Amplifier



A **special** kind of difference amplifier

1. Very High Differential-mode voltage gain
2. Very High Common mode Rejection ratio
3. Very High Input Resistance
4. Very Low output Resistance
5.

Ideal Operational Amplifier



1. Infinite Differential-mode voltage gain
2. Infinite Common mode Rejection ratio
3. Infinite Input Resistance
4. Zero output Resistance
5.

Example: LM 741

LM741

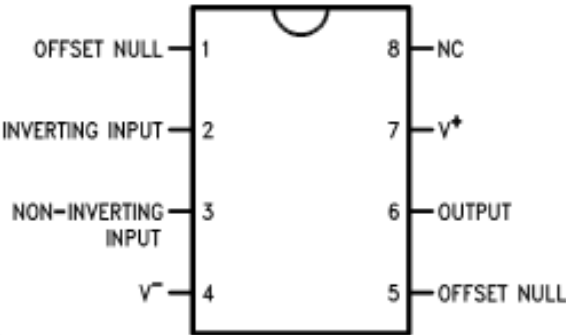
Operational Amplifier

General Description

The LM741 series are general purpose operational amplifiers which feature improved performance over industry standards like the LM709. They are direct, plug-in replacements for the 709C, LM201, MC1439 and 748 in most applications. The amplifiers offer many features which make their application nearly foolproof: overload protection on the input and output, no latch-up when the common mode range is exceeded, as well as freedom from oscillations.

The LM741C is identical to the LM741/LM741A except that the LM741C has their performance guaranteed over a 0°C to +70°C temperature range, instead of -55°C to +125°C.

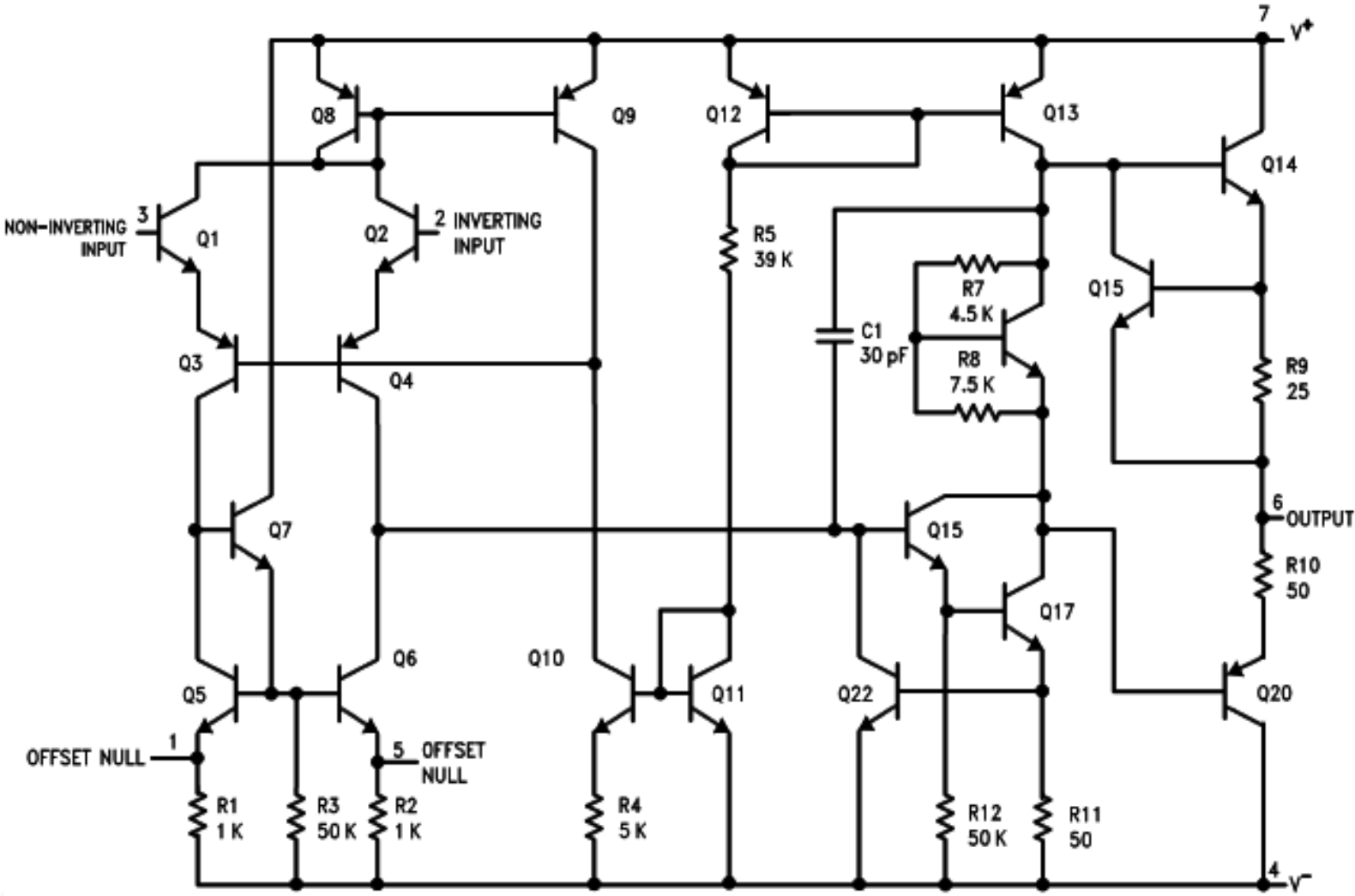
Dual-In-Line or S.O. Package



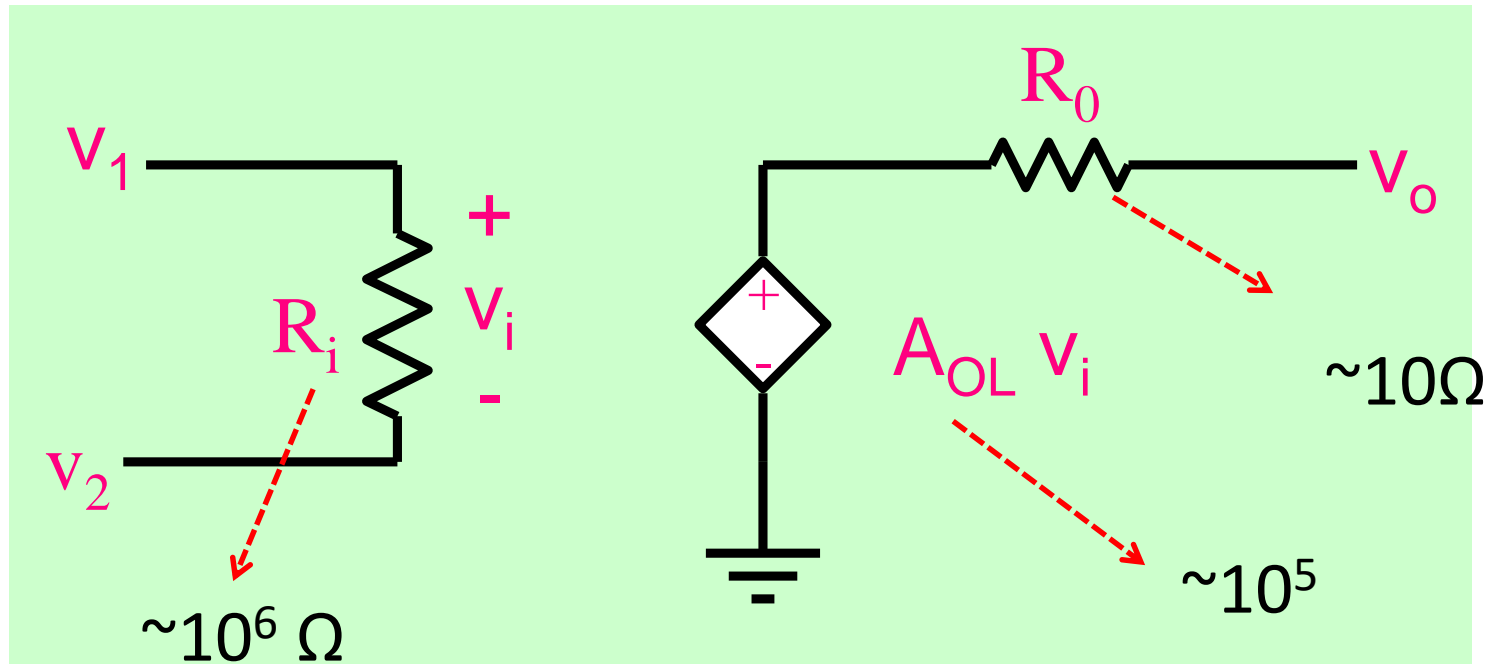
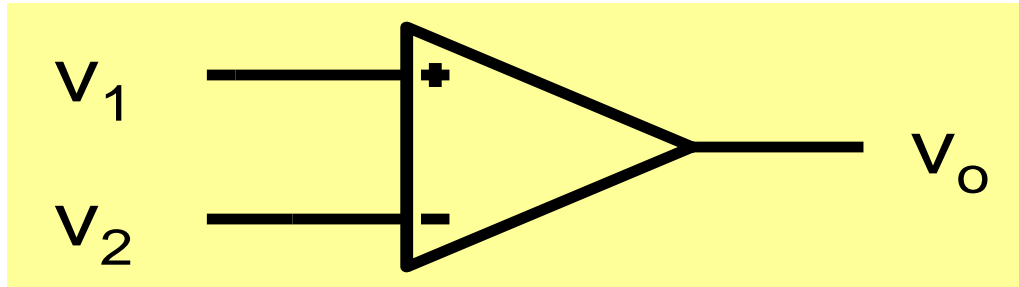
Parameter	Conditions	LM741A			LM741			LM741C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Input Resistance	$T_A = 25^{\circ}\text{C}, V_S = \pm 20\text{V}$	1.0	6.0		0.3	2.0		0.3	2.0		M Ω
	$T_{AMIN} \leq T_A \leq T_{AMAX}, V_S = \pm 20\text{V}$	0.5									M Ω
Large Signal Voltage Gain	$T_A = 25^{\circ}\text{C}, R_L \geq 2\text{ k}\Omega$ $V_S = \pm 20\text{V}, V_O = \pm 15\text{V}$ $V_S = \pm 15\text{V}, V_O = \pm 10\text{V}$	50			50	200		20	200		V/mV
Common-Mode Rejection Ratio	$T_{AMIN} \leq T_A \leq T_{AMAX}$ $R_S \leq 10\text{ k}\Omega, V_{CM} = \pm 12\text{V}$ $R_S \leq 50\Omega, V_{CM} = \pm 12\text{V}$										V/mV
		80	95		70	90		70	90		dB
											dB

Inside the opamp, there is a complicated circuit containing several transistors and resistors.

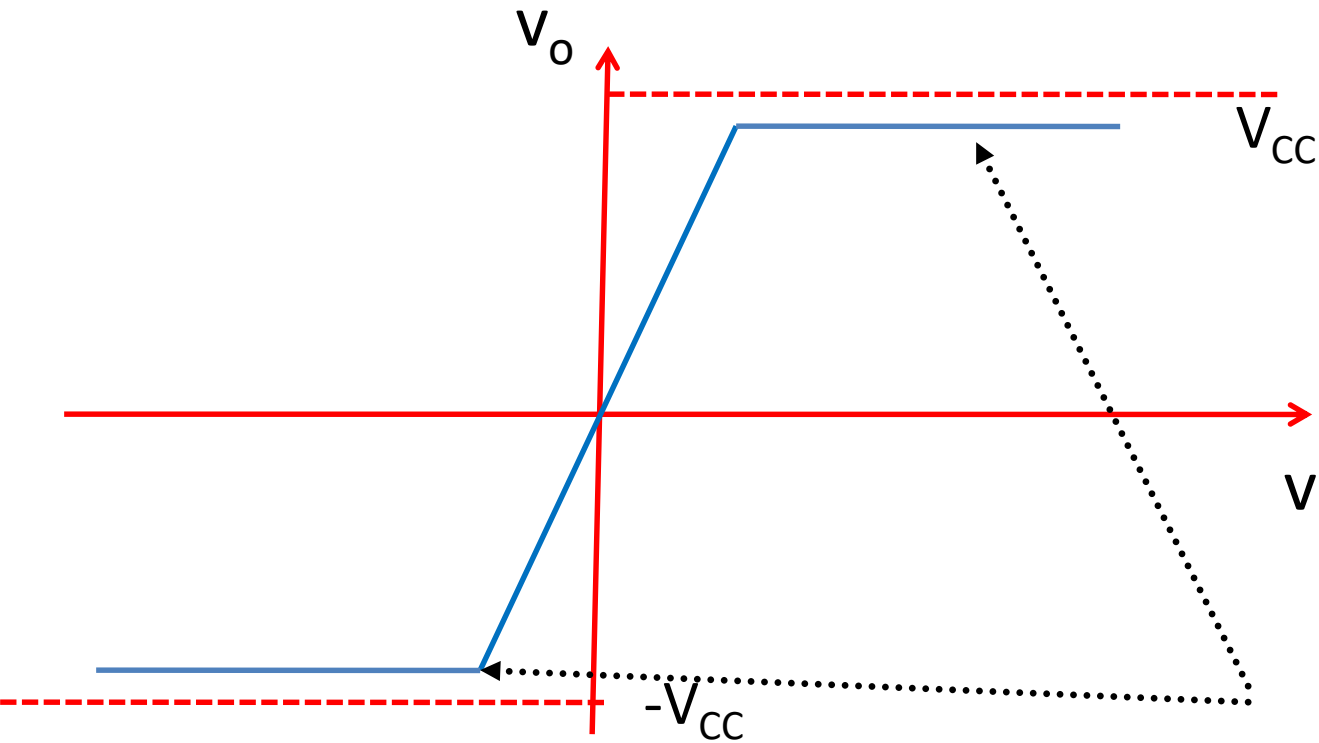
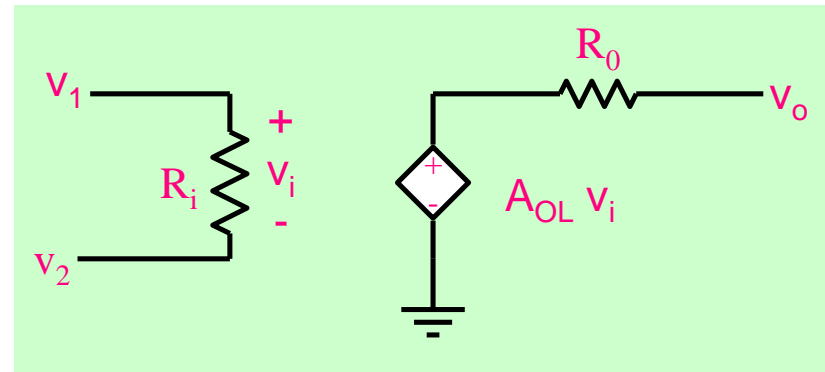
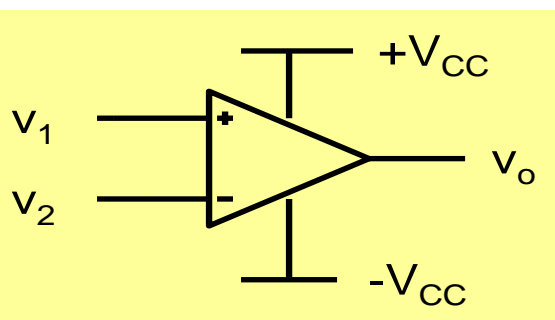
Schematic Diagram



Simple equivalent circuit model of an opamp

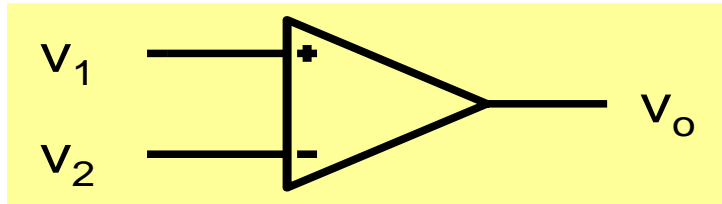


This assumes very high CMRR

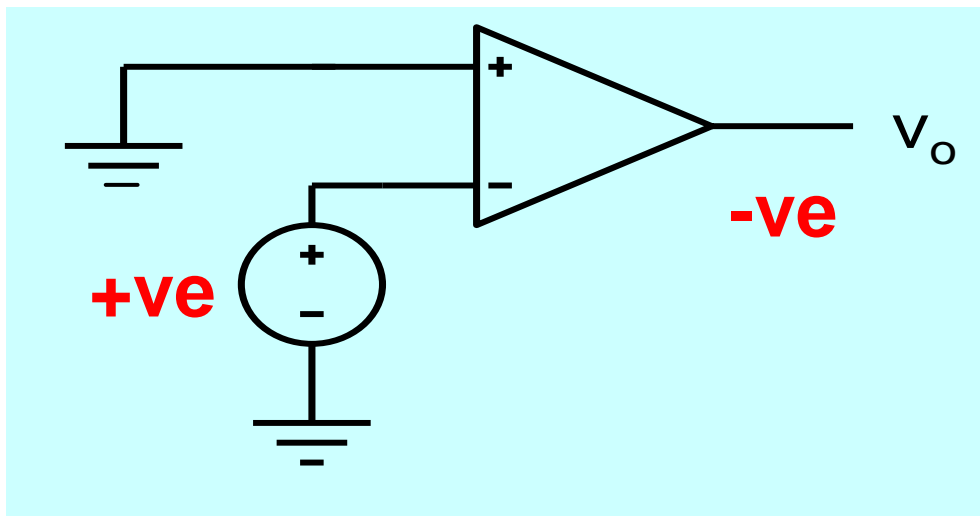
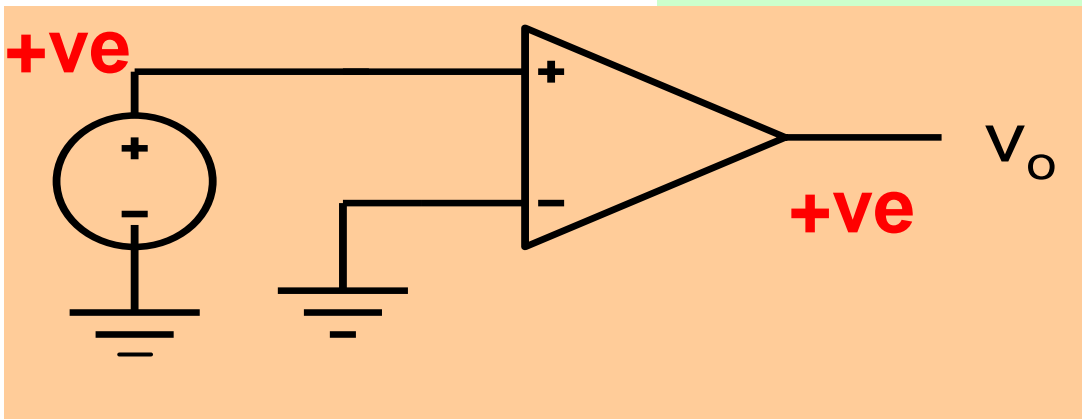
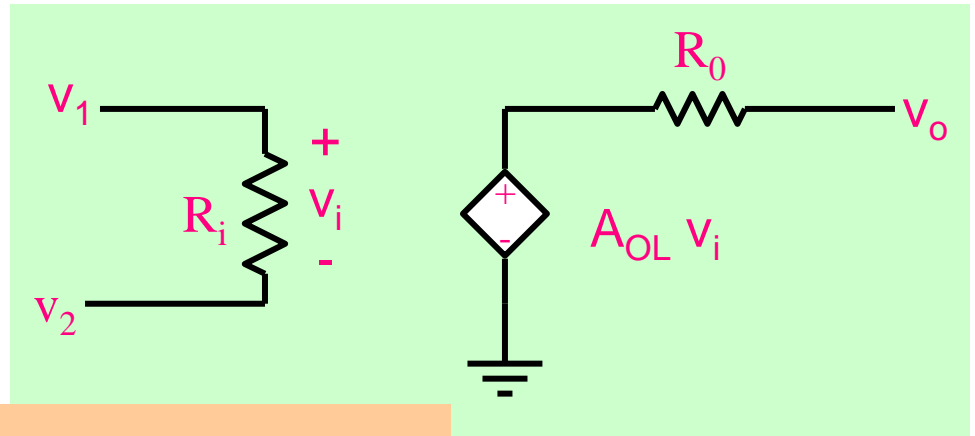


$$v_o = A_{ol} \times v_i$$

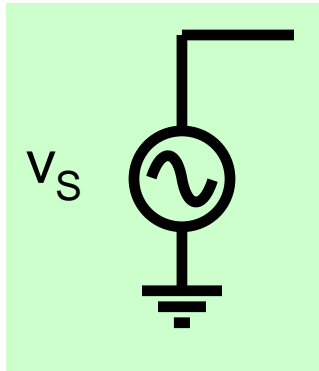
Opamp is said to be saturated



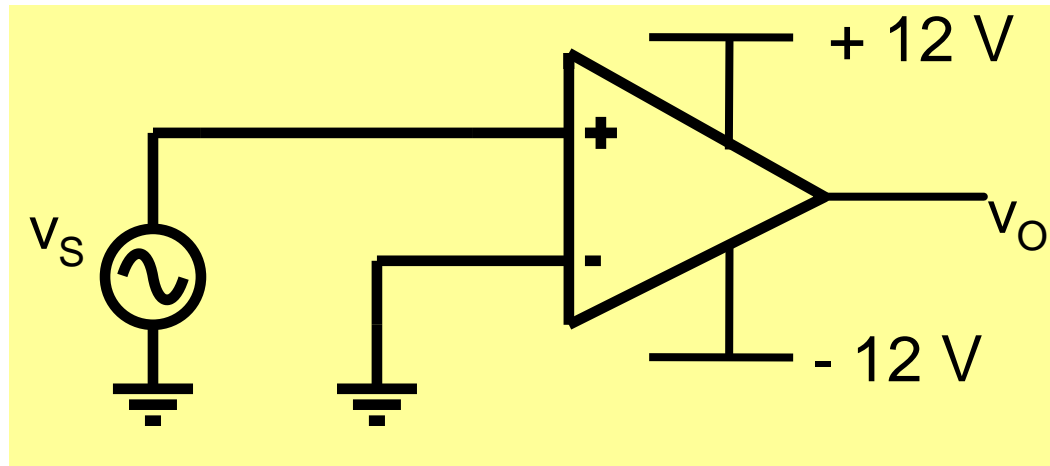
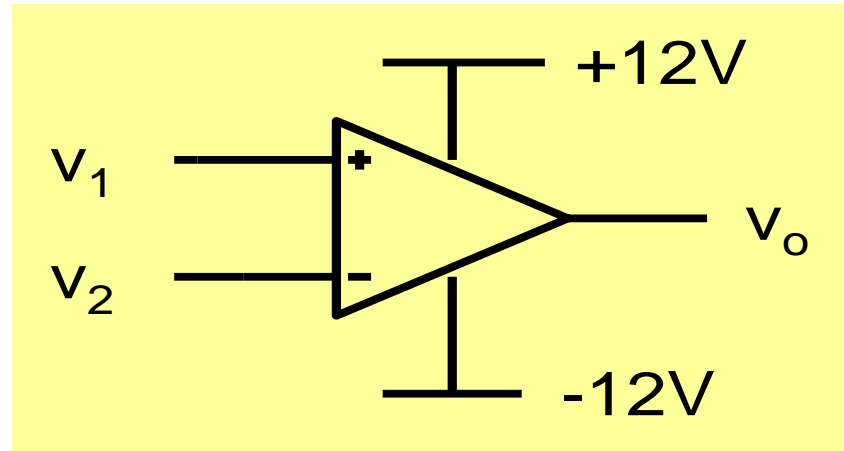
$$V_O = A_{OL} V_i = A_{OL} (V_1 - V_2)$$

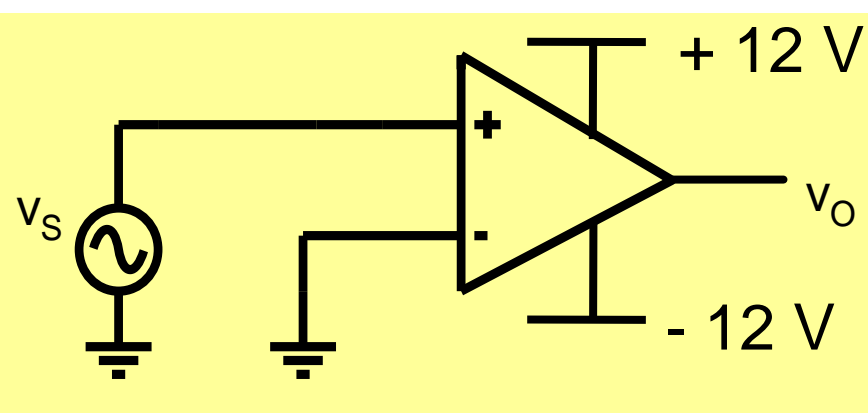


How do we amplify this signal?

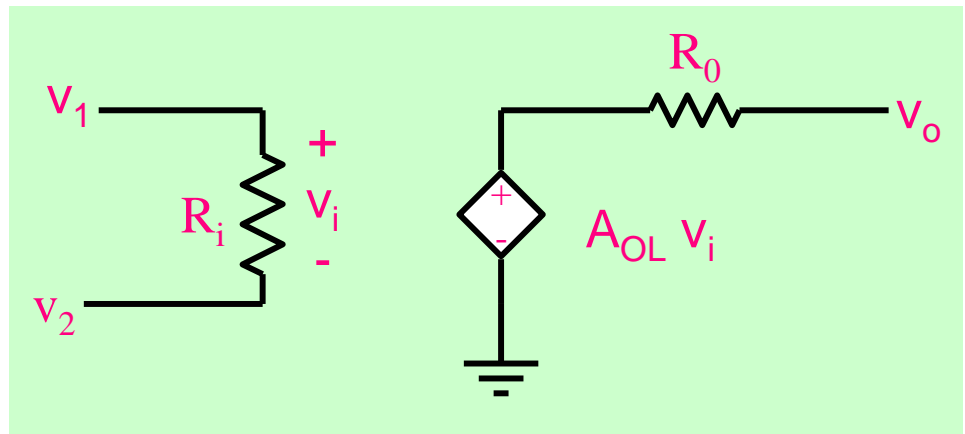


$$v_s = 1mV \sin(\omega t)$$

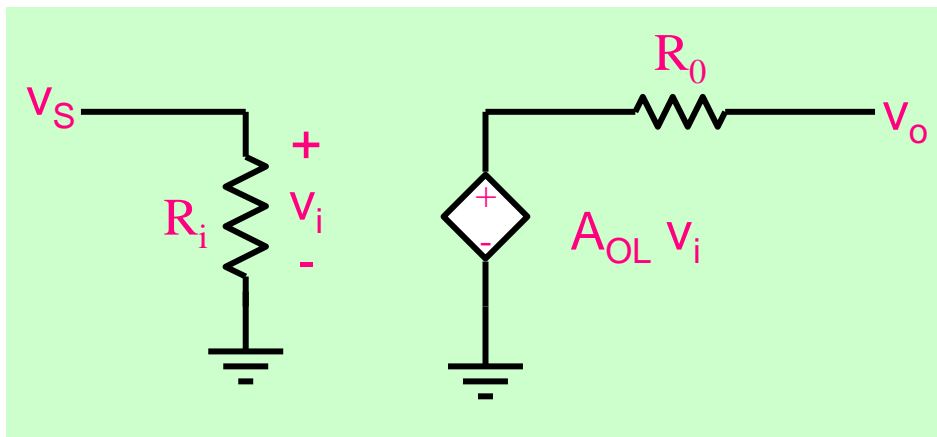




$$v_S = 1mV \sin(\omega t)$$



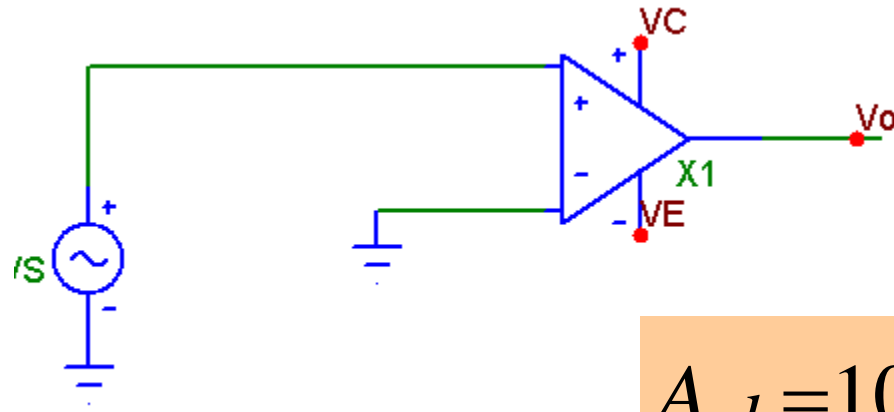
$$A_{ol} = 10^5$$



$$v_O = A_{ol} \times v_S = 10^2 \sin(\omega t)$$

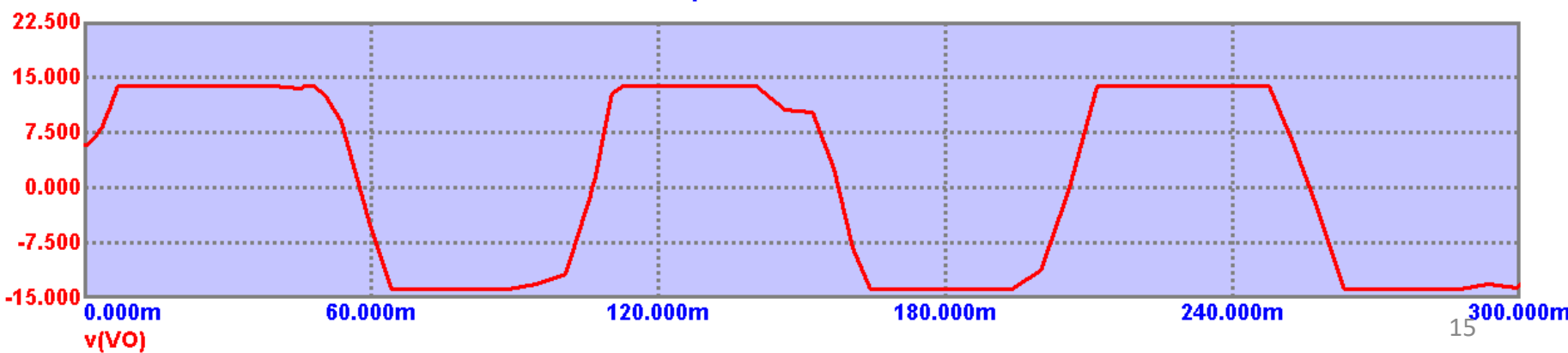
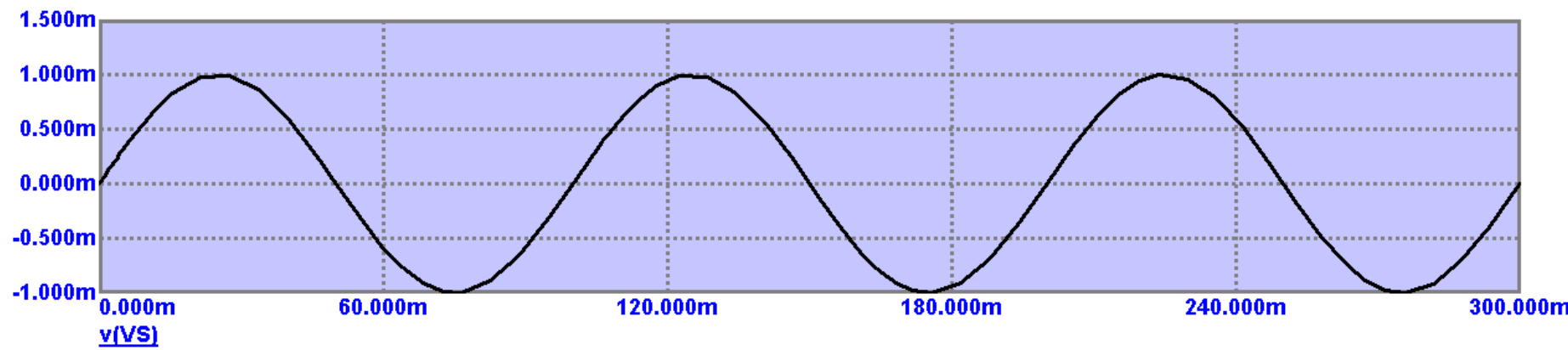
But opamp voltage is limited to $\pm 12V$

Simulation Results

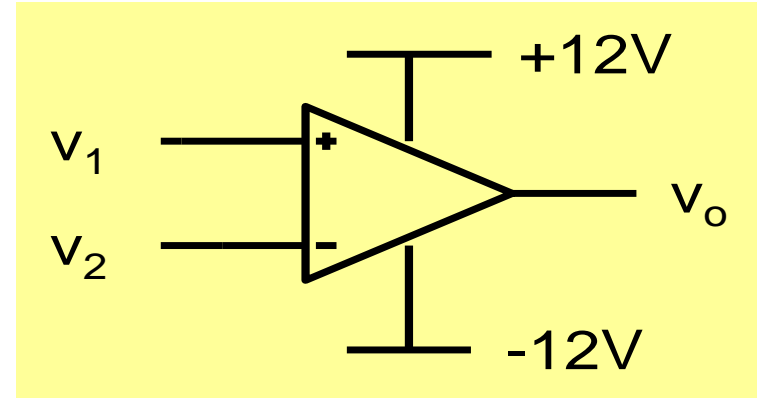
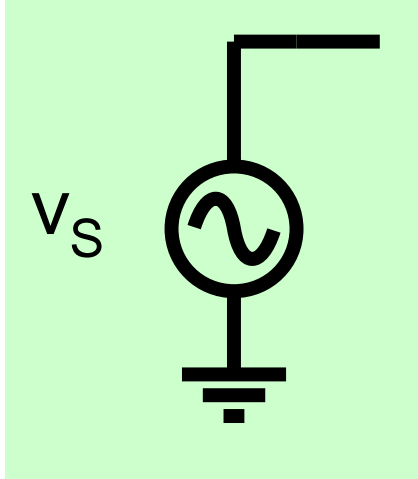


$v_s = 1mV \sin(\omega t)$

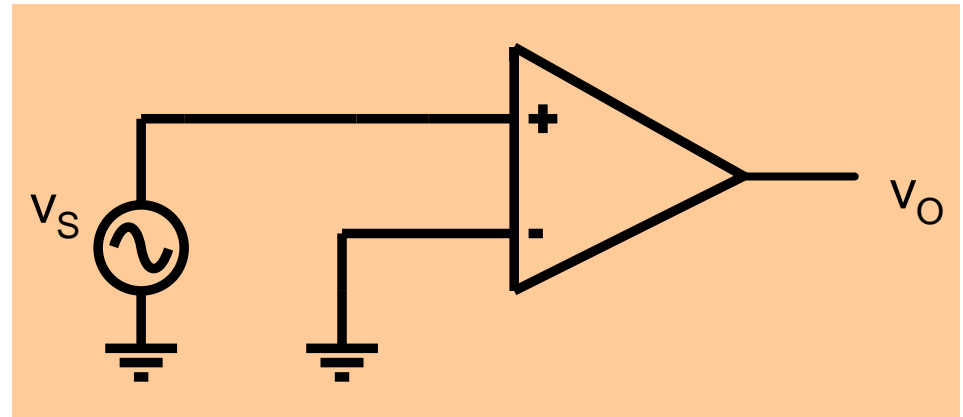
$A_{ol} = 10^5$



How do we amplify this signal then ?

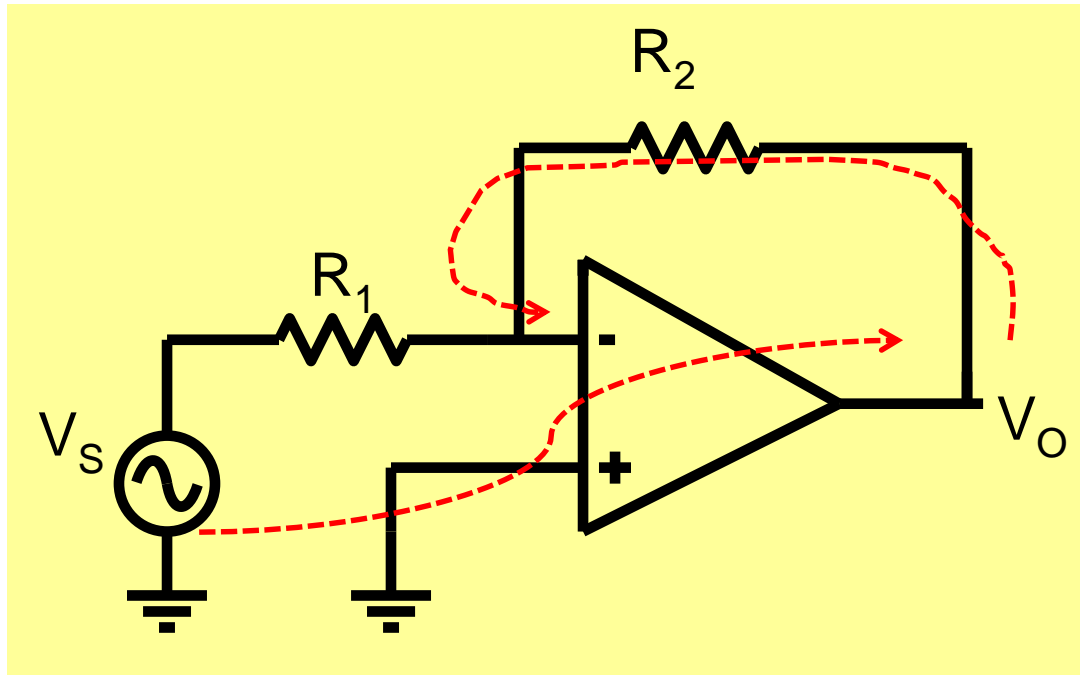


$$v_s = 1mV \sin(\omega t)$$



1. Attenuate the signal to 0.1mV and then amplify ?
2.

A Better Solution

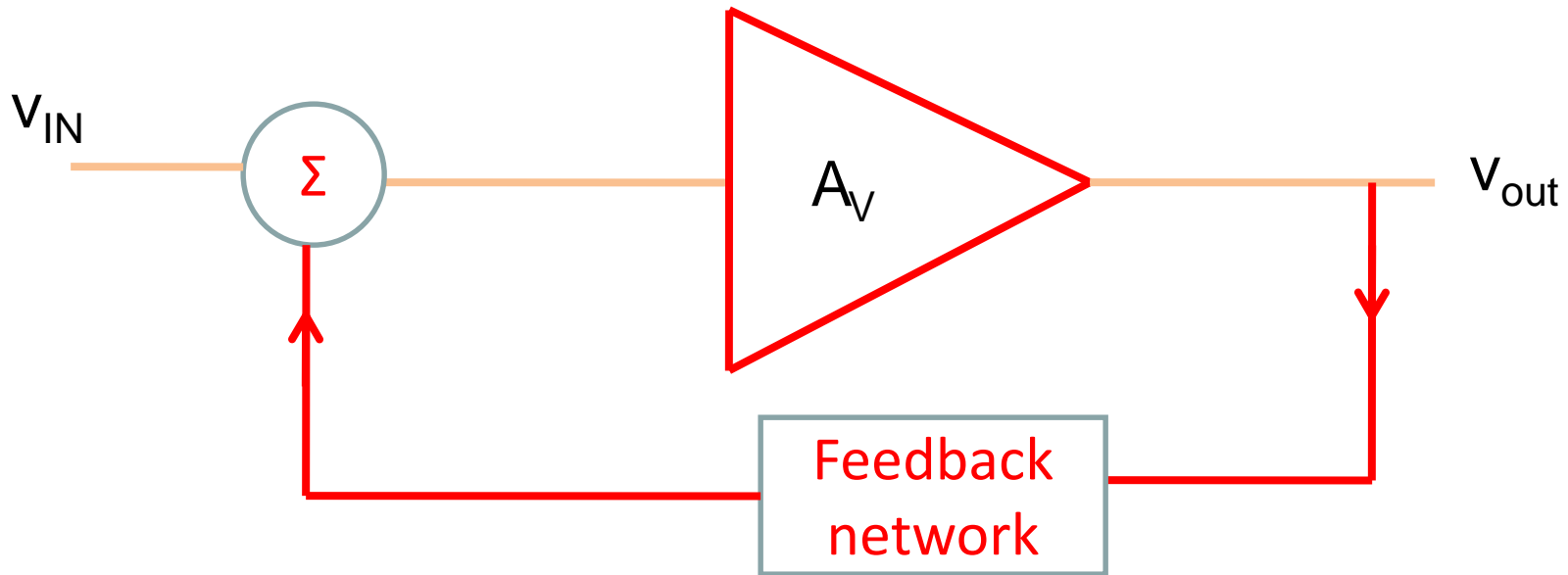
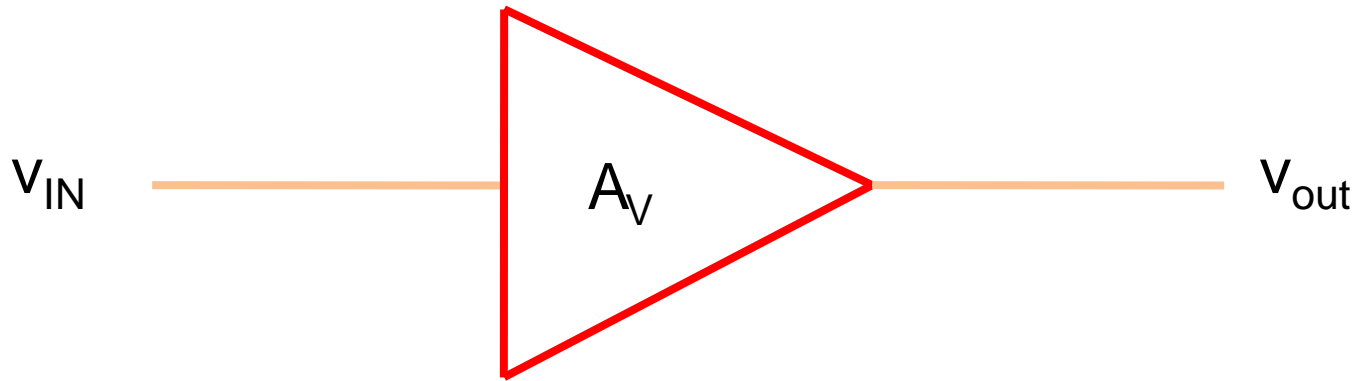


$$\frac{v_o}{v_S} = -\frac{R_2}{R_1}$$

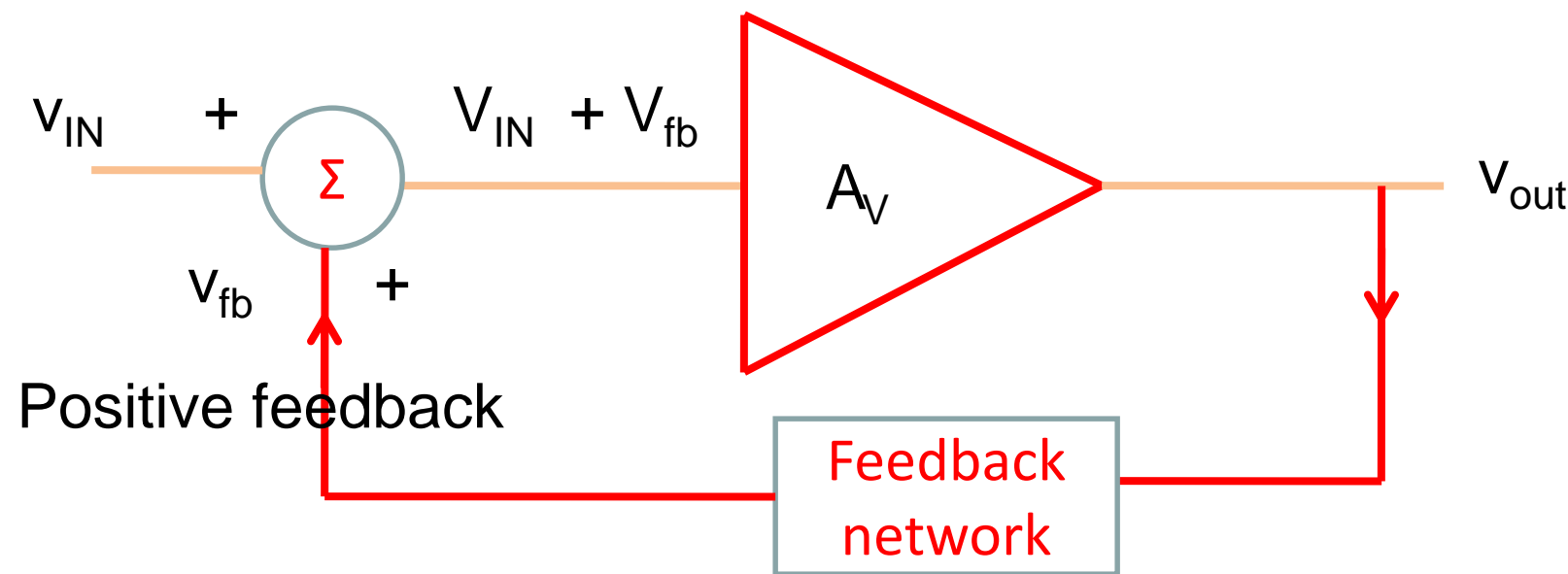
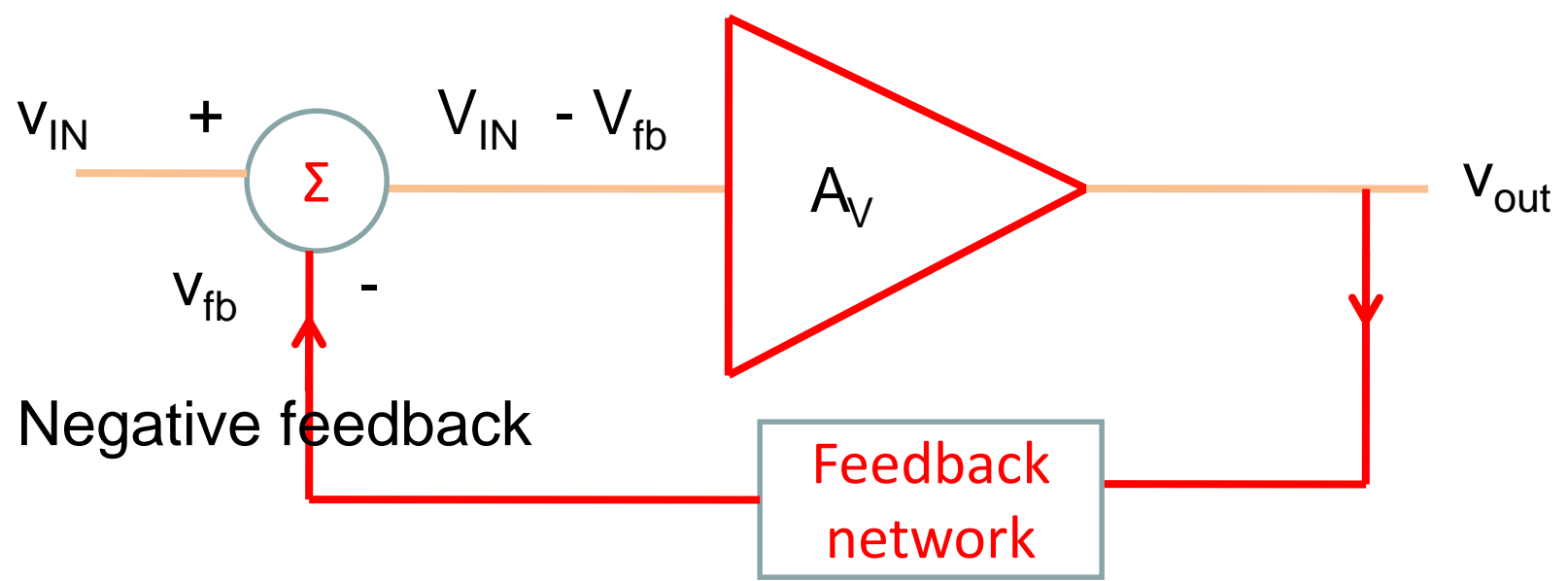
Amplifier has feedback

If the feedback signal helps the input voltage we have positive feedback, otherwise negative.

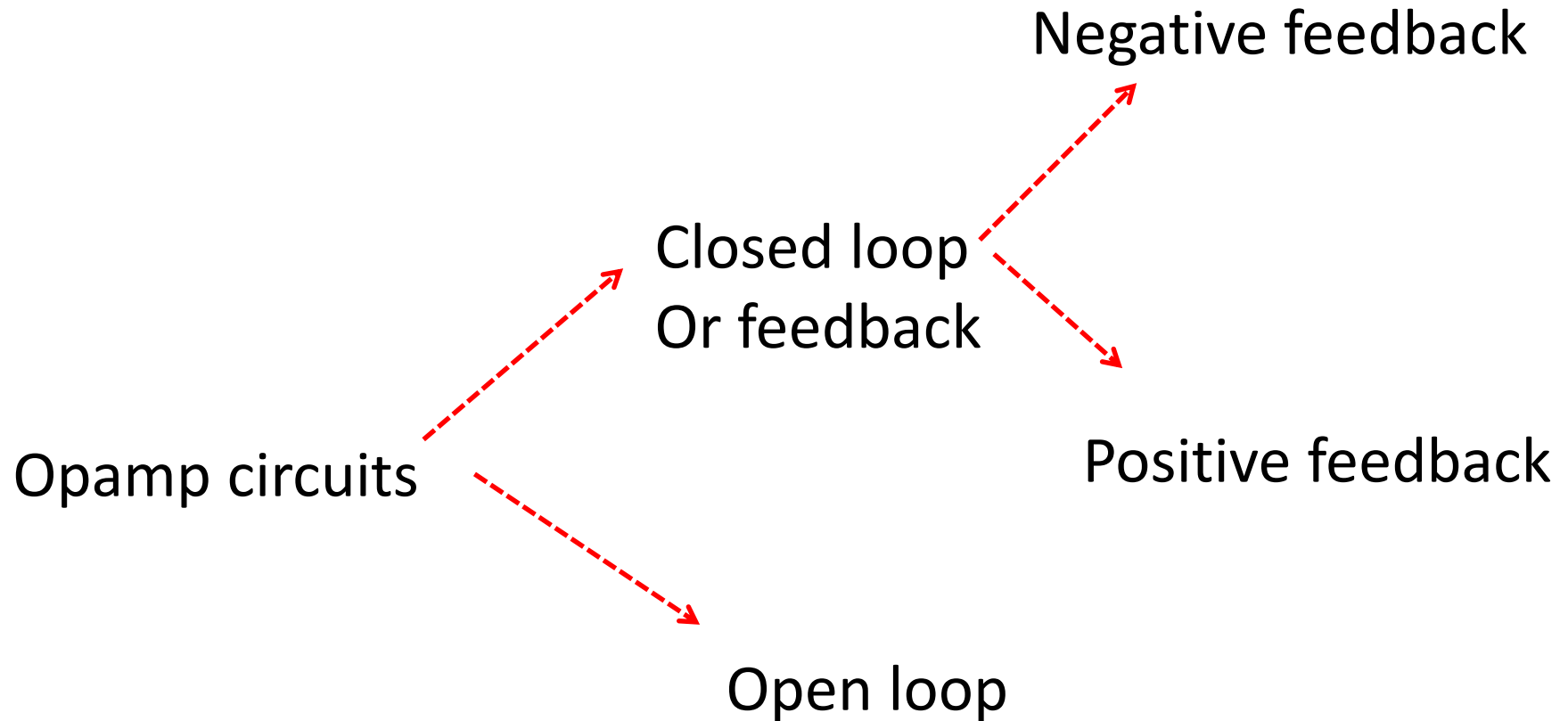
Feedback



Negative and Positive feedback

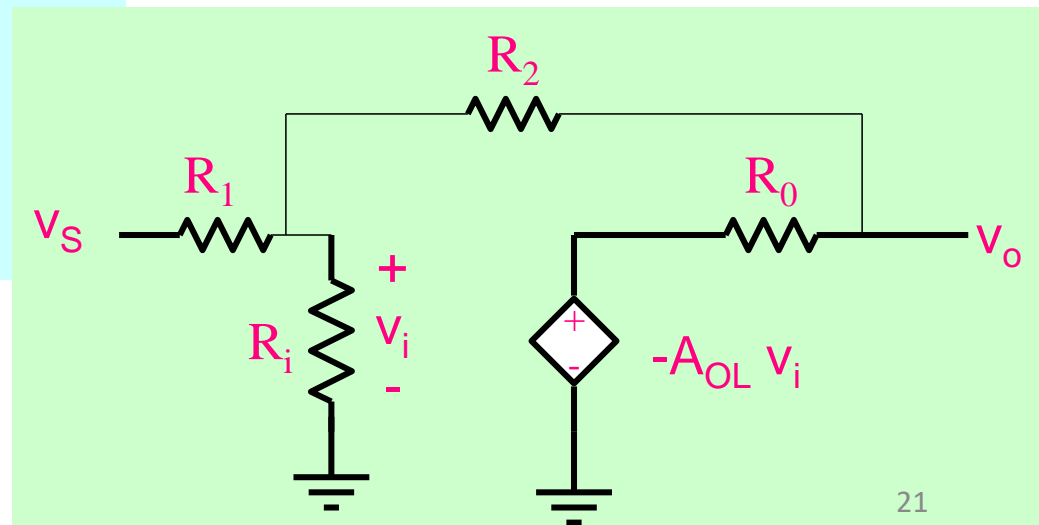
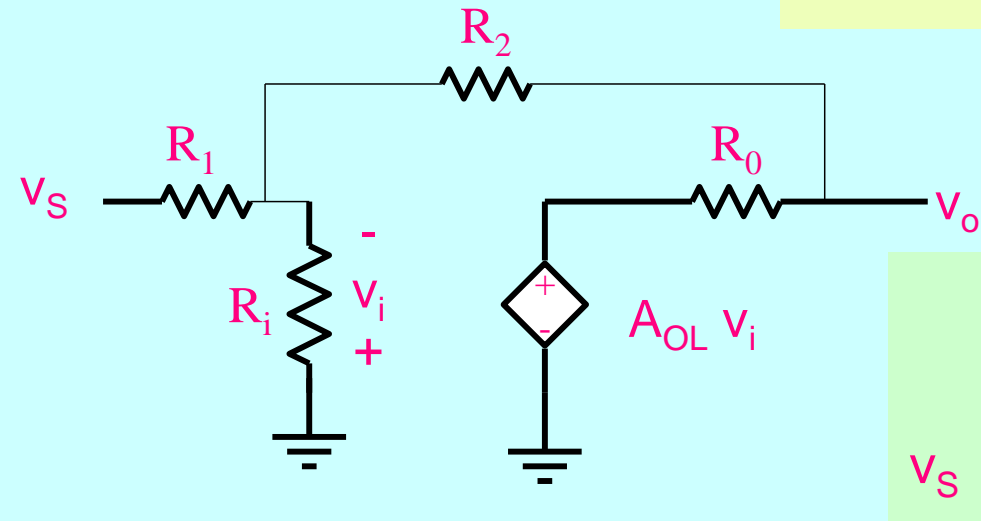
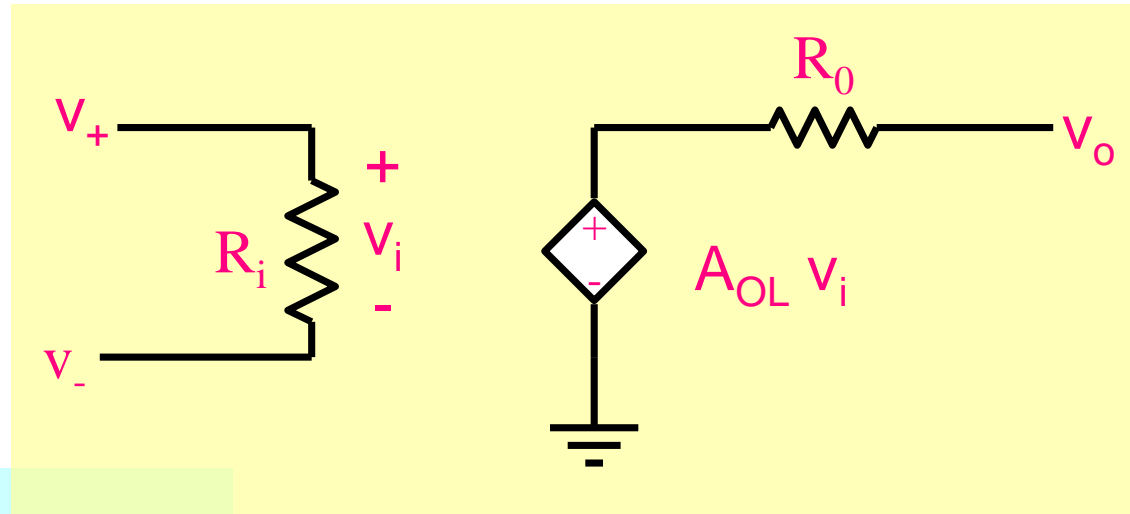
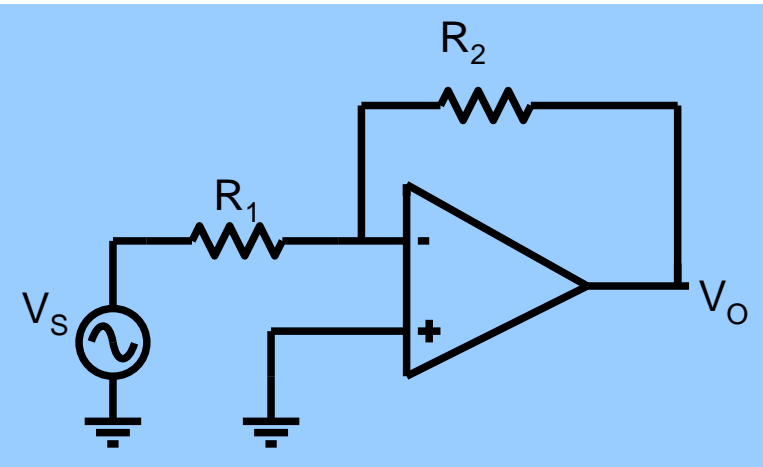


Opamp circuits classification

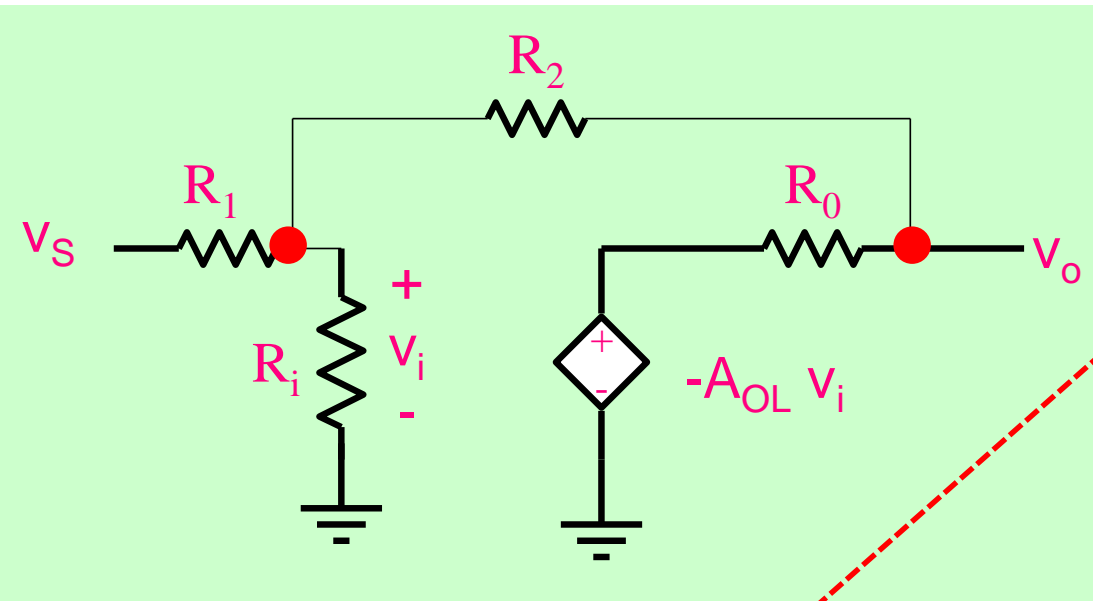


Most Opamp Circuits employ negative feedback

Inverting amplifier



Nodal Analysis



$$\frac{v_s - v_i}{R_1} = \frac{v_i}{R_i} + \frac{v_i - v_o}{R_2} \quad (1)$$

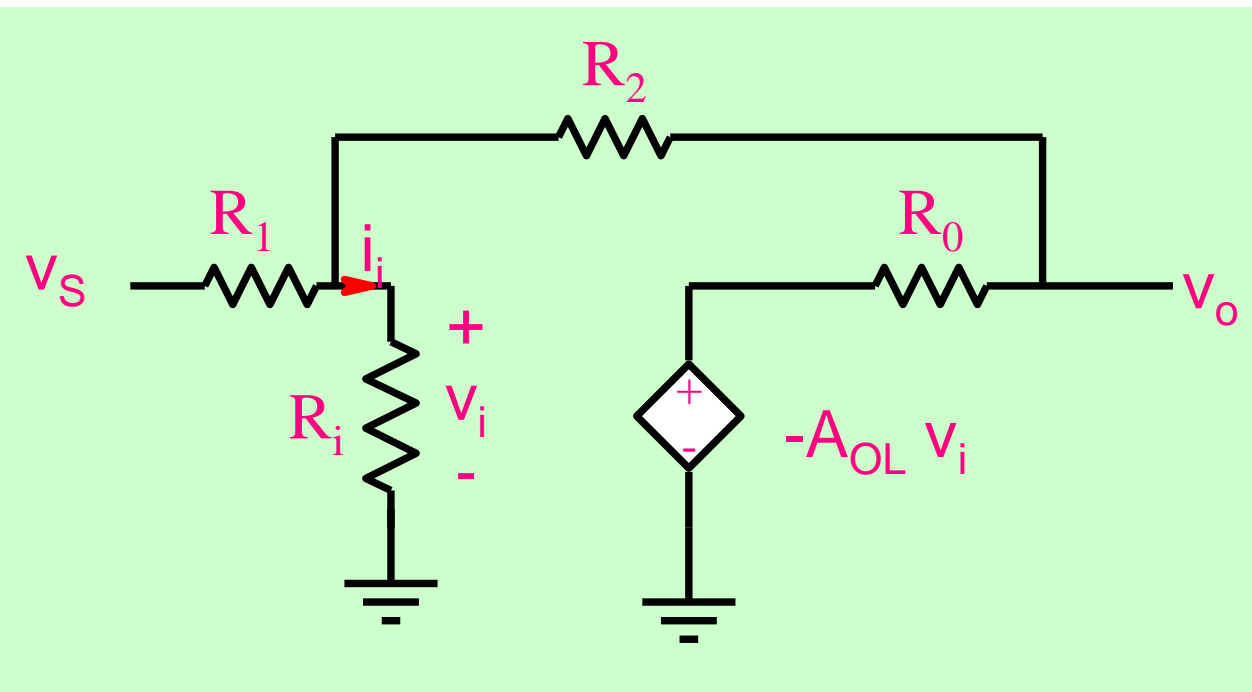
$$\frac{-A_{OL}v_i - v_o}{R_o} = \frac{v_o - v_i}{R_2} \quad (2)$$

$$\frac{v_s}{R_1} = -\frac{v_o}{R_2} + v_i \left(\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_i} \right) \quad (3)$$

$$v_i = v_o \frac{\frac{1}{R_o} + \frac{1}{R_2}}{\frac{-A_{OL}}{R_o} + \frac{1}{R_2}} \quad (4)$$

$$\text{As } A_{OL} \rightarrow \infty \quad v_i \rightarrow 0$$

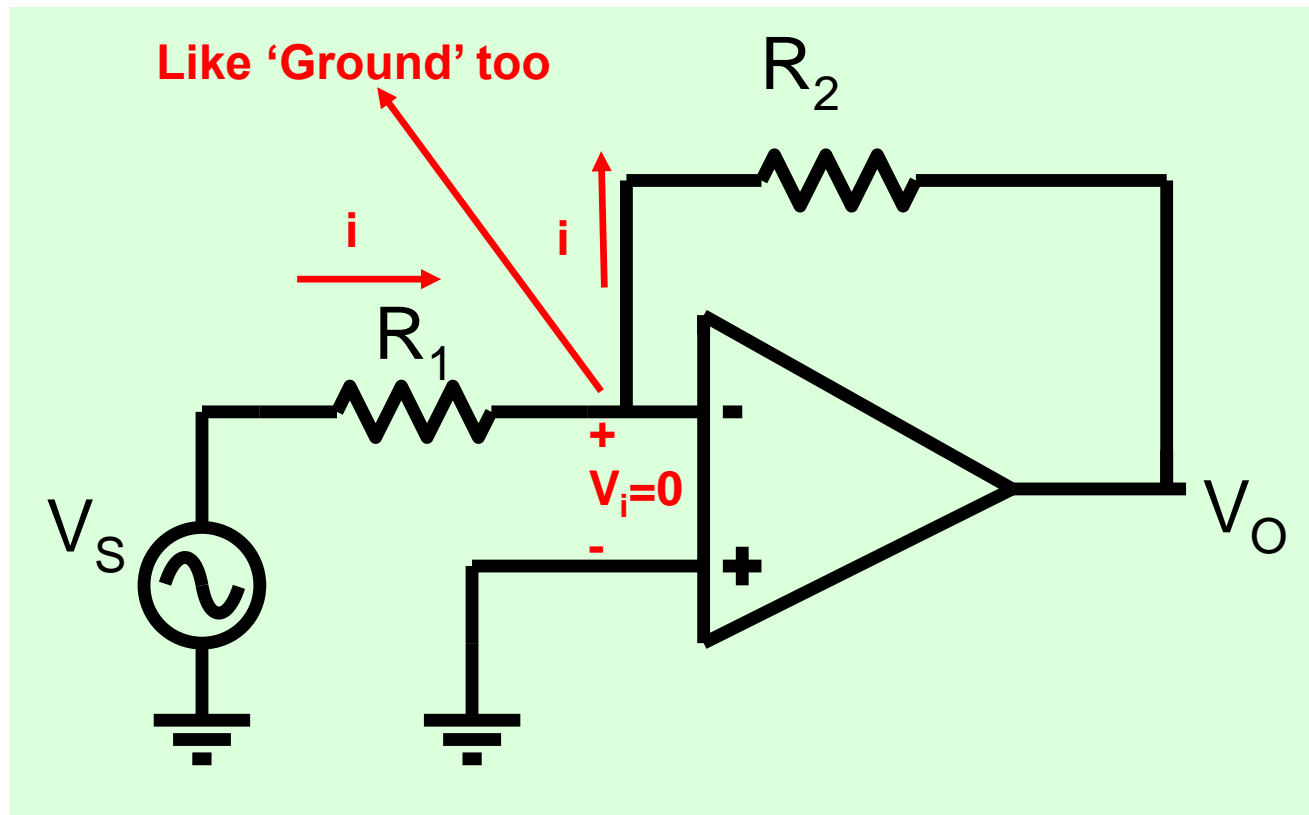
This is called the **Virtual Ground** property



$$\text{As } A_{OL} \rightarrow \infty \quad v_i \rightarrow 0$$

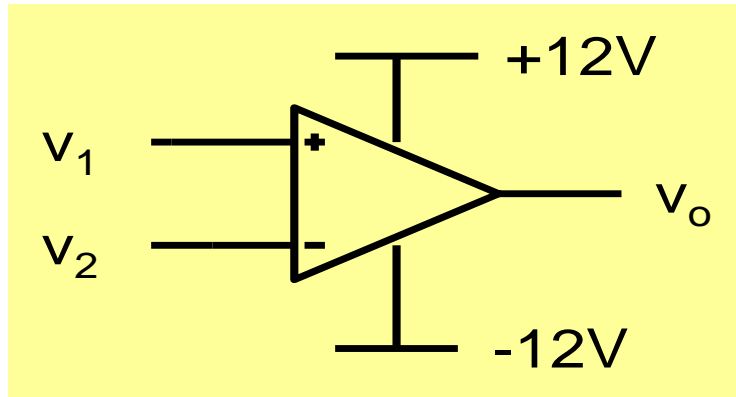
This implies that : $i_i \rightarrow 0$

No current flows in or out of either inverting or non-inverting terminals of an ideal opamp



Hence the name Virtual ground

Virtual Ground Property



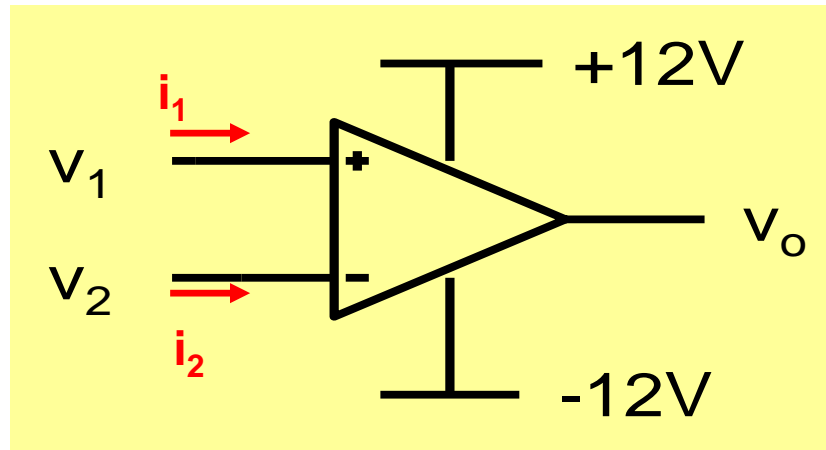
$$v_1 \cong v_2$$

In an opamp with **negative feedback**, the voltage of the inverting terminal is equal to the voltage of the non-inverting terminal if the **gain of the opamp is sufficiently high**

This property **does not** hold under certain conditions such as

- ☐ open loop,
- ☐ positive feedback
- ☐ or if the opamp is saturated.

Two important property for analyzing ideal opamp circuits under negative feedback



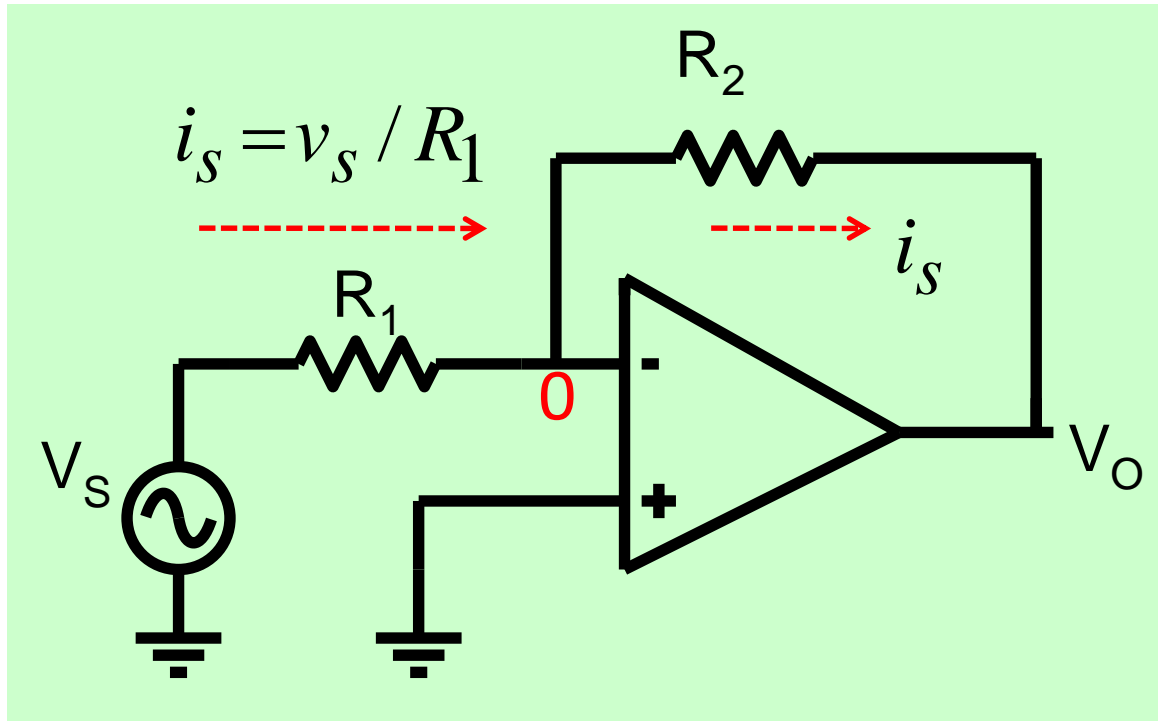
$$1. \quad v_1 = v_2$$

$$2. \quad i_1 = i_2 = 0$$

At the input side opamp appears to be like a short and an open circuit simultaneously !

Inverting amplifier

Re-analyze inverting amplifier with these properties



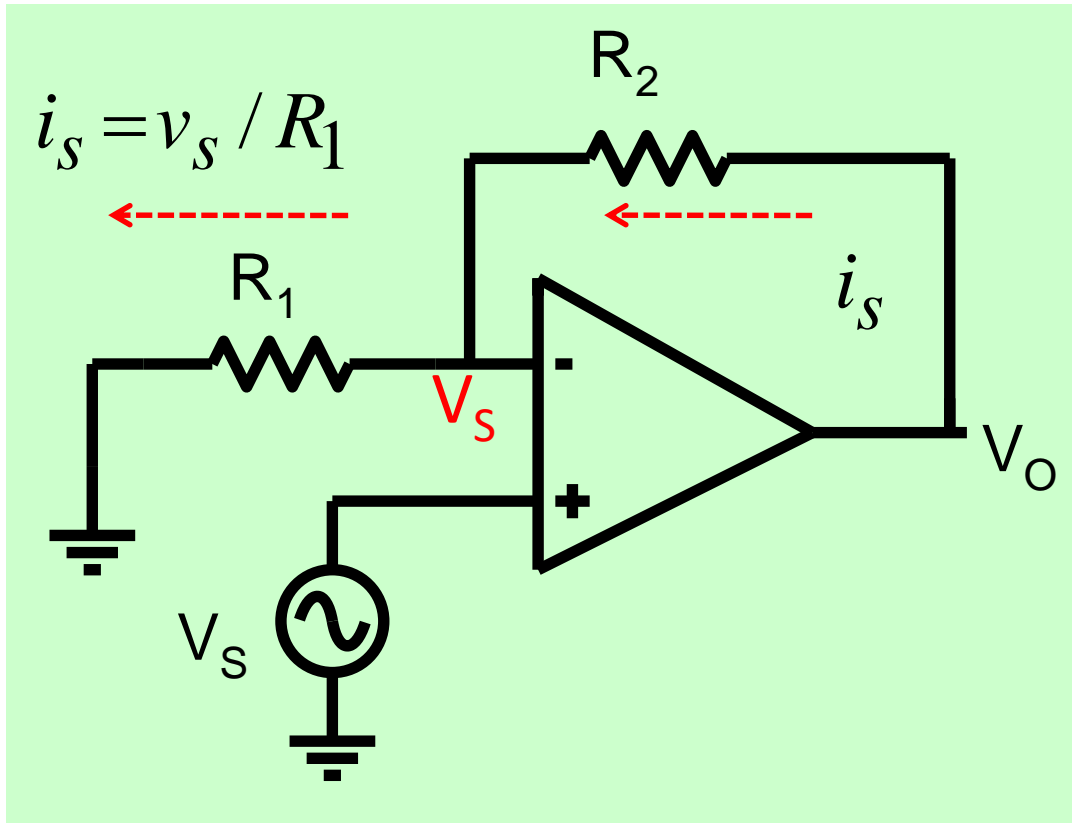
$$\frac{0 - v_o}{R_2} = i_s = \frac{v_s}{R_1}$$

$$\frac{v_o}{v_s} = -\frac{R_2}{R_1}$$

Non-Inverting Amplifier

$$1. \quad v_1 = v_2$$

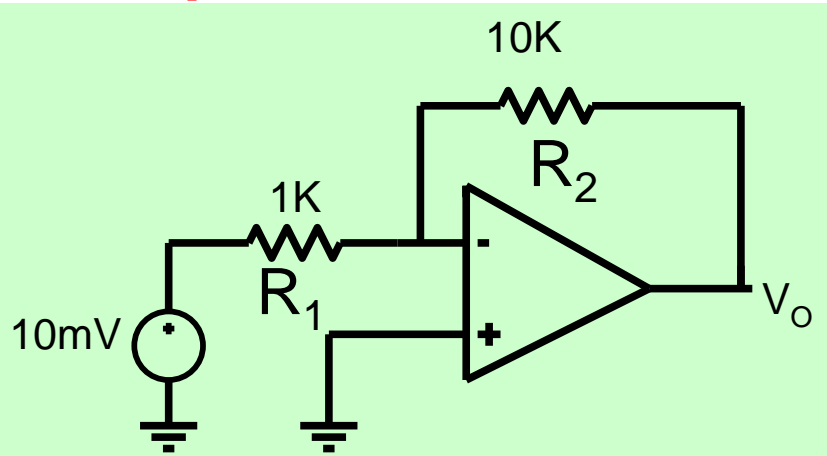
$$2. \quad i_1 = i_2 = 0$$



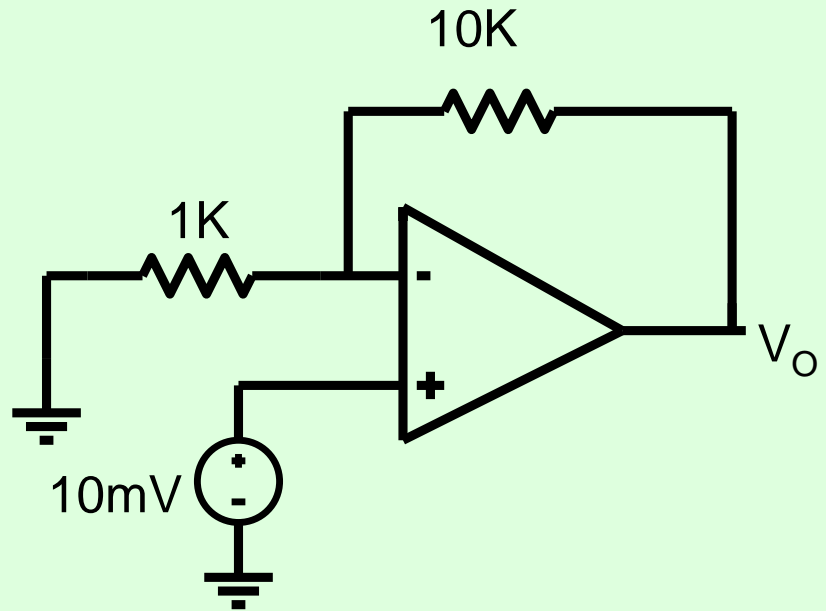
$$\frac{v_O - v_S}{R_2} = i_S = \frac{v_S}{R_1}$$

$$\frac{v_O}{v_S} = 1 + \frac{R_2}{R_1}$$

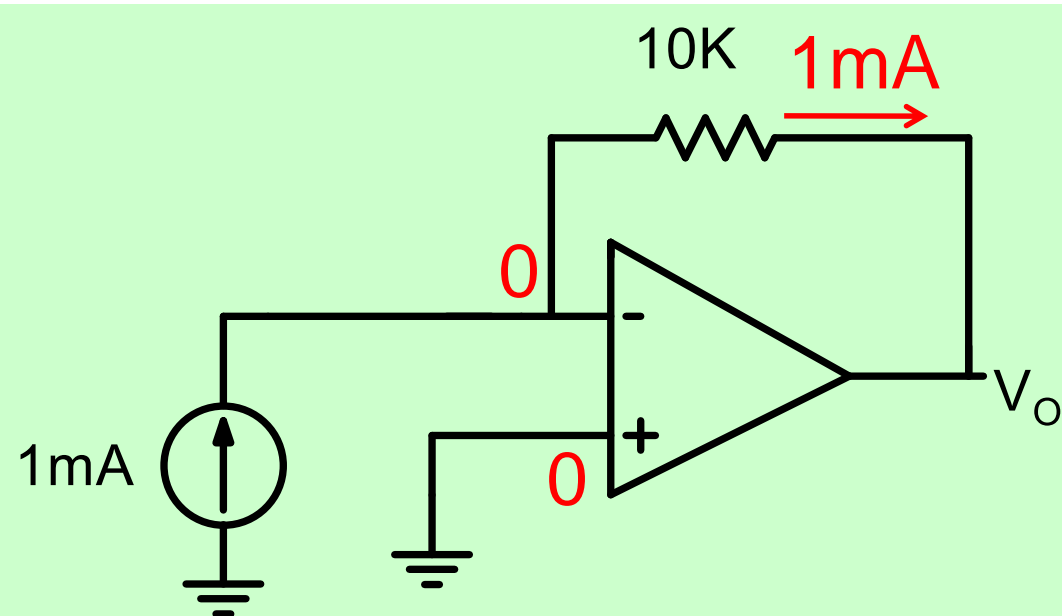
Examples



$$\frac{v_o}{v_S} = -\frac{R_2}{R_1} \Rightarrow v_o = -100mV$$



$$\frac{v_o}{v_S} = 1 + \frac{R_2}{R_1} \Rightarrow v_o = 110mV$$



$$\frac{0 - v_o}{10K} = 1mA$$

$$v_o = -10V$$