

ESC201A MidSem Part2

SAMYAK SINGHANIA

TOTAL POINTS

16.5 / 21

QUESTION 1

Q1 7 pts

1.1 1(a) 4 / 4

+ 4 pts Completely Correct

✓ + 1 pts *Vo correctly found for first circuit*

✓ + 1 pts *Vo correctly found for second circuit*

✓ + 2 pts *Vo correctly found for third circuit*

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

1.2 1(b) 3 / 3

✓ + 3 pts *Completely Correct*

+ 1 pts KCL Correct

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

+ 1 pts VPS max correct

+ 1 pts VPS min correct

QUESTION 2

Q2 8 pts

2.1 2(a) 3.5 / 4

+ 4 pts Completely Correct

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

✓ + 2 pts *Poles identified and transfer function form written correctly*

+ 2 pts Calculation for unknown frequency correct. Final transfer function correct

+ 1.5 Point adjustment

2.2 2(b) 0.5 / 4

+ 4 pts Completely Correct

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

+ 1 pts Circuit correct

+ 1 pts Circuit operation verified for $V_{in} < 2\text{ V}$

+ 1 pts Circuit operation verified for $2\text{ V} < V_{in} < 3\text{ V}$

+ 1 pts Circuit operation verified for $V_{in} > 3\text{ V}$

+ 0.5 Point adjustment

QUESTION 3

Q3 6 pts

3.1 3(a) 2.5 / 3

+ 3 pts Completely Correct

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

+ 2 pts Voltage gain calculated correctly

+ 1 pts Current gain calculated correctly

+ 2.5 Point adjustment

💬 Ai is in minus.

3.2 3(b) 3 / 3

✓ + 3 pts Completely Correct

+ 0 pts Completely Incorrect

+ 0 pts Not Attempted

+ 0 pts Copied

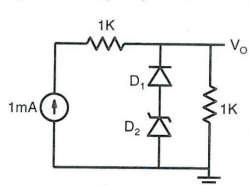
+ 1 pts vi graph correct

+ 1 pts vo graph correct

+ 1 pts io graph correct

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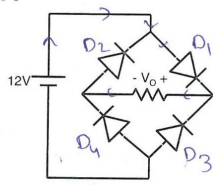
1 (a). Determine the output voltage (steady state output in case of sinusoidal input) for the circuits shown below assuming that cut-in voltage of diode and Zener diode is 0V and that Zener voltage is 3V. Give proper reasoning for your answer. [4]



If D_1 is on, then current through A would be negative. D_2 is off and acts like open circuit.

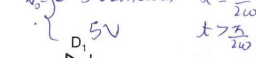
Current through $1K \Omega$ resistor is $1mA$.

$$\therefore V_0 = 1mA \times 1K \Omega = 1V$$



Here, if D_1 is on, then D_2, D_3, D_4 will be off. So no current flows in the circuit.

$$V_0 = 0V$$



Here, $V_1 = 5V \sin \omega t$ for half cycle, the current will flow in circuit and for the other half it will not. So once the capacitor becomes charged it will remain charged.

For $t \leq \frac{\pi}{2\omega}$, $V_0 = 5V \sin \omega t$, else $V_0 = 5V$

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1 (b). For the circuit shown below, determine the range of input voltage (minimum and maximum value) for which the circuit would provide a constant output voltage of 12V if the maximum and minimum current ratings of the Zener diode are 0.5A and 0.1A respectively. [3]

$$I = I_Z + I_L \quad V_Z = 12V$$

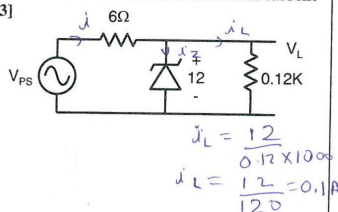
$$I = \frac{V_{PS} - V_Z}{6} = I_Z + I_L$$

$$\therefore 0.1 < \frac{V_{PS} - 12}{6} - I_L < 0.5$$

$$0.1 < \frac{V_{PS} - 12}{6} - 0.1 < 0.5$$

$$2.2 < \frac{V_{PS}}{6} < 2.6$$

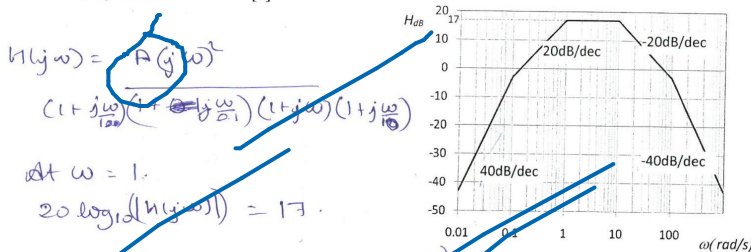
$$13.2V < V_{PS} < 15.6V$$



$$I_L = \frac{12}{0.12 \times 1000}$$

$$I_L = \frac{12}{120} = 0.1A$$

2(a). Obtain the transfer function $H(j\omega)$ corresponding to the Bode magnitude plot shown in the following figure. [4]



$$H(j\omega) = \frac{A(j\omega)^2}{(1 + j\frac{\omega}{10})(1 + j\frac{\omega}{100})}$$

$$\text{At } \omega = 1$$

$$20 \log_{10}(|H(j\omega)|) = 17$$

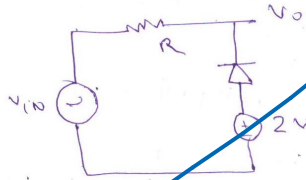
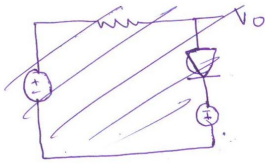
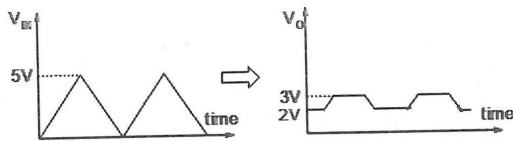
$$\log_{10} \left(\left| \frac{A}{(1 + j\frac{\omega}{10})(1 + j\frac{\omega}{100})} \right| \right) = \frac{17}{20}$$

$$\log_{10} \left(\frac{A}{10} \right) = 0.85$$

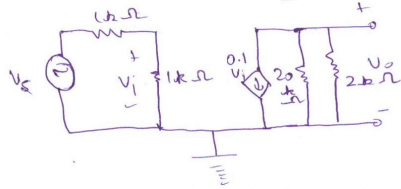
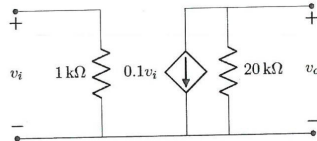
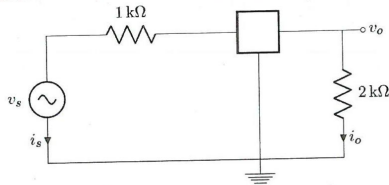
$$\therefore A = 0.707 \times 10 = 7.07$$

$$\therefore H(j\omega) = \frac{7.07(j\omega)^2}{(1 + j\frac{\omega}{10})(1 + j\frac{\omega}{100})}$$

2(b). Design a diode circuit to convert the triangular waveform into the waveform shown below. Give the circuit diagram along with typical component values. Assume ideal diodes with cut-in voltage of zero volts. [4]



3(a). Determine the voltage gain v_o/v_s and current gain i_o/i_s of the amplifier shown below on the left for the transistor model shown below on the right. [3]



$$N_{eff} = 20k\Omega \parallel 2k\Omega$$

$$= \frac{20 \times 2}{20 + 2} k\Omega$$

$$= \frac{20 \times 2}{22} = \frac{20}{11} k\Omega$$

$$\therefore V_o = -0.1 V_i \times \frac{20}{11} kV$$

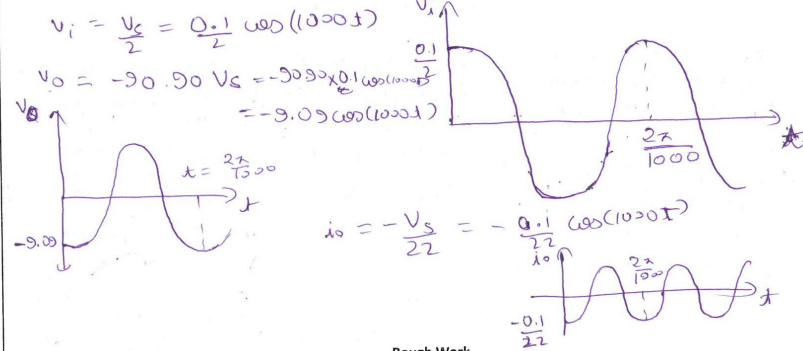
$$V_i = \frac{1}{2} V_s \quad \therefore V_o = -0.1 \times \frac{20}{11} \times \frac{1}{2} V_s kV$$

$$\Rightarrow \frac{V_o}{V_s} = -0.1 \times \frac{20}{11} \times \frac{1}{2} \times 1000 = -90.90$$

$$i_o = -0.1 V_i \times \frac{20}{22} = -0.1 \times \frac{20}{22} \times \frac{V_s}{2} = -\frac{V_s}{22} A$$

$$i_s = \frac{V_s}{(1+1)k\Omega} = \frac{V_s}{2} mA \quad \Rightarrow \frac{i_o}{i_s} = \frac{-\frac{V_s}{22} \times 1000}{\frac{V_s}{2} \times 1000} = -90.90$$

3(b). If $v_s = 0.1 \cos(1000t)$ in part 3(a), plot v_i , v_o , and i_o as a function of time. [3]



Rough Work

