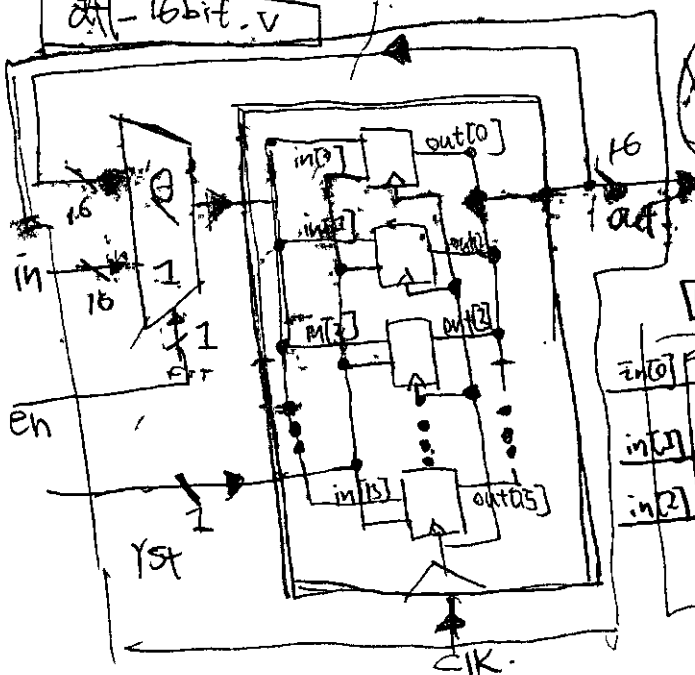


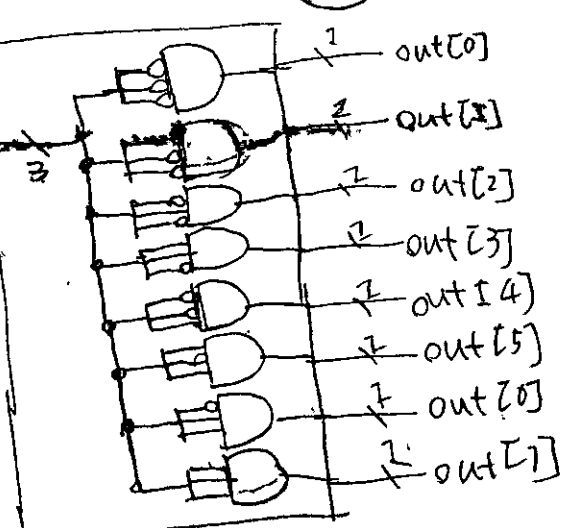
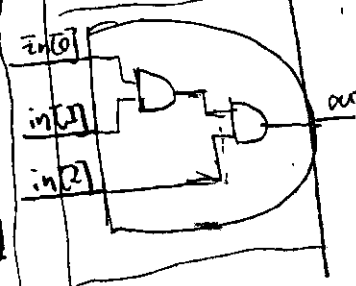
diff-16bit.v

decoder-3\_8.v

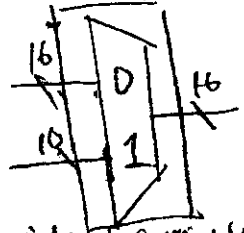
HW3.1



and-3.v

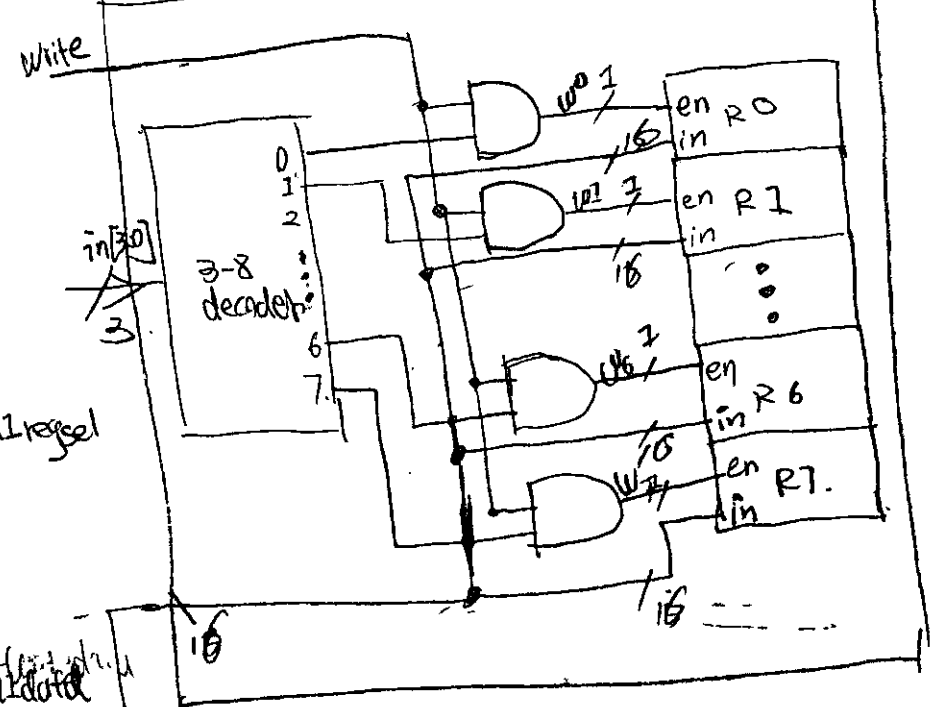


mux2-16bit.v

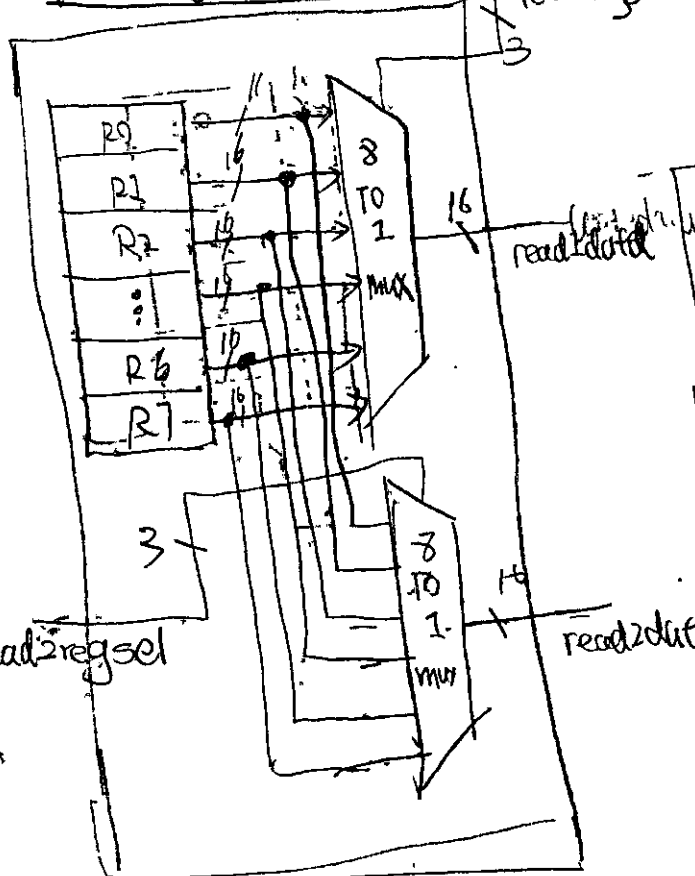


Provide from HW2-2

rf.v Left half



rf.v Right half



mux8-12-16bit.v

