The Elements of Theses

BY

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THESIS

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Chicago, Illinois

Defense Committee:

member1, Chair and Advisor
member2
italian member, Politecnico di Torino

ACKNOWLEDGMENTS

I want to "thank" my committee, without whose ridiculous demands, I would have graduated so, so, very much faster.

AB

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LIST OF ABBREVIATIONS

AMS	American Mathematical Society
CTAN	Comprehensive TEX Archive Network
TUG	TEX Users Group
UIC	University of Illinois at Chicago
UICTHESI	Thesis formatting system for use at UIC.

SUMMARY

A summary is required.

CHAPTER 1

INTRODUCTION

Every dissertation should have an introduction. You might not realize it, but the introduction should introduce the concepts, background, and goals of the dissertation.

1.1 A Section

Most chapters have sections. Neat huh?

1.1.1 A Subsection

Some chapters have subsections too! Cool!

TABLE I: BAYES FILTER ALGORITHM

Algorithm Bayes filter $(bel(x_{t-1}), u_t, z_t)$:
for all possible future states x_t do
$\overline{bel}(x_t) = \int p(x_t \mid u_t, x_{t-1}) bel(x_{t-1}) dx$
$bel(x_t) = \eta(z_t \mid x_t)\overline{bel}(x_t)$
endfor

return $bel(x_t)$

CHAPTER 2

PREVIOUS WORK

Some other research was once performed.

Figure 1: A first figure.

Figure 2: A figure with the maximum width you can use

CHAPTER 3

CONCLUSION

A conclusion is a good thing to have. Hopefully it is a good one! repeat some of the concepts expressed in the summary/intro

APPENDICES

Appendix A

SOME ANCILLARY STUFF

Ancillary material should be put in appendices.

Appendix B

SOME MORE ANCILLARY STUFF

Here is yet another appendix! Wahoo!

CITED LITERATURE

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14. Fuchs, D.: Device independent file format. TUGboat, 3(2):14–19, October 1982.

VITA

NAME	your name
EDUCATION	
	Master of Science in Electrical and Computer Engineering, University of Illinois at Chicago, May 2015, USA
	Specialization Degree in Embedded Systems Electronic Engineering , Jul 2015, Polytechnic of Turin, Italy
	Bachelor's Degree in Information Technology Engineering (ITE) -
	Electronic Engineering, Jul 2012, Polytechnic of Turin, Italy
LANGUAGE SKILLS	
Italian	Native speaker
English	Full working proficiency
	2013 - IELTS examination (27.0/9)
	A.Y. 2014/15 One Year of study abroad in Chicago, Illinois
	A.Y. 2013/14. Lessons and exams attended exclusively in English
SCHOLARSHIPS	
Spring 2015	Research Assistantship (RA) position (20 hours/week) with full tuition waiver plus monthly stipend
Fall 2014	RA position (10 hours/week) with full tuition waiver plus monthly stipend
Fall 2014	Italian scholarship for final project (thesis) at UIC
Fall 2014	Italian scholarship for TOP-UIC students
TECHNICAL SKILLS	
Basic level	making pizza
Average level	making pasta
Advanced level	eating pasta and pizza

VITA (continued)

WORK EXPERIENCE AND PROJECTS

Dec 2014 - May 2015	went on vacation
Sep 2014 - Dec 2014	made several cakes
2014	Top-down design of a custom DLX processor 2014
	Design of digital micro and macro-architectures: design, VHDL description, simulation, synthesis, place & route of DLX microprocessor in all its part starting from scratch, pipelined version, data and control hazard management.
2013-2014	Other Experiences:
	Use of prototyping boards, micro controllers, FPGAs, inside a bigger system or stand-alone (experience: realization of some measuring and control systems, simulations of designed components on FPGA)
	Development of all the parts of the software needed to realize measuring systems, data processing systems or mechanical control
	Scripts for simulation and synthesis of integrated circuits
	Multi-process and multi-thread C programs (in particular for encryption)