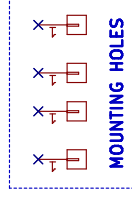
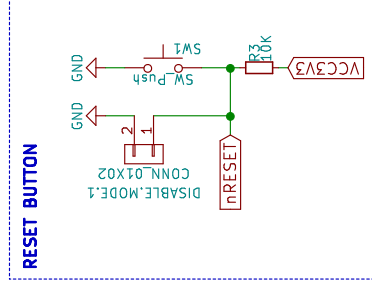


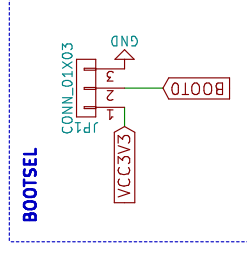
POWER SECTION



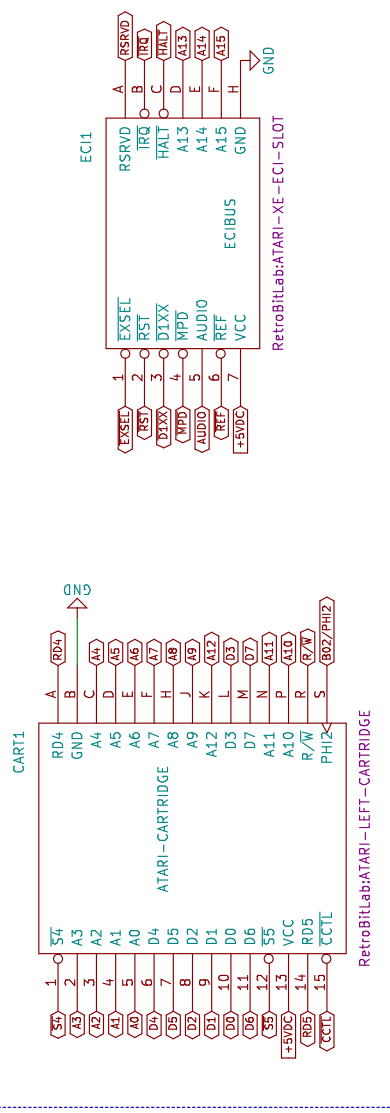
MOUNTING HOLES



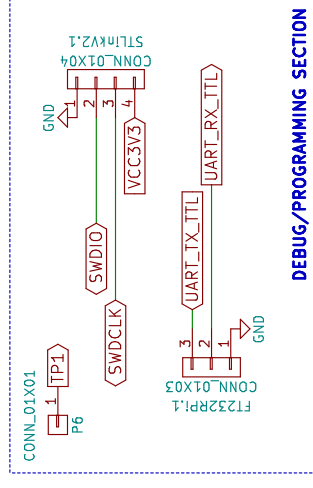
RESET BUTTON



BOOTSEL



ATARI CONNECTIONS



DEBUG/PROGRAMMING SECTION

MULTICARTRIDGE SDCard
MIO ICD Memory Expansion Compatible
1024K + 64K RAM EXPANSION
CART+ECI Interface to mapping 512K x 8 x 4 (x 2)

RetrobitLab

Sheet: /
File: atari23MTS_Upgrade.sch

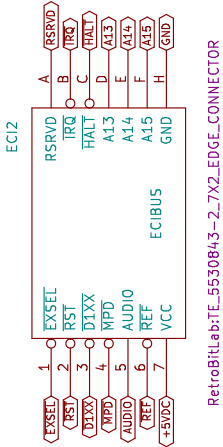
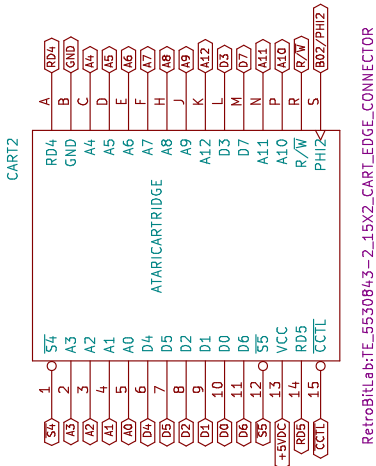
Title: ATARI XE 1 MegaRAM Expansion POWER/DEBUG/Logic

Size: A4	Date: 7 jan 2019
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KiCad E.D.A. kicad 4.0.2+dfsq1-stable

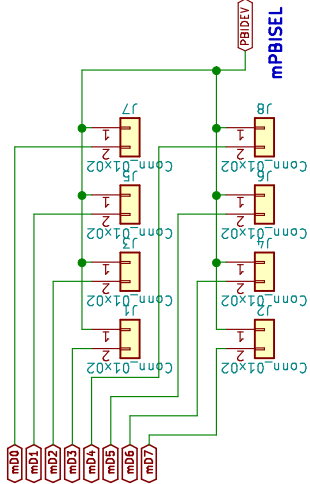
Rev: 1/4

EXPANSION BUS PASS THRU

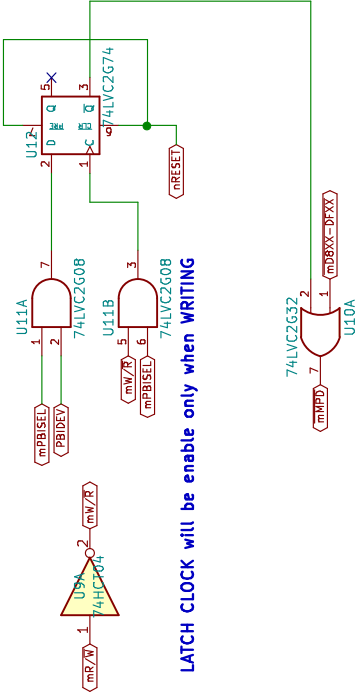


RetroBitLab:TE_5530B43-2_15X2_CART_EDGE_CONNECTOR

PBI Device Selection when accessing \$D1FF



mPBSEL will go high when accessing \$D1FF



LATCH CLOCK will be enable only when WRITING

MATH PACK DISABLE WHEN DEVICE ENABLED & ACCESSING D8XX-DFFF

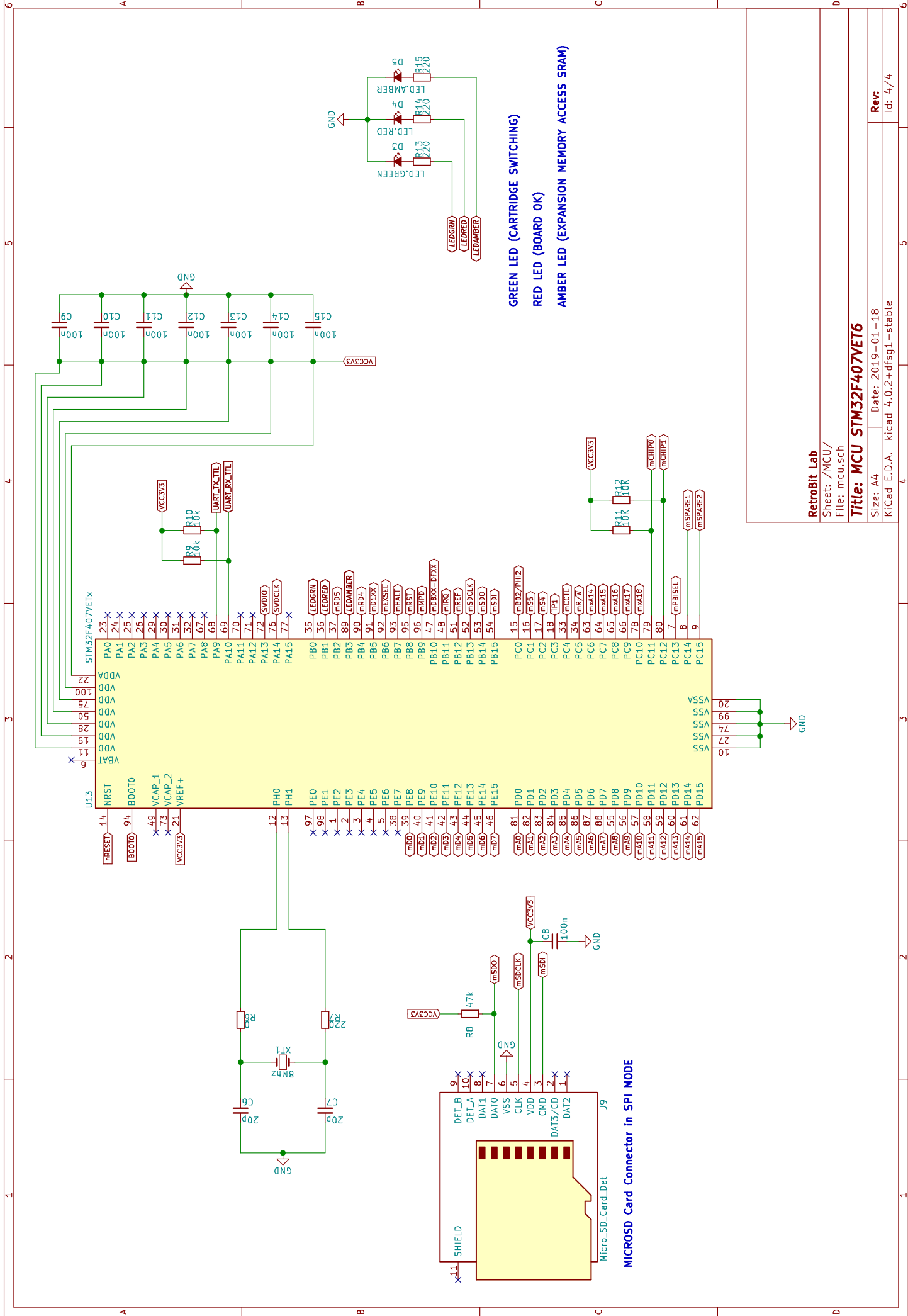
RetroBit Lab

Sheet: /Expansion Slots pass through/
File: expansionslotspt.sch

Title: ATARI EXPANSION BUS PASS THRU & DXXX Address Select

Size: A4 Date: 7 jan 2019 Rev:

KiCad E.D.A. kicad 4.0.2+dfsg1-stable Id: 3/4



RetroBit Lab

Sheet: /MCU/
File: mcu.sch

Title: MCU STM32F407VET6

Size: A4 Date: 2019-01-18
Kicad E.D.A. kicad 4.0.2+dfsg1-stable

Rev:

Id: 4/4