

Sheet: VeraModule

VERA FPGA

File: vera-fpga.sch

Sheet: BusDecoder

BUS DECODER

File: busdecoder.sch

Sheet: Vera FPGA flash

VERA SPI FLASH
SD CARD INTERFACE

File: vera-fpga-flash.sch

Sheet: CartridgeInterface

CARTRIDGE INTERFACE

File: cartridgeInterface.sch

Sheet: PowerSupply

POWER SUPPLY

File: powersupply.sch

FIDUCIAL TOP

FID3
Fiducial

FID4
Fiducial

FIDUCIAL BOTTOM

FID2
Fiducial

FID1
Fiducial

LOGO1
VERA X16 LOGO

LOGO3
KICAD DESIGN

LOGO5
ESP32_BOBOARD

LBL1

LOGO2
ATARI READY

LOGO4
ATARI DUO BUS

Gianluca Renzi

RetroBit Lab

Sheet: /
File: VERA-MODULE-RBL.sch

Title: VERA FPGA Audio & Video Board

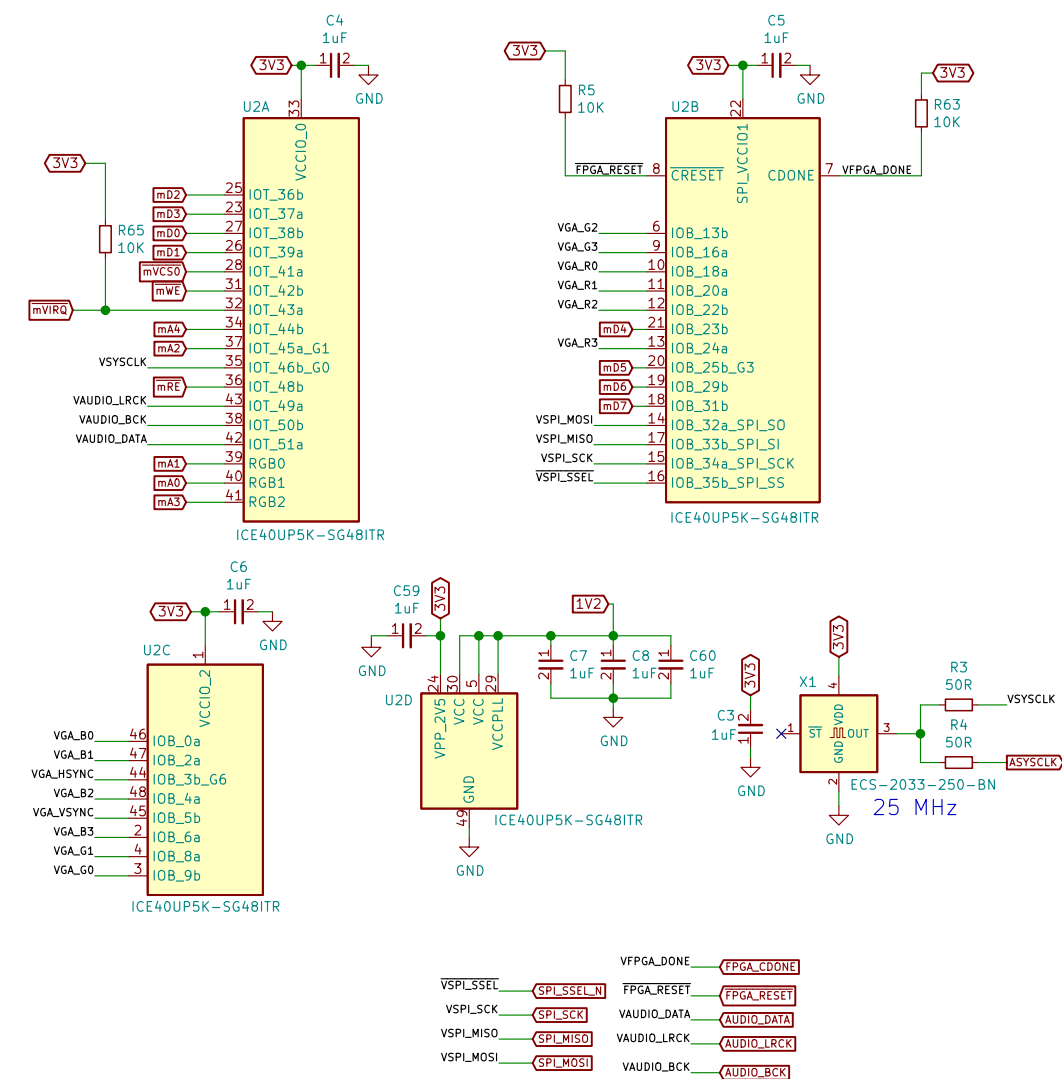
Size: A3 Date: 2025-09-23

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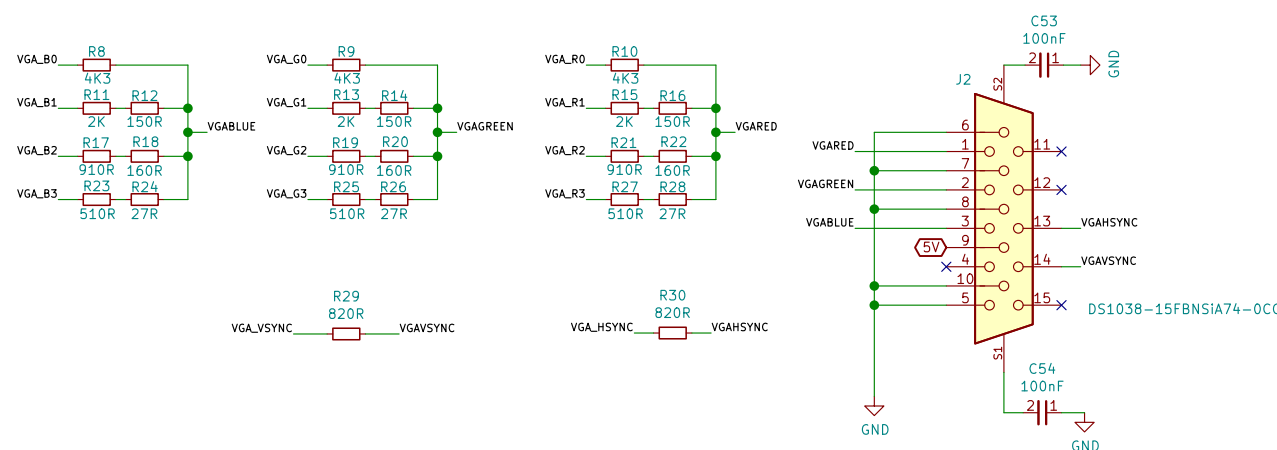
Rev: 1.0

Id: 1/6

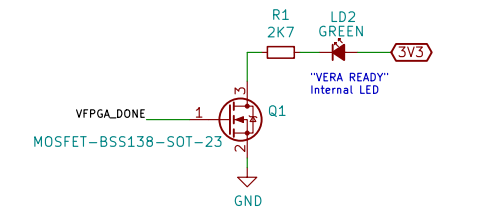
FPGA VERA LOGIC VIDEO & AUDIO CARD



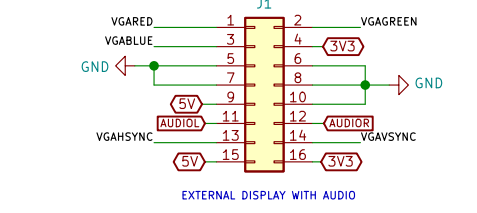
ANALOG VGA SIGNALS



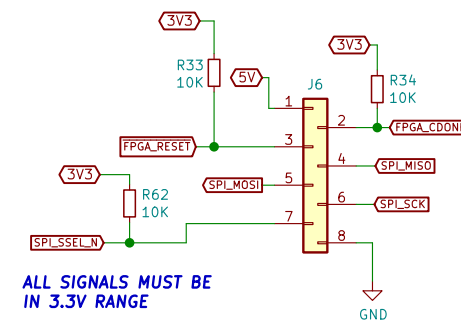
VERA FPGA PROGRAMMED OK



EXTERNAL VIDEO CONNECTOR



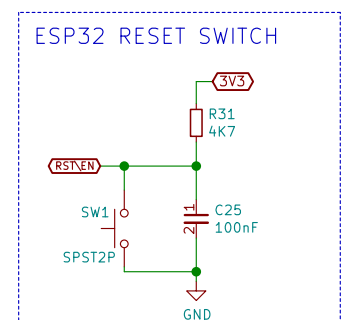
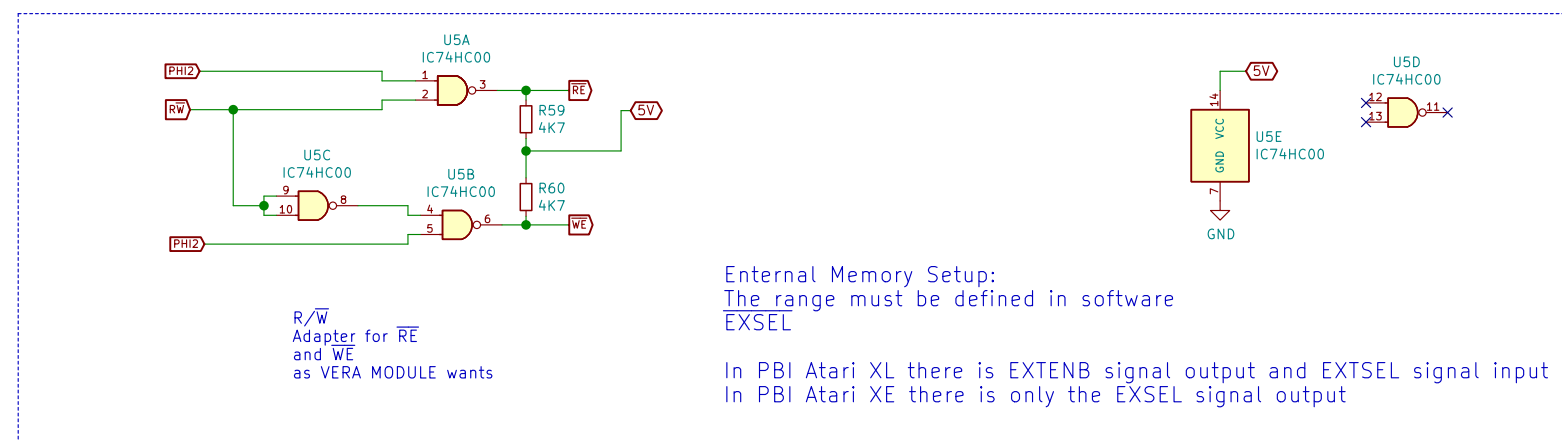
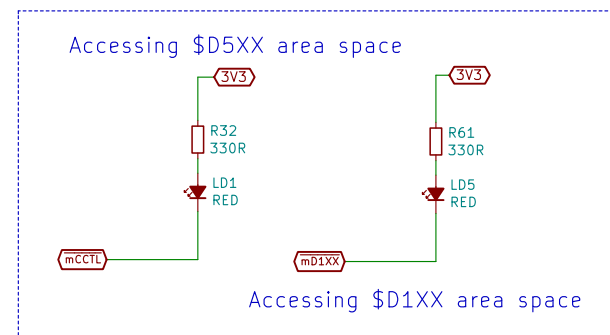
iceprog programmer USB FTDI / SPI



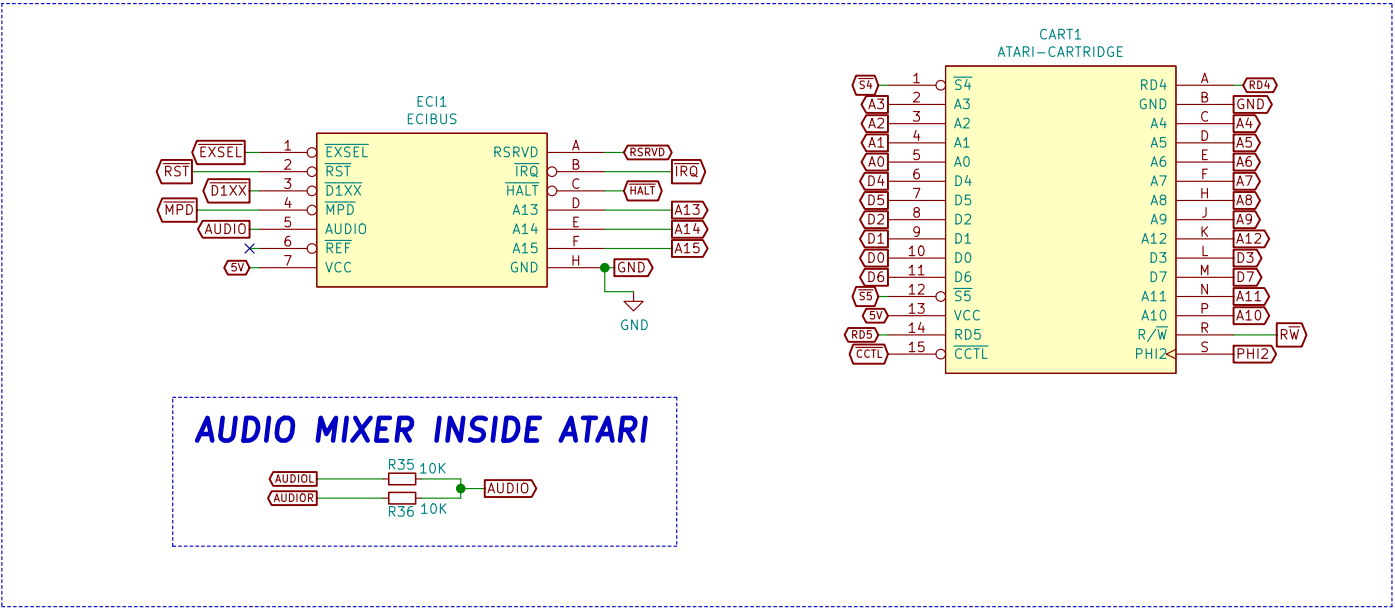


\$D1FF access & DATABUS = PBI DEVICE ID -> mVCS0 active/deactive

Those signals are valid in ATARI XE only



ATARI 130XE ECI & CARTRIDGE INTERFACE



BUS LOGIC LEVEL SHIFTERS

