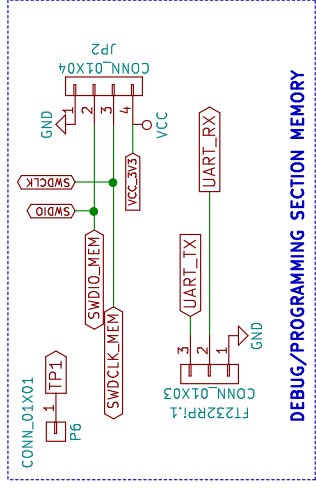
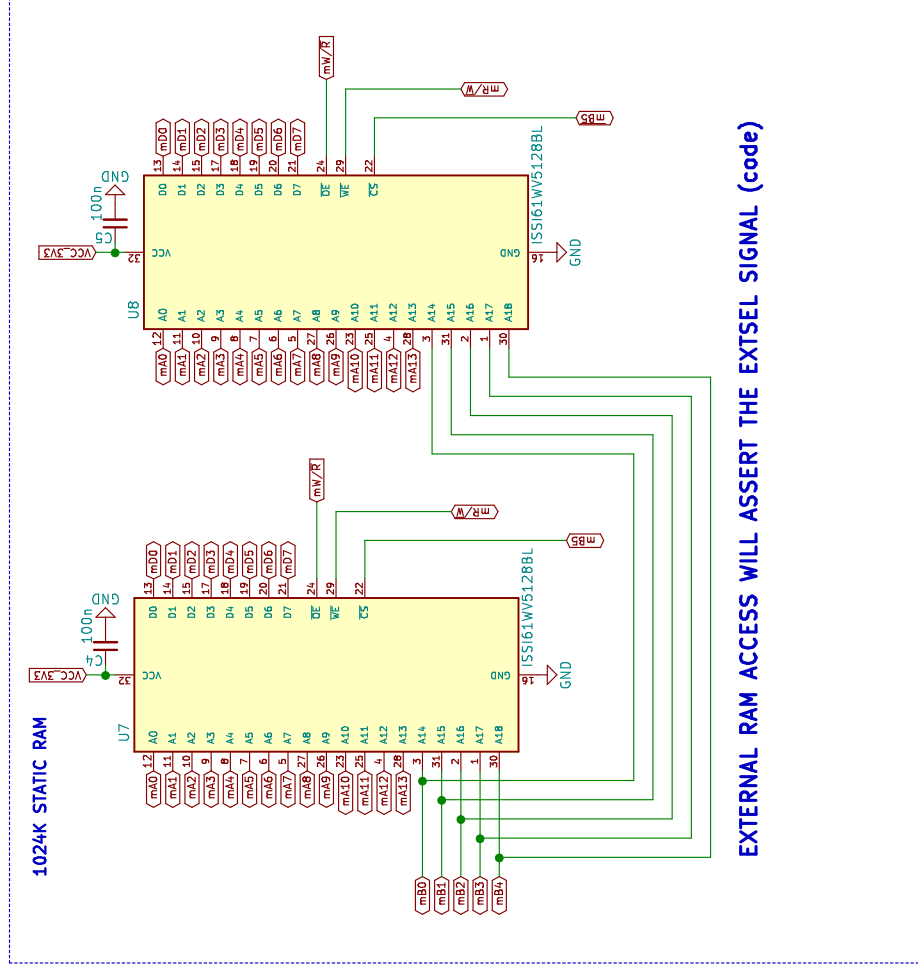
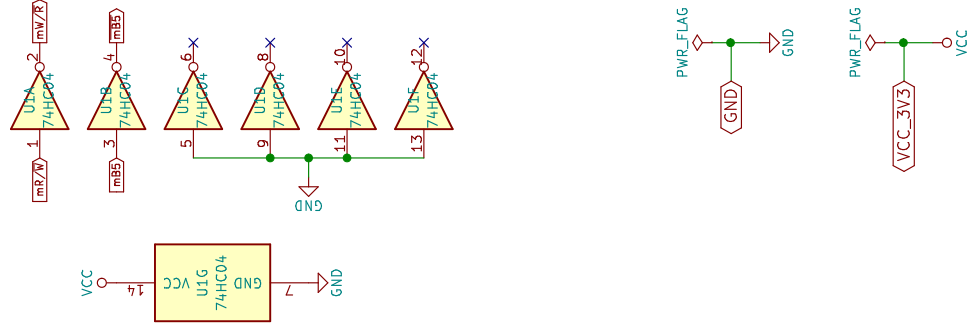


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Size: A4	Date: 2019-04-06	Rev:
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INVERTER LOGIC FOR SEPARATED READ/WRITE ACCESS



For ABEX Bus Extender
Level Shifters and Memory SRAM Connections
BUS ADAPTERS AND MEMORY LAYOUT

RetroBit Lab

Sheet: /Static RAM/
File: sram-levelshifters.sch

Title: STATIC RAM 1 MByte (512K x 8 x 2)

Size: A4	Date: 2019-04-06	Rev: 1.0
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