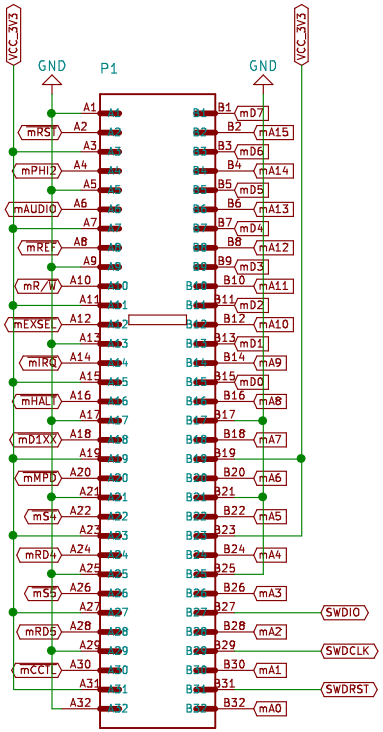
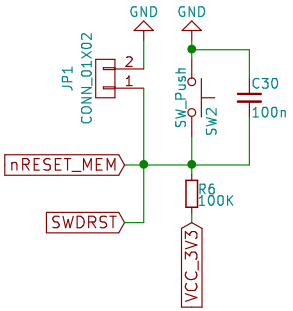
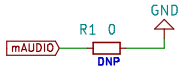
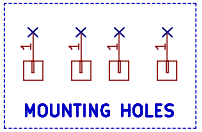
ABEX PCI EXPRESS CARD CONNECTOR 64 PIN

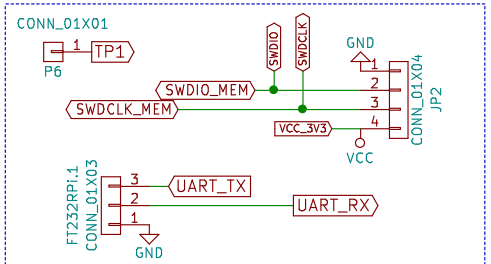


RESET BUTTON MEM









EXTENDED BUS PINS ARE CONNECTED IN THE SLOT BUT LEFT UNTRACKED

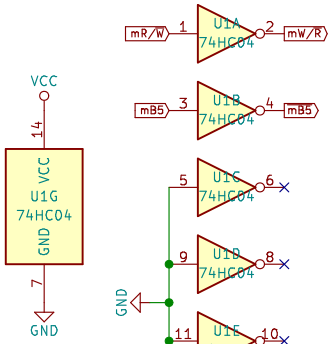
For ABEX Bus Extender
RAM EXPANSION BOARD
512K x 2 + 64K RAM EXPANSION
RAM Expansion Interface up to 1024K
RetrobitLab

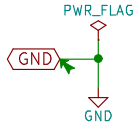
Sheet: /
File: abex-megaram.sch

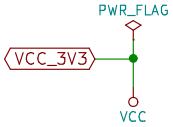
Title: ATARI XE 1 MegaRAM Expansion DEBUG/Logic

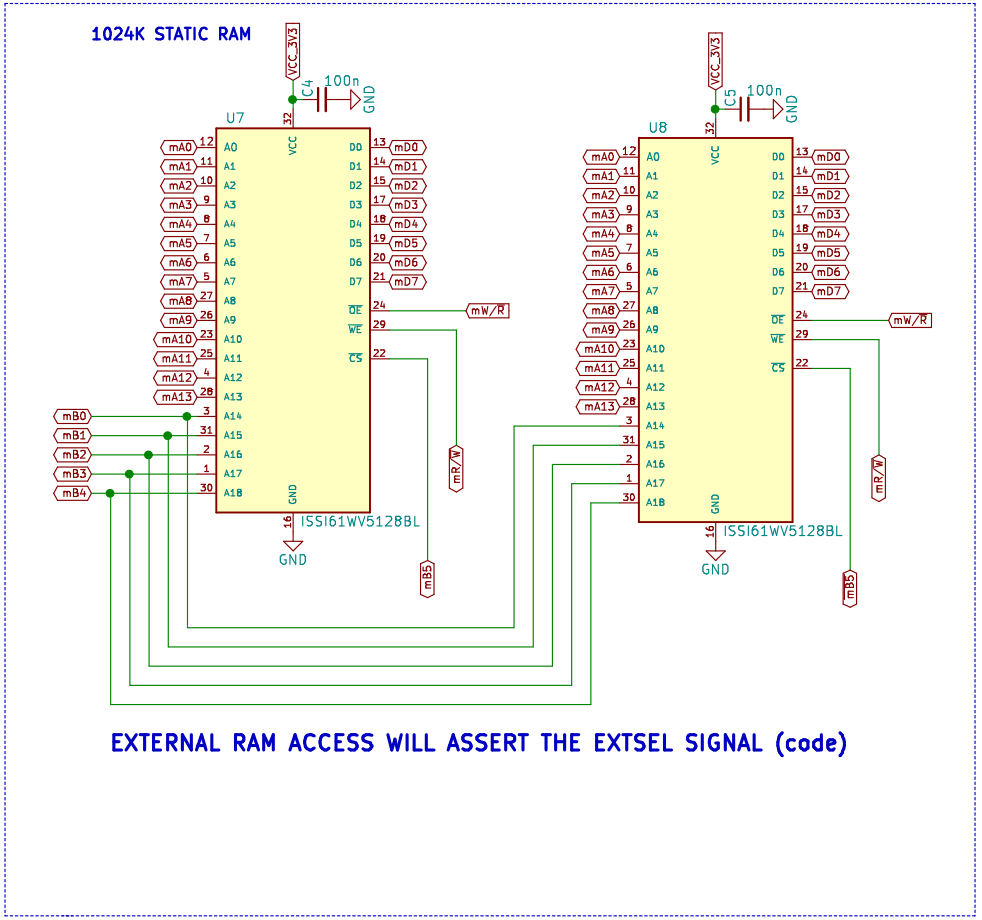
Size: A4	Date: 2019-04-06	**Rev:**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 1/3

INVERTER LOGIC FOR SEPARATED READ/WRITE ACCESS







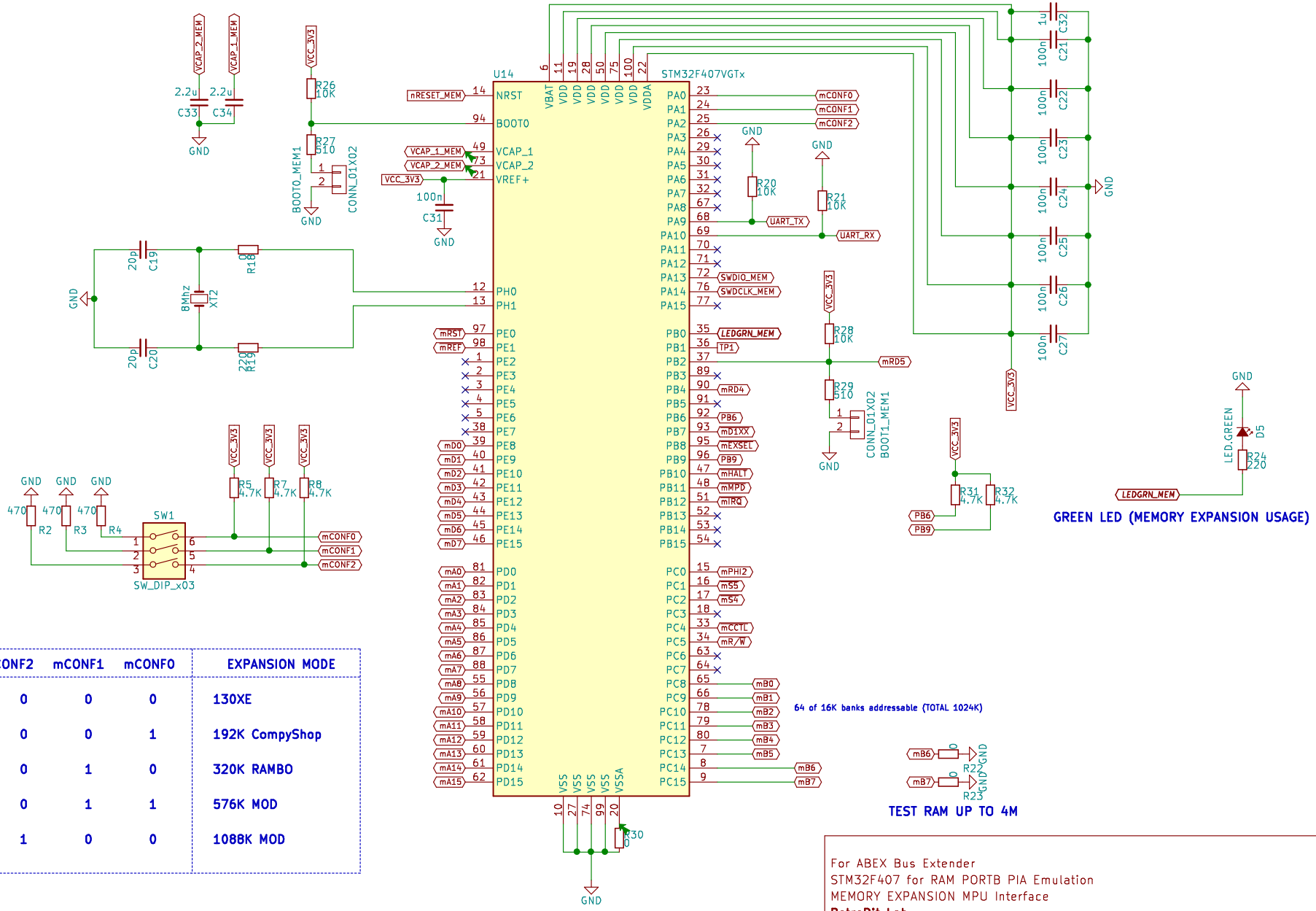


For ABEX Bus Extender
Level Shifters and Memory SRAM Connections
BUS ADAPTERS AND MEMORY LAYOUT
RetroBit Lab

Sheet: /Static RAM/
File: sram-levelshifters.sch

Title: STATIC RAM 1 MByte (512K x 8 x 2)

Size: A4	Date: 2019-04-06	**Rev: 1.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 2/3



mCONF2	mCONF1	mCONF0	EXPANSION MODE
0	0	0	130XE
0	0	1	192K CompyShop
0	1	0	320K RAMBO
0	1	1	576K MOD
1	0	0	1088K MOD

130XE the internal extra 64K RAM are disabled by default. Only External RAM is available.

For ABEX Bus Extender
STM32F407 for RAM PORTB PIA Emulation
MEMORY EXPANSION MPU Interface
RetroBit Lab

Sheet: /MCU_MEMORY/
File: memory_controller.sch

Title: MCU STM32F407VET6 MEMORY CONTROLLER

Size: A4	Date: 2019-04-06	**Rev: 1.0**
KiCad E.D.A. kicad 4.0.2+dfsg1-stable		Id: 3/3