

# Gianmarco Dogliani

ELECTRONIC ENGINEER · SR APPLICATION ENGINEERING

Turin, Italy

+46 765064927 | gianmarco.se@icloud.com | gianmarcodogliani | gianmarcodogliani

## Work Experience

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### Cadence Design Systems

Sr Application Engineer

- Providing technical support in the usage of Cadence tools for Multiphysics System Analysis
- Delivering SI/PI/EMC analysis services for PCB and IC Packages signoff
- **Technical Skills:** PCB/IC Packages, Signal Integrity (SI), Power Integrity (PI) and EMC Analysis, Simulation
- **Soft Skills:** Customer service, technical presentation, documentation and report writing, problem solving, time management, task prioritization

Stockholm, Sweden

August 2025 - Present

### Artedas Italia

Application Engineer

Turin, Italy

April 2022 - July 2025

- Providing technical support in the usage of Cadence tools for PCB Design and Analysis
- Developing Tcl/Tk and SKILL scripts for custom add-ons to enhance design workflows
- Leading onsite and online corporate training programs and workshops on related topics
- **Technical Skills:** CAD, Schematic Entry, PCB layout, Tcl/Tk and SKILL scripting, Signal Integrity (SI) and Power Integrity (PI) Analysis, Simulation
- **Soft Skills:** Customer service, technical presentation, documentation and report writing, problem solving, time management, task prioritization

## Certifications

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### Comitato Elettrotecnico Italiano

Milan, Italy

November 2024

Electromagnetic Compatibility, Signal and Power Integrity

- EMC, SI and PI fundamentals
- Overview of the main European EMC regulations
- General understanding of how physical design affects electrical performances
- **Technical Skills:** Electromagnetic Compatibility (EMC), Signal Integrity (SI), Power Integrity (PI), modeling, parasitic extraction

## Education

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### Politecnico di Torino

M.S. in Electronic Engineering

Turin, Italy

October 2020 - July 2023

- RTL Description, Synthesis and Physical Design of digital systems architectures
- Analysis of design techniques for testability and for signal integrity compliance
- **Technical Skills:** Embedded Systems, ASIC Design, FPGA Design, Design for testability (DFT), Signal Integrity (SI)

### Politecnico di Torino

B.S. in Electronic Engineering

Turin, Italy

October 2017 - October 2020

- Solid background on maths, physics, computer science and circuit theory
- In-depth aspects of electronic devices and circuits, electromagnetic theory and digital signal processing techniques
- **Technical Skills:** Python Programming, Circuit Analysis, Electromagnetic (EM) Theory, Digital Signal Processing (DSP)

## Publications

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### PCB E/T Co Simulation with Celsius PowerDC

Artesia Italia

focusonpcb

May 2024

- Speech at Focus on PCB Fair on the usage of Cadence Celsius PowerDC to run an Electro-Thermal Co Simulation of an high-speed digital PCB layout hosting FPGAs and DDR4 memories
- **Soft Skills:** Technical presentation skills

### M.S. Thesis on development of a medical wearable device for the self monitoring of CVDs

Politecnico di Torino

M.S. Thesis

July 2023

- Schematic design using OrCAD Capture CIS, layout development with OrCAD PCB Editor
- Combination of reflow and hand soldering for bare board assembly, PCBA testing
- **Technical Skills:** Schematic Entry, PCB layout

## Languages

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**English** Professional proficiency

**Italian** Native proficiency

**Swedish** Basic proficiency