

# Gianmarco Dogliani

ELECTRONIC ENGINEER · APPLICATION ENGINEERING

Turin, Italy

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## Personal Profile

Electronic Engineer with hands-on experience in PCB design, SI/PI analysis, and Tcl/Tk design automation scripting. Currently working as an Application Engineer for a Cadence channel partner, providing technical support and training for enterprise clients. Solid background in ASIC/FPGA design flows, with a strong interest in developing advanced solutions. Actively looking to grow in an R&D role within the semiconductor industry.

## Work Experience

### Artedas Italia - Cadence Channel Partner

Turin, Italy

Application Engineer

April 2022 - Present

- Providing technical support in the usage of Cadence tools for PCB Design and Analysis
- Developing Tcl/Tk and SKILL scripts for custom add-ons to enhance design workflows
- Leading onsite and online corporate training programs and workshops on related topics
- **Technical Skills:** CAD, Schematic Entry, PCB layout, Tcl/Tk and SKILL scripting, Signal Integrity (SI) and Power Integrity (PI) Analysis, Simulation
- **Soft Skills:** Customer service, technical presentation, documentation and report writing, problem solving, time management, task prioritization

## Certifications

### Comitato Elettrotecnico Italiano

Milan, Italy

Electromagnetic Compatibility, Signal and Power Integrity

November 2024

- EMC, SI and PI fundamentals
- Overview of the main European EMC regulations
- General understanding of how physical design affects electrical performances
- **Technical Skills:** Electromagnetic Compatibility (EMC), Signal Integrity (SI), Power Integrity (PI), modeling, parasitic extraction

## Education

### Politecnico di Torino

Turin, Italy

M.S. in Electronic Engineering

October 2020 - July 2023

- RTL Description, Synthesis and Physical Design of digital systems architectures
- Analysis of design techniques for testability and for signal integrity compliance
- **Technical Skills:** Embedded Systems, ASIC Design, FPGA Design, Design for testability (DFT), Signal Integrity (SI)

### Politecnico di Torino

Turin, Italy

B.S. in Electronic Engineering

October 2017 - October 2020

- Solid background on maths, physics, computer science and circuit theory
- In-depth aspects of electronic devices and circuits, electromagnetic theory and digital signal processing techniques
- **Technical Skills:** C Programming, Circuit Analysis, Electromagnetic (EM) Theory, Digital Signal Processing (DSP)

## Publications

### Manuale di scripting OrCAD X Tcl/Tk Extensions

Artedas Italia

🔗 orcap-tcltk

May 2024

- Guide on the usage of the Tcl/Tk extensions to optimize design flows in OrCAD X Capture
- **Technical Skills:** Tcl/Tk scripting
- **Soft Skills:** Documentation and report writing

### PCB E/T Co Simulation with Celsius PowerDC

Artedas Italia

🌐 focusonpcb

May 2024

- Speech at Focus on PCB Fair on the usage of Cadence Celsius PowerDC to run an Electro-Thermal Co Simulation of an high-speed digital PCB layout hosting FPGAs and DDR4 memories
- **Soft Skills:** Technical presentation skills

### M.S. Thesis on development of a medical wearable device for the self monitoring of CVDs

Politecnico di Torino

📖 M.S. Thesis

July 2023

- Schematic design using OrCAD Capture CIS, layout development with OrCAD PCB Editor
- Conjunction of reflow and hand soldering for bare board assembly, PCBA testing
- **Technical Skills:** Schematic Entry, PCB layout