

REVISION HISTORY:

BCM943362WCD8\_1 Rev01

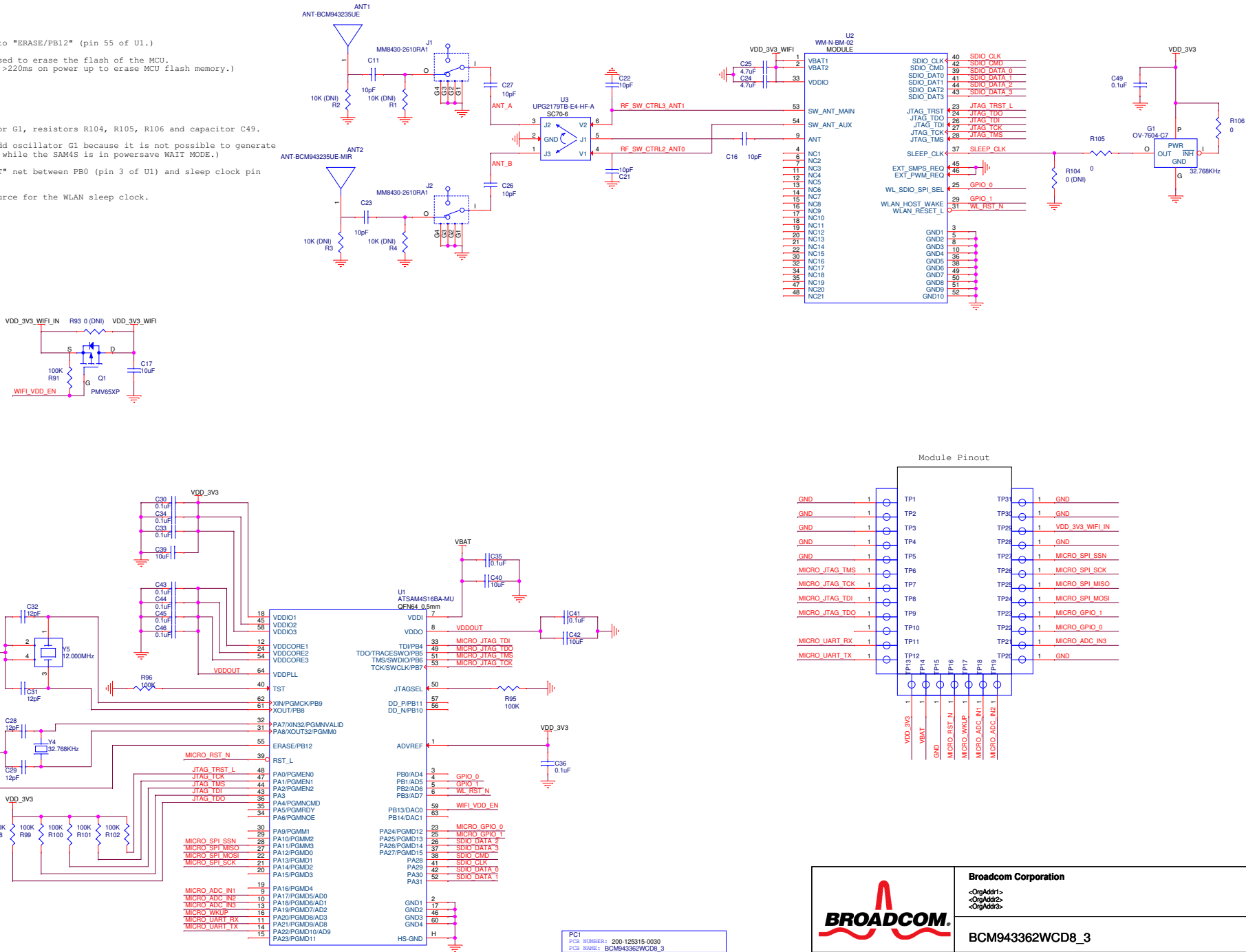
- 1) Initial release.


BCM943362WCD8\_2 Rev01

- 1) Added push button SW1 onto "ERASE/PB12" (pin 55 of U1).  
(Note: This push button is used to erase the flash of the MCU.  
Push and hold SW1 for >220ms on power up to erase MCU flash memory.)
- 2) Changed R97 to 100k.

BCM943362WCD8\_3 Rev01

- 1) Added 32.768kHz oscillator G1, resistors R104, R105, R106 and capacitor C49.  
(Note: It was necessary to add oscillator G1 because it is not possible to generate a PWM signal from PB0 while the SAM4S is in powersave WAIT MODE.)
- 2) Disconnected "32K\_PWM\_OUT" net between PB0 (pin 3 of U1) and sleep clock pin of WLAN chip.
- G1 is now used as the source for the WLAN sleep clock.



		Broadcom Corporation	
		BCM943362WCD8_3	
Size	C	DWG NO	824-125315-0030
Scale		Sheet	1 of 1
Wednesday, July 10, 2013		Rev	