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DOCUMENT

SCHEMATIC DIAGRAM

824-127858-0010

PRINTED CIRCUIT BOARD

200-127858-0010

Approvals		<div><div>Broadcom</div><div>Company Confidential</div></div>		
Designer	Date			
Reviewer	Date	SIZE	TITLE	
		B	BCM943909WCD1_1	
Reviewer	Date	Designer		Drawing Number
		VIJAY DESAI		824-127858-0010
Reviewer	Date			Version
				02
		DATE		19/06/2014:09:51
		SHEET		1 OF 9

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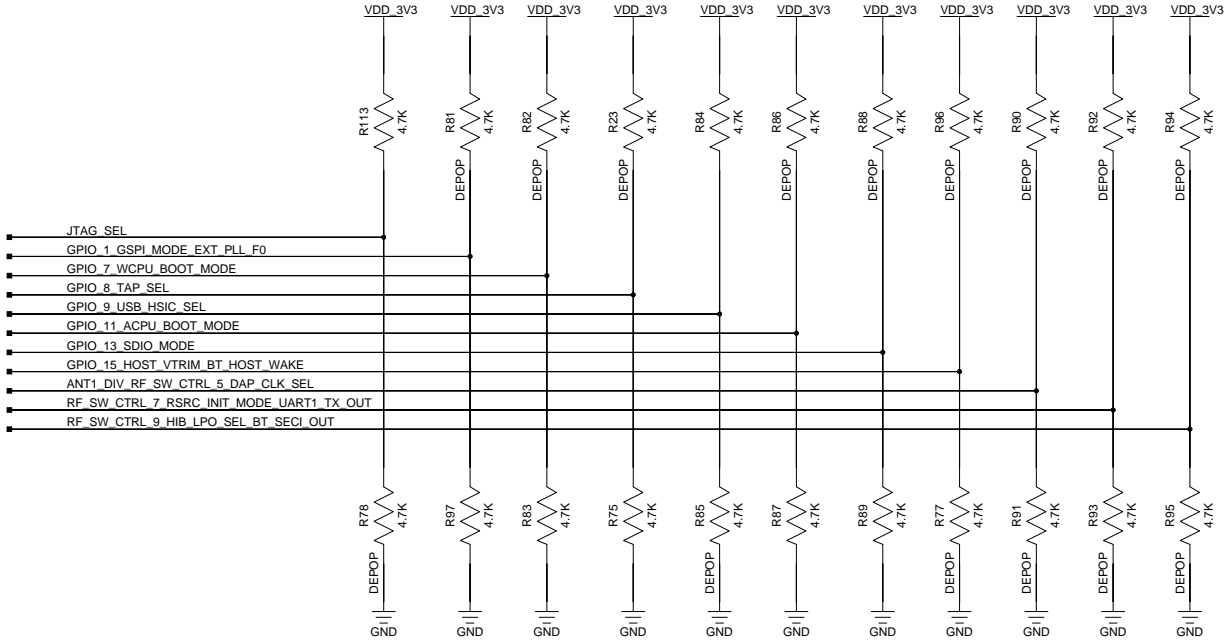
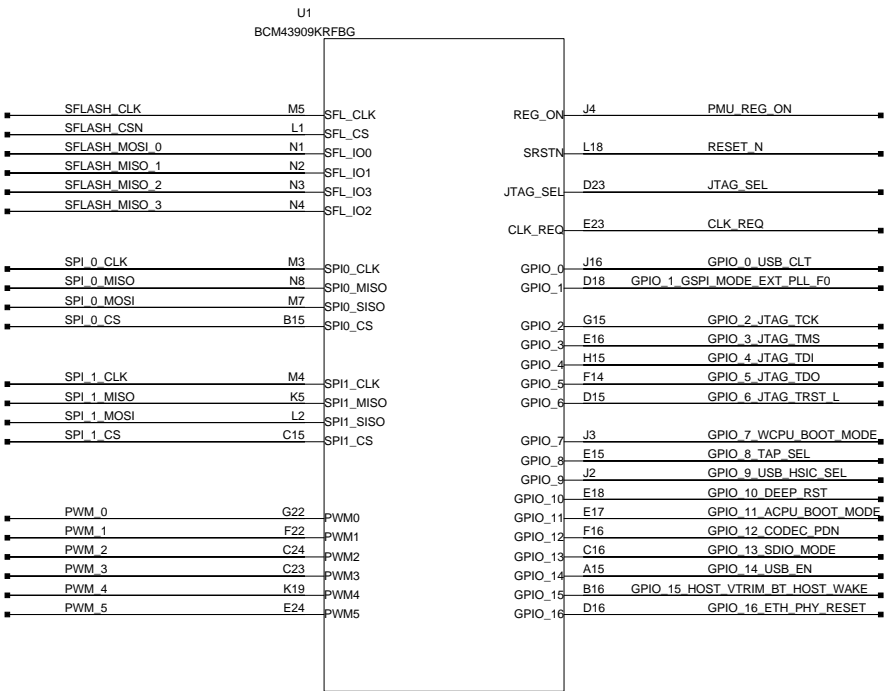
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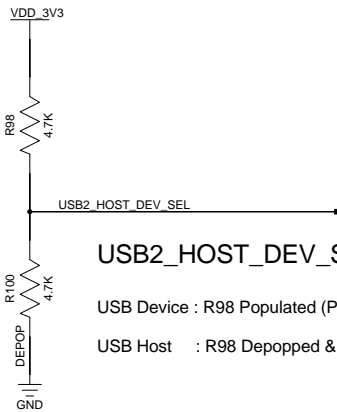
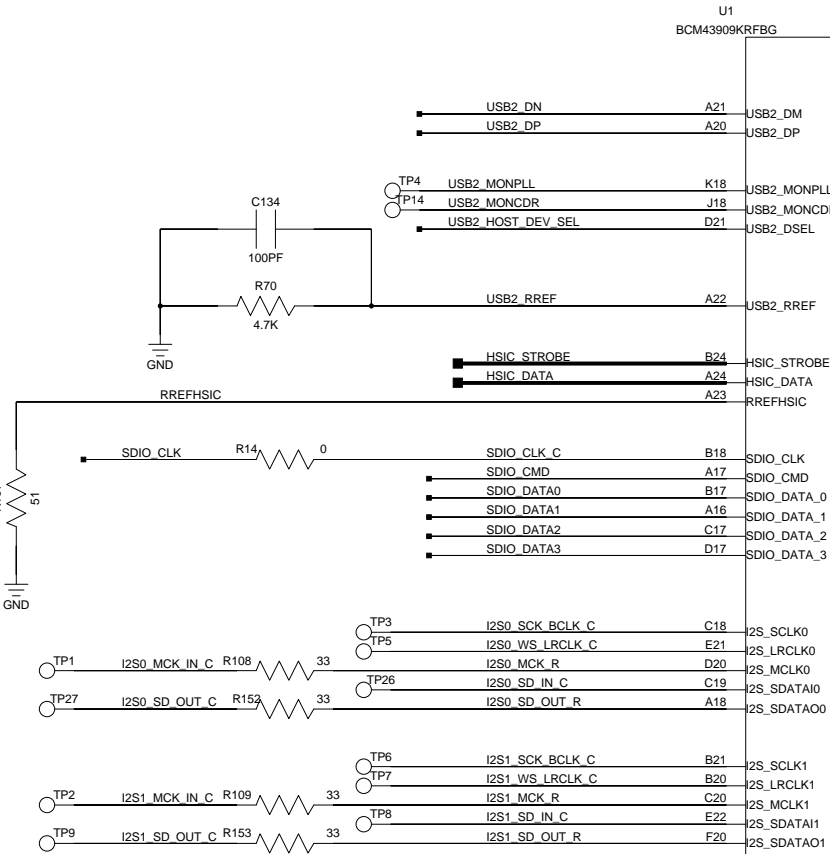
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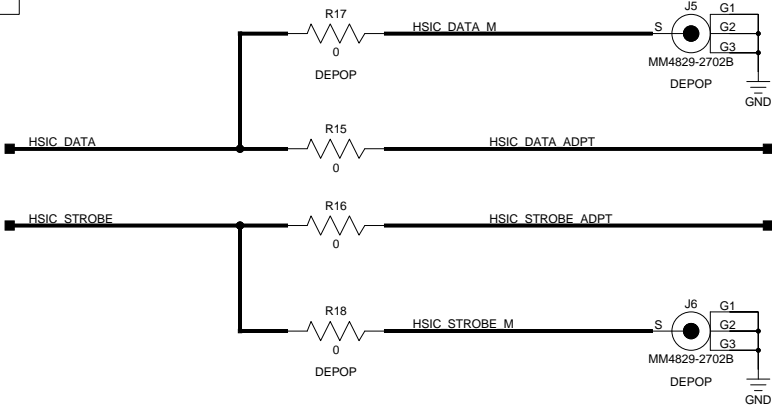
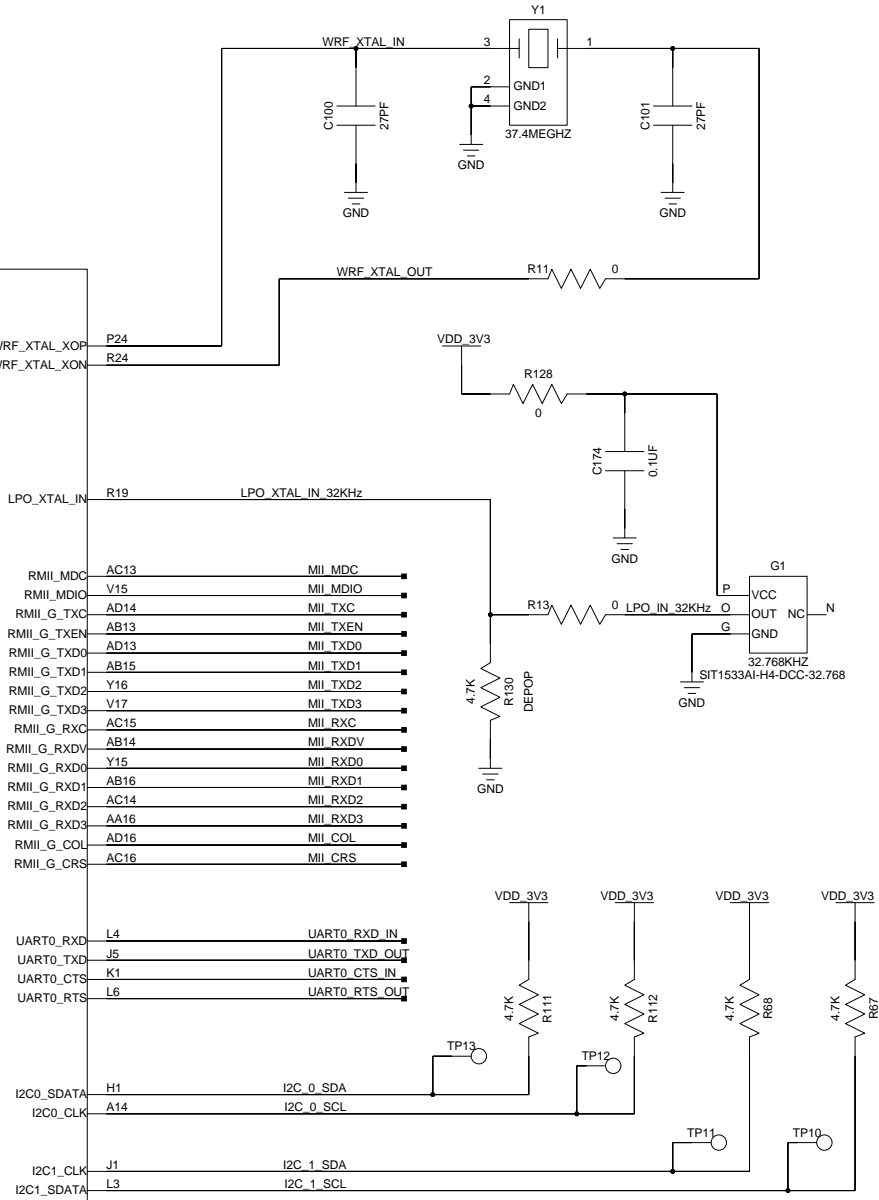
Pin	Strap Function	Strap Pull	
		Chip Default	Board Default
GPIO_1	gSPI Mode	0	1 R23= 4.7K to 3.3V
GPIO_7	WCPU Boot Mode	0 = TCROM Boot	1 = TCMSRAM Boot R96= 4.7K to 3.3V
GPIO_9	USB/HSIC Sel	0 = HSIC PHY	1 = USB PHY R82= 4.7K to 3.3V
GPIO_11	ACPU Boot Mode	0 = SOCROM Boot	1 = SOCSRAM Boot R84= 4.7K to 3.3V
GPIO_13	SDIO Mode	0 = SDIO Device	1 = SDIO Host R86= 4.7K to 3.3V
GPIO_15	Host Vtrim Enable	0 = Host Vtrim Disable	1 = Host Vtrim Enable R96= 4.7K to 3.3V
RF_SW_CTRL_5	Host DAP Clock Sel	0	1 R90= 4.7K to 3.3V
RF_SW_CTRL_7	Host RSRC Init	0	1 R92= 4.7K to 3.3V
RF_SW_CTRL_9	LPO Sel	0 = LPO from HIB	1 = Internal 32KHz LPO R94= 4.7K to 3.3V



### USB2\_HOST\_DEV\_SEL

USB Device : R98 Populated (Pulled High) & R100 Depop

USB Host : R98 Depopped & R100 Populated (Pulled Low)



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SIZE	TITLE	DATE	11/06/2014:13:47	SHEET	2	OF	9
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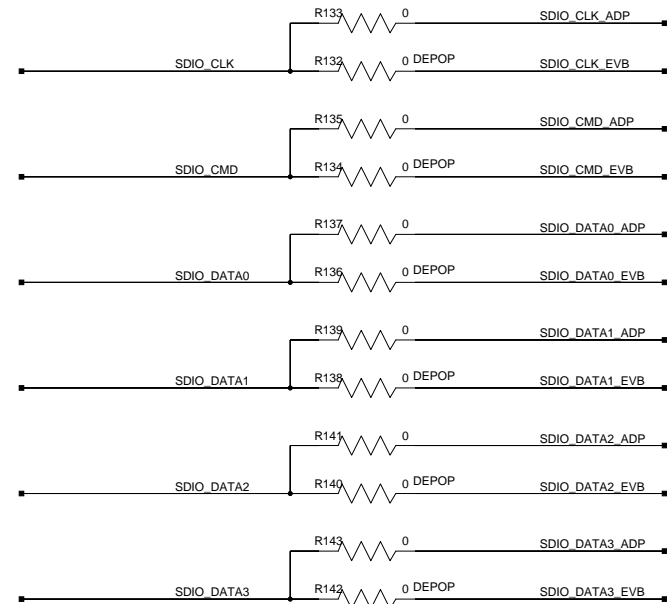
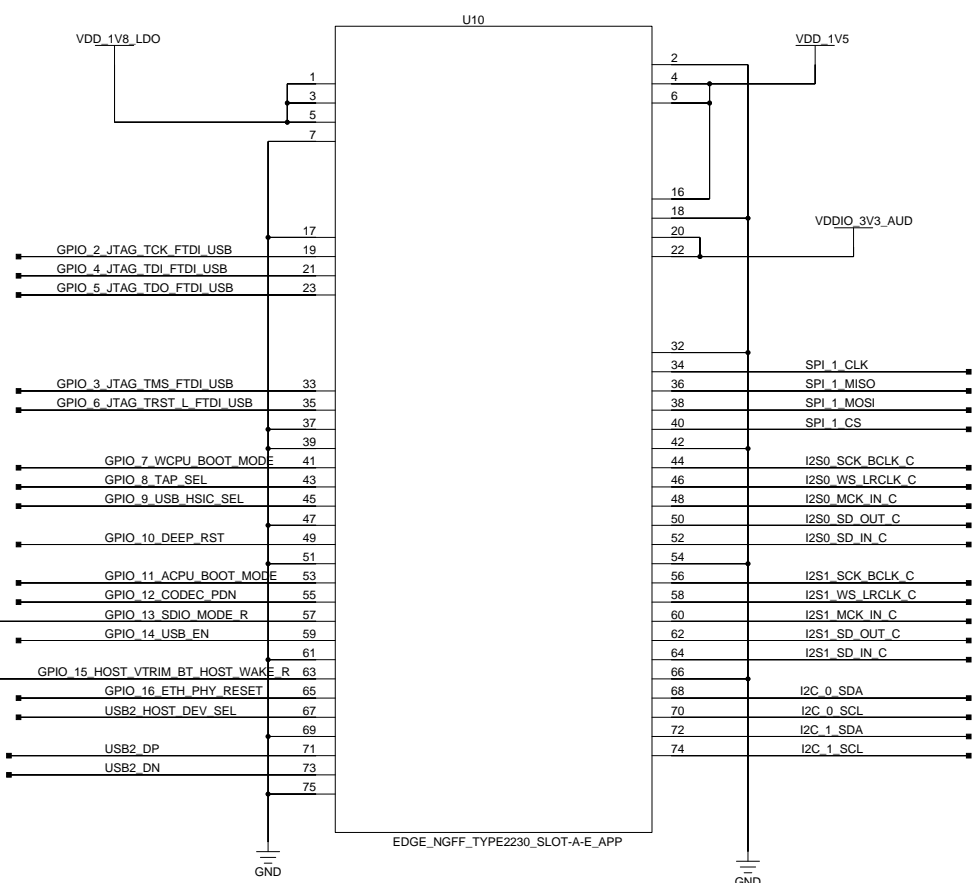
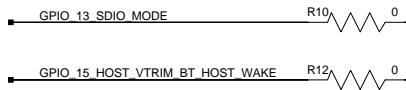
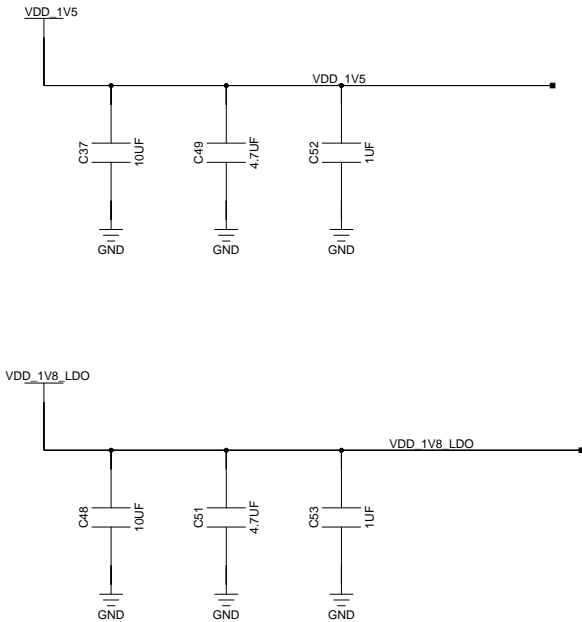
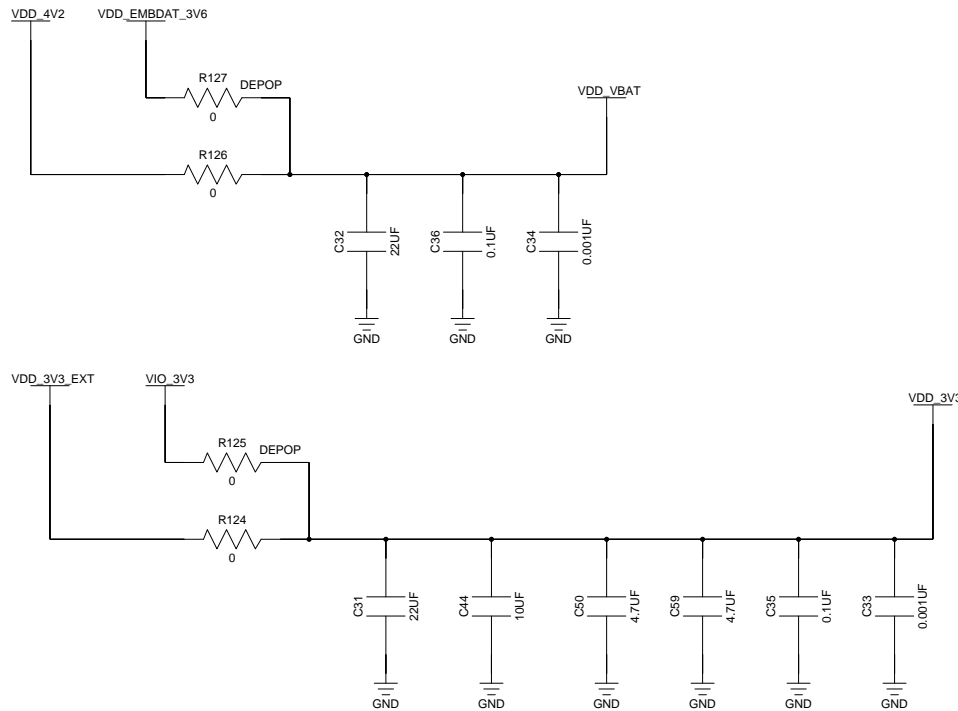
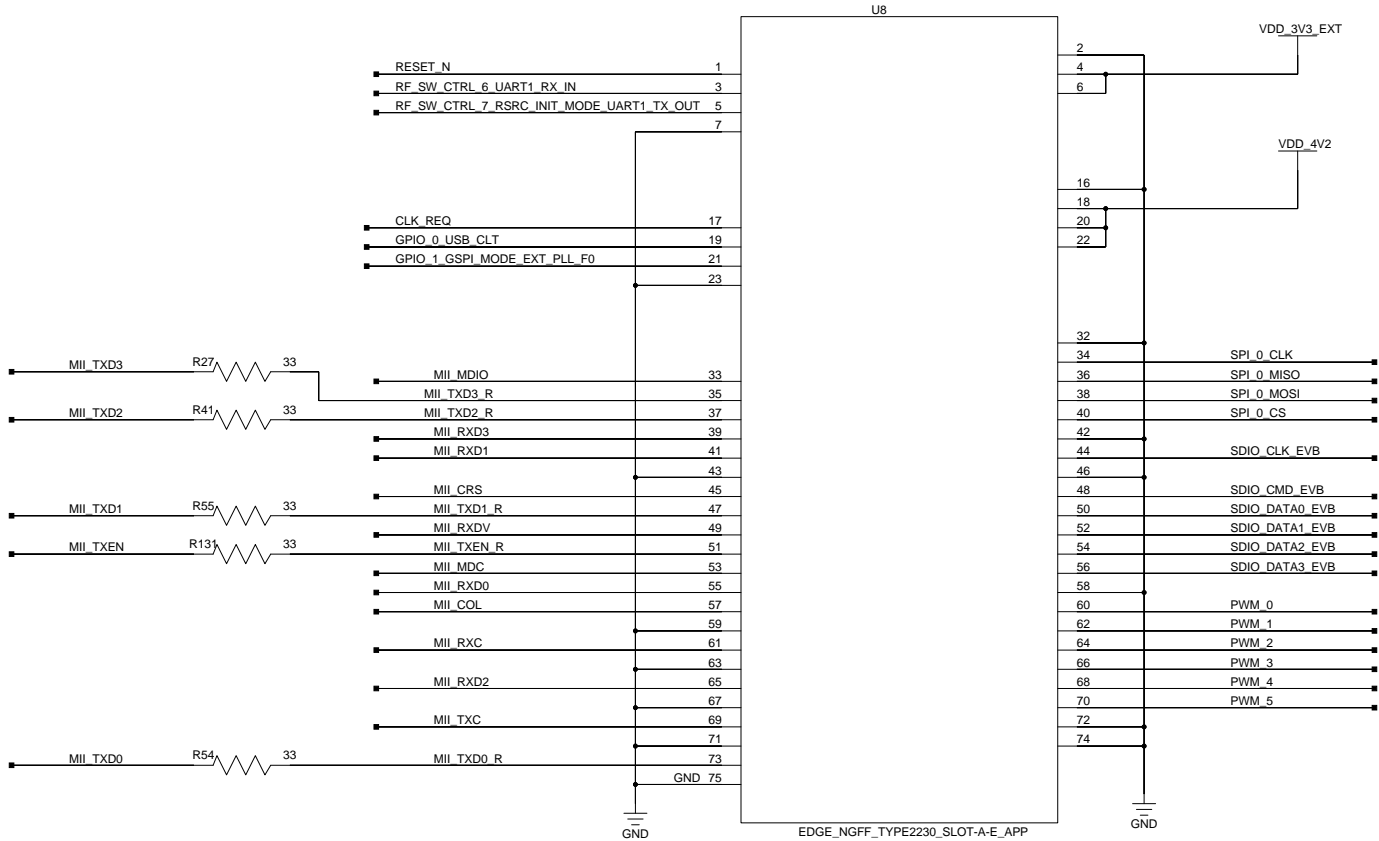
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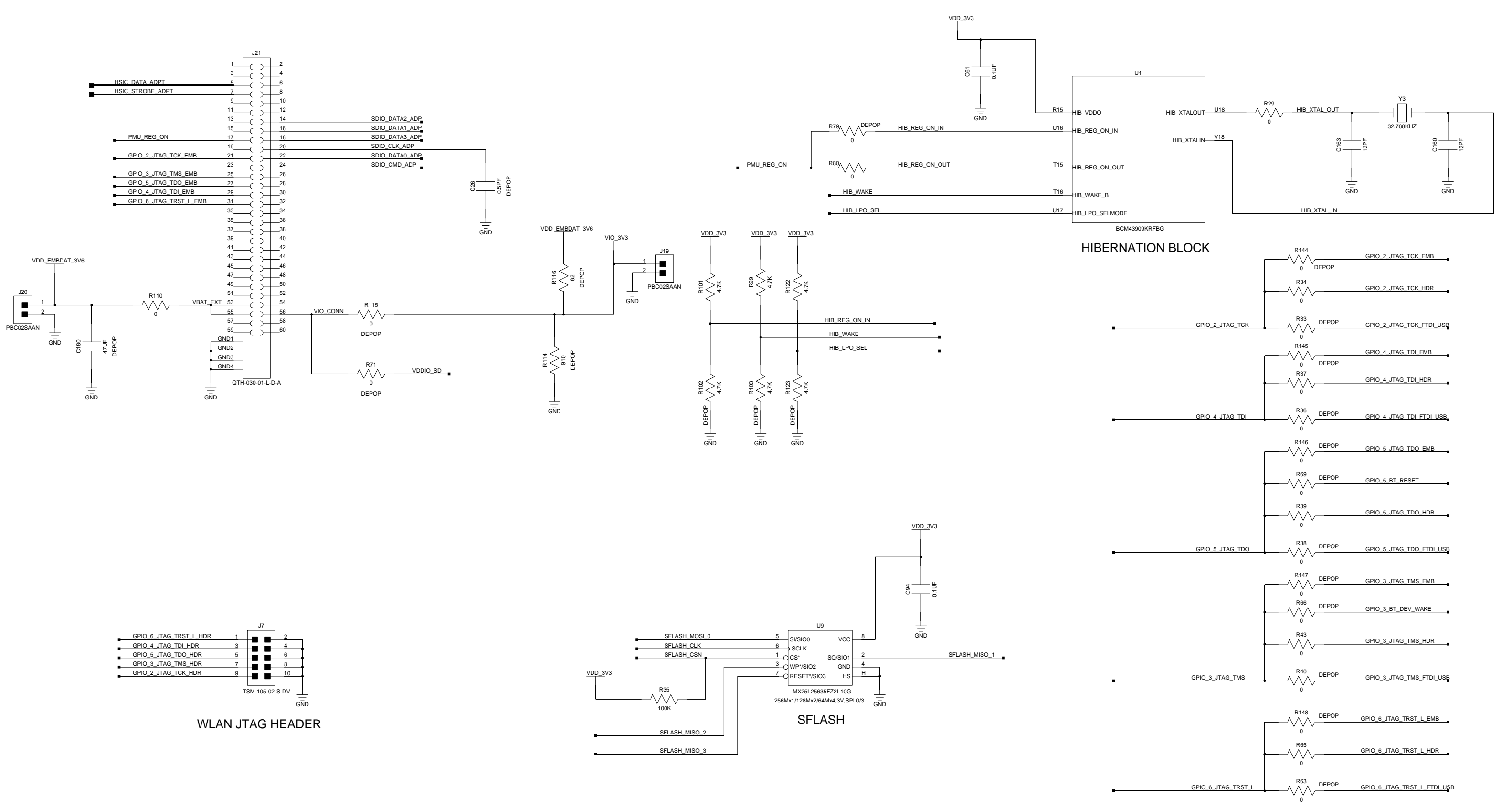
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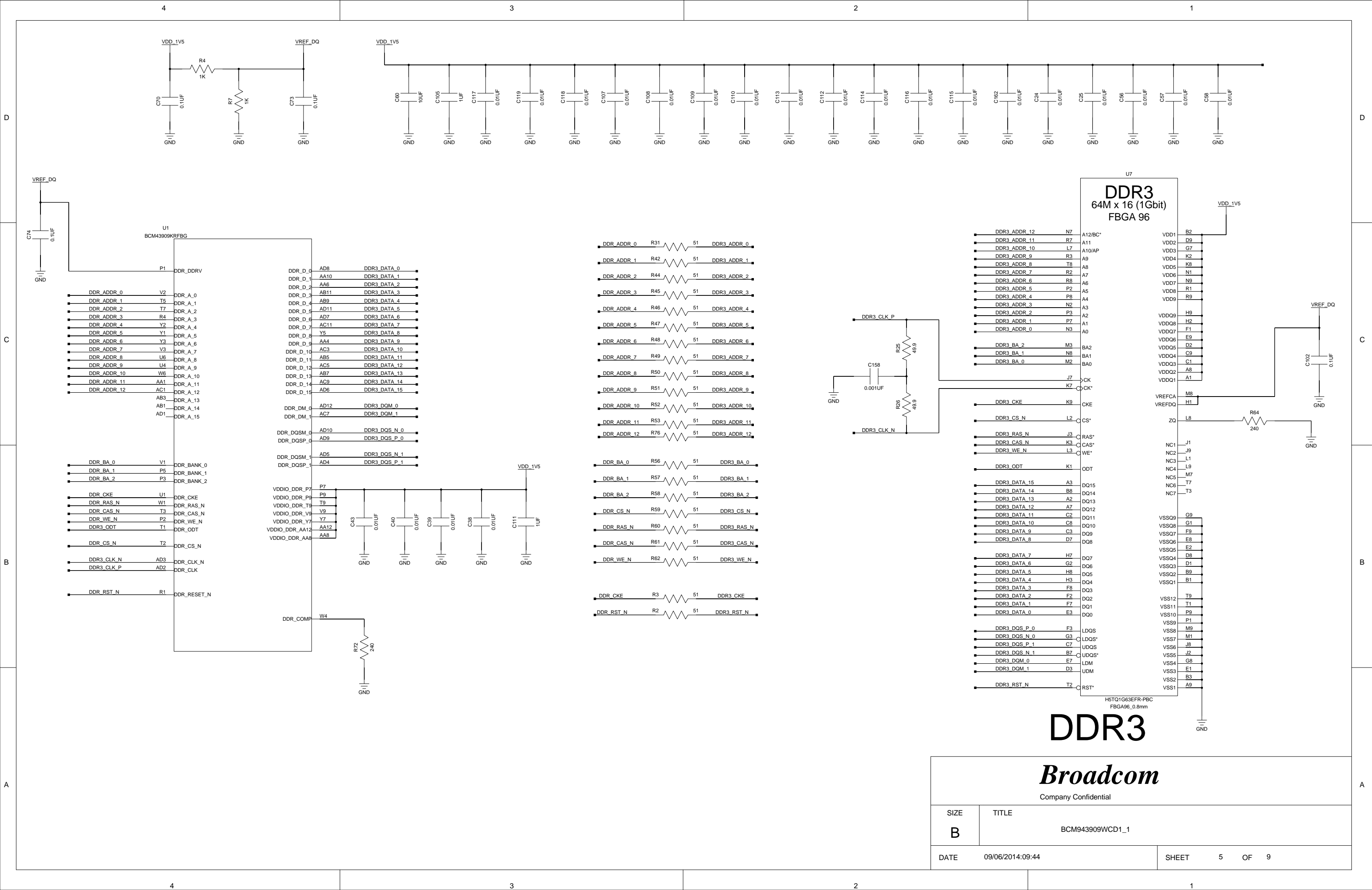
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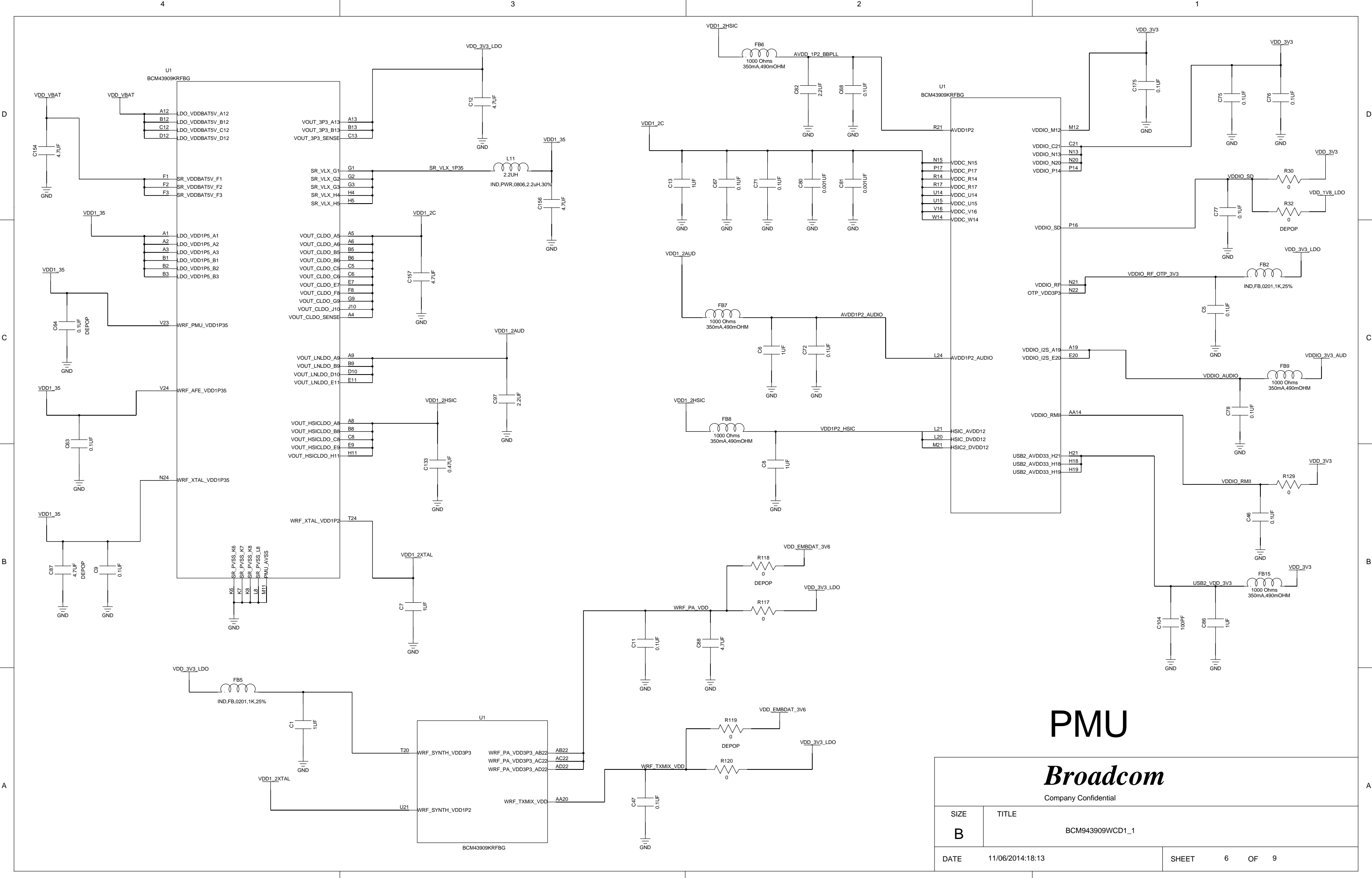


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SIZE	TITLE	DATE	SHEET	4	OF	9
B	BCM943909WCD1_1	06/06/2014:09:44				





PMU

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SIZE	TITLE		
B	BCM943909WCD1_1		
DATE	11/06/2014:18:13	SHEET	6 OF 9

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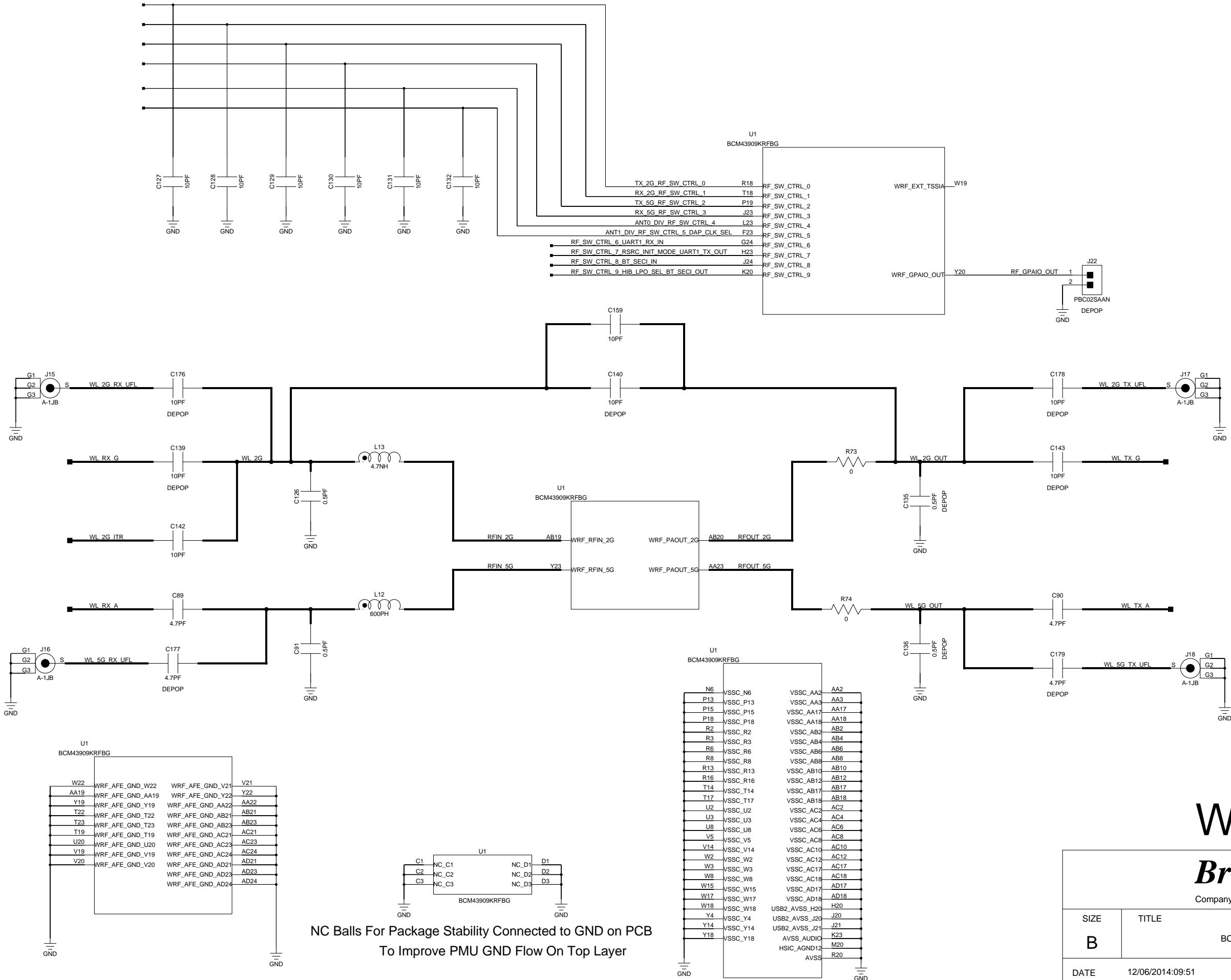
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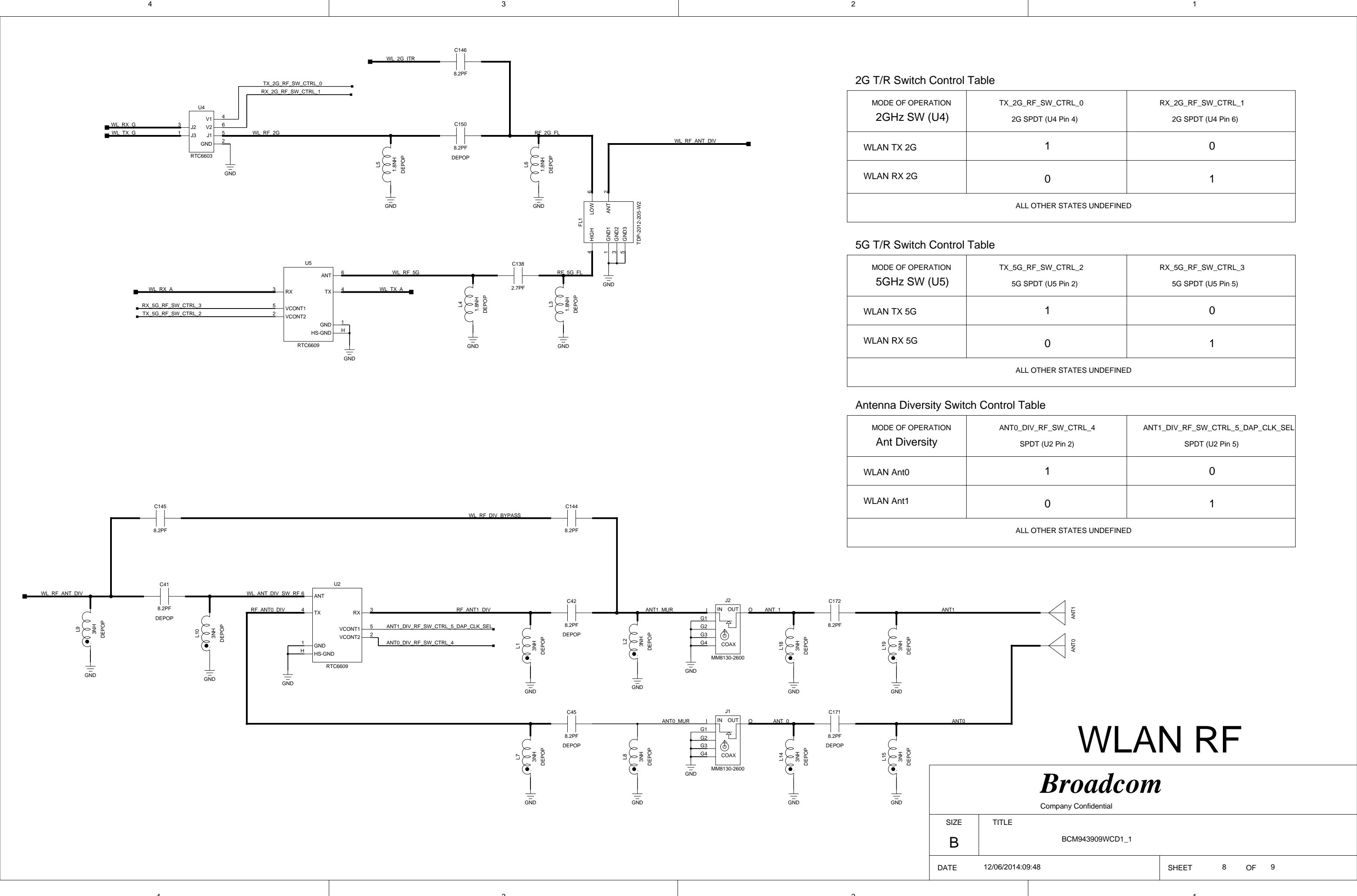
NC Balls For Package Stability Connected to GND on PCB  
To Improve PMU GND Flow On Top Layer

# WLAN RF

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SIZE	TITLE	DATE	12/06/2014:09:51	SHEET	7	OF	9
B	BCM943909WCD1_1						



2G T/R Switch Control Table

MODE OF OPERATION 2GHz SW (U4)	TX_2G_RF_SW_CTRL_0 2G SPDT (U4 Pin 4)	RX_2G_RF_SW_CTRL_1 2G SPDT (U4 Pin 6)
WLAN TX 2G	1	0
WLAN RX 2G	0	1
ALL OTHER STATES UNDEFINED		

5G T/R Switch Control Table

MODE OF OPERATION 5GHz SW (U5)	TX_5G_RF_SW_CTRL_2 5G SPDT (U5 Pin 2)	RX_5G_RF_SW_CTRL_3 5G SPDT (U5 Pin 5)
WLAN TX 5G	1	0
WLAN RX 5G	0	1
ALL OTHER STATES UNDEFINED		

Antenna Diversity Switch Control Table

MODE OF OPERATION Ant Diversity	ANT0_DIV_RF_SW_CTRL_4 SPDT (U2 Pin 2)	ANT1_DIV_RF_SW_CTRL_5_DAP_CLK_SEL SPDT (U2 Pin 5)
WLAN Ant0	1	0
WLAN Ant1	0	1
ALL OTHER STATES UNDEFINED		

WLAN RF

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SIZE	TITLE
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DATE	12/06/2014:09:48
SHEET	8 OF 9



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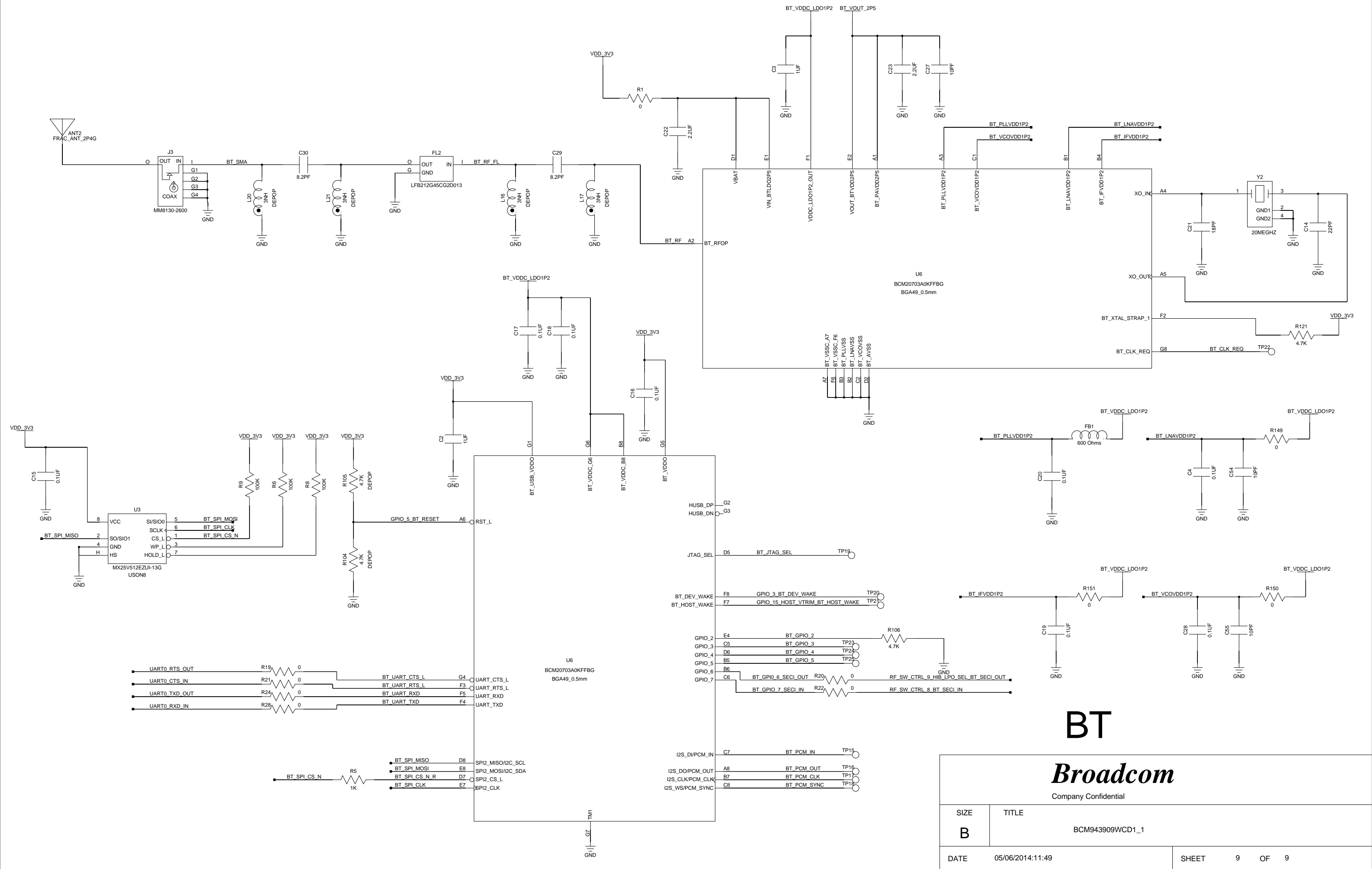
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SIZE	TITLE	DATE	SHEET	9	OF	9
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