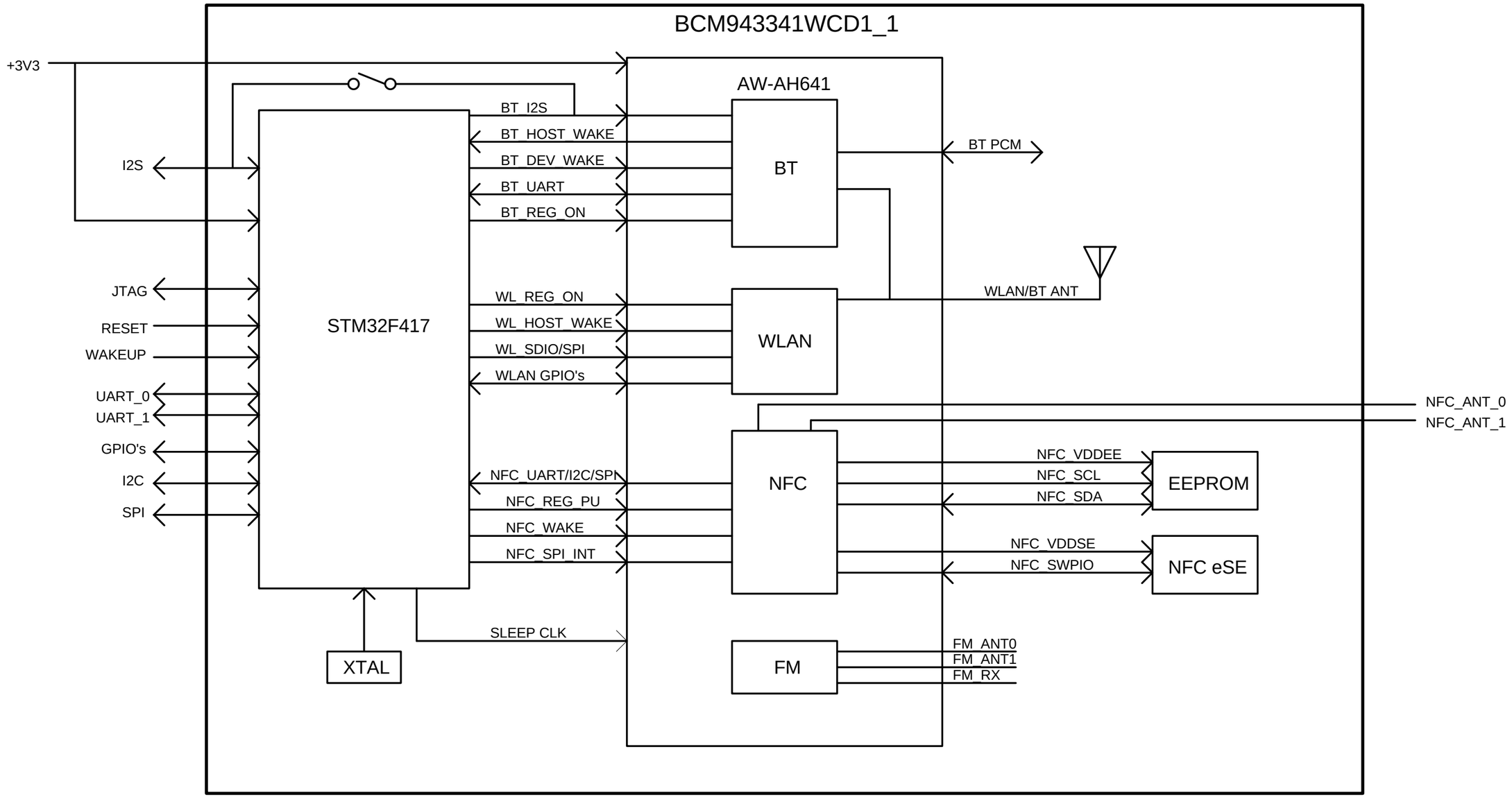
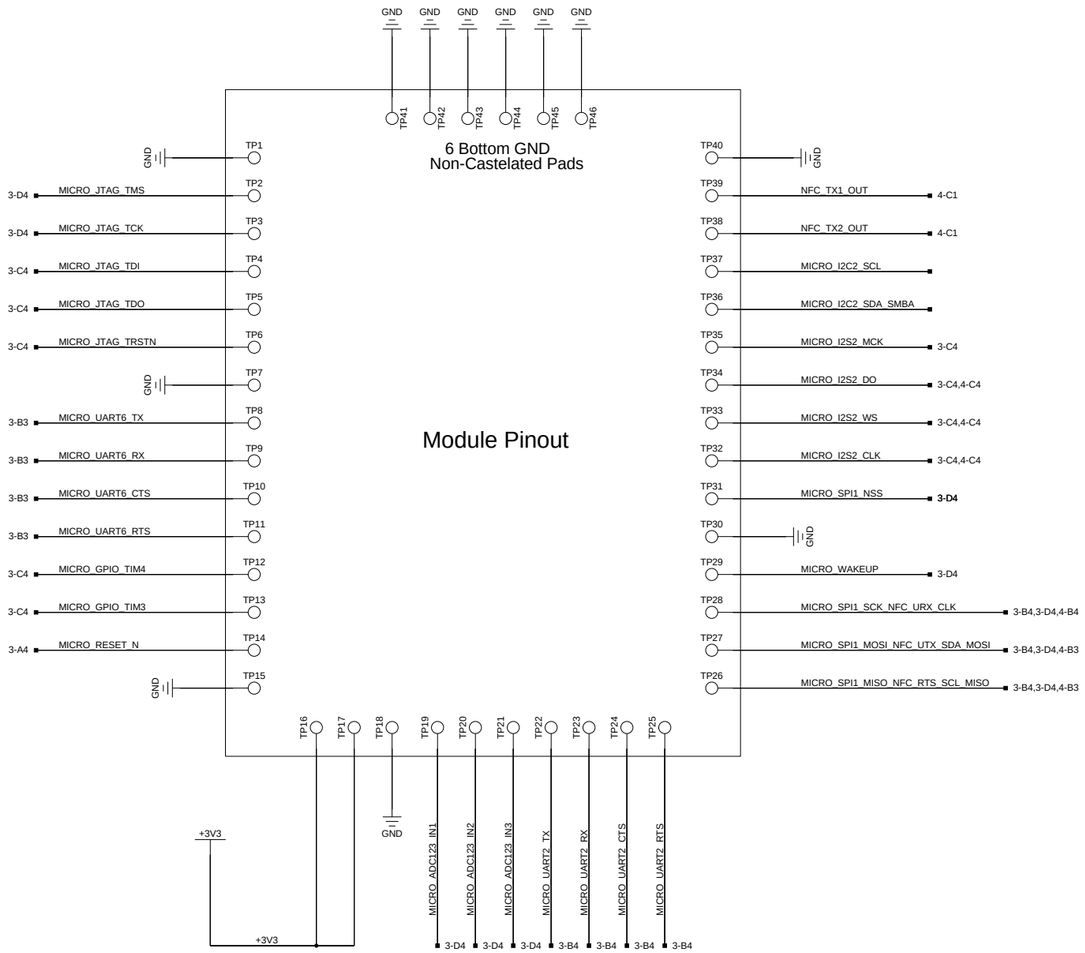


PAGE 1 - MODULE BLOCK DIAGRAM
PAGE 2 - MODULE TOP-LEVEL PINOUT
PAGE 3 - MICROCONTROLLER INTERFACE
PAGE 4 - 43341 SiP INTERFACE

Approvals		Broadcom		
Designer	Date	Company Confidential		
Reviewer	Date	SIZE: B	TITLE: BCM943341WCD1_2	
Reviewer	Date	Designer: ALEXIS ESPIRITU	Drawing Number: 824-126357-0020	Version: 02
Reviewer	Date	DATE: 30/01/2014:15:42	SHEET: 1 OF 4	

Parent Block is
Block bcm943341wcd1 Page 1 of 4



WLAN strapping options:

GPIO6_SDIO_SPI_SELECT	SDIO_DATA2_SPI_NC	MODE
0	X	SDIO
1	0	gSPI

SDIO/gSPI pin mapping:

	STM32F417 ball	STM32F417 port		AW-AH641 ball	Function
SDIO	A12	PC12	<--->	C2	SDIO_CLK
	D12	PD2	<--->	D2	SDIO_CMD
	G14	PC8	<--->	E2	SDIO_DATA_0
	F14	PC9	<--->	E3	SDIO_DATA_1
	B14	PC10	<--->	F3	SDIO_DATA_2
	B13	PC11	<--->	G3	SDIO_DATA_3
gSPI	P13	PB13	<--->	C2	SPI_CLK
	R15	PB15	<--->	D2	SPI_MOSI
	R14	PB14	<--->	E2	SPI_MISO
	B13	PC11	<--->	G3	SPI_CS
	F14	PC9	<--->	E3	SPI_IRQ

Note:
Only one set of communication lines are required to be connected between the microcontroller and wlan device (either SDIO or SPI).
This reference module connects up both for evaluation purposes only.

NFC strapping options:

NFC_SPI_INT	MODE
Pull Low	UART
Pull High	I2C
Floating	SPI

Note:
The host interface to NFC is dependant on the state of 'NFC_SPI_INT' at power-up boot as outlined in the table to the left.

NFC pin mapping:

	STM32F417 ball	STM32F417 port		AW-AH641 ball	Function
UART	N13	PD12	<--->	C5	UART_CTS
	N14	PD11	<--->	C6	UART_RTS
	P15	PD8	<--->	E4	UART_RX
	P14	PD9	<--->	E5	UART_TX
I2C	P6	PF12	<--->	C5	I2C_REQ
	A5	PB8	<--->	C6	I2C_SCL
	B5	PB7	<--->	E5	I2C_SDA
SPI	P6	PF12	<--->	C5	SPI_CS
	P3	PA6	<--->	C6	SPI_MISO
	P4	PA5	<--->	E4	SPI_CLK
	R3	PA7	<--->	E5	SPI_MOSI

DOC?
DOCUMENT
SCHEMATIC DIAGRAM
824-126357-0020

PCB?
PRINTED CIRCUIT BOARD
200-126357-0020

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SIZE	TITLE
B	BCM943341WCD1_2

DATE	17/01/2014:14:02	SHEET	2 OF 4

Parent Block is
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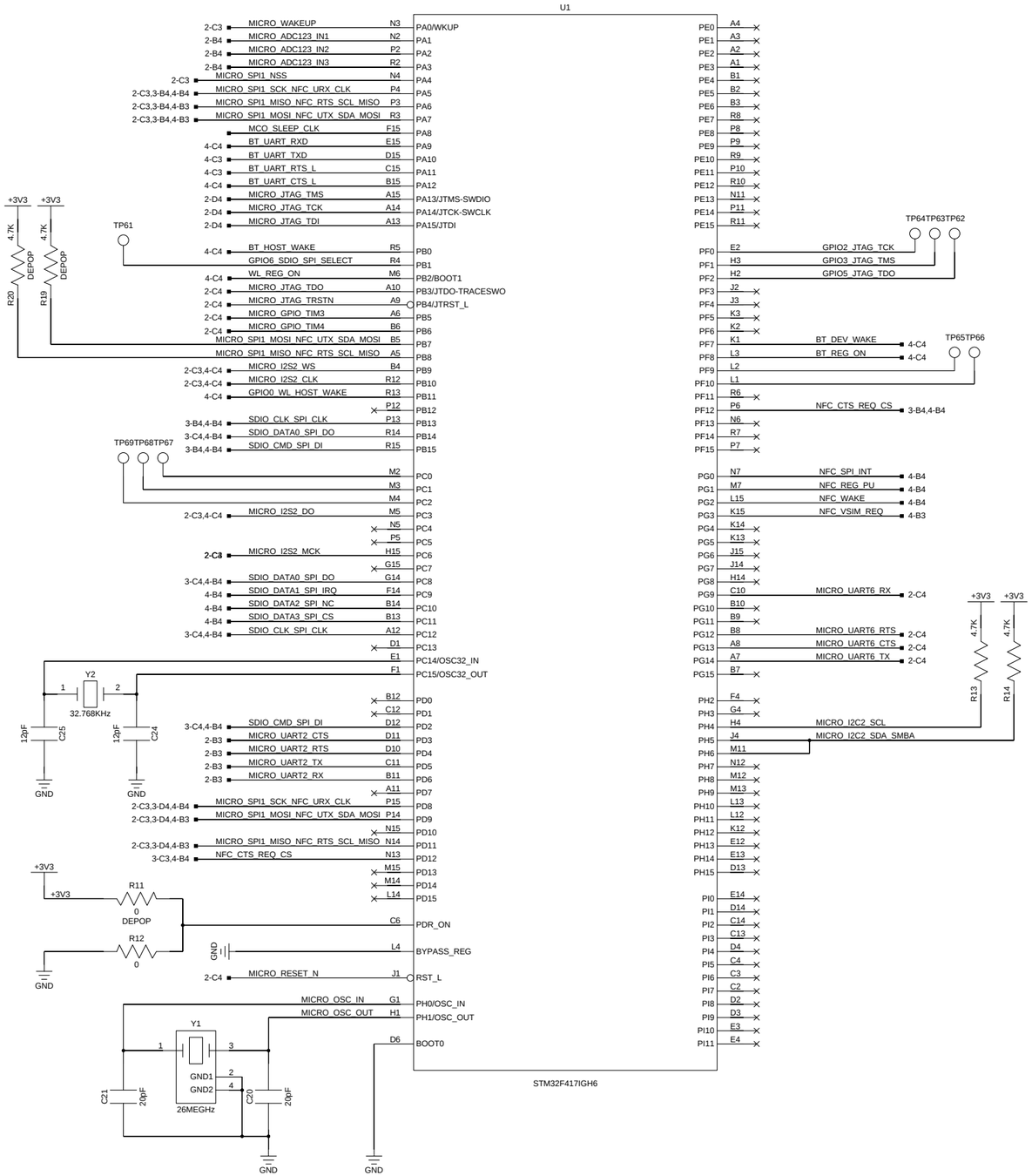

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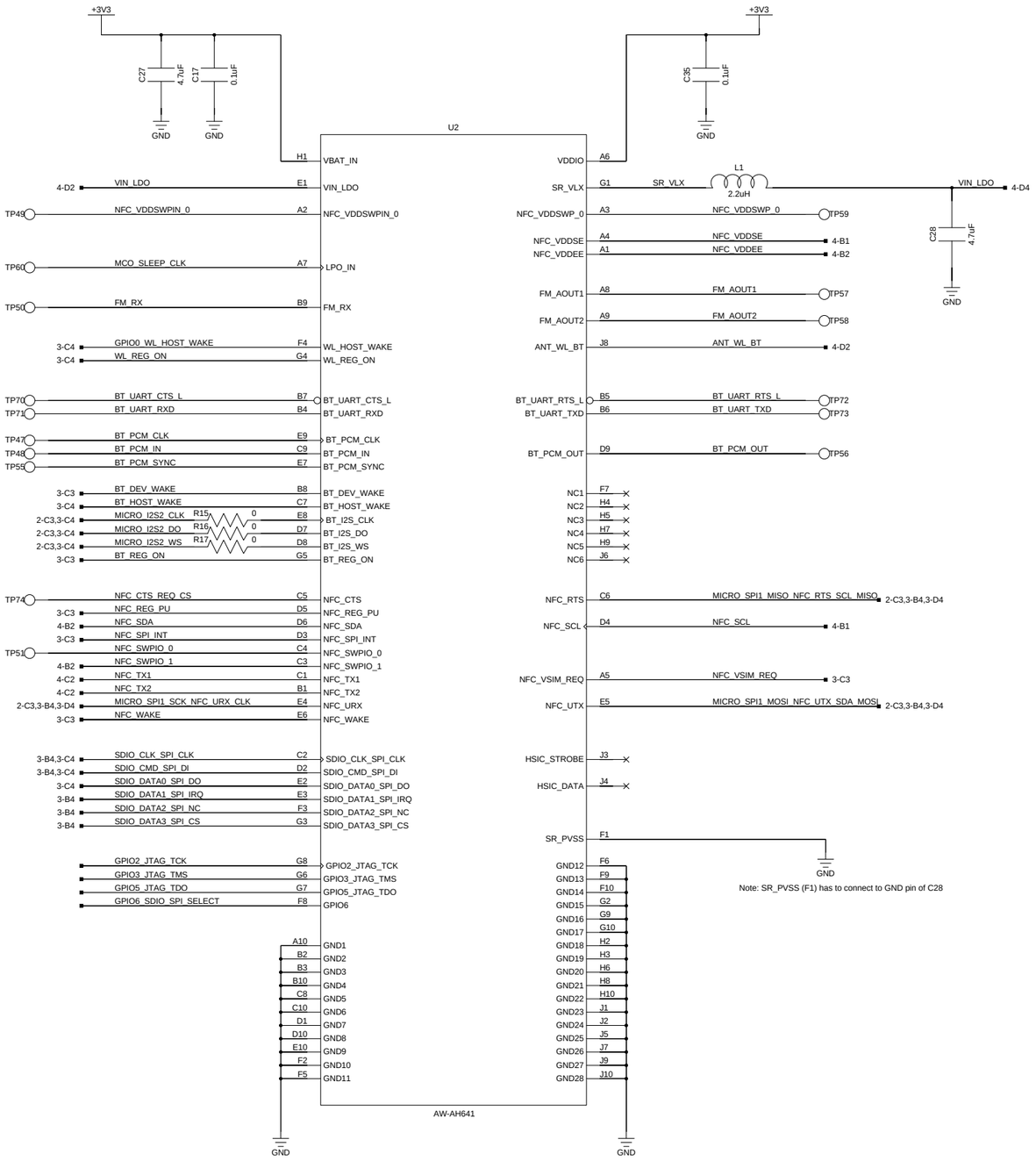
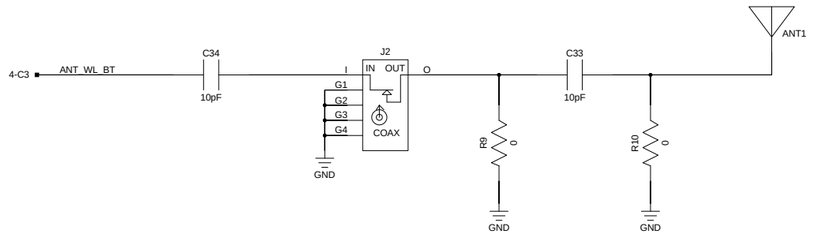
SIZE	TITLE
B	BCM943341WCD1_2

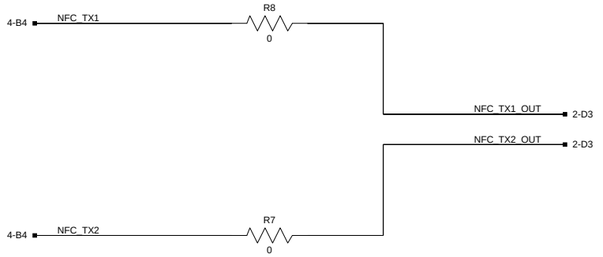
DATE	17/01/2014:14:02	SHEET	3 OF 4

Parent Block is
Block bcm943341wcd1 Page 3 of 4

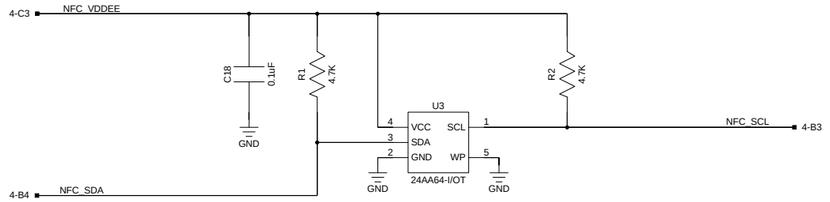




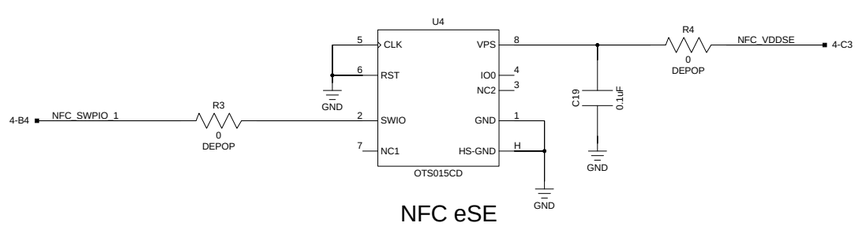
WLAN/BT antenna network



NFC antenna pins to module TP's
(matching network on eval board)



NFC EEPROM



NFC eSE

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SIZE	TITLE
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DATE	23/01/2014:12:32	SHEET	4 OF 4

Parent Block is
Block bcm943341wcd1 Page 4 of 4