Task 2 Team Apple

James Tate

Andrew Hegman

Wyatt Gamble

JD Ford

## Schematic Errata

Pin 33 on the dsPIC33EP512GP806 is mislabeled as TMS/AN10/CVREF/PMA13/RPI43/RB10. It should be TMS/AN10/CVREF/PMA13/RPI42/RB10.

C8 and C9 in the oscillator circuitry lack values.

The circuitry for LED3 reverses the logic from LED1 and LED2.

RB11 is not mappable, but it is used as the UART MCUTX pin. UART functionality will not work until a mappable pin is connected. RF0 was used in our case.