

James Tate

Andrew Hegman

Wyatt Gamble

JD Ford

Schematic Errata

Pin 23 on the dsPIC33EP512GP806 is mislabeled as TMS/AN10/CVREF/PMA13/**RPI43**/RB10. It should be TMS/AN10/CVREF/PMA13/**RPI42**/RB10.

C8 and C9 in the oscillator circuitry lack values.

The circuitry for LED3 reverses the logic from LED1 and LED2.

RB11 is not mappable, but it is used as the UART MCUTX pin. UART functionality will not work until a mappable pin is connected. RF0 was used in our case.

James had to remap RB10 to RF1 due to RB10 pin failure/malfunction. This was an isolated incident on James' board.

R6 and R7 are not connected. R6 has a floating leg.

Andrew's board has DACA and 3v3 shorted together. To remedy the problem, Team Apple's code is written to output on DACB, despite the problem being isolated to Andrew's board only.

CS pin on the DAC (pin 3) was not connected, so Team Apple decided to *blue wire* CS to RD0.

SCL and SDA were not broken out on the F14 board. Team Apple blue wired the SCL and SDA MCU pins to the SCL and SDA headers to make use of the pins.

The i2c library code was setup for an alternative i2c bus. This had to be modified to use our i2c bus.