System components: Main module
2x Flip-Flop counter < first for 0-9
                        second for 0-90
Seven_Segment
Clk_1_Sec

main counter

CLK_50 ●————————————————

Clock_N_Sec
clk | Clock 1 Sec | out

rst ●———— rst

counter
clk
en
rst
counts
from 0-9
For Seconds
out[3:0] 4   in[3:0]
carry_out

Seven_segment
Operates the
Seconds
Seven Segment
out[6:0] 7 ——→ Hex0 [6:0]

counter
en
clk
rst
counts
from 0-9
for tens
out[3:0] 4   in[3:0]
carry_out

Seven_segment
Operates the
tens.
Seven Segment
out[6:0] 7 ——→ Hex1 [6:0]