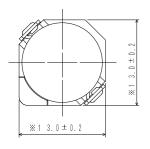
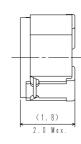
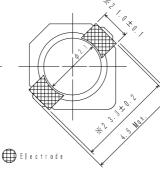


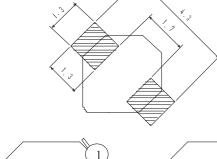
Dimension - [mm]

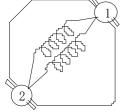




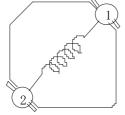


Land pattern and Schematics - [mm]









(4.7 μ H \sim 15 μ H)

Description

- Ferrite drum core construction.
- · Magnetically shielded.
- L × W × H:3.2 × 3.2 × 2.0 mm Max.
- Product weight: 65mg(Ref.)
- Moisture Sensitivity Level: 1
- RoHS compliance.
- Halogen Free available.

Environmental Data

- Operating temperature range: -40°C~+105°C (including coil's self temperature rise)
- Storage temperature range: -40°C~+105°C
- Solder reflow temperature: 260 °C peak.

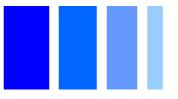
Packaging

- · Carrier tape and reel packaging
- 7.0"diameter reel
- 1000pcs per reel

Applications

• Ideally used in Mobilephone,PDA,MP3, DSC/DVC, etc. as DC-DC converter inductors.

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Electrical Characteristics

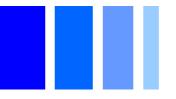
Part Name	Stamp	Inductance (μ H) [within] ※1	D.C.R. (mΩ) Max. (Typ.) (at 20℃)	Saturation Current (A) %2		Temperature Rise Current
				at 20℃	at100℃	(A) % 3
CDRH2D18/HPNP-R20NC	N	0.20±35%	22(17)	5.35	3.55	4.70
CDRH2D18/HPNP-R36NC	Р	0.36±35%	29(22)	4.62	3.00	4.10
CDRH2D18/HPNP-R56NC	Q	0.56±35%	33(25)	3.75	2.76	3.60
CDRH2D18/HPNP-R82NC	R	0.82±35%	39(30)	2.91	2.20	3.30
CDRH2D18/HPNP-1R1NC	S	1.10±35%	43(33)	2.50	1.90	2.90
CDRH2D18/HPNP-1R7NC	Α	1.70±30%	44(35)	1.85	1.36	2.20
CDRH2D18/HPNP-2R2NC	С	2.20±30%	60(48)	1.60	1.15	1.90
CDRH2D18/HPNP-3R3NC	E	3.30±30%	86(69)	1.45	1.10	1.55
CDRH2D18/HPNP-4R7NC	G	4.70±30%	140(110)	1.20	0.90	1.20
CDRH2D18/HPNP-6R3NC	I	6.30±30%	160(128)	1.05	0.78	1.15
CDRH2D18/HPNP-100NC	K	10.0±30%	245(195)	0.85	0.65	0.90
CDRH2D18/HPNP-150NC	М	15.0±30%	345(275)	0.70	0.53	0.64

[%]1. Inductance measuring condition: 0.20 μ H \sim 1.10 μ H at 7.96MHz; 1.70 μ H \sim 15.0 μ H at 100kHz

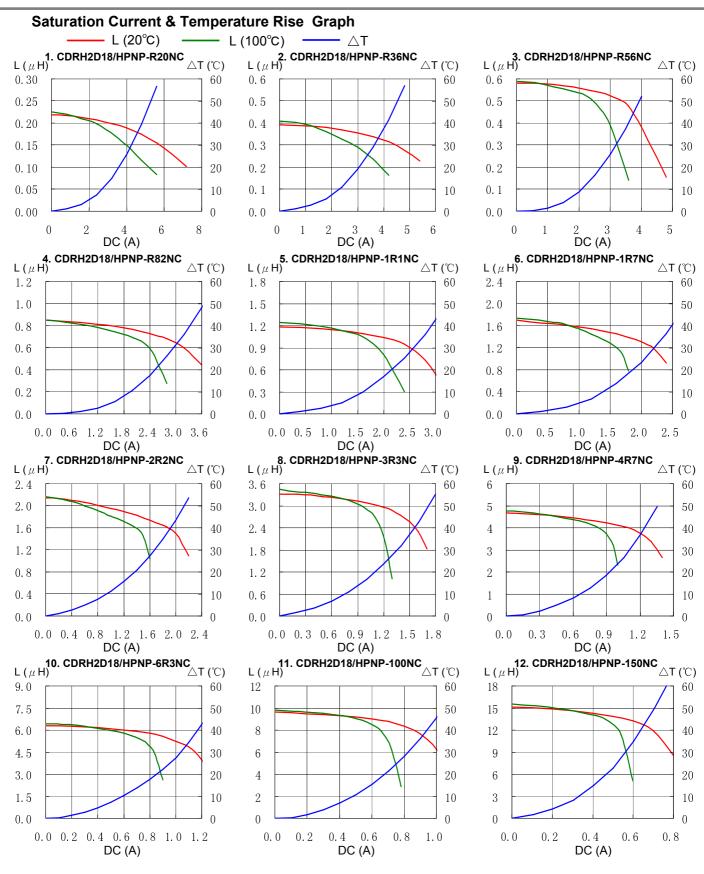
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 $[\]frak{\%}2$. Saturation current: The value of D.C. current when the inductance decreases to 65% of it's nominal value.

 $^{\ \%}$ 3. Temperature rise current: The value of D.C. current when the temperature rise is $\triangle t = 40 \, ^{\circ}\text{C} \, (Ta = 20 \, ^{\circ}\text{C})$.







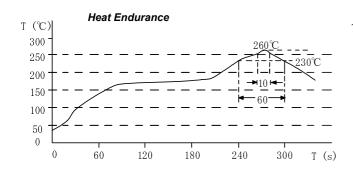
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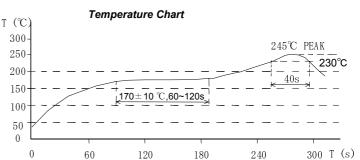
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Solder Reflow Condition





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