

# INPUT

# OUTPUT

The diagram shows the output stage of the MCP2561 CAN transceiver. The MCP2561 (U3) is connected to a 5V supply via a 100nF capacitor (C1). Its TXD pin is connected to TXCAN, RXD to RXCAN, and STDBY to GND. The VDD pin is connected to a 5V supply via a 300 ohm resistor (R3). The CANH pin is connected to a 60 ohm resistor (R4) and the CANL pin to a 60 ohm resistor (R5). Both resistors are connected to a common point that is also connected to a 4.7nF capacitor (C8) to GND. The common point is connected to the primary of a transformer (X1). The secondary of the transformer is connected to a 5V supply via a 4.7nF capacitor (C8) to GND.

[illegible]

# SPI-CAN

The diagram illustrates the SPI-CAN interface circuit. The central component is the MCP2515 CAN transceiver (U2). It is connected to a 5V supply and ground (GND) through a 100nF capacitor (C3) and a 10k resistor (R2) to the RESET pin. The RXCAN and TXCAN pins are connected to the RXCAN and TXCAN pins of the MCP2515. The OSC1 and OSC2 pins are connected to the OSC1 and OSC2 pins of the MCP2515. The CLKOUT pin is connected to the CLKOUT pin of the MCP2515. The CS, SCK, SI, and SO pins are connected to the CS, SCK, MOSI, and MISO pins of the MCP2515. The INT pin is connected to the INT pin of the MCP2515. The RX0BF, RX1BF, TX0RTS, TX1RTS, and TX2RTS pins are connected to the RX0BF, RX1BF, TX0RTS, TX1RTS, and TX2RTS pins of the MCP2515. The VDD and VSS pins are connected to the VDD and VSS pins of the MCP2515. The MCP2515 is also connected to a 5V supply and ground (GND) through a 100nF capacitor (C3) and a 10k resistor (R2) to the RESET pin. The MCP2515 is also connected to a 5V supply and ground (GND) through a 100nF capacitor (C3) and a 10k resistor (R2) to the RESET pin. The MCP2515 is also connected to a 5V supply and ground (GND) through a 100nF capacitor (C3) and a 10k resistor (R2) to the RESET pin.