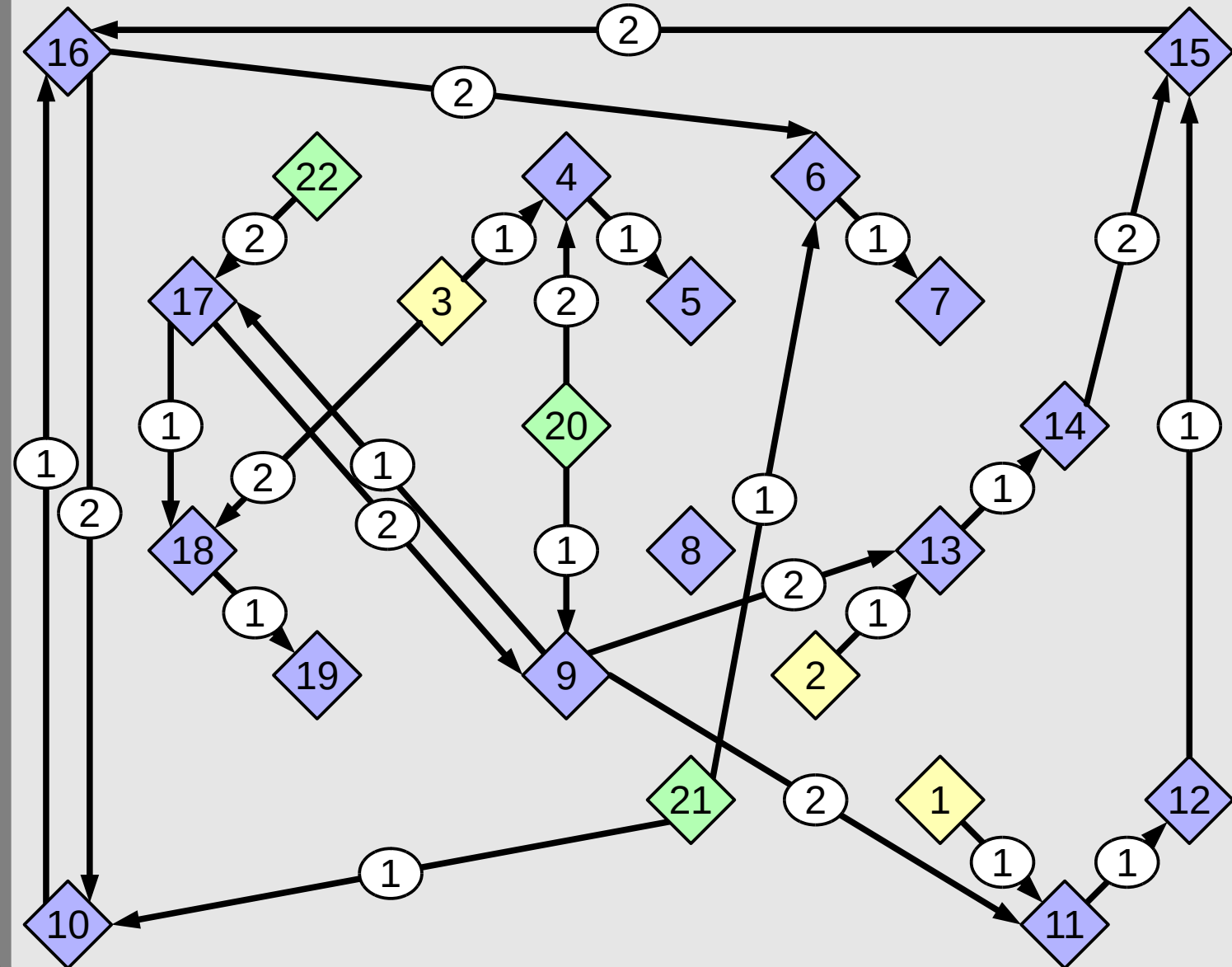
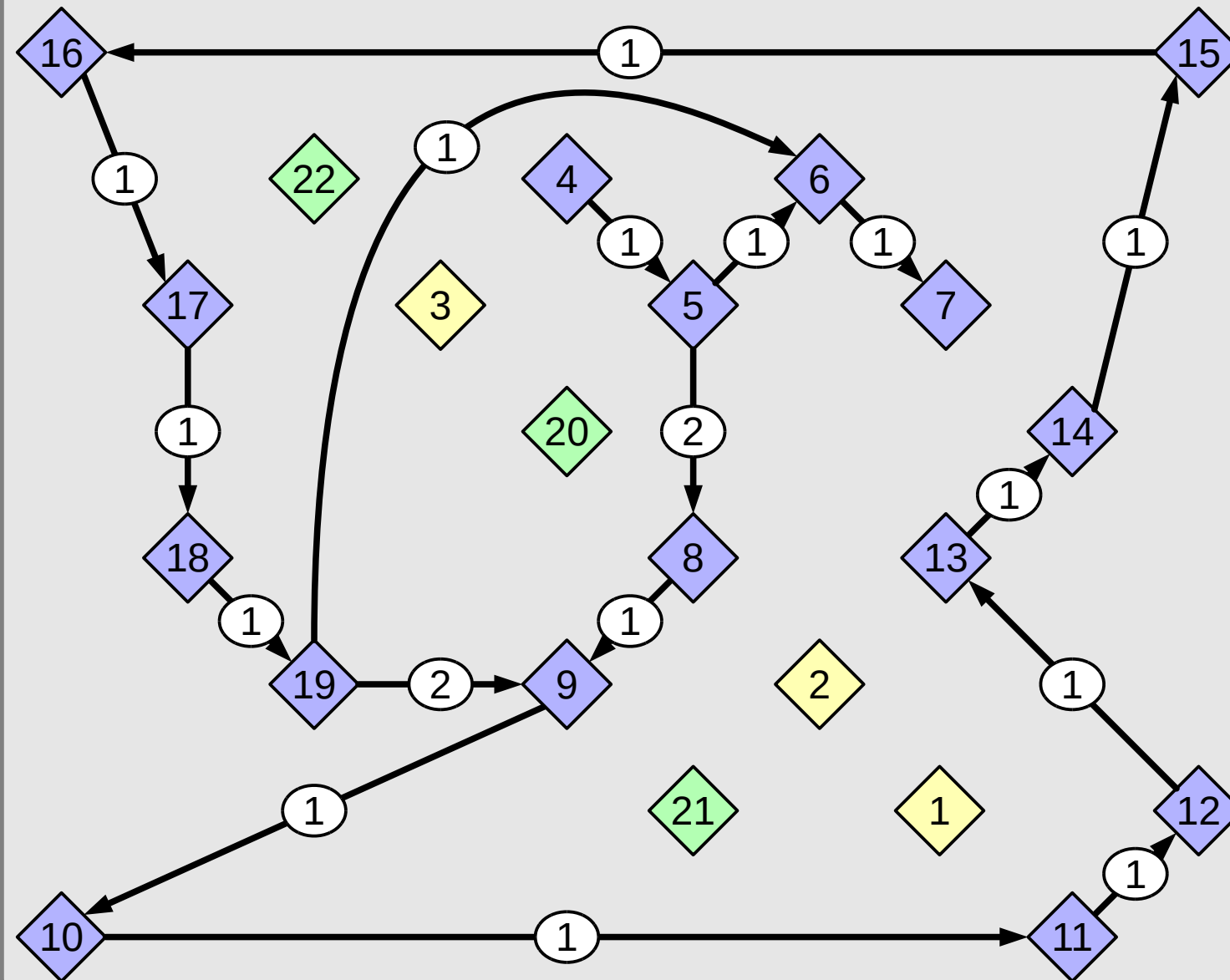


## Data Flow Graph



## Control Flow Graph



## Properties

<b>double*</b>	←	1	
<b>double*</b>	←	2	
<b>i64</b>	←	3	
<b>i1</b>	←	4	→ <b>icmp eq</b>
<b>double</b>	←	5	→ <b>cond br</b>
	←	6	→ <b>phi</b>
	←	7	→ <b>ret</b>
	←	8	→ <b>br</b>
<b>i64</b>	←	9	→ <b>phi</b>
<b>double</b>	←	10	→ <b>phi</b>
<b>double*</b>	←	11	→ <b>gep</b>
<b>double</b>	←	12	→ <b>load</b>
<b>double*</b>	←	13	→ <b>gep</b>
<b>double</b>	←	14	→ <b>load</b>
<b>double</b>	←	15	→ <b>fmul</b>
<b>double</b>	←	16	→ <b>fadd</b>
<b>i64</b>	←	17	→ <b>add</b>
<b>i1</b>	←	18	→ <b>icmp eq</b>
	←	19	→ <b>cond br</b>
<b>i64</b>	←	20	→ <b>0</b>
<b>double</b>	←	21	→ <b>0.0</b>
<b>i64</b>	←	22	→ <b>1</b>