

		Pin Interno	banco	Pin
Consultar_Rank	Input	P	JP1	24
Inicio	Input	PIN_A7	JP1	19
Reloj	Input	PIN_R8	INTERNO	
Resetrn	Input	PIN_D5	JP1	14
Start	Input	PIN_A3	JP1	6
TecladoAnt[9]	Input	PIN_C3	JP1	4
TecladoAnt[8]	Input	PIN_D3	JP1	2
TecladoAnt[7]	Input	PIN_F8	JP1	26
TecladoAnt[6]	Input	PIN_E8	JP1	25
TecladoAnt[5]	Input	PIN_E7	JP1	23
TecladoAnt[4]	Input	PIN_E6	JP1	22
TecladoAnt[3]	Input	PIN_D6	JP1	18
TecladoAnt[2]	Input	PIN_B7	JP1	17
TecladoAnt[1]	Input	PIN_A6	JP1	16
TecladoAnt[0]	Input	PIN_B6	JP1	15
UsAnt[4]	Input	PIN_B5	JP1	10
UsAnt[3]	Input	PIN_A4	JP1	9
UsAnt[2]	Input	PIN_B4	JP1	8
UsAnt[1]	Input	PIN_B3	JP1	7
UsAnt[0]	Input	PIN_A5	JP1	13
Acerto	Output	PIN_E11	JP1	33
altera_reserved_tdo	Output			
Display1[3]	Output	PIN_P14	JP2	32
Display1[2]	Output	PIN_R14	JP2	27
Display1[1]	Output	PIN_P15	JP2	25
Display1[0]	Output	PIN_G15	JP3	17
Display2[3]	Output	PIN_G16	JP3	16
Display2[2]	Output	PIN_F14	JP3	15
Display2[1]	Output	PIN_A14	JP3	5
Display2[0]	Output	PIN_F15	JP3	13
Display3[3]	Output	PIN_D14	JP3	12
Display3[2]	Output	PIN_D15	JP3	11
Display3[1]	Output	PIN_D16	JP3	10
Display3[0]	Output	PIN_C15	JP3	9
Display4[3]	Output	PIN_C16	JP3	8
Display4[2]	Output	PIN_C14	JP3	7
Display4[1]	Output	PIN_B16	JP3	6
Display4[0]	Output	PIN_A2	JP1	5
Display5[3]	Output	PIN_C6	JP1	20
Display5[2]	Output	PIN_C8	JP1	21
Display5[1]	Output	PIN_F9	JP1	27
Display5[0]	Output	PIN_E9	JP1	28
Display6[3]	Output	PIN_C9	JP1	31
Display6[2]	Output	PIN_D9	JP1	32
Display6[1]	Output	PIN_C11	JP1	35
Display6[0]	Output	PIN_B11	JP1	36
Display7[3]	Output	PIN_A12	JP1	37
Display7[2]	Output	PIN_D11	JP1	38

Display7[1]	Output	PIN_D12	JP1	39
Display7[0]	Output	PIN_B12	JP1	40
Display8[3]	Output	PIN_F13	JP2	2
Display8[2]	Output	PIN_T15	JP2	4
Display8[1]	Output	PIN_T14	JP2	5
Display8[0]	Output	PIN_T13	JP2	6
Display9[3]	Output	PIN_R13	JP2	7
Display9[2]	Output	PIN_T12	JP2	8
Display9[1]	Output	PIN_R12	JP2	9
Display9[0]	Output	PIN_T11	JP2	10
EnDisplay1	Output	PIN_T10	JP2	13
EnDisplay2	Output	PIN_R11	JP2	14
EnDisplay3	Output	PIN_P11	JP2	15
EnDisplay4	Output	PIN_R10	JP2	16
EnDisplay5	Output	PIN_N12	JP2	17
EnDisplay6	Output	PIN_P9	JP2	18
EnDisplay7	Output	PIN_N9	JP2	19
EnDisplay8	Output	PIN_L16	JP2	21
EnDisplay9	Output	PIN_K16	JP2	22
Finalizar	Output	PIN_R16	JP2	23
ledOut[4]	Output	PIN_L15	JP2	24
ledOut[3]	Output	PIN_P16	JP2	26
ledOut[2]	Output	PIN_N16	JP2	28
ledOut[1]	Output	PIN_N15	JP2	31
ledOut[0]	Output	PIN_N14	JP2	34
state[5]	Output	PIN_J13	JP2	39
state[4]	Output	PIN_K15	JP2	38
state[3]	Output	PIN_J16	JP2	37
state[2]	Output	PIN_L13	JP2	36
state[1]	Output	PIN_M10	JP2	35
state[0]	Output	PIN_L14	JP2	33