

# Assignment 7

Giridhar Paida

January 2021

## 1 Question

For any two statements  $p$  and  $q$  the negation of expression  $p \vee (p \wedge q)$  is:

1.  $\sim p \wedge \sim q$
2.  $p \wedge q$
3.  $p \leftrightarrow q$
4.  $\sim p \vee \sim q$

The correct answer is (1).

## 2 Boolean Expression

$$F = \overline{p} \overline{q} \quad (1)$$

using NAND logic, we get the following equation

$$F = \overline{\overline{\overline{p} \overline{q}}} \quad (2)$$

## 3 Circuit Diagram

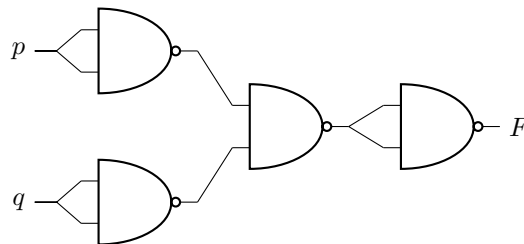


Figure 1: Logic gates