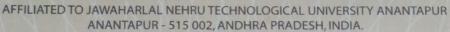


## SREE VIDYANIKETHAN ENGINEERING COLLEGE

(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET - 517 102, ANDHRA PRADESH, INDIA







## **GRADE SHEET**

EXAMINATION: BRANCH: NAME OF THE STUDENT: II B.Tech I Sem.Regular
INFORMATION TECHNOLOGY
BATTA GIRIJA
B PRASADNAIDU

HALL TICKET NO.:

MONTH & YEAR OF EXAM.:

SI.No. G.S.:

13121A1210 November 2014 026469

5. No.	SUBJECT CODE	SUBJECT TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL MARKS	GRADE POINT	LETTER GRADE	CREDITS
1	10BT3BS01	PROBABILITY AND STATISTICS	30	66	96	10	0+	4
2	10BT30421	ELECTRONIC DEVICES AND CIRCUITS	30	48	78	7	A	4
3	10BT30221	BASIC ELECTRICAL ENGINEERING	30	65	95	10	0+	4
4	10BT30422	DIGITAL LOGIC DESIGN	29	61	90	9	0	4
5	10BT30501	DISCRETE MATHEMATICAL STRUCTURES	30	60	90	9	0	4
6	10BT30502	DATA STRUCTURES	30	47	77	7	A	4
7	10BT30431	ANALOG AND DIGITAL ELECTRONICS LAB	25	50	75	10	0	+ 2
8	10BT30511	DATA STRUCTURES LAB	24	47	71	9	(	2

TOTAL

228

444

672

28

GPA:

8.79

Date:

23rd January 2015

CONTROLLER OF EXAMINATIONS

P.C. K. CLOUP