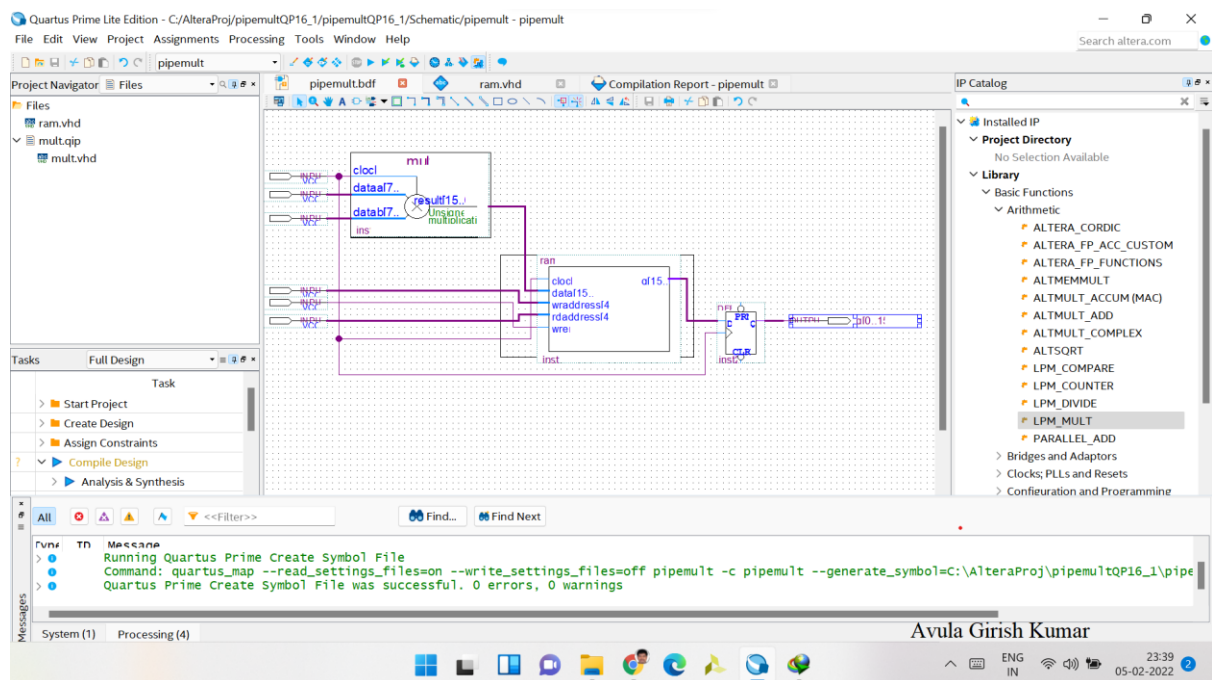
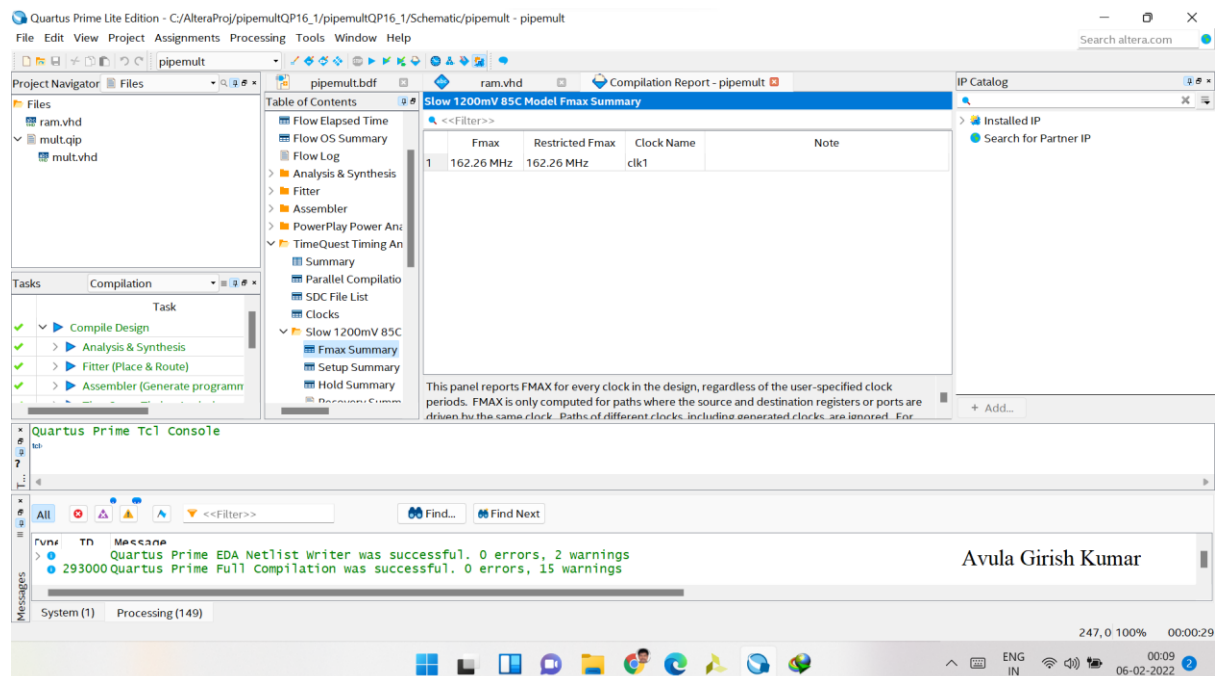


1. Video 6



2. Video 7

Fmax



%Utilization

Quartus Prime Lite Edition - C:/AlteraProj/pipemultQP16_1/pipemultQP16_1/Schematic/pipemult - pipemult

File Edit View Project Assignments Processing Tools Window Help

Search altera.com

Project Navigator Hierarchy

EntityInstance

MAX 10: 10M08DAF484C8GES

> pipemult

Table of Contents

Flow Summary

Flow Status: Successful - Sun Feb 06 00:04:23 2022

Quartus Prime Version: 16.1.0 Build 196 10/24/2016 SJ Lite Edition

Revision Name: pipemult

Top-level Entity Name: pipemult

Family: MAX 10

Device: 10M08DAF484C8GES

Timing Models: Preliminary

Total logic elements: 6 / 8,064 (< 1 %)

Total registers: 21

Total pins: 44 / 250 (18 %)

Total virtual pins: 0

Total memory bits: 512 / 387,072 (< 1 %)

Embedded Multiplier 9-bit elements: 1 / 48 (2 %)

Total PLLs: 0 / 2 (0 %)

UFM blocks: 0 / 1 (0 %)

ADC blocks: 0 / 1 (0 %)

IP Catalog

Installed IP

Search for Partner IP

Tasks

Compilation

Task

Compile Design

Analysis & Synthesis

Filter (Place & Route)

Assembler (Generate program)

Quartus Prime Tcl Console

System (1) Processing (149)

Avula Girish Kumar

2 new notifications

247,0 100% 00:00:29

00:05 06-02-2022

3.Video 8

RTL Viewer - C:/AlteraProj/pipemultQP16_1/pipemultQP16_1/Schematic/pipemult - pipemult2

File Edit View Tools Window Help

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Page: 1 of 1

Netlist Navigator

pipemult2:1

pipemult

clk1

dataa[7..0]

datab[7..0]

rdaddress[4..0]

wraddress[4..0]

wren

multinst

lpm_multlpm_mult_component

mult_bloauto_generated

result[15..0]

raminst1

inst2[15..0]

q[0..15]

Avula Girish Kumar

100% 00:00:01

00:29 06-02-2022

4.Video 10

Fmax

The screenshot displays the Quartus Prime Lite Edition interface. The main window shows the 'Compilation Report - pipemult2' with the 'Slow 1200mV 85C Model Fmax Summary' selected. The report table is as follows:

	Fmax	Restricted Fmax	Clock Name	Note
1	128.21 MHz	128.21 MHz	clk1	

The console shows the following messages:

```
Quartus Prime Tcl Console
> Quartus Prime TimeQuest Timing Analyzer was successful. 0 errors, 8 warnings
> 293000 Quartus Prime Flow was successful. 0 errors, 19 warnings
```

The status bar indicates 'System (2) Processing (131)'.

%Utilization

The screenshot displays the Quartus Prime Lite Edition interface. The main window shows the 'Compilation Report - pipemult2' with the 'Flow Summary' selected. The report table is as follows:

Flow Status	Successful - Sun Feb 06 07:47:39 2022	
Quartus Prime Version	16.1.0 Build 196 10/24/2016 SJ Lite Edition	
Revision Name	pipemult2	
Top-level Entity Name	pipemult	
Family	MAX 10	
Device	10M08DAF484C8GES	
Timing Models	Preliminary	
Total logic elements	6 / 8,064 (< 1 %)	
Total registers	21	
Total pins	44 / 250 (18 %)	
Total virtual pins	0	
Total memory bits	512 / 387,072 (< 1 %)	
Embedded Multiplier 9-bit elements	1 / 48 (2 %)	
Total PLLs	0 / 2 (0 %)	
UFM blocks	0 / 1 (0 %)	
ADC blocks	0 / 1 (0 %)	

The console shows the following messages:

```
Quartus Prime Tcl Console
> Quartus Prime EDA Netlist Writer was successful. 0 errors, 2 warnings
> 293000 Quartus Prime Full Compilation was successful. 0 errors, 27 warnings
```

The status bar indicates 'System Processing (159)'.

5.Video 11

