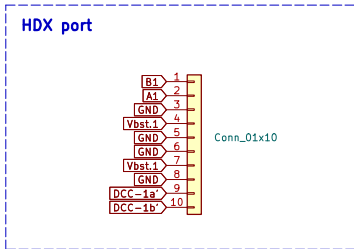
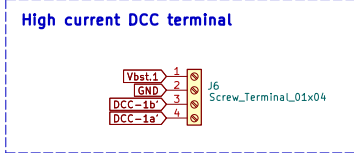
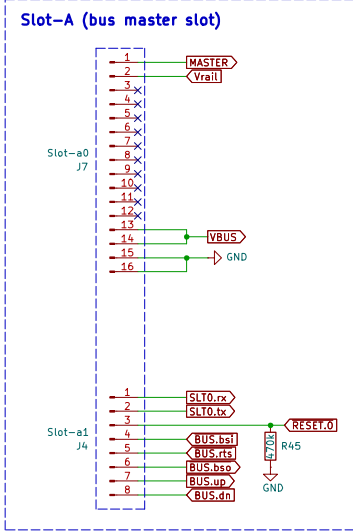
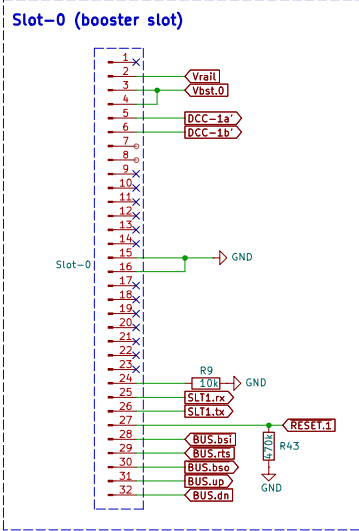


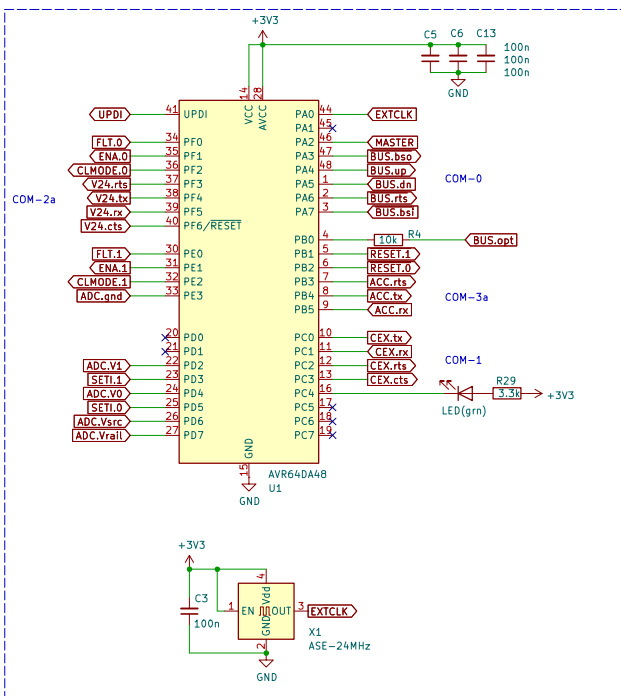
Connector



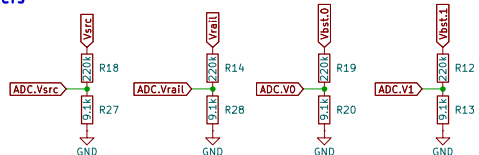
- H1 MountingHole
- H5 MountingHole
- H6 MountingHole
- H4 MountingHole
- H8 MountingHole
- H9 MountingHole
- H2 MountingHole
- H3 MountingHole

Powerboard		
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Sheet: /Connectors/		
File: C11con.kicad_sch		
Title: RTB C11 Module		
Size: A4	Date: 2023-10-22	Rev: 4
KiCad E.D.A. 9.0.5		Id: 2/7

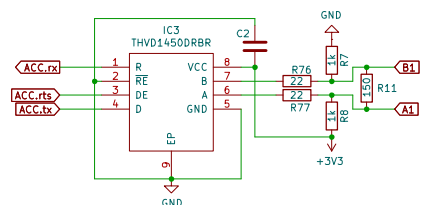
CPU



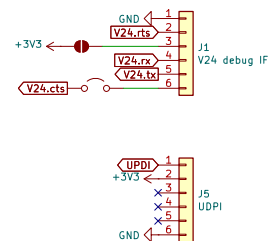
Voltage dividers



HDX interface



UPDI & Debug IF



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Frank Schumacher

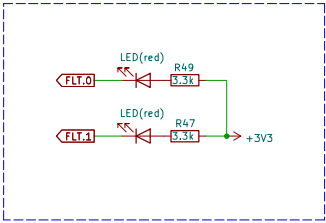
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Title: RTB C11 Module

Size: A4	Date: 2023-10-22
KiCad E.D.A. 9.0.5	

Rev: 4
Id: 3/7

LED



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Sheet: /LED/

File: C11led.kicad_sch

Title: RTB C11 Module

Size: A4

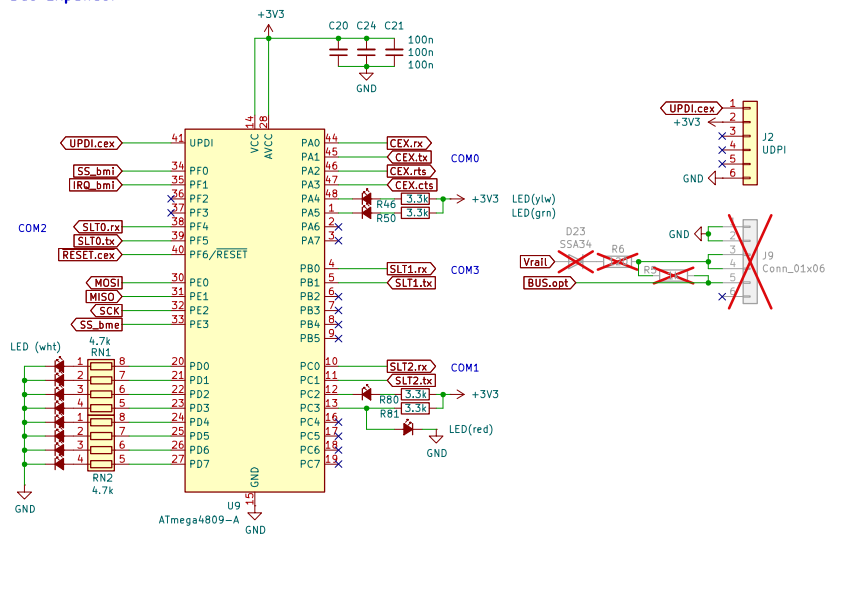
Date: 2023-10-22

Rev: 4

KiCad E.D.A. 9.0.5

Id: 4/7

Expander



Powerboard

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Sheet: /Expander/
File: C11exp.kicad_sch

Title: RTB C11 Module

Size: A4	Date: 2023-10-22
KiCad E.D.A. 9.0.5	

Rev: 4
Id: 5/7

FDX interface



FDX ground loop current sensor

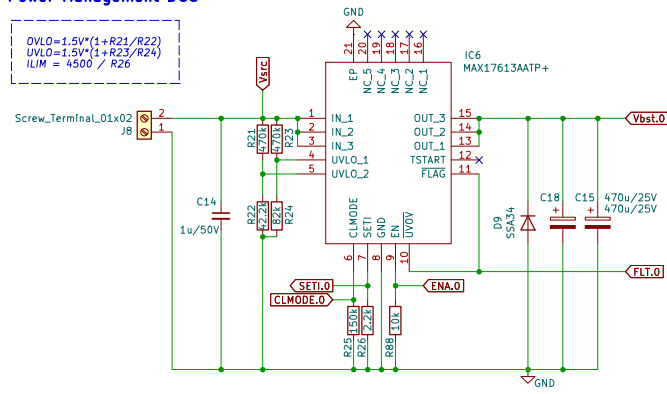


Sheet: /BUS/
File: C11bus.kicad_sch

Power

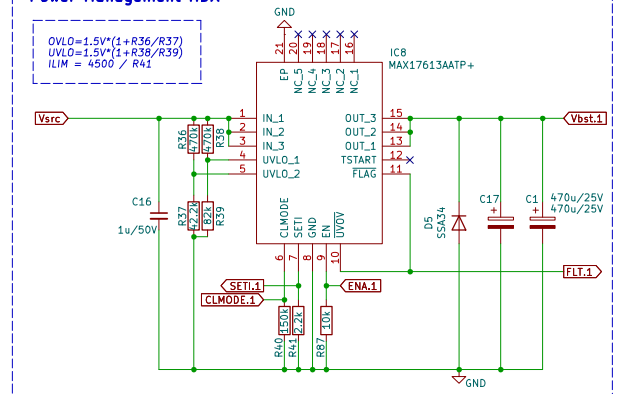
Power Management DCC

$$\begin{aligned} OVLO &= 1.5V * (1 + R21/R22) \\ UVLO &= 1.5V * (1 + R23/R24) \\ ILIM &= 4500 / R26 \end{aligned}$$

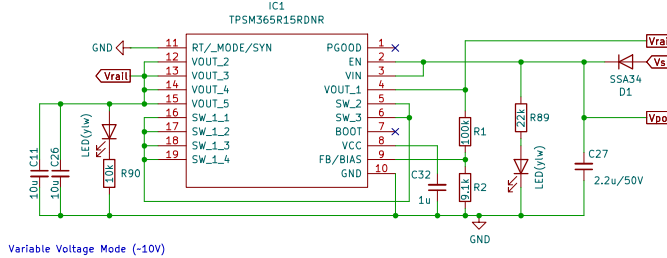


Power Management HDX

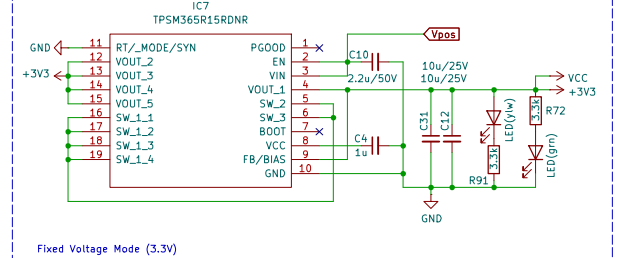
$$\begin{aligned} OVLO &= 1.5V * (1 + R36/R37) \\ UVLO &= 1.5V * (1 + R38/R39) \\ ILIM &= 4500 / R41 \end{aligned}$$



Rail Power



Local CPU Power



Powerboard
Frank Schumacher

Sheet: /Power/
File: C11pwr.kicad_sch

Title: RTB C11 Module

Size: A4 Date: 2023-10-22
KiCad E.D.A. 9.0.5

Rev: 4
Id: 7/7