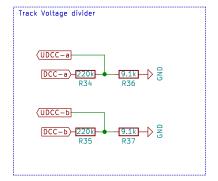
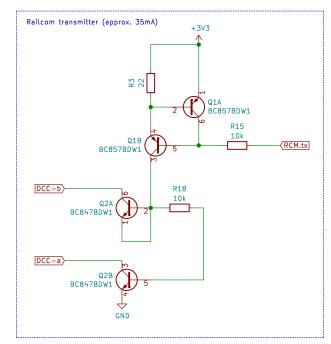
CPU CPU LED_Small +3V3 (LED.hbt) 3.3k LED.R.wht 27 UPDI (UPDI) 1 D J12 Conn_01x01 PFO/TOSC1 EXTCLK/PAO 50 UDCC-b) PA1 31 COM-0 1 J9 Conn_01×01 21 PF1/TOSC2 UDCC-a √V24.rx 22 PF2 PA2 32 LED.hbt 1 D J10 Conn_01x01 PA3 COUPLER.2 PA4 PWM2 COM-2 PA5 PWM1 26 PF6/RESET COUPLER.1) PA6 white PA7 BNO.res LED.F.cab 33k GND uSENS1) (LED.R.wht 10 PD0 BNO.tx) PC0 11 PD0 11 PD1 12 PD2 13 PD3 14 PD4 15 PD5 16 PD6 17 PD7 LED.R.cab COM-1 BNO.rx PC1 LED.F.cab PC2 LED.aux1 DCC-a white LED.F.wht (LED.R.cab (LED.aux2 (Vref J16 📮 🔒 J13 J15 J14 Conn_01x01 VTRK)-VTRK> AVR-64DA32 PMZB200UNEYL U1 GND PMZB200UNEYL COUPLER.1 COUPLER.2 $_{\sf GND}$ $\stackrel{\bot}{\smile}$ GND 🕁 9-axis IMU (optional) U3 BN0055 UPDI & Debug IF VDD LED.aux1 3.3k 1 J8 VDDIO × 27 10 10 10 XIN32 BNO.res 11 NRESET LED.aux2 3.3k 1 J1 X0UT32 26 × 14 × 10k 4 NBOOT NBOOT_LOAD_PIN UDPI & Debug IF BNO.tx 19 COM1 10 10k PIN10 18 15 COM2 PIN15 17 сомз PIN16 V24.rx> GNDIO GND 🕁 GND 100n GND D15.2 Minitrix V160 (#12460) Frank Schumacher Sheet: /CPU/ File: D15cpu.sch Title: RTB D15 Decoder Size: A4 Date: 2024-09-29 Rev: 2 KiCad E.D.A. kicad (5.1.12)-1 ld: 2/4

RcmTX





D15.2

Minitrix V160 (#12460)

Frank Schumacher

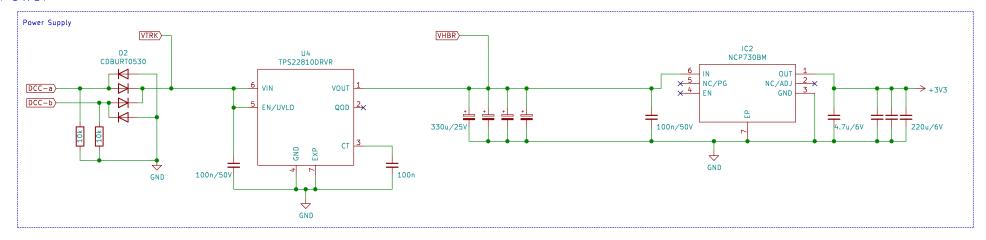
Sheet: /Railcom/ File: D15rcm.sch

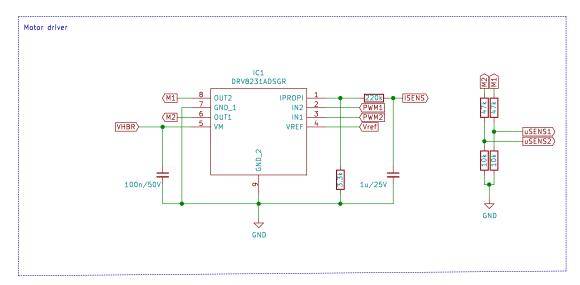
Title: RTB D15 Decoder

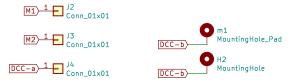
 Size: A4
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 Rev: 2

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 Id: 3/4









D15.2

Minitrix V160 (#12460)

Frank Schumacher

Sheet: /Power/ File: D15pwr.sch

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