

CPU CPU LEDs +3V3 AUX1> Dummy are solder pads (UPDI) 27 UPDI AUX2> PF0/TOSC1 EXTCLK/PA0 COM-0 21 PF1/TOSC2 ISENS PA1 √V24.rx AUX3> PA2 DCC-b 33k PF3 PA3 F0f LED R1 (RCM.tx PF4 PA4 PWM1) LED.hbt> COM-2 PA5 26 PF6/RESET D1 V24.cts PA6 4× PA7 5× PC0 6 PDO UDCC-a USENS1)

[IFL)

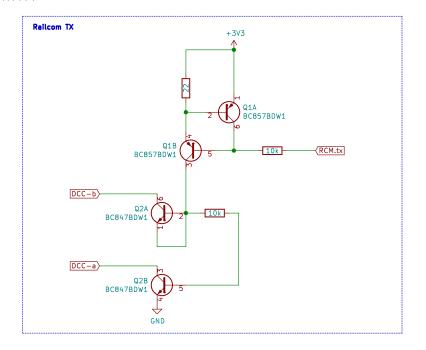
(AUX1)

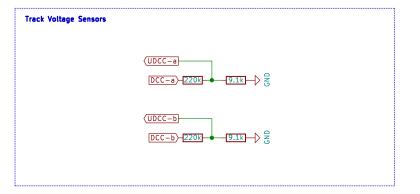
(AUX2)

(AUX3)

(Vref COM-1 11 PD1 PC1 33k DCC-a 12 PD2 PC2 PD3 NEM651 PD5 16 PD6 uSENS2 NEM651 AVR-64DA32 U1 GND UPDI & Debug IF +3∨3 ← Service IF GND 🗸 V24.rx) Debug IF may be removed in the future and only UPDI will remain Licensed under the Apache License, Version 2 D16.4 Lok Decoder NEM651 Frank Schumacher Sheet: /CPU/ File: D16cpu.sch Title: RTB D16 NEM651 Decoder Size: A4 Date: 2024-11-16 Rev: 4 KiCad E.D.A. kicad (5.1.12)-1 Id: 2/4

RcmTX





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D16.4

Lok Decoder NEM651

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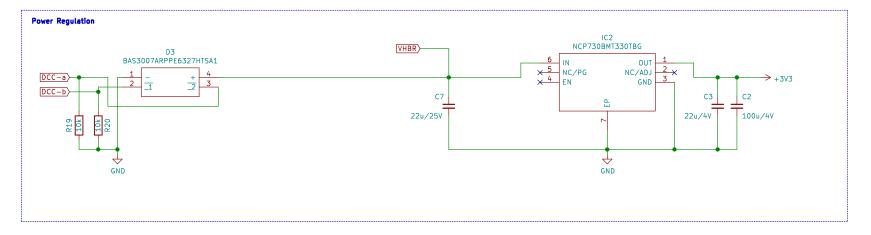
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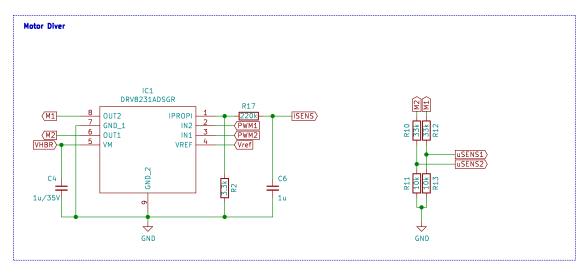
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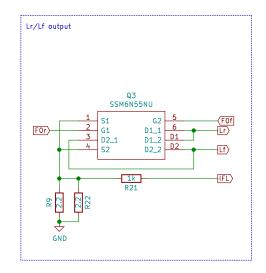
 Size: A4
 Date: 2024-11-16
 Rev: 4

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 Id: 3/4

Power







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D16.4

Lok Decoder NEM651

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Sheet: /Power/ File: D16pwr.sch

Title: RTB	D16 NEM651	Decod	er
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Size: A4	Date: 2024-11-16	Rev: 4
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