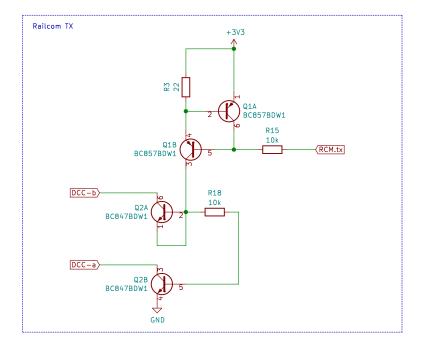
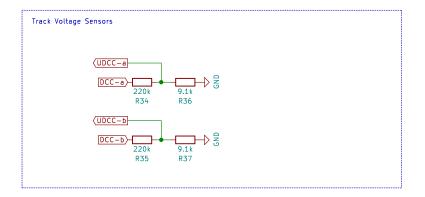


CPU CPU LEDs +3V3 LED.R.wht (UPDI) 27 UPDI LED.R.red PF0/TOSC1 EXTCLK/PA0 -\V24.tx\\
-\V24.rx\\
-\F0r\\
-\F0f\\ UDCC-b COM-0 21 PF1/TOSC2 PA1 PA2 LED.F.wht> PA3 D1 LED PF3 (RCM.tx PF4 PA4 PWM1) COM-2 (V24.rts) (V24.cts) PF5 PA5 LED.F.red 26 PF6/RESET PA6 PA7 PC0 6 COM-1 uSENS1 PD1 PC1 DCC-a PC2 (LED.R.wht) NEM651 (LED.F.wht PD5 16 PD6 (Vref uSENS2) NEM651 GND AVR-64DA32 U1 UPDI & Debug IF Service IF V24.rx D16.4 Lok Decoder NEM651 Frank Schumacher Sheet: /CPU/ File: D16cpu.sch Title: RTB D16 NEM651 Decoder Size: A4 Date: 2023-10-24 Rev: 4 KiCad E.D.A. kicad (5.1.12)-1 ld: 2/4

RcmTX





D16.4

Lok Decoder NEM651

Frank Schumacher

Sheet: /Railcom/ File: D16rcm.sch

Title: RTB D16 NEM651 Decoder

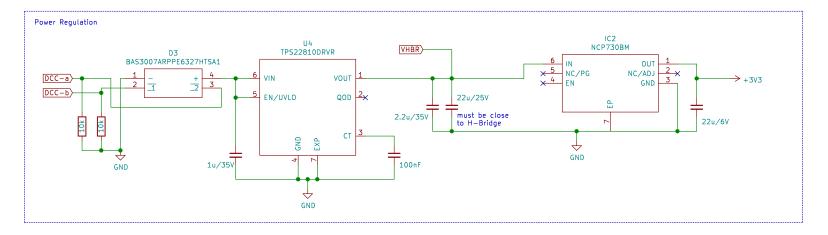
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 Date: 2023-10-24
 Rev: 4

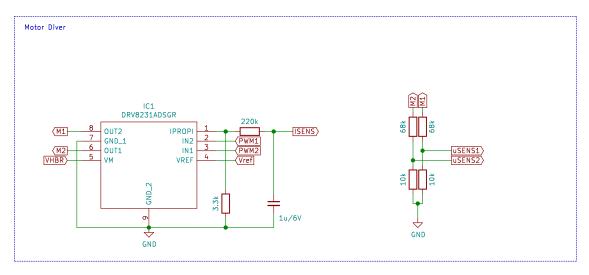
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 Id: 3/4

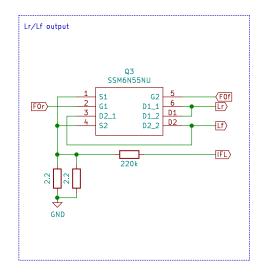
7

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Power







D16.4

Lok Decoder NEM651

Frank Schumacher

Sheet: /Power/ File: D16pwr.sch

Title: RTB D16 NEM651 Decoder

 Size: A4
 Date: 2023-10-24
 Rev: 4

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 Id: 4/4