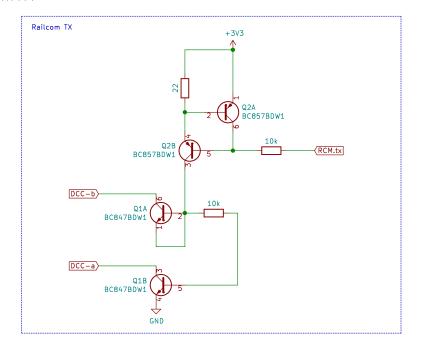
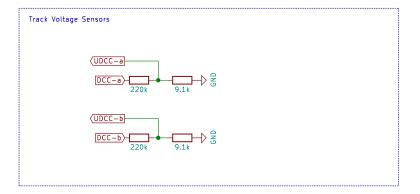
CPU CPU LEDs AUX2 1 5 Conn_01x01 AUX3 1 J8 Conn_01x01 AUX4 1 D J10 Conn_01×01 P1 1 J6 Conn_01x01 P2 1 J7 Conn 01x01 LED AVR64DD28-I_STX IC1 PF1_(XTAL32K2) 21 20 PF0_(XTAL32K1) GND_1 19 (FOr PWM1 LED.hbt> LED.hbt PA4 PA5 → +3V3 {DCC-b'} {Vref} {ISENS PA6 AVDD PA7 PD7 PD6 COM-1 (RCM.tx PD5 NEM651 VHBR 2 NEM651 NEM651 D3 BAT30F4 UPDI Clamping required for PD7 +3V3 (UPDI) 1 D J2 Conn_01x01 DCC-b' 68k GND (J9 Conn 01x01 +3V3 BAT30F4 D4 D20.1 Double sided NEM651 Frank Schumacher Sheet: /CPU/ File: D20cpu.sch Title: RTB D20 NEM651 Decoder Size: A4 Date: 2024-11-13 Rev: 1 KiCad E.D.A. kicad (5.1.12)-1 ld: 2/4

RcmTX





D20.1

Double sided NEM651

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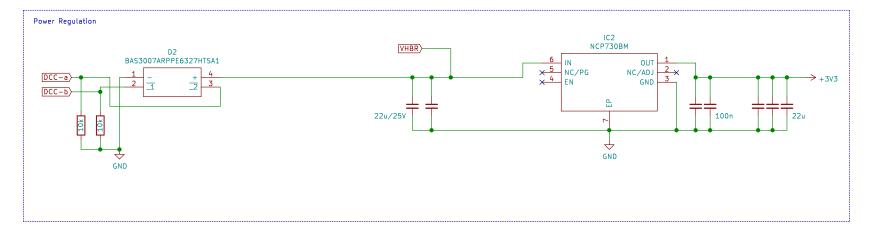
Sheet: /Railcom/ File: D20rcm.sch

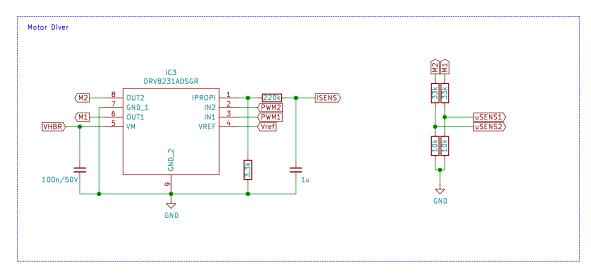
Title: RTB D20 NEM651 Decoder

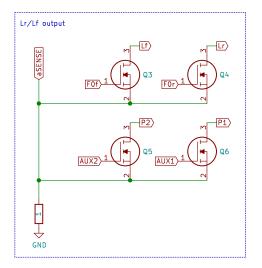
 Size: A4
 Date: 2024-11-13
 Rev: 1

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 Id: 3/4

Power







D20.1

Double sided NEM651

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Sheet: /Power/ File: D20pwr.sch

Title: RTB D20 NEM651 Decoder

 Size: A4
 Date: 2024-11-13
 Rev: 1

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 Id: 4/4