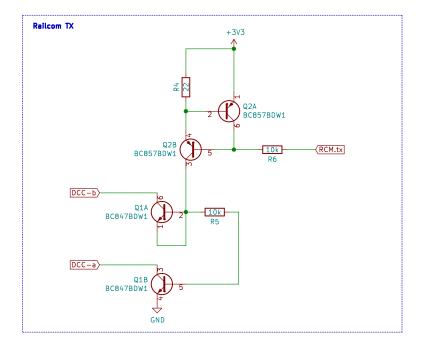
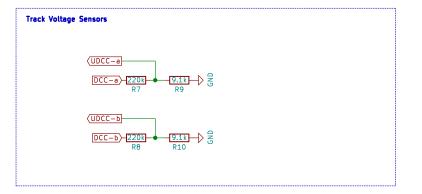


CPU CPU **LEDs** AUX1 1 AUX2 1 (AUX3) 1 -SUSI.clk AUX4 1 SUSI.dat P1 1 -P2 1 -LED AVR64DD28-I_STX PF1_(XTAL32K2) 20 PF0_(XTAL32K1) 19 (FOr PWM1 PA3 LED.hbt> - 3.3k R3 LED.hbt PA4 PA5 GND_1 → +3V3 {DCC-b'} {Vref} {ISENS PA6 AVDD PA7 PD7 PD6 COM-1 (RCM.tx PD5 Connectors VHBR 2 Capacitor NEM651 D3 BAT30F4 UPDI Clamping required for PD7 +3V3 GND (1 P J9 pad +3V3 BAT30F4 D4 Licensed under the Apache License, Version 2 D20.1 Double sided NEM651 Frank Schumacher Sheet: /CPU/ File: D20cpu.sch Title: RTB D20 NEM651 Decoder Size: A4 Date: 2024-11-15 Rev: 1 KiCad E.D.A. kicad (5.1.12)-1 ld: 2/4

RcmTX





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D20.1

Double sided NEM651

Frank Schumacher

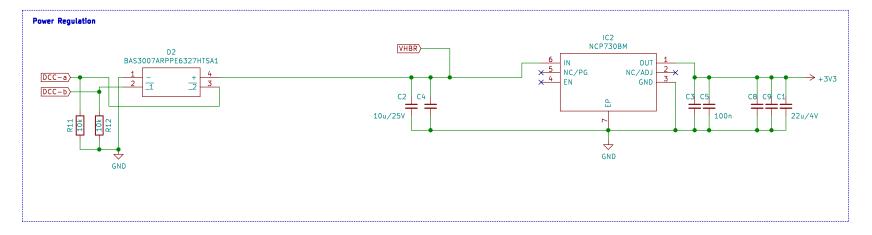
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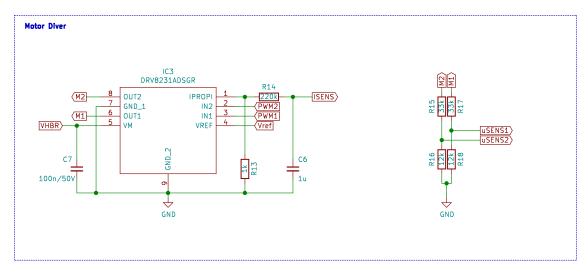
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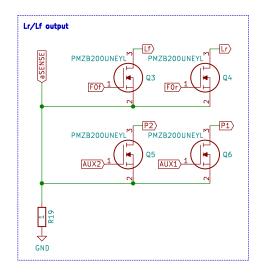
 Size: A4
 Date: 2024-11-15
 Rev: 1

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 Id: 3/4

Power







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D20.1	
Double sided NEM651	
Frank Schumacher	
Sheet: /Power/	1
File: D20pwr.sch	l
Title: RTB D20 NEM651 Decoder	1

 Size: A4
 Date: 2024-11-15
 Rev: 1

 KiCad E.D.A. kicad (5.1.12)-1
 Id: 4/4