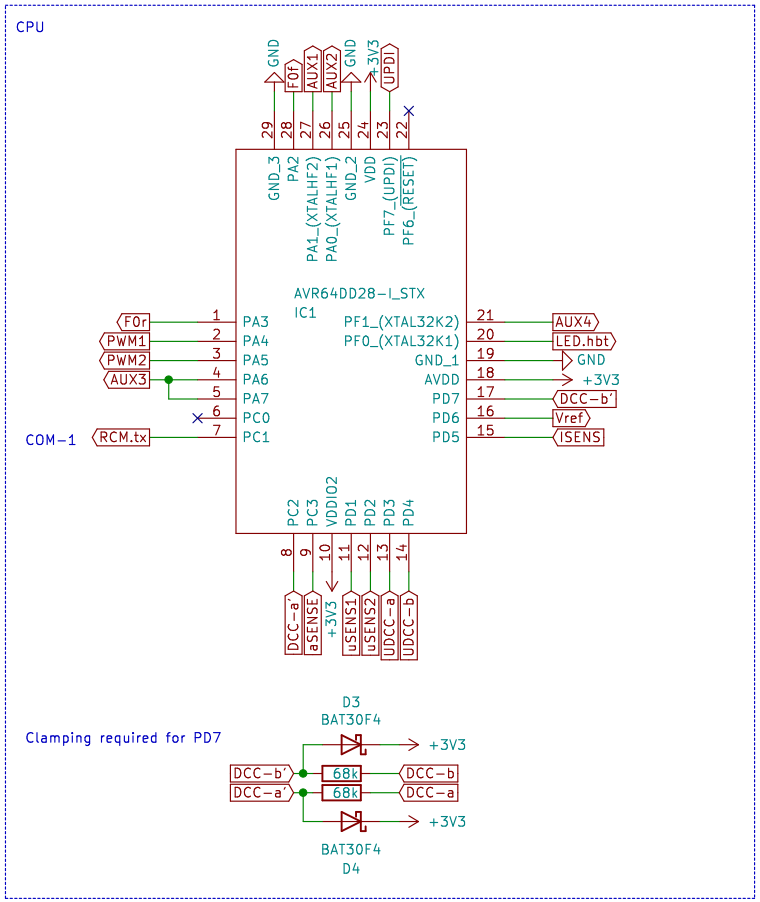
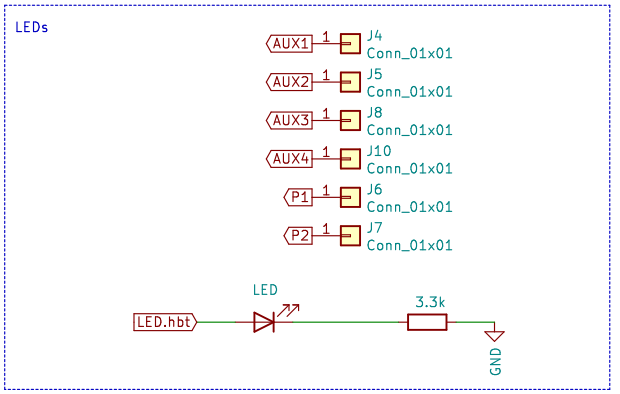
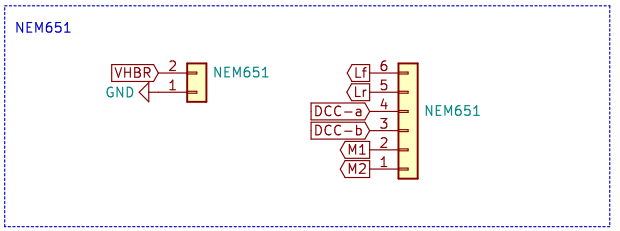
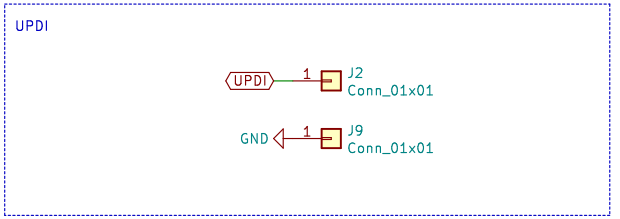
CPU









D20.1
Double sided NEM651
Frank Schumacher

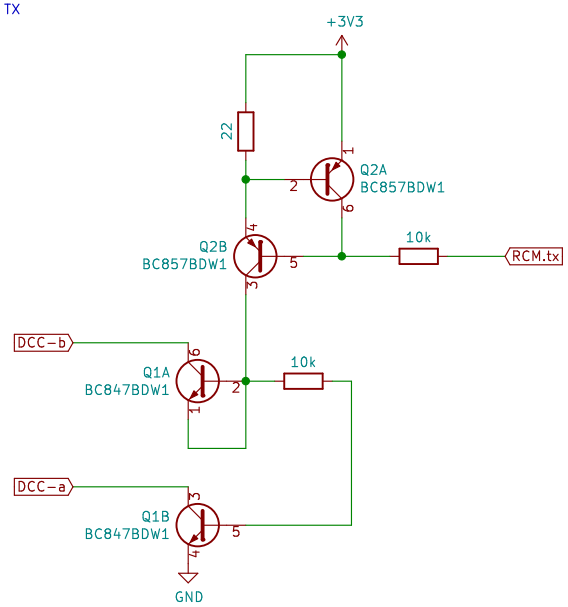
Sheet: /CPU/
File: D20cpu.sch

Title: RTB D20 NEM651 Decoder

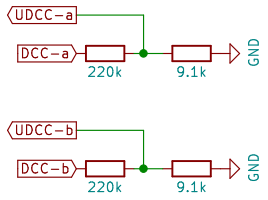
Size: A4	Date: 2024-11-13	**Rev: 1**
KiCad E.D.A. kicad (5.1.12)-1		Id: 2/4

RcmTX

Railcom TX



Track Voltage Sensors



D20.1
Double sided NEM651
Frank Schumacher

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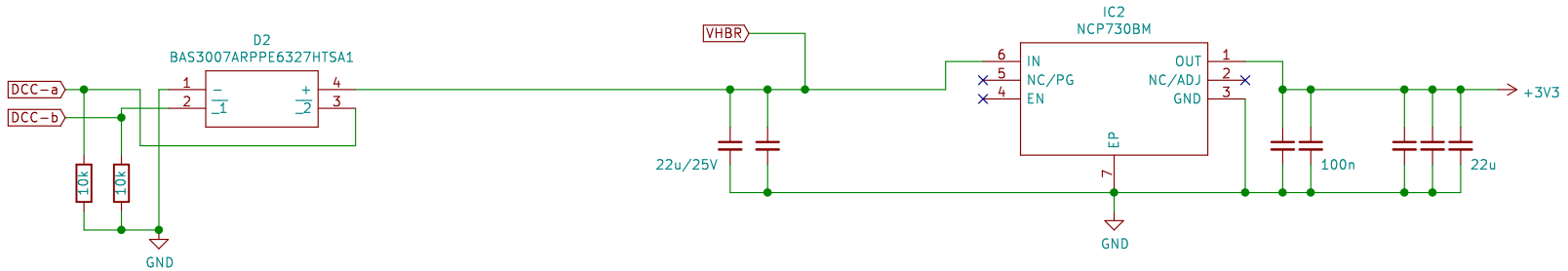
Title: RTB D20 NEM651 Decoder

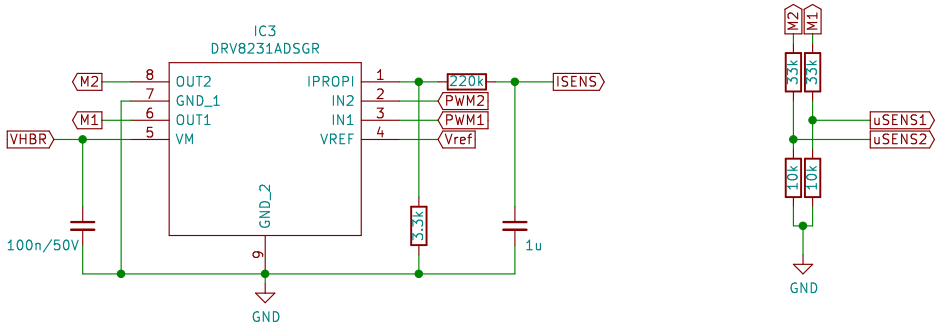
Size: A4	Date: 2024-11-13	**Rev: 1**
KiCad E.D.A. kicad (5.1.12)-1		Id: 3/4

Power

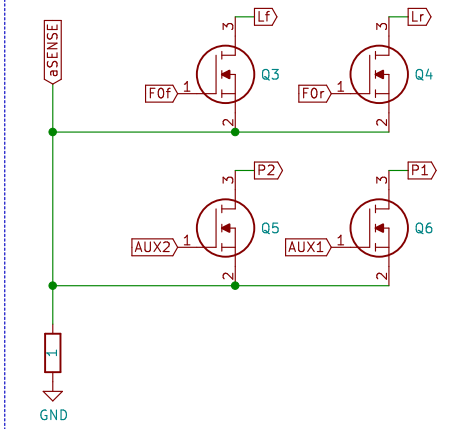
Power Regulation



Motor Diver



Lr/Lf output



D20.1 Double sided NEM651 **Frank Schumacher**		
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Size: A4	Date: 2024-11-13	**Rev: 1**
KiCad E.D.A. kicad (5.1.12)-1		Id: 4/4