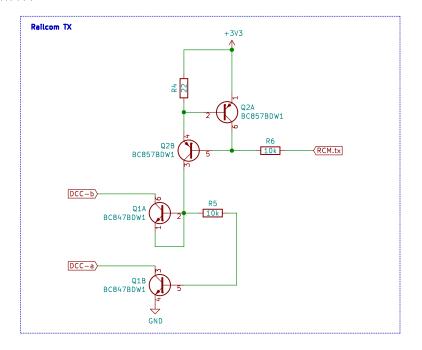
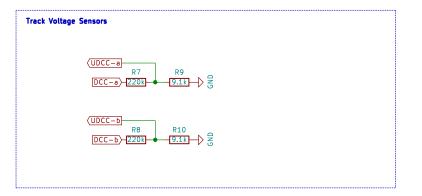


CPU CPU LEDs +3V3 C8 100n LED R3 LED.hbt> 3.3k GND AVR64DD28-I_STX IC1 PF1_(XTAL32K2) 20 PF0_(XTAL32K1) 19 18 AVDD 17 -AUX3> -LED.hbt> --> GND AUX1 2 PA3 PA4 PA5 PA6 Plux16 (PWM1 PWM2 J1 AUX4 +3/3 5 PA7 PC0 PD7 17 16 PD6 15 Vref \(\square\) (SUSI.dat COM-1 PD5 15 COM-0 UPDI D1 SM5819L2-TP SM5819L2-TP +3V3 D2 Licensed under the Apache License, Version 2 D22.0 Lok Decoder Plux16 Frank Schumacher Sheet: /CPU/ File: D22cpu.sch Title: RTB D22 Plux16 Decoder Size: A4 Date: 2025-04-20 Rev: 0 KiCad E.D.A. kicad (5.1.12)-1 ld: 2/4

RcmTX





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D22.0

Lok Decoder Plux16

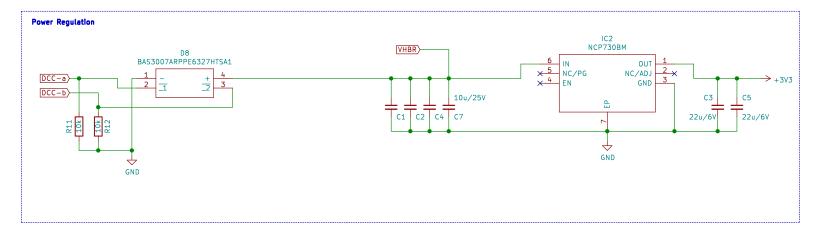
Frank Schumacher

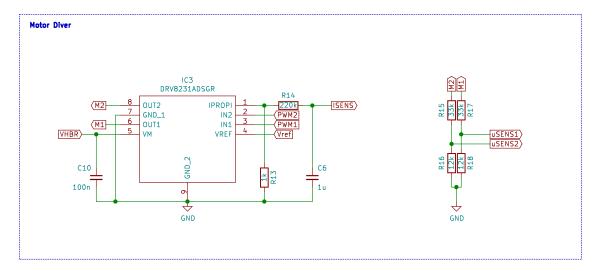
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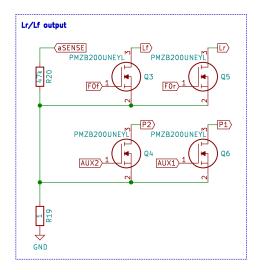
Title: RTB D22 Plux16 Decoder

Size: A4 Date: 2024-11-18 Rev: 0 KiCad E.D.A. kicad (5.1.12)-1 ld: 3/4

Power







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D22.0

Lok Decoder Plux16

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Sheet: /Power/ File: D22pwr.sch

Title: RTB D22 Plux16 Decoder

Size: A4	Date: 2024-11-18	Rev: 0
KiCad E.D.A. k	icad (5.1.12)-1	ld: 4/4