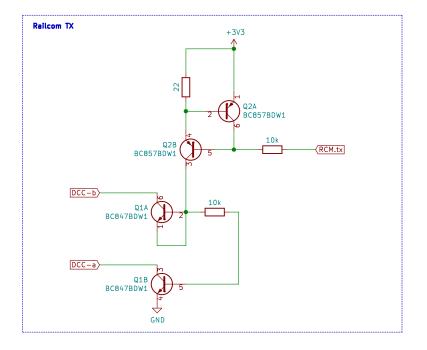
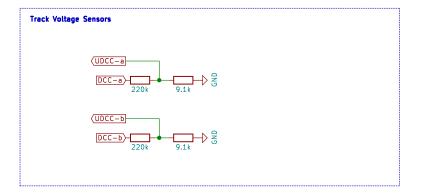


CPU CPU LEDs +3V3 LED R3 LED.hbt> 3.3k C4 C7 GND SUSI (VHBR) 1 -AVR64DD28-I\_STX AUX5 IC1 PF1\_(XTAL32K2) 20 20 FF0\_(XTAL32K1) GND\_1 19 1 PA3 (AUX1 2 PA4 PA5 AUX6 AUX6 > PWM2 4 PA6 5 PA7 AVDD 18 GND ( → +3V3 -{DCC-b'} -{Vref} -{ISENS AUX5 AUX4 AUX3 PD7 17 PD6 16 6 PC0 7 PC1 COM-1 (RCM.tx PD5 NEM652 GND/P1 NEM 652 GND J1 D3 UPDI (UPDI) 1 J2 Conn\_01x01 D\_Schottky +3V3 DCC-b' 33k DCC-b
DCC-a' 33k DCC-a Solder pads D\_Schottky +3V3 VHBR VHBR VHBR VHBR D4 P1 AUX1.od P3 1 AUX3.od P2 AUX2.od P4 AUX4.od D23.0 Lok Decoder NEM652 Frank Schumacher Sheet: /CPU/ File: D23cpu.sch Title: RTB D23 NEM652 Decoder Size: A4 Date: 2025-03-17 Rev: 0 KiCad E.D.A. kicad (5.1.12)-1 ld: 2/4

## RcmTX





D23.0

Lok Decoder NEM652

Frank Schumacher

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Title: RTB D23 NEM652 Decoder

 Size: A4
 Date: 2025-03-17
 Rev: 0

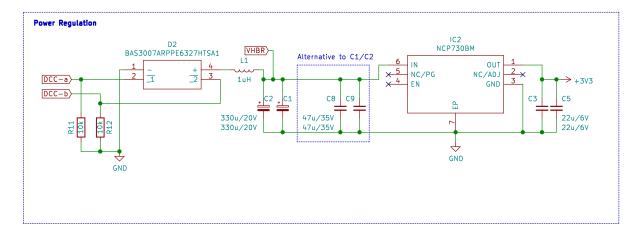
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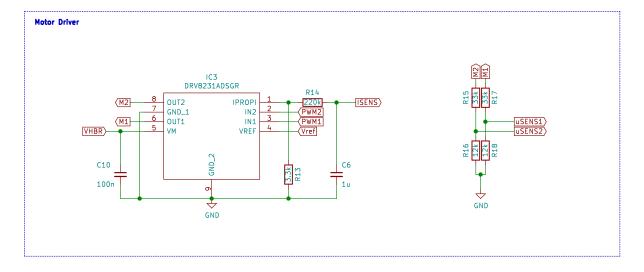
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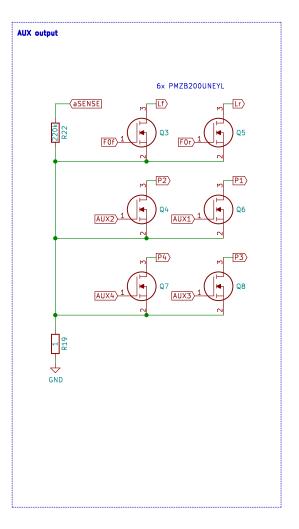
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5

## Power







D23.0

Lok Decoder NEM652

Frank Schumacher

Sheet: /Power/ File: D23pwr.sch

Title:	RTB	D23	NEM652	Decod	ler
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Size: A4	Date: 2025-03-17	Rev: 0
KiCad E.D.A.	kicad (5.1.12)-1	ld: 4/4

4

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