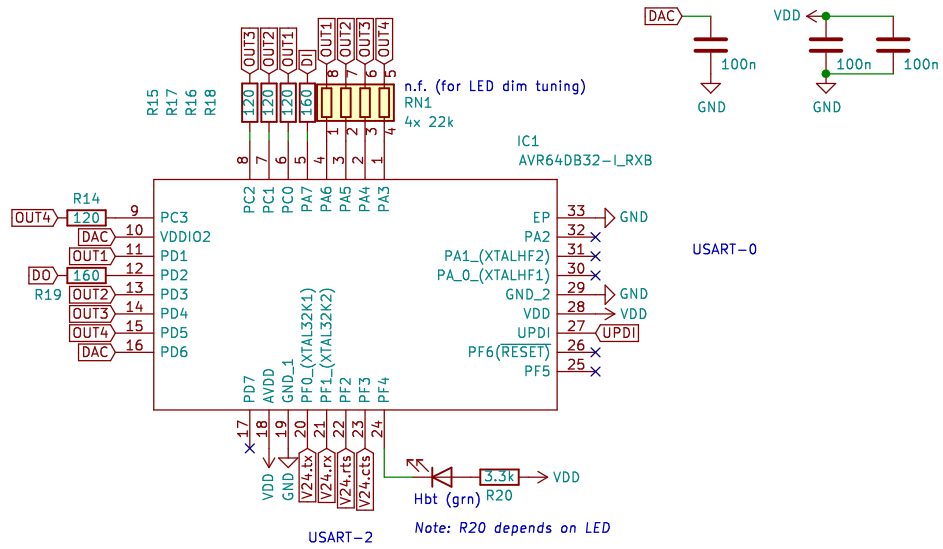
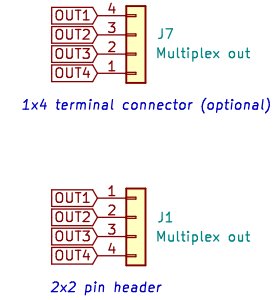


## CPU

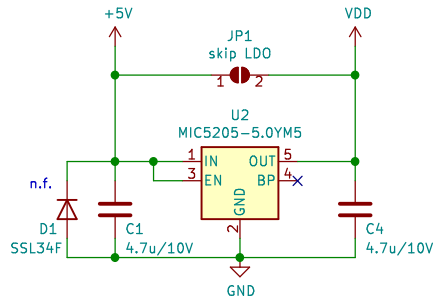


## Multiplex Signal connector

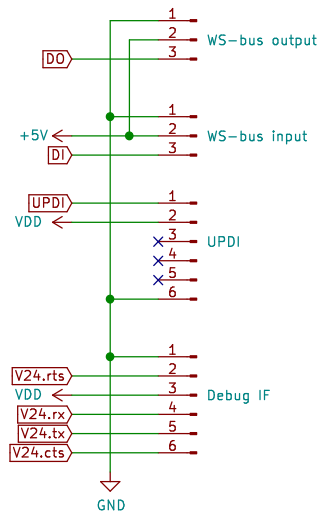


## Power

LDO is optional and only to protect CPU against high voltage on the bus. The D1 protects against reverse polarity. Close JP1 if LDO is not fitted.



## Interfaces



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WS2811 chip emulator for 12 multiplex LEDs  
**Frank Schumacher**

Sheet: /

File: E13.sch

**Title: RTB E13 module**

Size: A4	Date: 2024-11-14
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Rev: 1

Id: 1/1

H1 MountingHole      H2 MountingHole      H4 MountingHole      H3 MountingHole