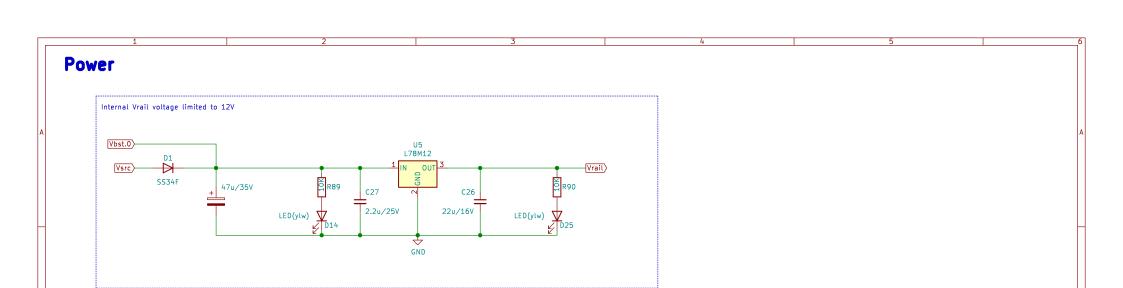


**Slots** J3 J7 Slot: Booster Slot: Master Vrail Vbst.0 Vrail -4× -5× -6× -7× -8× -9× -10× -11 -12 -13 DCC-1a' - 6 - 7 × - 8 × - 9 × - 10 × - 11 × - 13 × - 14 × - 15 · - 17 · - 18 · - 19 × - 20 × - 21 × - 22 × - 23 × - 24 · - 25 · - 26 · - 27 · - 28 · - 29 · - 30 · -Isolated USB power domain DCC power domain PS1 TDN\_5-0912WI 100 × 4 ON/OFF +VOUT 2 -VIN\_(GND) -VOUT R1 14 NC 5 × +VIN\_(VCC) 15 16 - GND GND (ISET) SLT1.rx) SLT1.tx) RESET.1 Development purpose (do not open bridge) External connections SLTO.rx SLTO.tx RESET.0 Vsrc 1 5 J1 Screw\_Terminal\_01x02 BUS.bsi BUS.rts Jumper\_2\_Bridged BUS.bso BUS.bso BUS.up BUS.bsi BUS.up) DCC-16 2 0 J6

DCC-16 3 0 Screw\_Terminal\_01x04 Barrel\_Jack\_Switch O H5 MountingHole Choice: Barrel Jack may be 2.1mm or 2.5mm H3 MountingHole Licensed under the Apache License, Version 2 Base board Frank Schumacher O H9 MountingHole Sheet: /SLOT/ File: P15slt.sch Title: RTB P15 Module Size: A4 Date: 2025-09-04 Rev: 2 KiCad E.D.A. kicad (5.1.12)-1 Id: 2/4 BUS RTB bus debug connector (do not use) J21 Conn\_01x16 -(BUS.dn BUS.up BUS.bso BUS.rts BUS.bsi <del>→</del> +3V3 Licensed under the Apache License, Version 2 Base board Frank Schumacher Sheet: /BUS/ File: P15bus.sch Title: RTB P15 Module Size: A4 Date: 2025-09-04 Rev: 2 KiCad E.D.A. kicad (5.1.12)-1 ld: 3/4



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Base board
Frank Schumacher
Sheet: /Power/
File: P15pwr.sch

Title: RTB P15 Module
Size: A4 Date: 2025-09-04 Rev: 2
KiCad E.D.A. kicad (5.1.12)-1 Id: 4/4