



HPC SDK version 24.5

CUDA Fortran Programming

Preface

Intended Audience

Organization

Conventions

Related Publications

- 1. Introduction
- 2. Programming Guide
 - 2.1. CUDA Fortran Host and Device Code
 - 2.2. CUDA Fortran Kernels
 - 2.3. Thread Blocks
 - 2.4. Memory Hierarchy
 - 2.5. Subroutine / Function Qualifiers
 - 2.5.1. Attributes(host)
 - 2.5.2. Attributes(global)
 - 2.5.3. Attributes(device)
 - 2.5.4. Attributes(host, devic
 - 2.5.5. Attributes(grid_globa
 - 2.5.6. Restrictions
 - 2.6. Variable Qualifiers
 - 2.6.1. Attributes(device)
 - 2.6.2. Attributes(managed)
 - 2.6.3. Attributes(constant)
 - 2.6.4. Attributes(shared)
 - 2.6.5. Attributes(pinned)
 - 2.6.6. Attributes(texture)
 - 2.6.7. Attributes(unified)
 - 2.7. Datatypes in Device Subprograms
 - 2.7.1. Half-precision Floating Point
 - 2.8. Predefined Variables in Device Subprograms
 - 2.9. Execution Configuration
 - 2.10. Asynchronous Concurrent Execution
 - 2.10.1. Concurrent Host

CUDA Fortran Programming (<u>PDF</u>) - HPC SDK version 24.5 (<u>Documentation Archives</u>) - Last updated May 21, 2024 - <u>Send Feedback</u> -

NVIDIA CUDA Fortran Programming Guide

Preface

This document describes CUDA Fortran, a small set of extensions to Fortran that supports and is built upon the CUDA computing architecture.

Intended Audience

This guide is intended for application programmers, scientists and engineers proficient in programming with the Fortran, C, and/or C++ languages. The tools are available on a variety of operating systems for the x86-64, OpenPOWER and Arm server hardware platforms. This guide assumes familiarity with basic operating system usage.

Organization

The organization of this document is as follows:

Introduction

contains a general introduction

Programming Guide

serves as a programming guide for CUDA Fortran

Reference

describes the CUDA Fortran language reference

Runtime APIs

describes the interface between CUDA Fortran and the CUDA Runtime API

Examples

provides sample code and an explanation of the simple example.

Conventions

This guide uses the following conventions:

italic

is used for emphasis.

Constant Width

is used for filenames, directories, arguments, options, examples, and for language statements in the text, including assembly language statements.

Bold

is used for commands.

[item1]

