

Lecture #16

(finish from last lecture)

Cut-off frequency

→ Frequency at which the magnitude of input current, I_i equals the load current, I_d

* From circuit with C_m :

$$I_i = j\omega(C_{gst} + C_m)V_{gs}, \quad I_d = g_m V_{gs}$$

Magnitude of current gain: $\left| \frac{I_d}{I_i} \right| = \frac{g_m}{2\pi f(C_{gst} + C_m)} = 1$

f_T occurs at a gain of 1

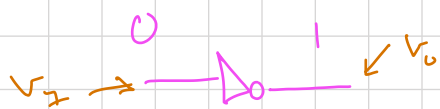
$\omega = 2\pi f$

$$\Rightarrow f_T = \frac{g_m}{2\pi(C_{gst} + C_m)} = \frac{g_m}{2\pi C_G}$$

input gate capacitance

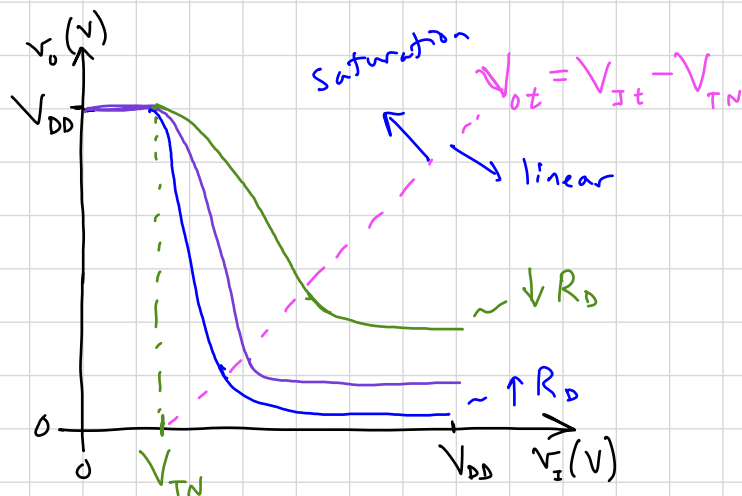
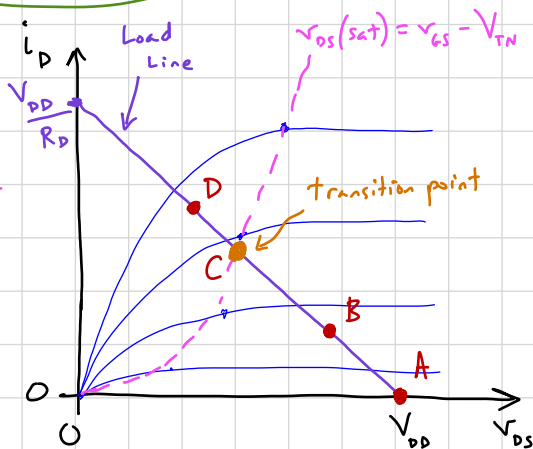
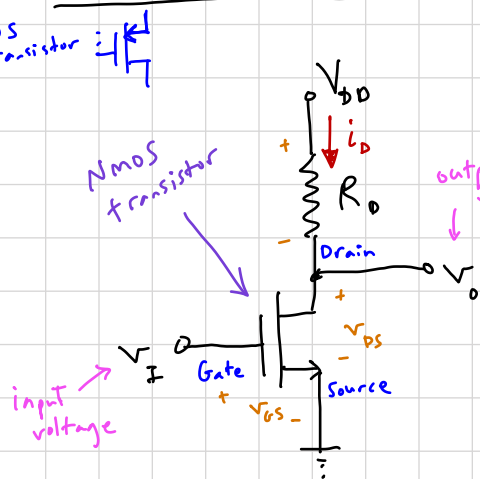
for the ideal case, where $C_G = C_{ox} \cdot W \cdot L = C_{gs}$ and $g_m = \frac{W \mu_n C_{ox}}{L} (V_{gs} - V_T)$

$$f_T = \frac{\mu_n (V_{gs} - V_T)}{2\pi L^2} \quad (\text{ideal MOSFET})$$



NMOS Inverter with Resistive Load

→ a resistor fabricated in Si substrate



A: $V_I \leq V_{TN}$

→ transistor is OFF

$$i_D = 0$$

$$V_O = V_{DD}$$

LOGIC "1"

B: $V_I \text{ just } > V_{TN}$

→ transistor is in saturation

$$V_O = V_{DD} - i_D R_D$$

$$i_D(sat) = K_n (V_I - V_{TN})^2$$

$$V_O = V_{DD} - K_n R_D (V_I - V_{TN})^2$$

C: V_I at transition point

$$V_{Ot} = V_{It} - V_{TN}$$

V_{Os} V_{GS} at trans. pt.

$$K_n R_D (V_{It} - V_{TN})^2 + (V_{It} - V_{TN}) - V_{DD} = 0$$

D: $V_I > V_{It}$

→ transistor in linear

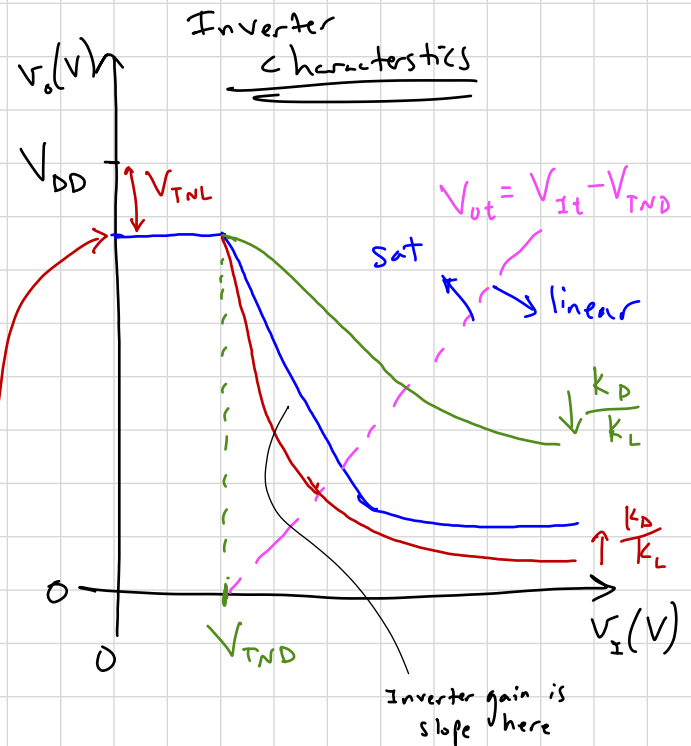
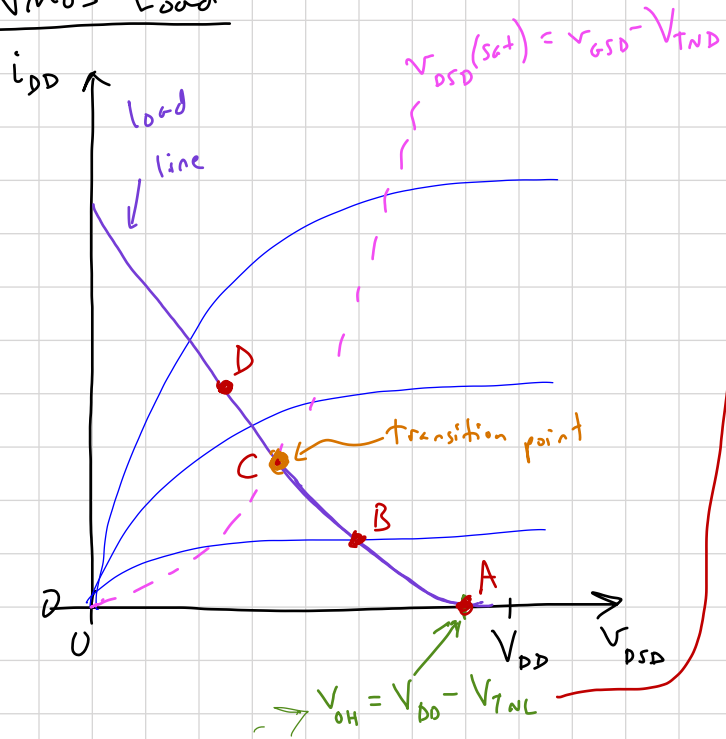
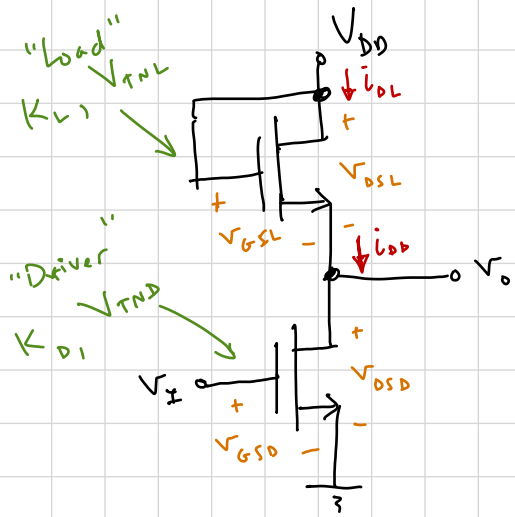
$$i_D = K_n [2(V_I - V_{TN})V_O - V_O^2]$$

then:

$$V_O = V_{DD} - R_D i_D$$

★ $\uparrow R_D$ gives much better inverter characteristics, but difficult to make in Si (takes a lot of space)

NMOS Inverter with NMOS Load



A: $V_I < V_{TND}$

→ Driver is OFF

$$i_{DL} = 0 = K_L (V_{DSL} - V_{TNL})^2$$

→ Load transistor always operates in saturation when $V_{GSL} = V_{DSL} \geq V_{TNL}$

Since : $V_{DSL} = V_{bn} - V_o$

$$V_{DSL} - V_{TNL} = V_{DD} - V_0 - V_{TNL} = 0$$

→ and max. output voltage of:

$$V_{o, \max} = V_{oH} = V_{DD} - V_{TNL}$$

D: $V_i > V_{it}$

→ still have $i_{DD} = i_{DL}$

→ Driver now in linear region

$$K_D [2(v_1 - V_{TN0})v_0 - v_0^2] = K_L (V_{DD} - v_0 - V_{TNL})^2$$

B: V_2 just $> V_{TND}$

→ At steady-state, $i_{DL} = i_{DD}$

$$K_D (V_{GSD} - V_{TND})^2 = K_L (V_{GSL} - V_{TNL})^2$$

→ expressed in terms of input/output:

$$K_D (v_1 - V_{TNH})^2 = K_L (v_{DD} - v_0 - V_{TNL})^2$$

Giving:

$$\star V_D = V_{DD} - V_{TNL} - \sqrt{\frac{K_D}{K_L}} (V_1 - V_{TNB})$$

V_1 at transition point

$$V_{DS}(sat) = V_{GS} - V_{TN}$$

or

$$V_{ot} = V_{it} - V_{TND}$$

$$V_{it} = \frac{V_{DD} - V_{TNL} + V_{TNL} \left(1 + \sqrt{\frac{K_D}{K_L}} \right)}{1 + \sqrt{\frac{K_D}{K_L}}}$$

Inverter Gain: Slope in the transition region:

$$\frac{dr_o}{dr_i} = -\sqrt{\frac{k_o}{k_L}}$$

★ Want to be ↑

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