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Roll No:

(To be filled in by the candidate)

PSG COLLEGE OF TECHNOLOGY, COIMBATORE 641 004

SEMESTER EXAMINATIONS. JANUARY 2023

MCA Semester: 1

20MX13 V DATA STRUCTURES

Time: 3 Hours Maximum Marks: 100

	INSTRUCTIONS:	-0	ے ا	~G *]		
	Answer ALL questions. Each question carries 25 Marks.						
٢	2. Course Outcome : Qn.1 CO 1	Qn.2 CO 2	Qn.3 CO 3	Qn.4 CO 4			
	A Praise	1	46				

a) What is Abstract data type (ADT)? Define String ADT.

(5)

b) How are multidimensional matrices stored in memory? Write the addressing function corresponding to each representation. A Toeplitz Matrix is a square matrix in which the elements on a diagonal are same. Following is an example of Toeplitz Matrix:

10	28	3	4	. 5 🦠
6.1	10	2	3,	₹ 4
6	6	10	Q	3
8	7	6	10	2
9	80	27	6	10

Suggest an efficient storage representation for storing Toeplitz Matrix and write the corresponding addressing function.

- c) Formulate algorithms for linear and binary search. Trace the algorithms on sample input to show the correctness of the algorithm. Write the best and worst case complexity of both algorithms. (12)
- a) What is a circular Queue? Highlight the difference between linear and circular queue.
 Write algorithms for the primitive operations on circular queue.
 - b) Write an algorithm to check the well formedness of parenthesis in an expression. Consider (,), [,], { and } types of parentheses. Trace the algorithm on the inputs {a + [((c-d) * e)] } and {a + [c-(a+d])}.
 (8)
 - c) i) Write algorithms for the primitive operations on array based stack. Formulate an algorithm converting an infix expression to postfix form using stacks. Trace the algorithm on the expression a * (b + c *d) / e.

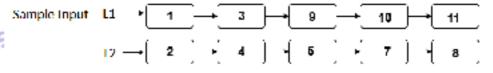
(OR)

- ii) a. What is a dequeue? What are the different types of dequeue? (4)
 - Formulate an algorithm to evaluate postfix expressions. Trace the algorithm on the expression abc*+cd-* where a = 2, b = 4, c = 2, d = 1.
- 3. a) i. Differentiate static and dynamic memory allocation. (8)
 - ii. Write an algorithm to count the number of nodes in a circularly linked list. _____(3)

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 Formulate an algorithm to merge two sorted lists. Sample input and corresponding output are shown below:



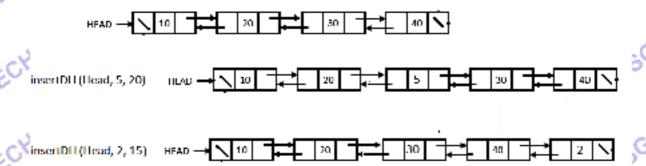
Output
$$(1) (2) (3) (4) (5) (7) (8) (9) (10) (11)$$

- TECH PSG TECH i) 1) How can a polynomial be represented using linked lists? Represent 5x⁵ + 4x⁸ + 3 using linked lists. Write an algorithm to add two polynomials represented using linked lists.
 - Represent the following sparse matrix as a linked list.

,	0	0	3	0
۱	0	0	э	8
	1	0	.3	,0
	0	0	7	0

- 1) What is a linked queue? Write algorithms for the primitive operations on linked (6)
- PSGTECH PSGTI 2) Write an algorithm insertDLL(Head, elt, value) to add elt, in a doubly linked list next to the given value if the value is present in the list. Otherwise add the element at the end of the list.

Sample inputs and outputs are shown below:

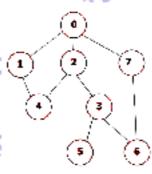


- a) Write the order in which the nodes are visited in the different types of traversals in a Binary tree. Draw the binary tree whose inorder traversal sequence is: g d h b e i a f j c and preorder traversal sequence is: a b d g h e i c f j.
 - PSG TECH PSG TECH b) i) What are the different types of representation of graphs? Give an example for the using various following types of graphs and represent them PSG TECH PSG representations :

Directed Graph, Undirected weighted graph.

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First Search (DFS) and write the traversal sequences. Indicate the data structure used in BFS and DFS. Fig 1 PSG TECH SG TECH PSG



PSG TECH PSG TECH Fig 1

c) What is hashing? Give any two hash functions. What are collisions in hashing? What are the ways of resolving collisions? Describe how elements are located with different types of collision resolution methods. How can the hash performance improved? Consider the following items to be inserted.

35, 18, 60, 89, 70, 12 and a heart of the choice and in the hash performance in the control of the choice and in the hash performance in the choice and in the hash performance in the choice and A & A of you (12) (12) PSG TECH PSG TEC PSGTECH PSGTEC 35, 18, 60, 89, 70, 12 and a hash table of size 11. Use any one hash function of your choice and illustrate all types of collision resolution techniques. (12)