

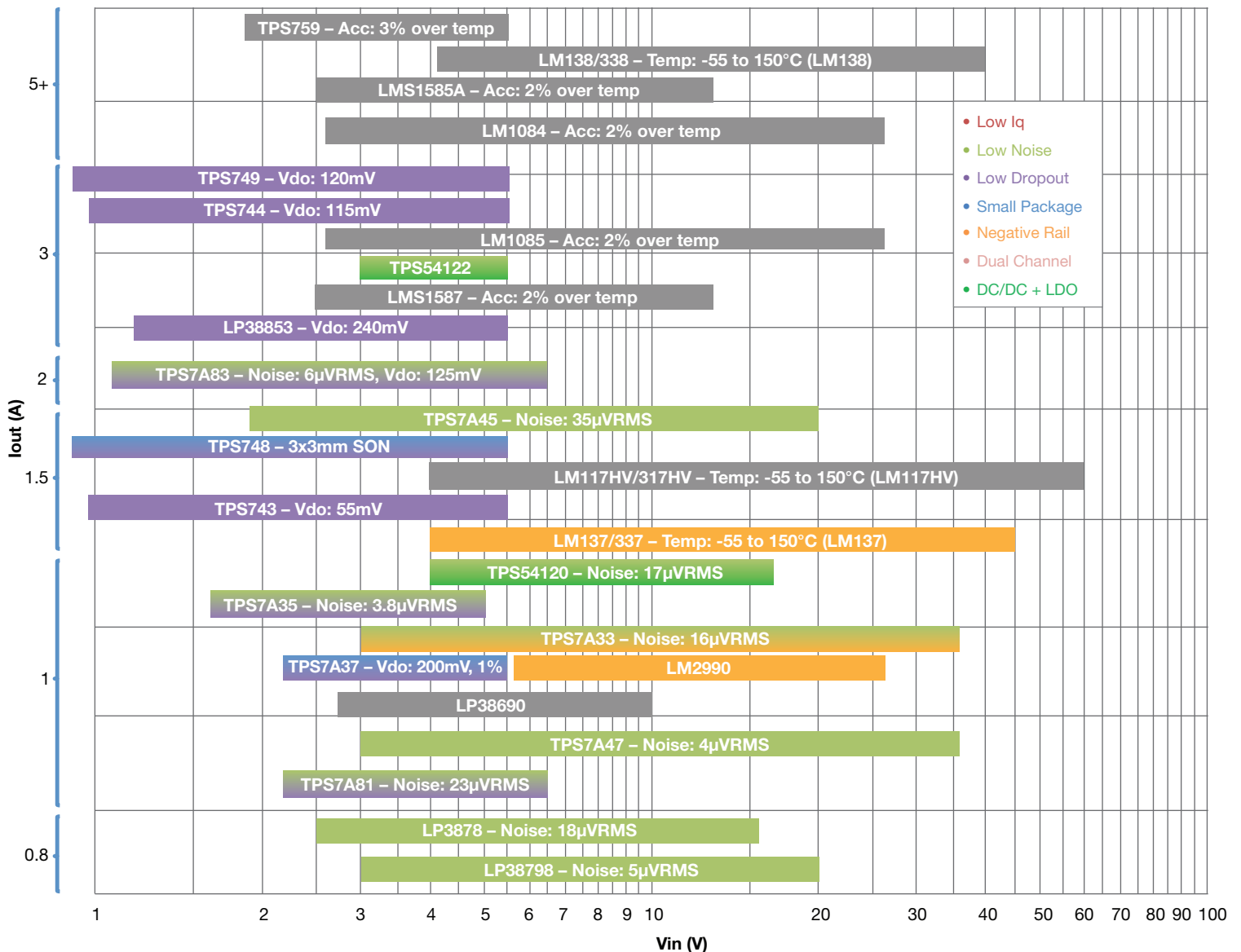
Low Dropout Regulators

Quick Reference Guide



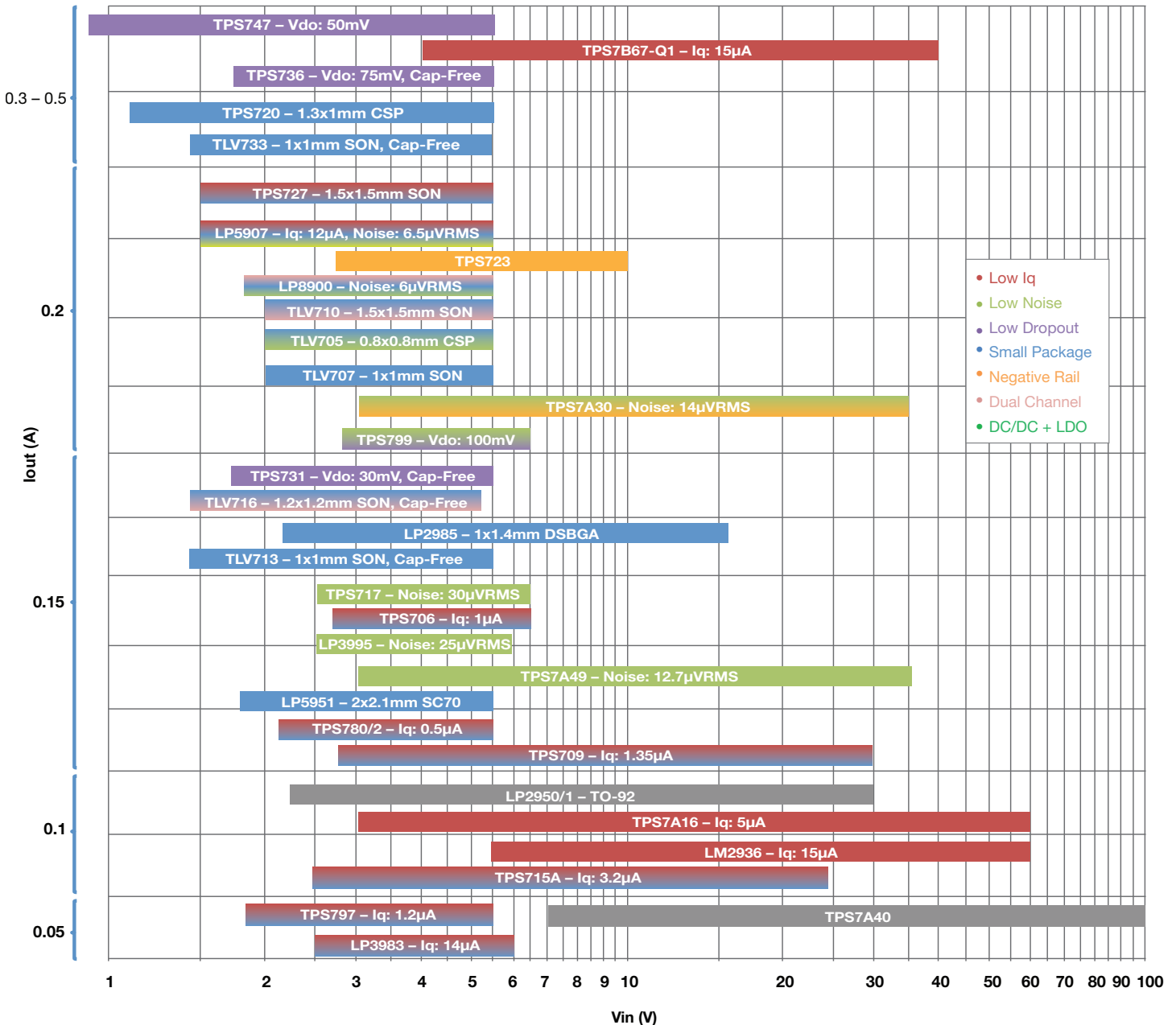
Low dropout regulators (LDOs) are a simple, inexpensive way to regulate an output voltage powered from a higher voltage input. The most critical LDO parameters are input voltage range, output voltage, output current, dropout voltage, packaging size, power dissipation capability, and noise. On this quick reference card you will find TI's most popular LDOs and linear voltage regulators for any kind of application.

To see the complete LDO portfolio:
www.ti.com/lldo



Most Critical LDO Parameters

- High PSRR (40dB+ @ 100kHz): TPS7A47, LP38798, TPS7A35, LP5907
- Low noise (<20µVRMS): TPS7A47, LP38798, TPS7A35, LP5907
- Low IQ (<15µA): TPS780/2 (0.5µA), TPS797(1.2µA), TPS709 (1.3µA)
- Low dropout voltage: TPS731 (30mV @ 150mA), TPS7A83 (125mV @ 2A), TPS744 (115mV @ 3A)
- Small packaging: TLV713 (1x1mm), LP5907 (0.65x0.65mm), TLV705 (0.8x0.8mm)
- Wide input: TPS7A16 (60V VIN), TPS7A40 (100V VIN), TPS709 (30V VIN)
- Features: Power good, cap-free operation, reverse current protection, reverse voltage protection, soft-start and output discharge



To see the complete LDO portfolio:
www.ti.com/ldo

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