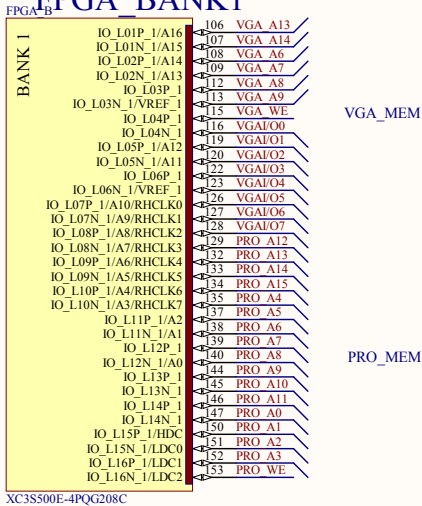
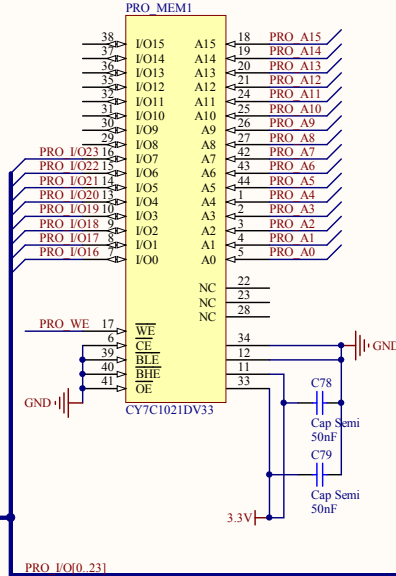


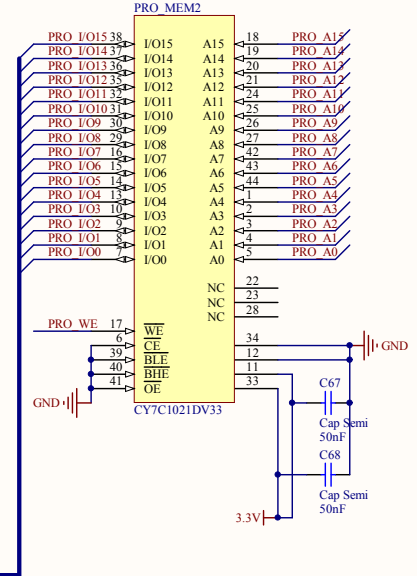
FPGA_BANK1



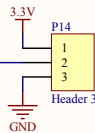
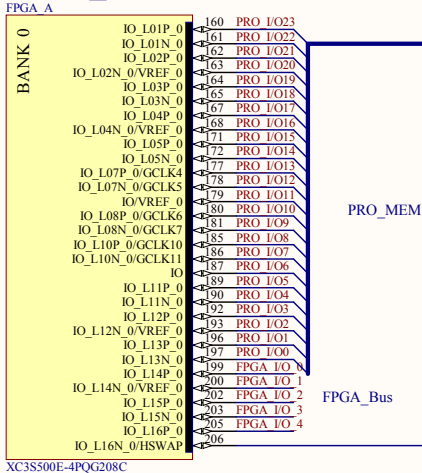
PRO_MEM_1



PRO_MEM_2



FPGA_BANK0



FPGA_Bus