

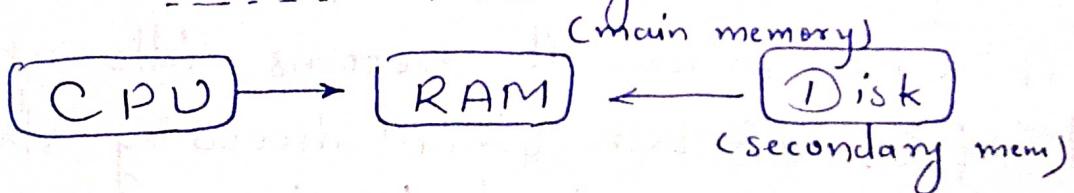
Operating System

Goals

- Easy to use / Good UI. → Primary goal
- Efficiency → Utilising max. resources. → Secondary goal

Function of Operating System

i) Process management.



Process vs Program.

↓
Program in execution ↳ Passive set of instruction

ii) Memory Management (Main memory)

Allocations & deletion of tasks on main memory. w.r.t. the address.

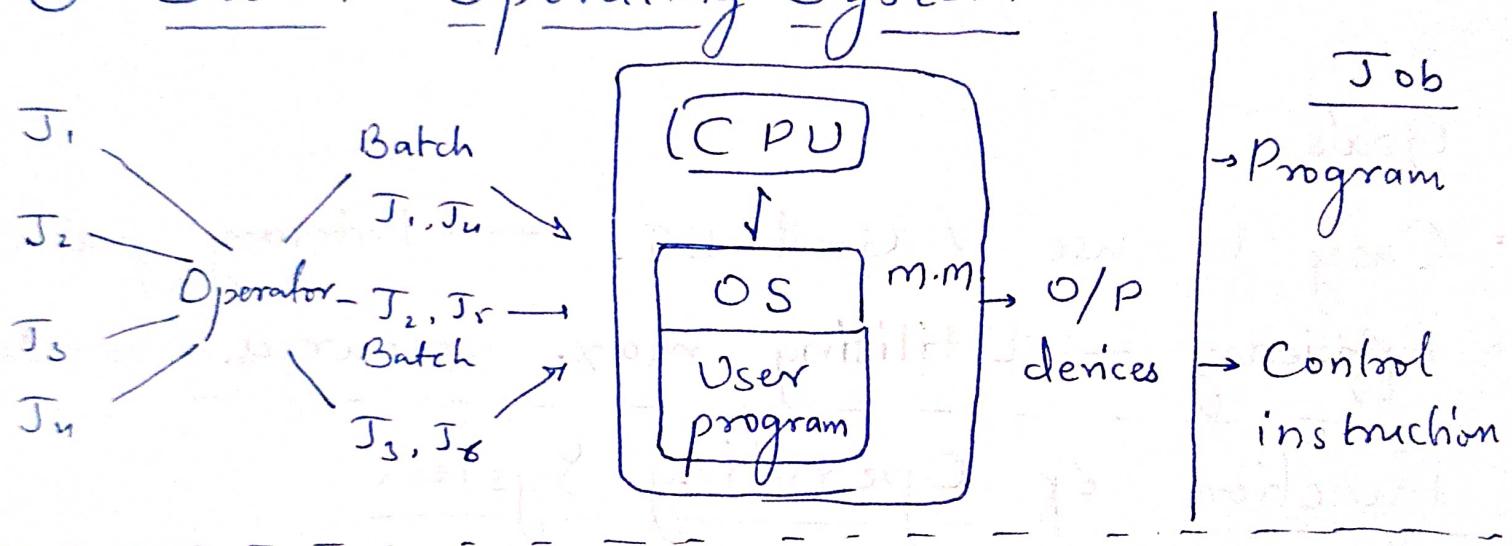
iii) File Management

iv) Secondary Memory / storage management.

- v) I/O Management
- vi) Security & Protection
- vii) Network Management

Evolution of Operating System.

① Batch Operating System



② Multiprogramming OS

Utilization of CPU when it becomes idle after I/O bound job. If we simultaneously store CPU bound job & then load it into CPU after saving its state.

③ Time sharing O.S. - (Multitasking OS)

All the multiple jobs are stored & given specific time limit to complete its task, if time limit exceed next task is executed & this job is paused. ~~So~~ Multiprogramming is still applied.

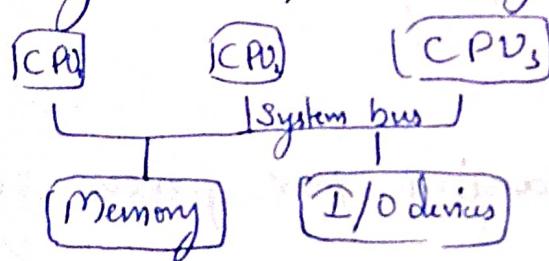
Throughput: No. of programs completed or executed per unit time.

Spooling: Arranging request for I/O or memory resources, then executing it one by one [P₁, P₂, ...]

Tightly

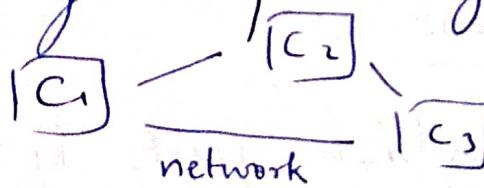
④ Multi CPU system (Multiprocessing O.S.)

i) Tightly Coupled System.



Access to I/O & memory resources is shared, it reduces time to access resources.

ii) Loosely Coupled System



Spooling is decreased

Advantages of Multi CPU system

- ↳ Increased throughput
- ↳ Increased Reliability
- ↳ Cost efficient (Tightly is cheaper than loose)

⑤ Network & Distributed OS

Network OS - Loosely Coupled System

Distributed OS When a big job is given to Network OS & they are free to distribute the job

⑥ Clustered OS

When distributed OS only shares data, (no I/O)
It may be a network of long distance system.

⑦ Real time OS

System Calls

① Process Management

- Creation & deletion of user & system process
→ `createprocess()`
- Suspend & resume of process
→ `wait()`
- It provides mechanism for process synchronisation
→ `sharedmemory()`

② Memory Management

- Keeping track of which parts of memory (main) are currently being used & by whom.
- Deciding which process are to be loaded into memory when memory space becomes available.
- Allocation & deallocation of memory spaces.

③ File Management

- Creation & deletion of directories.

- Supporting primitives for manipulating files & data.
- Mapping files into secondary storage.
- Backing of files in stable storage media.

(4) I/O device Management

- Spooling - Storing I/O in disk till its completion in swap space (Multiprog.. OS)
- A memory management component that includes buffering, caching, spooling.
- A general devices driver interface.
- Diverse Drivers for specific hardware devices.

(5) Secondary Storage Management.

- Free space management
- Disk scheduling.
- Storage Allocation

(6) Networking

(7) Protection & security.

- Authentication -
- Authorization - Giving or removing permission to user to access certain resources.

⑧ Command Line Interface.

→ Graphical User Interface (GUI).

Operating System Services

◆ User services : Authorisation given to user program to increase convinience, etc.

→ Program execution.

→ I/O access.

→ File system management

→ Communication

→ Shared memory format - two or more program share memory & can communicate/transfer data through that space.

→ Message passing - A packet is made with address of sender/reciver & data to comm.

→ Error detection.

◆ System Services :

→ Resources Allocation.

→ Protection & Security.

→ Accounting.

◆ System Calls

→ Interface bet user & operating system.

→ Assembly language was used initially

① Process Control

- Load, execute.
- abort, end
- Create, delete.
- get / set process attribute.
- wait event, signal event.

② File management

③ Device management.

- Read, write, reposition
- Request, release device.
- Information maintenance
- get / set system data.

④ Communication

- Open & closed connection.
- Create / Delete connection
- Send message & receive message.
- Transfer user status info.

UNIT - II Process Management

Program : Passive entity which contains set of text section.

Program Counter : A register that contains the address of the instruction being executed at the current time & it stores next instruction as well.

Process : - - - processor register program counter, stack - local variable, func parameter, data data-section. - global variable..

Attributes

- ① Process ID → Initialization etc.
 - ② Program counter
 - ③ Process State
 - ④ Pointer
 - ⑤ CPU Registers → stores data when program 1/2... are not in execution.
 - ⑥ Memory location management -
 - ⑦ I/O status : list of I/O devices allocated to process information
 - ⑧ Accounting.
- Attributes are stored in PCB / TCB
Process Control Block.

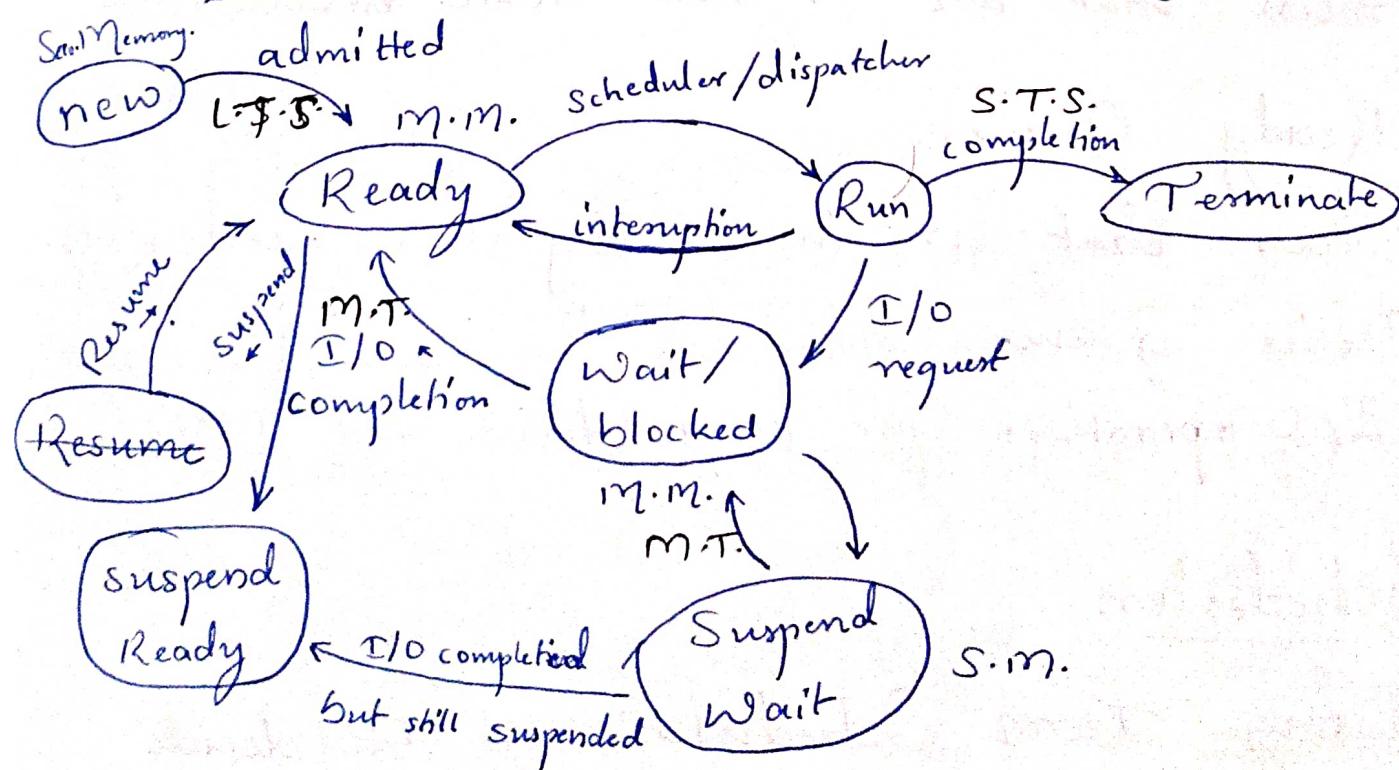
P-CB / T-CB (Context of the process)

Process ID	Process State.
Program Counter	
CPU Registers	
Memory limits	
list of open files.	

Context Switching.

When CPU switches from P_1 process to P_2 , it stores data/context of P_1 in PCB, & loads data/context of P_2 from PCB.

Process State Transition Diagram.



I/O operation can be done from Secondary Mem

When some important process that has to be performed.

- 1) To make space wait process for I/O operation is suspended.
- 2) Else ~~Ready~~ wait process is suspended
- 3) wait process perform I/O operation from sim it gets completed & it resume from suspend ready to ready state

Scheduling Queue

① Job Queue

(Queue is implemented as linked list)

Process that are in new state need J.Q.

② Ready Queue.

Process that are in ready state need R.Q.

③ Device Queue.

I/O operations for every devices need D.Q.

⇒ Schedulers

① Long Term Scheduler - It decide degree of Multi-programming (no. of programmes in memory)

② Short Term Scheduler - Decides which program to load from ready to run state.

S.T.S. takes upto 10 ms of time to decide.

Dispatcher - Loads & save PCB from Ready state. (Also switches User mode to Kernel mode vice versa)

If 100 ns is time quantum.

$$\left(\frac{10}{100+10}\right) \times 100 \rightarrow 10\% \text{ of time is extra required}$$

Operations on Process

① Process Creation.

② Process Termination.

Process create multiple child processes to complete task.

i) Address are different

In this data set is different of C & P.

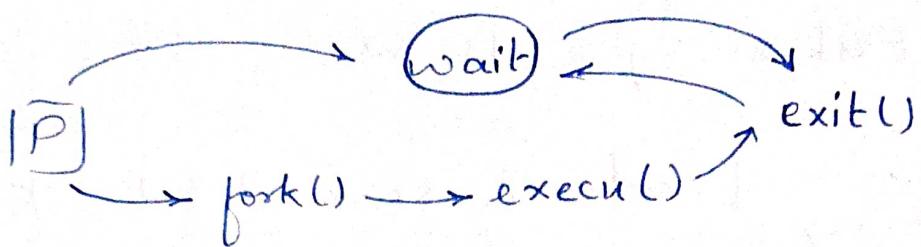
ii) Address is duplicate.

a) Resource are shared. (queue system to get access to resources)

b) C & P perform different tasks & don't share resources.

Parent process can't be terminated before all child process are not completed.

- Cascading - When parent process aborts all the child start to terminated from last to parent



When ^{process} parent execute last statement & asks the system to delete it using 'exit()' by system call

It returns its data to its parent

Process resources are deallocated.

Parent process may terminate the execution of child process using `abort()` system call

⇒ Abort system call:

- i) Child has exceeded the usage of resources allocated.
- ii) Task assigned to child is no longer required.
- iii) The parent is exiting & the operating system doesn't allow child to continue its process.

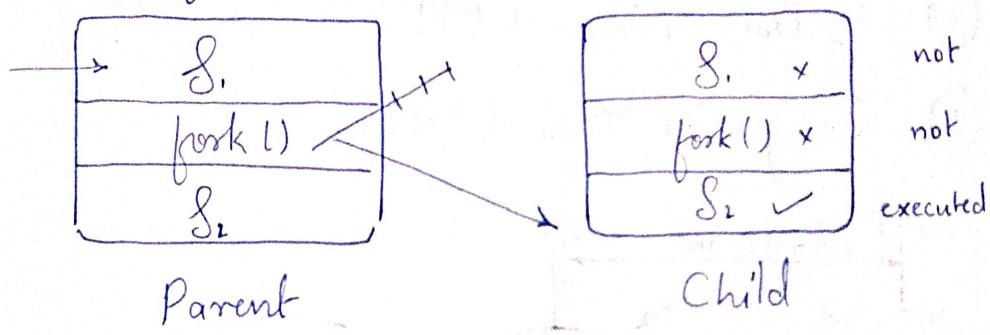
Cascading Termination takes place

fork() - system call

- (1) Used to create new process.
- (2) No argument is passed in fork().
- (3) After child exit(), fork() return +ve value to 'P' & 0 to 'C'.

→ If there is some problem

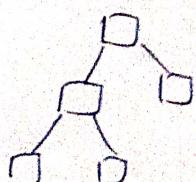
fork() return -ve value to both 'P' & 'C'.



f. main() {
 fork(); pri
 printf("CSE") → 2 CSE
 fork()
 printf("student") 4 student

→ If no conditional statement
n - no. of fork().
Total no. of cprocess = 2^n
Total no. of e. process = $2^n - 1$.

f. main() {
 if (fork() || fork())
 fork()
 printf("Hello")



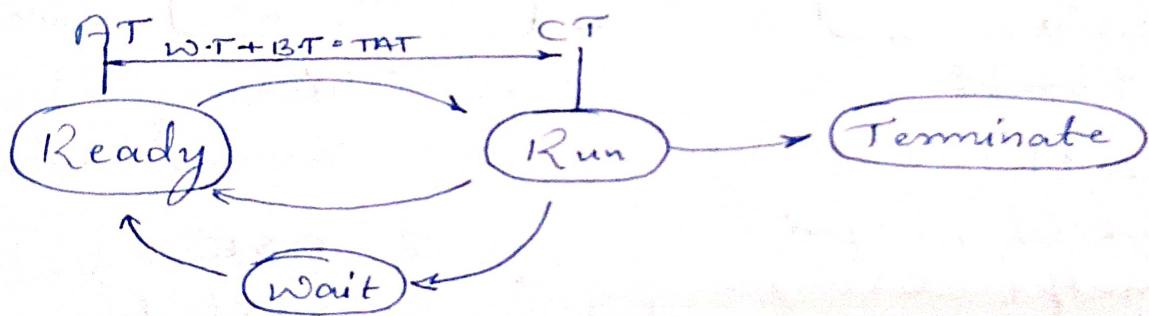
```

Q. main() {
    if(fork() & & fork())
        fork()
    printf("Hello")
}

```

CPU Scheduling Terminologies

- ① Arrival Time (AT)
- ② Burst Time (BT)
- ③ Completion Time (CT)



- ④ Turn Around Time (TAT) - $W.T + B.T$
- $C.T. - A.T.$
- ⑤ Waiting Time (WT) - Time spent by program in ready queue (at the start + wait).
- ⑥ Response Time (R.T.) = first allocated to C.P.U. - A.T.

❖ Criteria

- ① CPU utilization ↑
- ② throughput ↓
- ③ Average TAT
- ④ Average WT
- ⑤ Average RT

F.C.F.S. (First Come First Serve).

Criteria - Arrival Time.

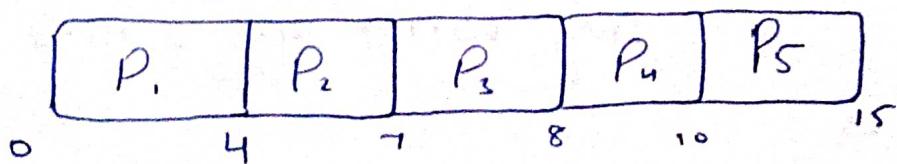
Non-Preemptive - One scheduler.

Easy to implement.

❖ CPU Scheduling Algorithm.

P. no.	A.T.	B.T.	C.T	TAT	WT	R
P ₁	0	4	4	4	0	0
P ₂	1	3	7	6	3	3
P ₃	2	1	8	6	5	5
P ₄	3	2	10	7	5	5
P ₅	4	5	15	11	6	6

→ GANTT CHARTS (Job Sequencing)



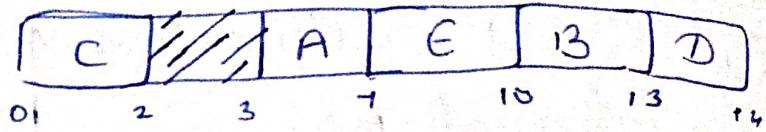
$$\text{Average T.A.T.} = \frac{34}{5} = 6.8$$

$$\text{Average W.T.} = 3.8$$

$$\text{Avg. R.T.} = 3.8$$

P.no. A.T. B.T.

A	3	4
B	5	3
C	0	2
D	5	1
E	4	3



P.no. A.T. B.T. C.T. TAT W.T. R.T.

P. ₁	0	24	24	24	0	0
P. ₂	0	3	27	27	24	24
P. ₃	0	3	30	30	27	27

P. ₁	P. ₂	P. ₃
0	24	27

$$\text{Avg. W.T} = 17$$

$$\text{Avg. TAT} = 27$$

P.no. A.T. B.T. C.T. TAT W.T. R.T.

P. ₁	0	20	20	20	0	0
P. ₂	1	2	22	21	19	19
P. ₃	1	1	23	22	21	21

P. ₁	P. ₂	P. ₃
0	20	22

$$\text{Avg. W.T} = \frac{40}{3} = 13.3$$

P.no. AT B.T. C.T. TAT W.T.

P. ₁	1	20	23	22	2
P. ₂	0	2	2	2	0
P. ₃	0	1	3	3	2

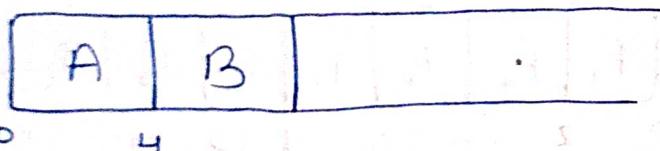
- o Conroy Effect : When a program with larger burst time increases the waiting time.

Shortest Job First (SJF).

g. S=1 (Context loading time)

P.no. A.T. B.T.

A	0	3
B	1	2
C	2	1
D	3	4
E	4	5
F	5	2



g. P.no. AT BT

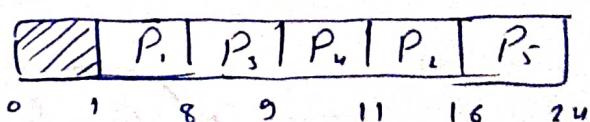
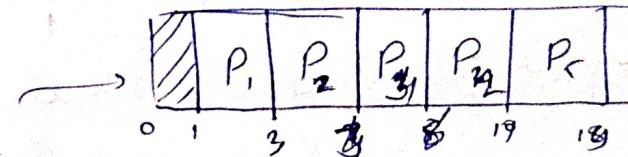
P₁ 1 7/2

P₂ 2 5

P₃ 3 1

P₄ 4 2

P₅ 5 8



	P.no.	AT	B.T.	W.T.	P.no.	HT	WT	WT
P ₁	0	20	0	0	P ₁	2	20	20
P ₂	1	1	20	20	P ₂	0	1	0
P ₃	2	1	21	21	P ₃	1	1	0

P ₁	P ₂	P ₃
0	20	21

$$AWT = \frac{20+21}{3}$$

P ₂	P ₃	P ₁
0	1	2

$$AWT = \frac{22}{3}$$

Round Robin Algorithm.

→ Preemptive Algo.

P. no.

A.T.

B.T.

1

0

2 4

2

1

3 8

3

2

2

4

3

1

5

4

2 8

6

5

2 3

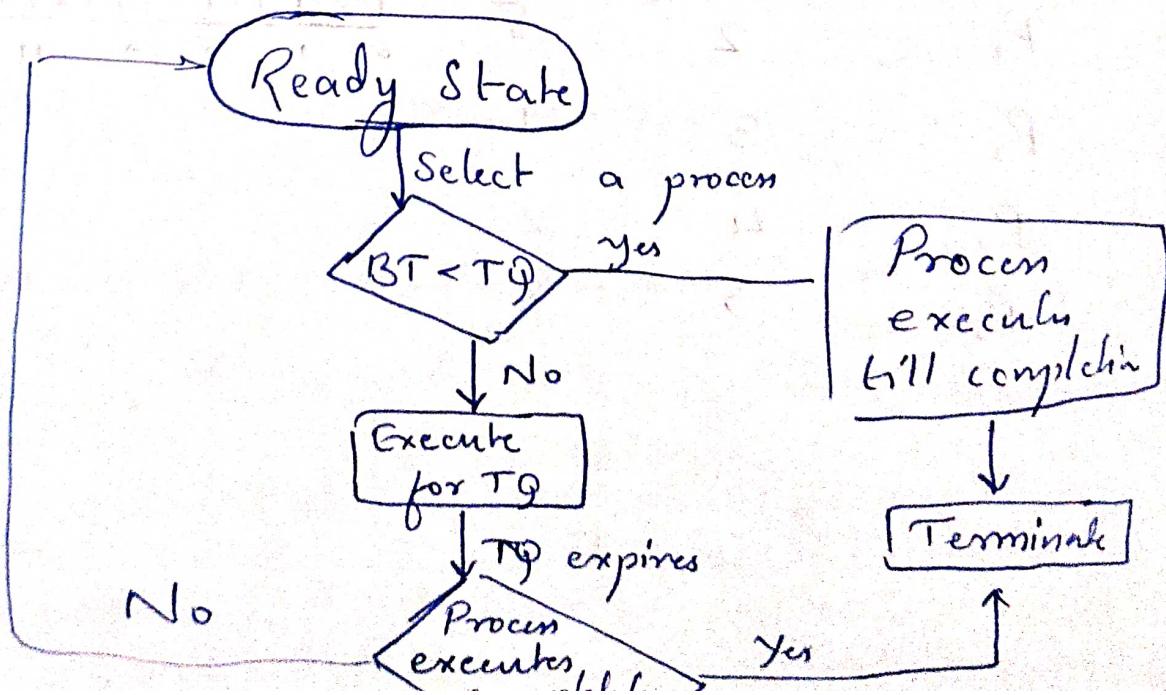
P ₆	P ₅	P ₂	P ₅	P ₂	P ₆	P ₅
15	17	18	15	17	18	19

Ready Queue : P₁, P₂, P₃, P₄, P₅, P₆

P₅, P₂, P₈, P₃

TQ²
B.T.

8
18
6
9
2
19



$TQ = 4$	P_1	P_2	P_3	P_4	P_5	P_2	P_6	P_5	P_1
0 4 8 10 11 15 16 19 21									

Ready Queue: $P_1, P_2, P_3, P_4, P_5, P_2, P_6, P_5$

f. $TQ = 3$.

Pno. AT BT

1	5	48
2	4	36
3	3	147
4	1	368
5	2	2
6	6	8

	P_4	P_5	P_3	P_2	P_4	P_1	P_6	P_3	P_2	P_4	P_1	P_3
0 1 4 6 9 12 15 18 21 24 27 30 32 33												

Ready Queue: $P_4, P_5, P_3, P_2, P_4, P_1, P_6, P_3, P_2, P_4, P_1, P_3$

f. AT BT

P_1 0 284

P_2 0 X

P_3 0 8

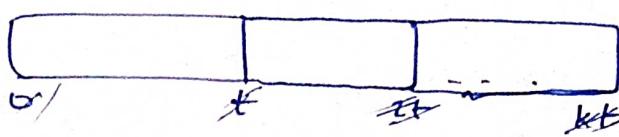
P_4 0 X

what is C.T. of P_1 Ready Queue: $P_1, 2, 3, 4, 5, 3$
when $TQ = 1$.

P_1	P_2	P_3	P_4	P_1	P_3	P_1	P_3	P_1
0 1 2 3 4 5 6 7 8								

C.T. for $P_1 = 9$

Q. If s units what must be TQ such that the no. of context switches are reduced but at the same time each process is guaranteed to get the turn for $\frac{t}{n}$ seconds (in process)



$$\frac{kt + ns}{8}$$

$$t \leq n \cdot s + (n-1)g$$

$$g > \frac{t - ns}{(n-1)}$$

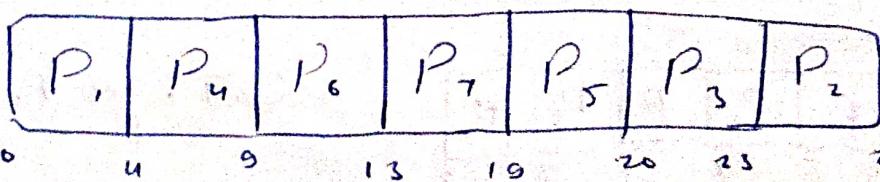
~~Priority Queue~~

Priority Scheduling.

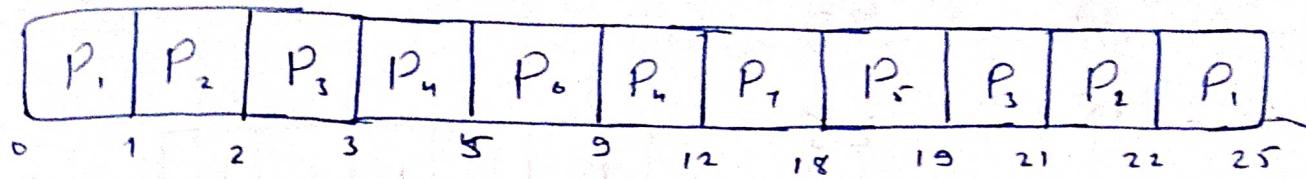
P. no.	Priority	A.T.	B.T.	C.T.	TAT	W.T.	R.T.
--------	----------	------	------	------	-----	------	------

P ₁	2	10	4	25	25	21	0
P ₂	4	1	2	22	21	19	0
P ₃	6	2	3	21	19	16	0
P ₄	10	3	5	12	9	4	0
P ₅	8	4	1	19	15	14	0
P ₆	12	5	4	9	4	0	0
P ₇	9	6	6	18	12	6	6

Non-preemptive

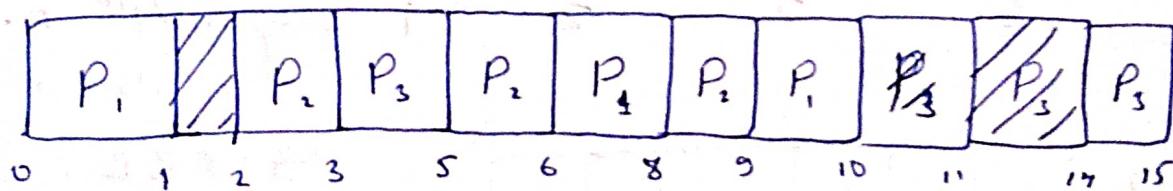


Preemptive



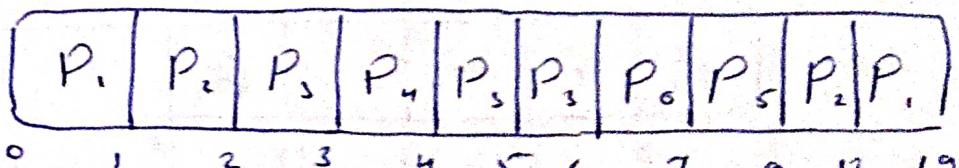
f. Preemptive Priority.

P no.	AT.	Prio	CPU	I/O	CPU
P ₁	0	2	1	8 _u	3 _s
P ₂	2	3 (L)	3 ₂	3	1
P ₃	3	1 (H)	2	3	1



◊ Shortest Remaining Time First. (SRTF).

Pno.	AT.	BT.
P ₁	0	7
P ₂	1	5
P ₃	2	3
P ₄	3	1
P ₅	4	2
P ₆	5	1



J. P. no. A.T. B.T.

P_1 0 20

P_2 15 25

P_3 30 10

P_4 45 15

P_1	P_2	P_3	P_4	P_5
-------	-------	-------	-------	-------

0 20 30 40 50 60 70

J. P. no. AT BT I/O BT { In this case
A.T. is absolute

P_1 0 3 2 2

P_2 0 2 4 1

P_3 2 1 3 2

P_4 5 2 2 1

Ready Queue
prefen $\frac{P_1}{P_2, P_3}$ $\frac{A.T.}{P_4}$

$P_2 - 6$
 ~~$P_3 - 6$~~
 $P_4 - 8$

P_2	P_3	P_1	P_2	P_4	P_3	P_5	P_1
-------	-------	-------	-------	-------	-------	-------	-------

0 2 3 6 7 9 11 12 14

RQ - P_2, P_3, P_4

$P_4 - 11$

P_2	P_3	P_1	P_2	P_4	P_3	P_5	P_1
-------	-------	-------	-------	-------	-------	-------	-------

2 3 6 7 9 11 12 14

P_2	P_1	P_4	P_3	P_5	RQ
-------	-------	-------	-------	-------	------

7 9 11 13 15 16

◇ Longest Job First (LJF)

P.	P. no.	A.T	B.T	P. ₁	P. ₂	P. ₃	P. ₄	P. ₅	R. ₁
P.	0	0	3 ₂						
P. ₁	1	1	2						Preemptive.
P. ₃	2	2	4 ₃						
P. ₄	3	3	5 ₄						
P. ₅	4	4	6						Non-Preemptive, Round robin

◊ Longest Remaining Time First (LRTF).

P. no.	A.T.	B.T.	P. ₁	P. ₂	P. ₃	P. ₄	P. ₅	P. ₂	P. ₁
P. ₁	1	2		1	2	3	4	12	17
P. ₂	2	4							
P. ₃	3	6						7	8
P. ₄	4	8						9	10

◇ Highest Response Ratio Next (HRRN)

Criteria $\text{Ratio} = \frac{W.T. + B.T.}{B.T.}$ $\left\{ \frac{W + S.S.}{S} \right\}$

HRRN not only favours Shorter Job but also limits the W.T. of larger Job

(Nature: Non- Preemptive)

Q. P no. A.T. / B.T.

P.	P ₁	P ₂	P ₃	P ₄	P ₅
P.	0	3			
P ₁	2	6			
P ₂	4	4	$R.R_1 = \frac{(9-4)+4}{5} \cdot 2 \cdot 25$	$R.R_3 = \frac{(9-6)+5}{5} \cdot 2 \cdot 25$	
P ₃	6	5			
P ₄	8	2	$R.R_2 = \frac{(13-6)+5}{5} \cdot 2 \cdot 4$	$R.R_4 = \frac{(13-8)+2}{2} \cdot 2$	

Process Synchronization

◇ Cooperating Processes.

Process are dependent on each other to complete a task.

◇ Independent Process.

Process that can't affect or be affected by ^{other} processes.

Need for P.S.

- ① Modularity
- ② Information Speedup.
- ③ Computation speedup.
- ④ Convenience

Producers - Consumer Problem

Bounded buffer.

```
#define BUFFERSIZE = 10
typedef struct {
    item item[BUFFERSIZE];
    int in = 0;
    int out = 0;
} buffer;

while(1) {
    counter = 0;
    while (((in+1) % BUFFERSIZE) == out)
        ; // do nothing
    buffer[in] = next produced;
    in = (in+1) mod BUFFERSIZE;
    counter++;
}

while(1) {
    counter = 0;
    while (in == out)
        ; // do nothing.
    next consumerd = buffer[out];
    out = (out+1) mod BUFFERSIZE;
    counter--;
}
```

Race Condition

- A situation where several processes manipulate the data & the outcome of the execution depends on particular order in which the access takes place.

Critical Section Problem.

```
do {  
    [entry cond]  
    critical section  
    [exit cond]  
    Remainder section.  
} while (1);
```

Soll criteria for Critical Section Problem.

- ① Mutual Exclusion.
- ② Program
- ③ Bounded Waiting.