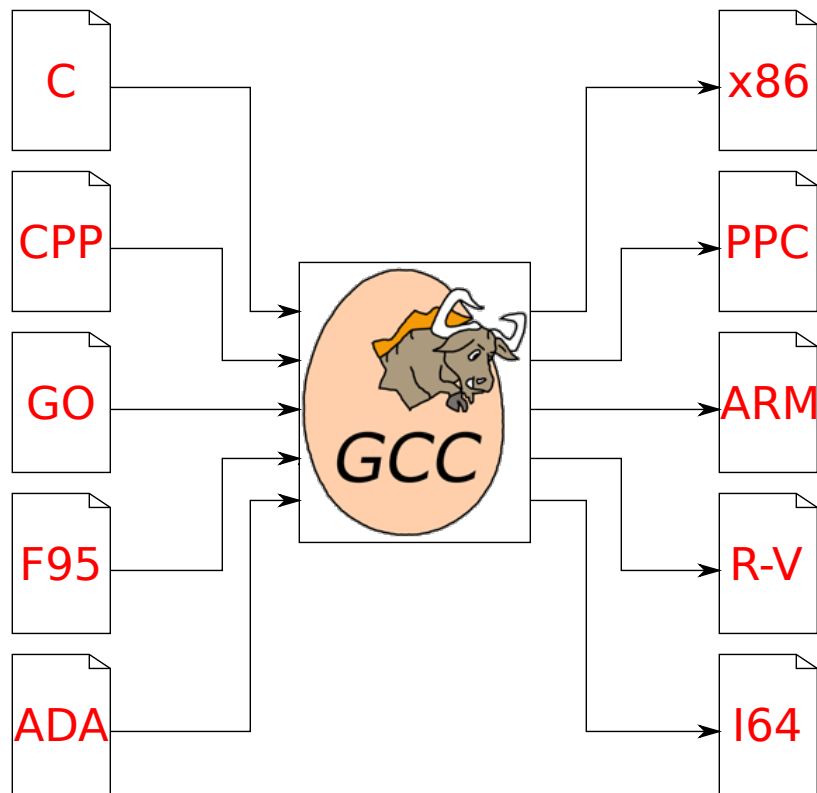


Parallelizing GCC with Threads



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1 About Me

Currently pursuing a Master's Degree in Computer Science at the University of São Paulo with a Computer Science Bachelor's degree in the same institution. I've always been fascinated by topics such as High-Performance Computing and Code Optimization, having worked with a parallel implementation of a Boundary Elements Method software in GPU, and I am currently conducting research in compiler parallelization. Therefore, I am applying to the GNU Compiler Collections (GCC) both as a research project in parallelization of compilers, and mainly to become a part of the free software community.

Skills: Strong knowledge in C, Concurrency, Shared Memory Parallelism, Vim, and command line utilities (grep, sed, ...)

1.1 Contributions to GCC

I've submitted some patches, mainly adding inline optimizations to trigonometric functions. These kind of patches requires deep testing to guarantee that the optimization did not yield severely incorrect results, which concluded in a blog post about the patch *Optimize $\sin(\arctan(x))$* ¹. I did this blog post both to register how to add these new kinds of optimizations, and also to encourage newcomers to contribute to GCC.

Name	Status
Optimize $\sin(\arctan(x))$	Accepted
Optimize $\sinh(\operatorname{arctanh}(x))$	Accepted
Fix typo 'exapnded'	Accepted
Split 'opt and gen' variable	Working
Update $\sin(\arctan(x))$ test	Waiting Stage1
Fix PR89437	Wilco Dijkstra version accepted

2 Parallelization Project

While looking for topics in compiler's field that touched subjects that I am interested in for the subject of my master's thesis, I found this parallelization project in GSoC 2018. With this in mind, I started a discussion in the mailing list to understand what this project is about, which was parallelizing the GCC internals to be able to compile big files faster, which got my interest². This was way before the list of GSoC accepted organizations.

As stated in PR84402³, there is a parallelism bottleneck in GCC concerning huge files, with hundreds of thousands of lines of code. In the course of the discussion, Bin Cheng⁴ reported that he is facing a similar issue with his project, stating that parallelizing the compiler may solve his problem. These topics supports the interest in the community for this project.

¹<https://flusp.ime.usp.br/updates/2019/03/26/making-gcc-optimize-some-trigonometric-functions/>

²<https://gcc.gnu.org/ml/gcc/2018-11/msg00073.html>

³https://gcc.gnu.org/bugzilla/show_bug.cgi?id=84402

⁴<https://gcc.gnu.org/ml/gcc/2018-12/msg00079.html>

2.1 Current Status

in PR84402, Martin Liška posted a graphic showing the existence of parallelism bottleneck in GCC compilation due to huge files such as `gimple-match.c` in a 128-cores machine. He also posted a amazing patch to GNU Make to collect the data and a script to plot the graphic, which I used to reproduce the same behaviour in a 64-cores machine that is available in my university.

Unfortunately, I found this approach not easy to reproduce, as it requires compiling and installing a custom version of Make, generated gigabytes of data which requires parsing by a script that often crashed due to a very large SVG. With this in mind, I created a set of tools⁵ using a complete distinct approach, generating less data, is more stable, and plotted better graphics, such as the one in Figure 1.

I also explored the GCC codebase in order to find the performance bottleneck for such huge files. Analyzing the most time-consuming file in GCC's project (`gimple-match.c`), I found that the method `finalize_compilation_unit` of class `symbol_table` takes around 50s to compile with a `-disable-checking` GCC under `-O2`, with the `expand_all_functions` routine taking most of this time. Currently, my strategy is to try to parallelize this part of the compilation, as it seems possible because I've already made GCC output functions in reverse orders.

Furthermore, I am also studying the theoretical background behind `GIMPLE`, and `cgraphs`. I have read the `GIMPLE` documentation⁶, and I am looking for how `cgraph` works internally, both in theory and in GCC.

2.2 Planned Tasks

Currently, my plan consists in three high-level topics, but more details will be provided as I proceed into the project. These topics are:

1. *Document the global variables and states.* The `cgraph` clearly was not implemented with this kind of parallelism in mind, as it has a lot of global states (i.e., RTL initialization, possible node dependency, Garbage Collector, ...). This has to be rewritten into something that could be handled locally for each thread, or insert locks.
2. *Develop a parallel prototype.* GCC supports a lot of systems with different threading schemes. Discussion in the mailing list suggested the use of pthreads and C++11 threads, however, this may not be the easiest way to develop parallel software and I may use OpenMP to accelerate the development.
3. *Refactor the parallel prototype.* Here the aim is to improve the previous implementation with proper cross-platform support, and remove any OpenMP code.

⁵<https://github.com/giulianobelinassi/gcc-timer-analysis>

⁶<https://gcc.gnu.org/onlinedocs/gccint/>

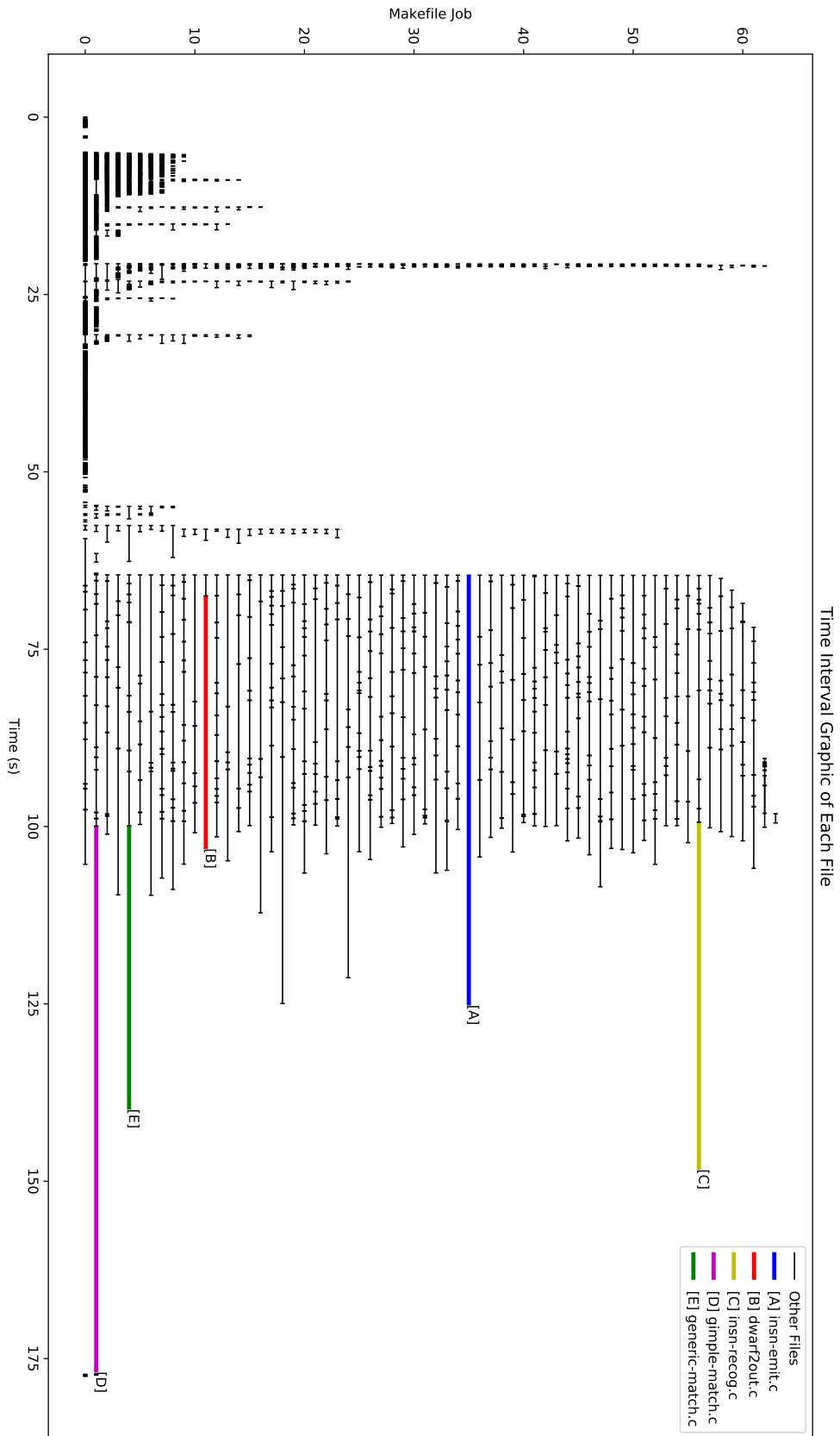


Figure 1: Elapsed time analysis in GCC compilation for a 64 cores machine, No bootstrap