

Zero Crossing Detection Using Comparator Circuit

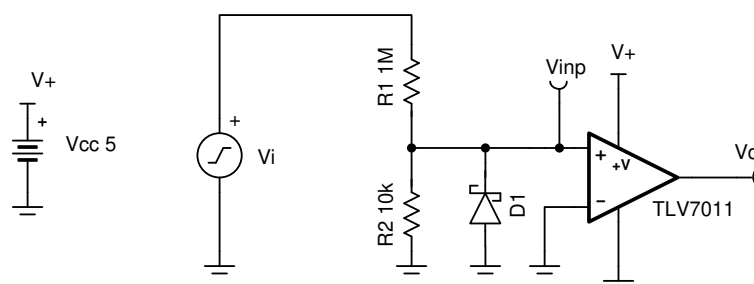


Design Goals

| Supply | | | Input Signal | | MAX AC Mains Leakage Current |
|----------|----------|--------|--------------|-------|------------------------------|
| V_{cc} | V_{ee} | Type | V_i | f | I_{ac} |
| 5 V | 0 V | Single | 240 V AC RMS | 50 Hz | <500 μ A |

Design Description

The zero crossing detector circuit changes the comparator output state when the AC input crosses the zero reference voltage. This is done by setting the comparator inverting input to the zero reference voltage and applying the attenuated input to the noninverting input. The voltage divider R_1 and R_2 attenuates the input AC signal. The diode D_1 is used to insure the noninverting input never goes below the negative input common mode limit of the comparator. Zero crossing detection is often used in power control circuits.



Design Notes

1. Some hysteresis should be used to prevent unwanted transitions due to the slow speed of the input signal.
2. Select a comparator with a large input common mode range
3. The phase inversion protection feature of the TLV7011 can prevent phase reversal in situations where the input goes outside of the input common mode limits
4. A diode should be used to protect the comparator when the input goes below the negative input common mode limit.

Design Steps

1. Calculate the peak value of the input signal.

$$V_p = V_{RMS} \times \sqrt{2} = 340V$$

2. Select the resistor divider to attenuate the input 340 V signal down to 3.4 V in order to be within the positive common range of the comparator.

$$340V \times G = 3.4V$$

$$G = 0.01 \frac{V}{V}$$

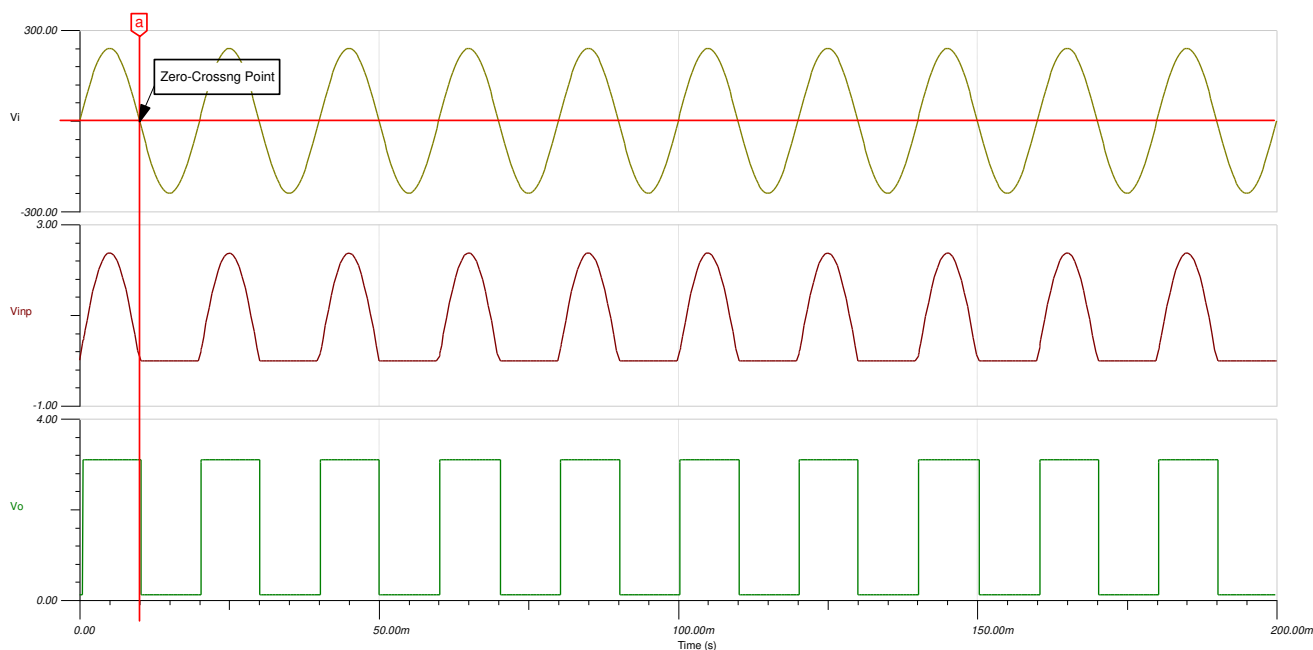
$$\left(\frac{R_2}{R_1 + R_2} \right) = 0.01$$

3. Select R_1 as 1M Ω and R_2 as 10 k Ω (the closest 1% value).
4. Select the diode, D_1 , in order to limit the negative voltage at the noninverting input. A zener diode with a voltage rating of 0.3 V can be used.
5. Calculate the AC mains leakage current to check if it meets the leakage current design goal of less than 500 μA .

$$I_{ac} = \frac{V_p}{R_1} = 340\mu A$$

Design Simulations

Transient Simulation Results



Design References

See [Analog Engineer's Circuit Cookbooks](#) for TI's comprehensive circuit library.

See circuit spice simulation file, [SBOMAP5](#).

For more information on many comparator topics including hysteresis, propagation delay and input common mode range please see, [TI Precision Labs](#).

Design Featured Comparator

| TLV7011 | |
|-------------------------|--------------|
| V_{SS} | 1.6 to 5.5V |
| V_{inCM} | Rail-to-rail |
| t_{pd} | 260ns |
| V_{OS} | 0.5mV |
| V_{HYS} | 4mV |
| I_q | 5 μ A |
| Output Type | Push-Pull |
| #Channels | 1 |
| TLV7011 | |

Design Alternate Comparator

| TLV3201 | |
|-------------------------|----------------|
| V_{SS} | 2.7 V to 5.5 V |
| V_{inCM} | Rail-to-rail |
| t_{pd} | 40 ns |
| V_{OS} | 1 V |
| V_{HYS} | 1.2 mV |
| I_q | 40 μ A |
| Output Type | Push-Pull |
| #Channels | 1 |
| TLV3201 | |

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