



LPCXpresso55SXX.PrjPcb

Elaborated by: Eduardo Teofilo

Checked by: José Ogando

Revision: A2

Last revision date: 18/06/2019

Change Log

25-04-2018	<ul style="list-style-type: none">- Power supply of Link2 circuitry replaced from 3.3v to 2.5v- Package of LPC5500 changed from BGA to HLQFP
08-05-2018	<ul style="list-style-type: none">- Added 330hms Resistor for DP and DM pins of USB0 FS- Added TestPoints for U2 debug signals- Tied P0_5 to P7 Pin7- SD0 Pull-ups tied to VDD_SD- Added Pull-up for P0_5-ISP1- Added notes and descriptions- Added jumper to P25- Added connections to P19- Added debug probe for U2- Removed P22 Debug probe for U1- Changed Y3 form 12MHz to 16Mhz
16-05-2018	<ul style="list-style-type: none">- Removed external battery support- Added Support for 1.1v external power supply
21-05-2018	<ul style="list-style-type: none">- Exchange of functions between PIO1_5 and PIO1_7
25-05-2018	<ul style="list-style-type: none">-Changed function of PIO0_15, PIO0_18, PIO0_22, PIO0_28 and PIO1_13-Added Capacitors C97, C100
31-05-2018	<ul style="list-style-type: none">-Fixed values of R82, R86-Changed PN of U8, U9, U10-Removed U4-Changed P14 2X1 to 2X2 header
1-06-2018	<ul style="list-style-type: none">-Removed U17
6-06-2018	<ul style="list-style-type: none">- Added P28
29-08-2018	<ul style="list-style-type: none">- Changed U10 XC6504A251MR-G to XC6223H251MR-G
05-11-2018	<ul style="list-style-type: none">-Added P29, R85
05-11-2018	<ul style="list-style-type: none">-Added C2-L10 Replaced from CBC2518T4R7M to TDK MLZ2012M4R7HT000
15-11-2018	<ul style="list-style-type: none">-R120 and R121 were changed from 100K to 2.2k-Fix SDA connections-Updated Revision to A1
29-05-2019	<ul style="list-style-type: none">-Added LPC55S69 and LPC55S28 variants-Changed J11, J12, R73, R114, R128 to DNI-Restore P20 to variants-Changed R74 from 100K to 330K-Added variants to silk screen-Updated Revision to A2

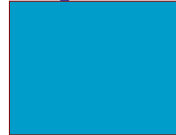
Bridge Accelerometer
BRIDGE-ACCELEROMETER.SchDoc



USB Power
LPCXpresso55SXX POWER-USB.SchDoc



Link2 Power
LINK2 LPC4322-USB-BRPWR.SchDoc



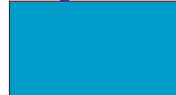
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CODEC.SchDoc



MCU
LPCXpresso55SXX MCU.SchDoc

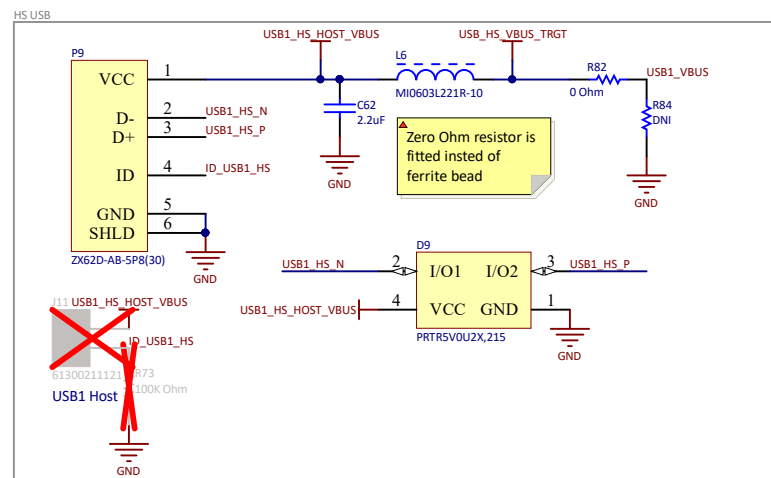


Link2 Debug
LINK2 LPC4322 PERIPH-DEBUG.SchDoc

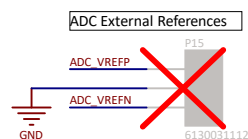
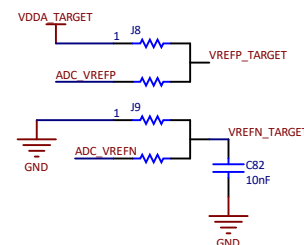
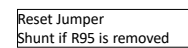
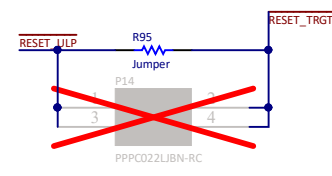
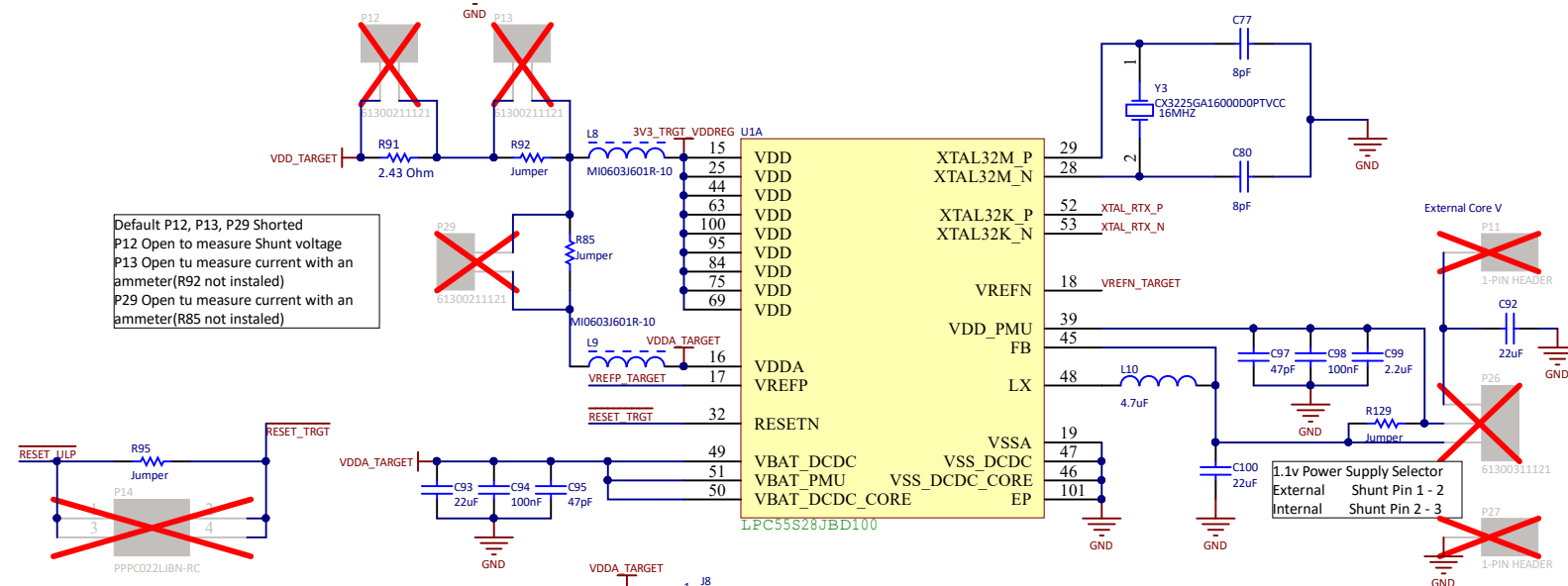
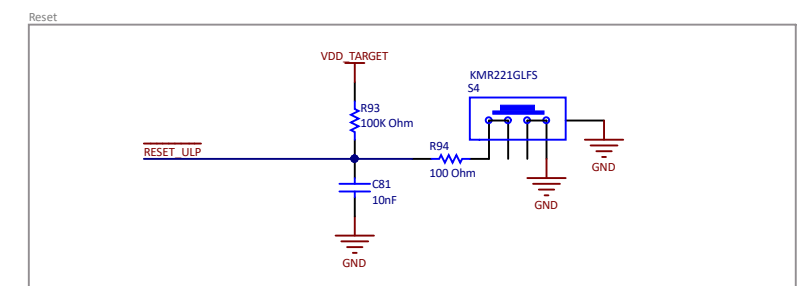
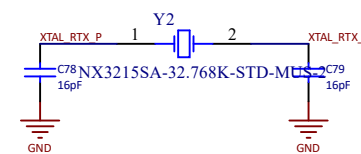
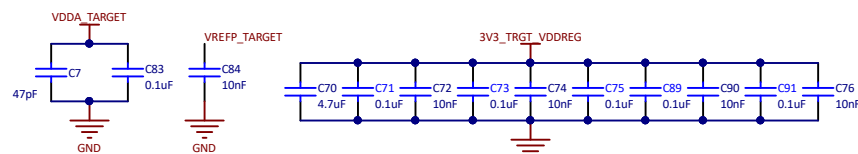
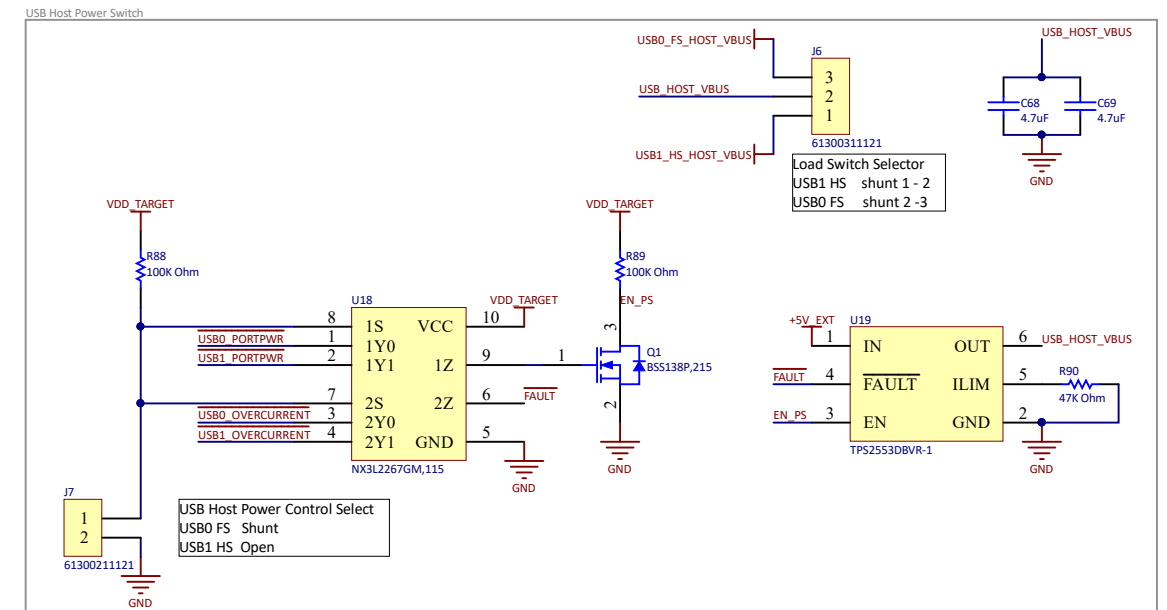
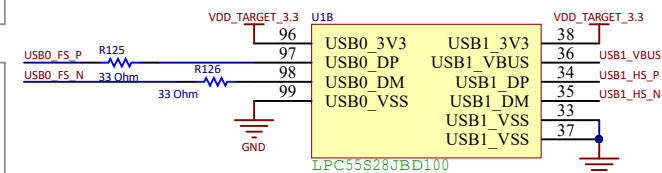
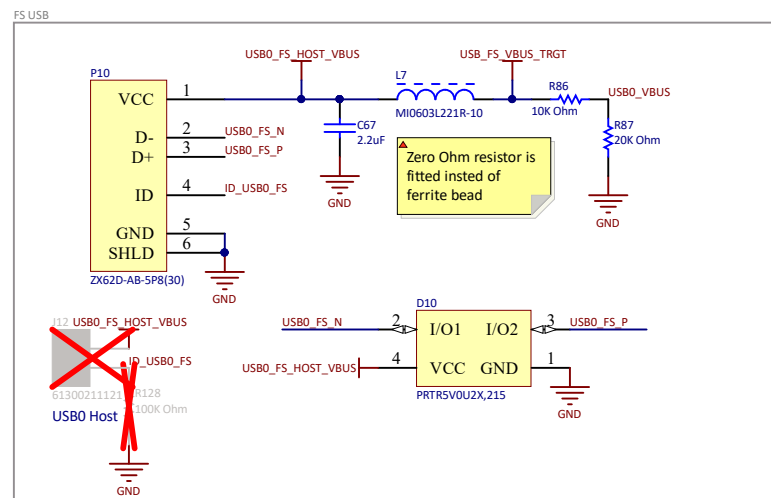


Connectors
SHIELD-uSD-MIKROE-PMOD.SchDoc





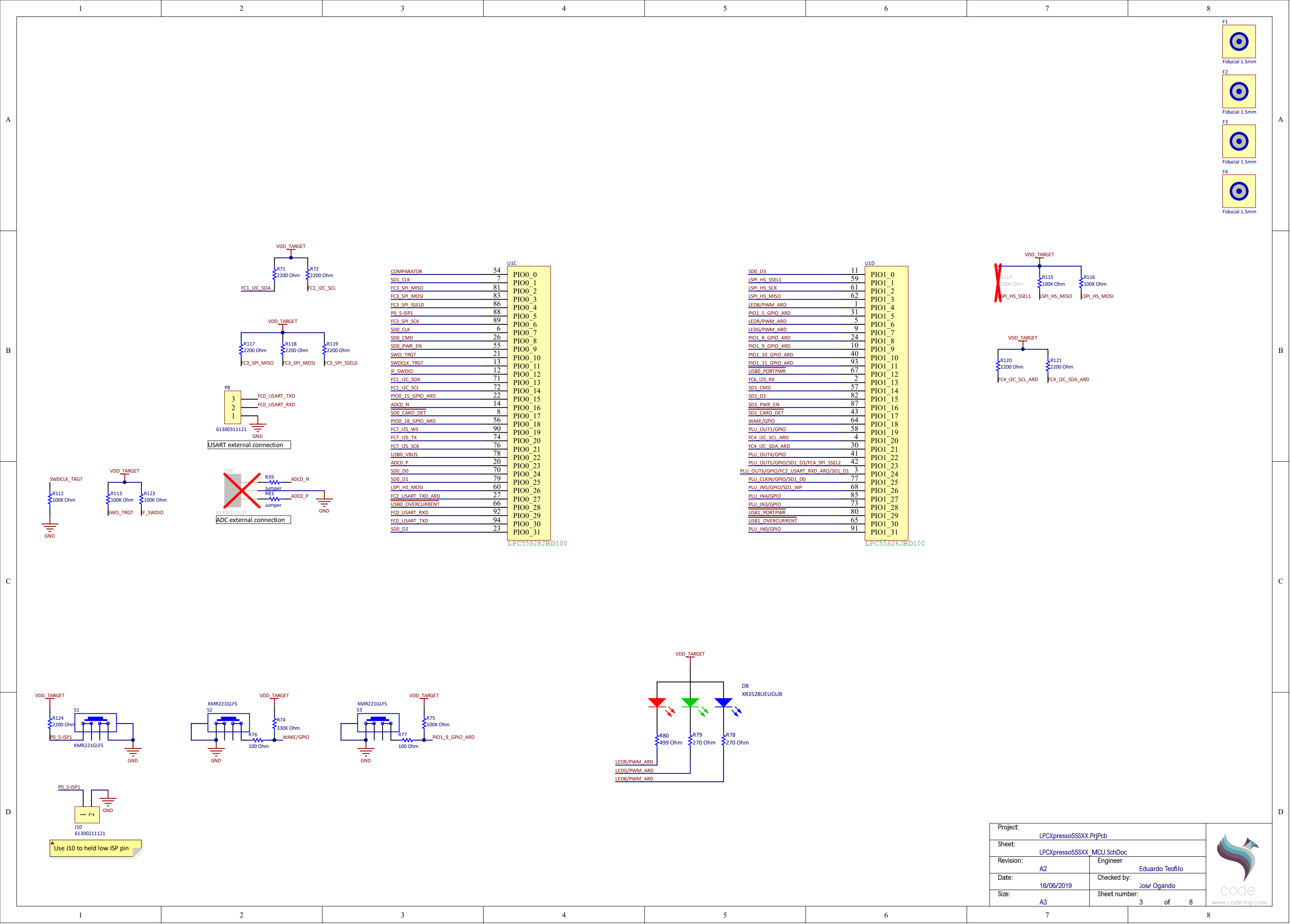
Note: R84 20k and R82 on some production boards.

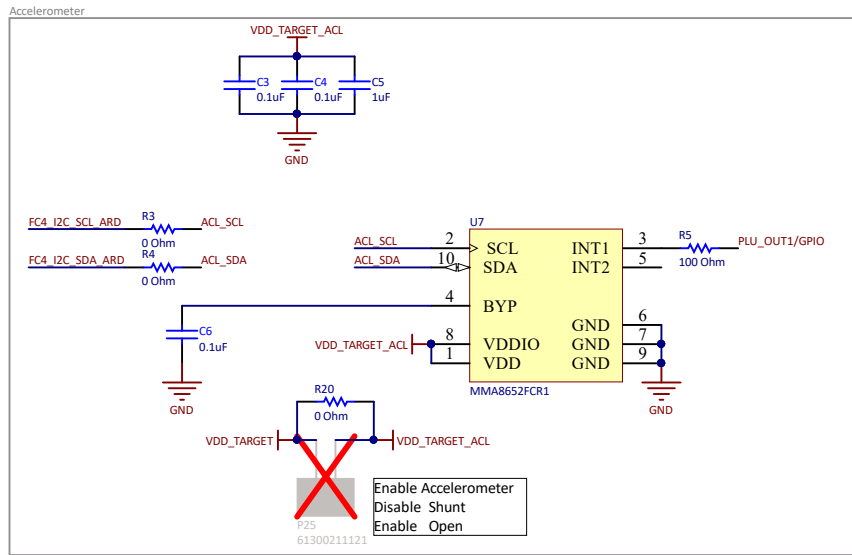
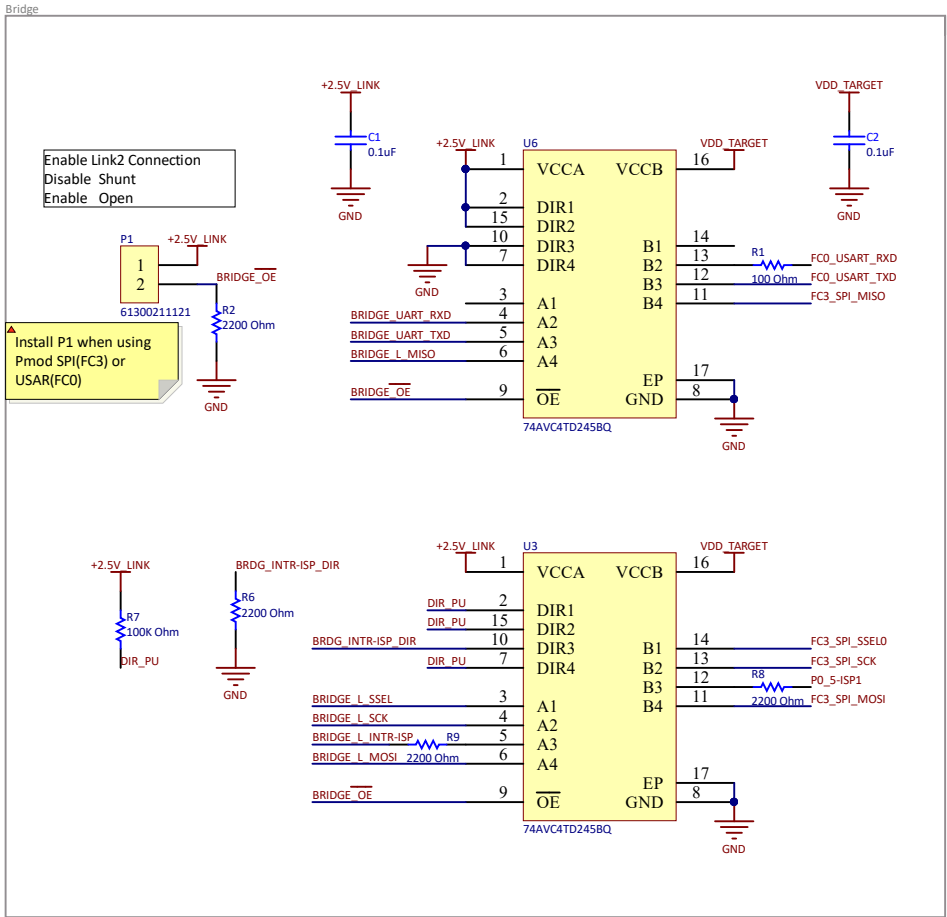


▲ To use external references for ADC jumper of J8 and J9 must be on 2-3 pads

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Date:	18/06/2019	Checked by:	José Ogando
Size:	A3	Sheet number:	2 of 8



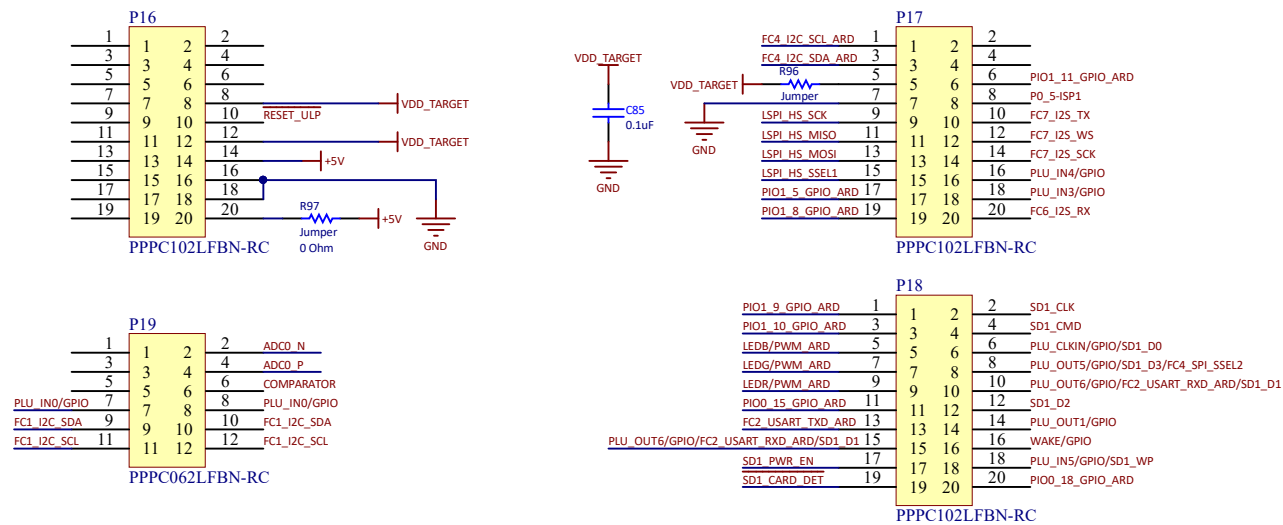




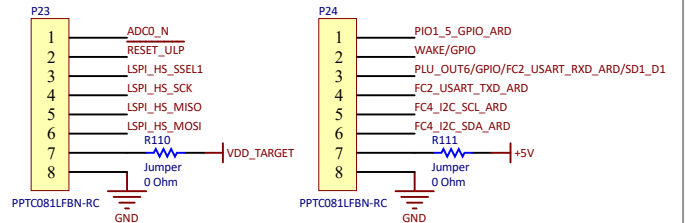
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Revision:	A2	Engineer	Eduardo Teofilo
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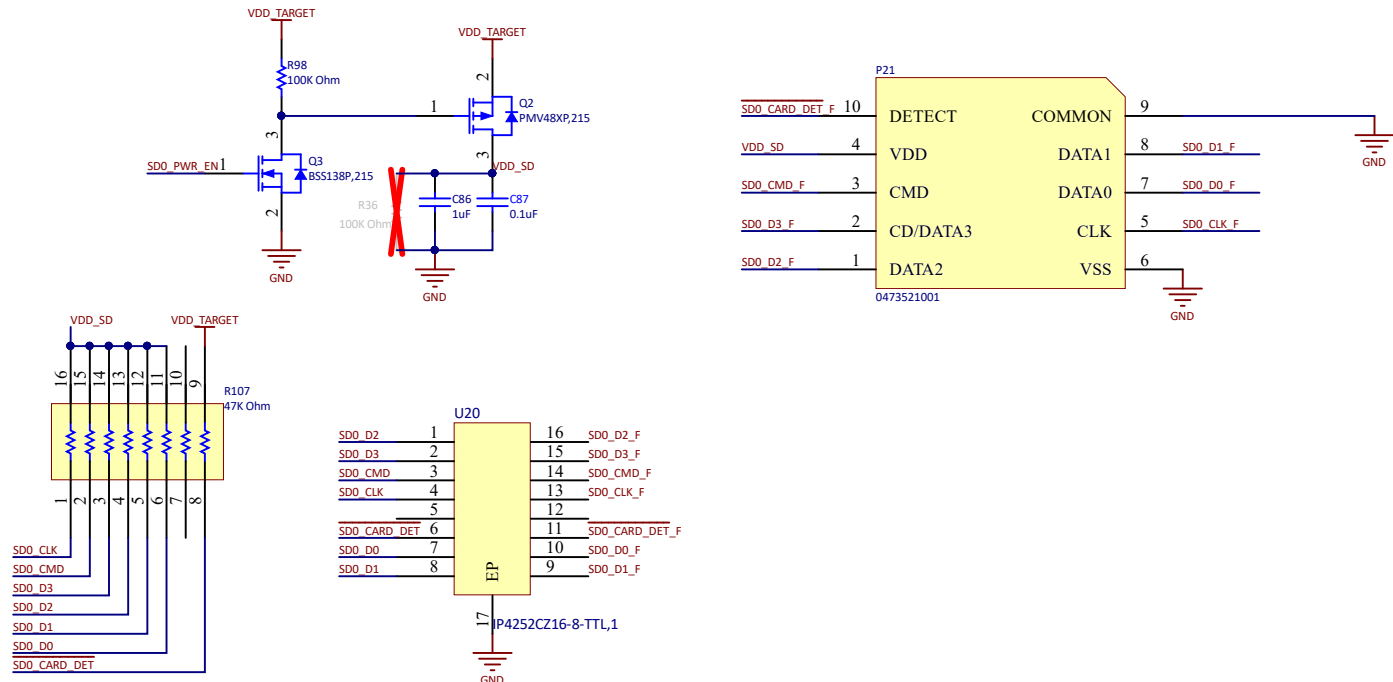
Expansion Connectors



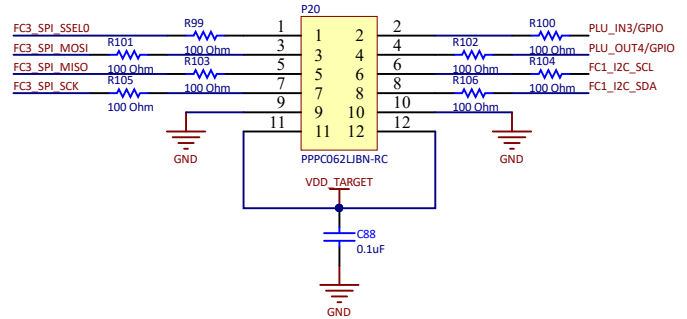
Mikroe Connector



Micro SD Card

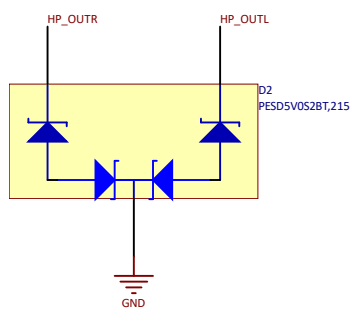
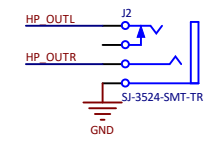
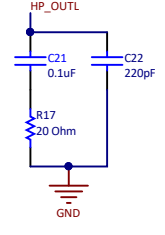
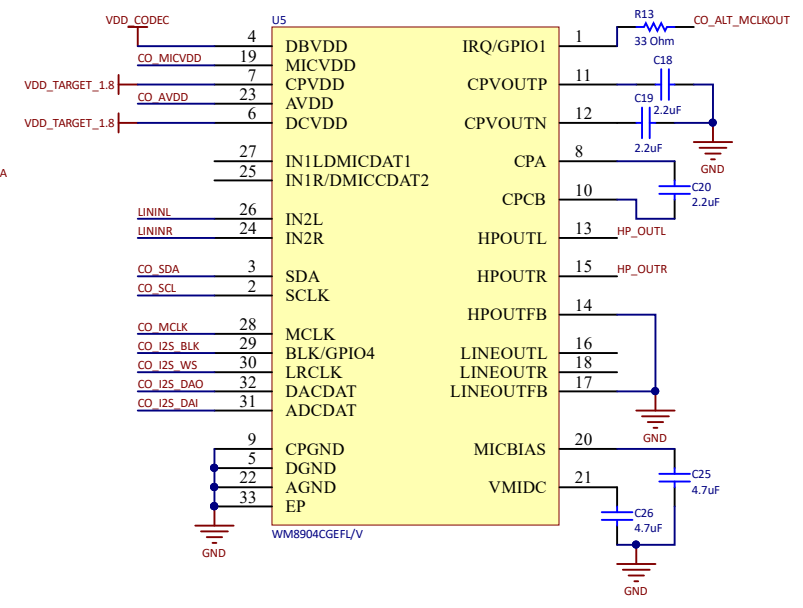
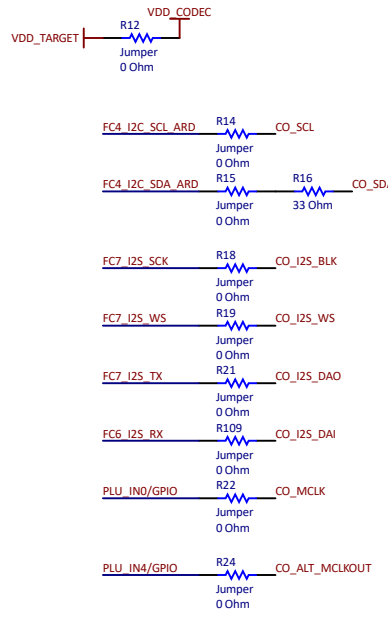
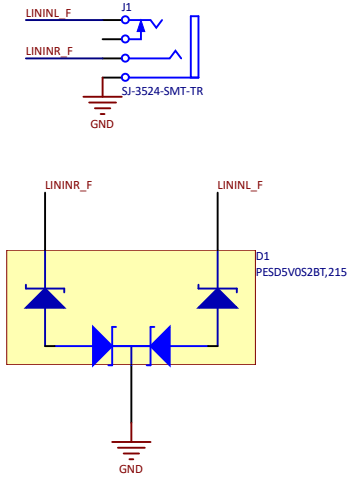
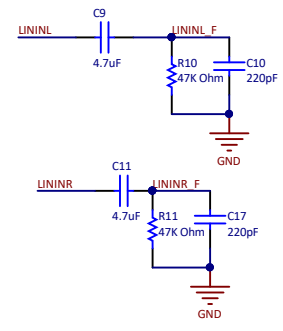
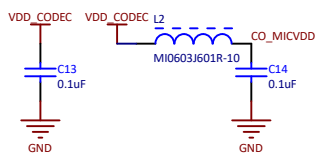
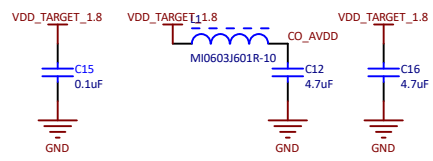


PMod Connector



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Project:	LPCXpresso55SXX.PrjPcb		
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