

NuMicro Quick Start Guide for Keil

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1 Introduction

1.1 About the Quick Start Guide

This Quick Start Guide will instruct you on how to use the Keil™ Microsoft Windows based software development tools with the NUC1XX development board. It gives you an overview of the most commonly used features in Keil MDK μVision4 and provides the necessary information for your own projects.

1.2 About NUC1xx series IC

The NUC1xx series include NUC100, NUC120, NUC130 and NUC140 series.

The NUC1xx series are 32-bit microcontrollers with embedded ARM® Cortex™-M0 core for industrial control and applications which need rich communication interfaces. The Cortex™-M0 is the newest ARM embedded processor with 32-bit performance and at a cost equivalent traditional 8-bit microcontroller.

The **NUC100** series embeds Cortex™-M0 core running up to 50MHz with 32K/64K/128K-byte embedded flash and 4K/8K/16K-byte embedded SRAM. It also equips with plenty of peripheral devices, such as Timers, Watchdog Timer, RTC, PDMA, UART, SPI/SSP, I²C, I²S, PWM Timer, GPIO, 12-bit ADC, Analog Comparator, Low Voltage Detector and Brown-out detector.

The **NUC120** series embeds Cortex™-M0 core running up to 50MHz with 32K/64K/128K-byte embedded flash and 4K/8K/16K-byte embedded SRAM. It also equips with plenty of peripheral devices, such as Timers, Watchdog Timer, RTC, PDMA, UART, SPI/SSP, I²C, I²S, PWM Timer, GPIO, USB 2.0 FS Device, 12-bit ADC, Analog Comparator, Low Voltage Detector and Brown-out detector.

The **NUC130** series embeds Cortex™-M0 core running up to 50MHz with 64K/128K-byte embedded flash and 8K/16K-byte embedded SRAM. It also equips with plenty of peripheral devices, such as Timers, Watchdog Timer, RTC, PDMA, UART, SPI/SSP, I²C, I²S, PWM Timer, GPIO, LIN, CAN, 12-bit ADC, Analog Comparator, Low Voltage Detector and Brown-out detector.

The **NUC140** series embeds Cortex™-M0 core running up to 50MHz with 64K/128K-byte embedded flash and 8K/16K-byte embedded SRAM. It also equips with plenty of peripheral devices, such as Timers, Watchdog Timer, RTC, PDMA, UART, SPI/SSP, I²C, I²S, PWM Timer, GPIO, LIN, CAN, USB 2.0 FS Device, 12-bit ADC, Analog Comparator, Low Voltage Detector and Brown-out detector.

1.3 About Keil™ μ Vision4 IDE

The μ Vision4 IDE is a Windows-based software development platform that combines a robust editor, project manager, and makes facility. The μ Vision4 integrates all tools including the C compiler, macro assembler, linker/locator, and HEX file generator. The μ Vision4 helps expedite the development process of your embedded applications by providing the following:

- Full-featured source code editor,
- Device database for configuring the development tool setting,
- Project manager for creating and maintaining your projects,
- Integrated make facility for assembling, compiling, and linking your embedded applications,
- Dialogs for all development tool settings,
- True integrated source-level Debugger with high-speed CPU and peripheral simulator,
- Advanced GUI interface for software debugging in the target hardware and for connection to Keil™ ULINK,
- Flash programming utility for downloading the application program into Flash ROM,
- Links to development tools manuals, device datasheets & user's guides.

2 Quick Start

2.1 Installing the Keil™ Software

You can download the Keil™ RealView® Microcontroller Development Kit Evaluation software from <http://www.keil.com/>. It contains the Keil™ µVision4 IDE. The evaluation version of the tools has a 32K bytes limit on images, but comes in a license-free version.

More information please reference [Read Me First](#) document from Keil™ about how to install Keil™ µVision4 software.

2.2 Connecting to your target

The target is powered via your PC, through its USB port or 5volt DC adaptor. The Keil™ ULINK family of adapter connects the USB port of your PC to the Serial Wire Debug (SWD) port of your target board allowing you to download and debug embedded programs running on your target hardware.

The ULINK2 connects to the NUC1xx using the 20-pin ribbon cable.

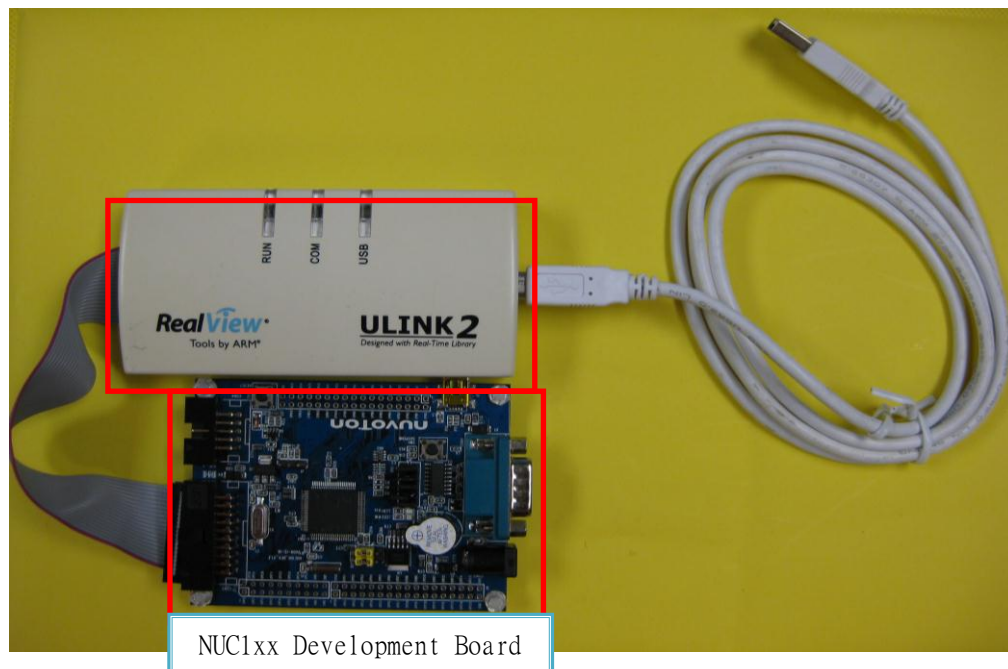


Figure 2-1

2.3 μ Vision4 Overview

μ Vision4 has two operating modes:

- **Build Mode:** Allows you to translate all the application files and to generate executable programs. The features of the Build Mode are described under Creating Applications.
- **Debug Mode:** Provides you with a powerful debugger for testing your application. The Debug Mode is described in Testing Programs.

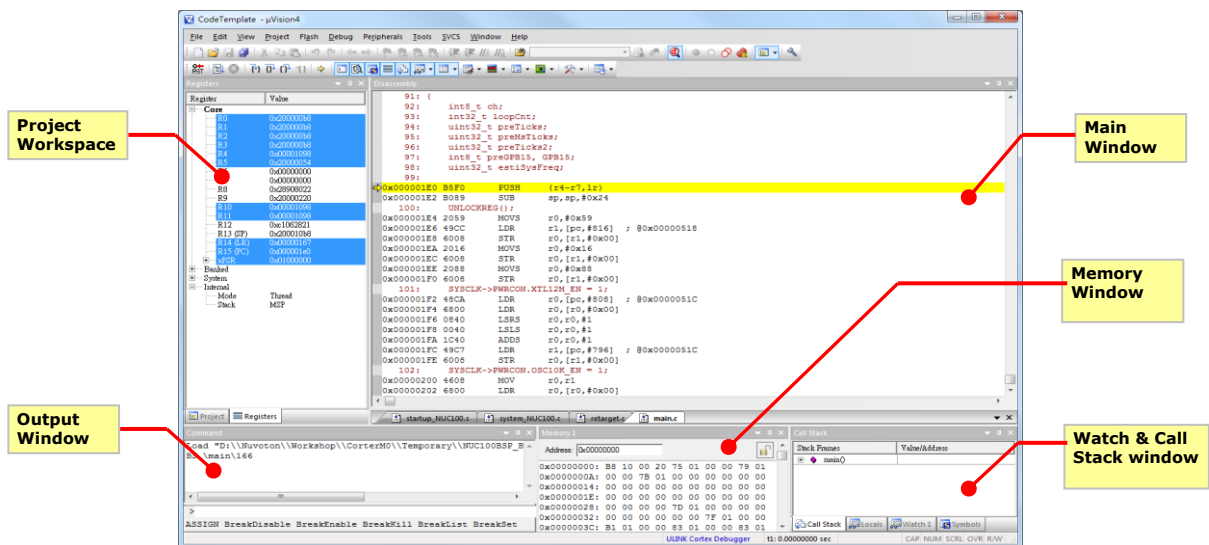


Figure 2-2

2.3.1 Build Process

The Build Target command runs the Compiler and Assembler. The tools automatically generate file dependencies so only those files that have changed are retranslated. You may enable additional Global Code Optimizations which are performed by incremental re-compilations of C modules and other utilities. The Project menu provides access to project files and dialogs for project management.

Build Toolbar



Only describe functions in common use as shown as below:

Command Option	Tool Button	Function Description	Hot key
Translate...		Translate current file	None
Build Target		Translate modified files and build application	F7
Rebuild Target		Re-translate all source files and build application	None
Batch Build		Execute build component on the selected project/targets of a Multi-project workspace	None
Stop Build		Stop current build process	None
Flash Download		Call Flash download utility as configure under options.	None
Target Option		Set project components, configure tool environment and manage books.	None
Select Current Project Target		Select current target	None
Manage Project		Set Project Component, Configure tool environment and manage books.	None

2.3.2 Debugger












The μ Vision4 IDE/Simulator/Debugger accelerates your learning efforts by providing a single environment for editing, simulating, and testing target hardware. Most debugger and editor functions may be quickly accessed from the toolbar.

You may use the context menu or the Editor Toolbar to insert breakpoints. Breakpoints you set while editing are activated when you start your debugging session. μ Vision4 marks the status of each source line in the Attributes column of the editor window. This provides a quick overview of the current breakpoint and execution status.

Debugger



Only describe functions in common use as shown as below:

Command Option	Tool Button	Function Description	Hot key
Reset CPU		Set CPU to reset state	Ctrl+F5
Go		Run until the next active breakpoint	F5
Halt Execution		Stop running	ESC
Single step into		Execute a single step into a function	F11
Step Over		Execute a single step over a function	F10
Step Out		Step out of current function	None
Run till current line		Execute until the current cursor line is reached	None
Show next statement		Show next executable statement /instruction	None
Disassembly		Show or hide Disassembly window	None
Watch & Call Stack window		Show or hide Watch & Call Stack window	None
Memory window		Show or hide Memory window	None

2.4 Step-by-Step

This section details all of the materials necessary to download code to an ARM-based development board for debug in the Keil™ μ Vision4 IDE using the JTAG debug agent.

2.4.1 Starting the software

Double-click on the Keil μ Vision4 icon to start the user interface. The compiler, assembler, linker and Scope will be called from within Keil μ Vision4 in this tutorial. After you invoke Keil μ Vision4, the window shown as below appears. From this window, you can create projects, edit files, configure the tool, assemble, link, and invoke the debugger.

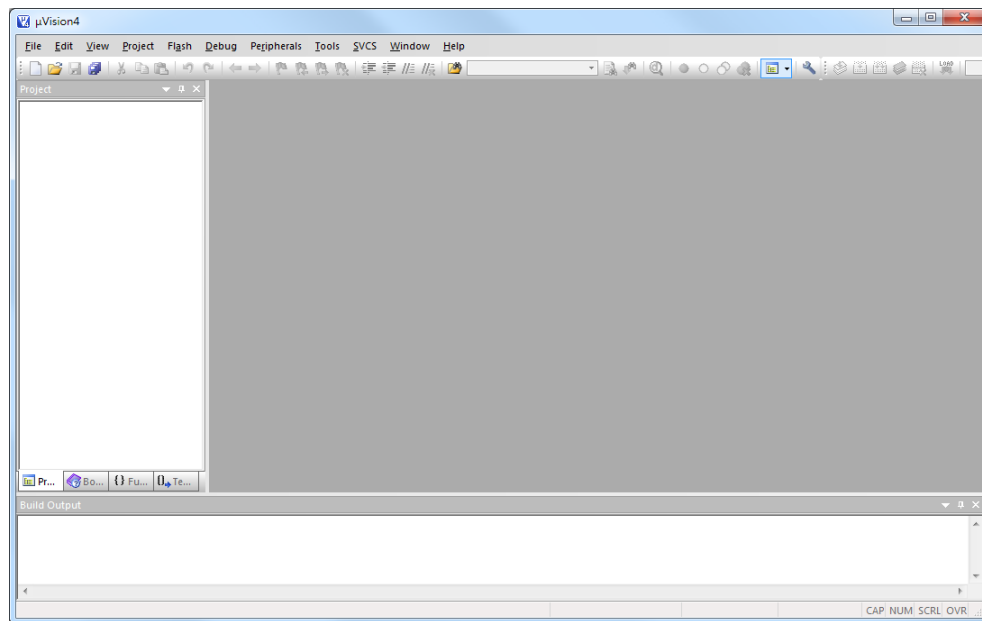


Figure 2-3

2.4.2 Creating a new project

Before writing any C-code, a project associated with our code needs to be created. This is done by first creating a new folder in the Keil directory in which your project will be saved. Next the Keil uV4 application can be launched and a new project is created. This is achieved by completing the following steps.

- Create a folder named “CodeTemplate” in your setting path
- Launch the uV4 application. Start -> Programs -> Keil μ Vision4
- Create a new project. From the main window, choose the ‘Project’ menu and select New project . And then a new window appears as shown below
- Select the folder that you've created previously (CodeTemplate) and on the bottom of the window type the name of your new project, eg. CodeTemplate and press SAVE.

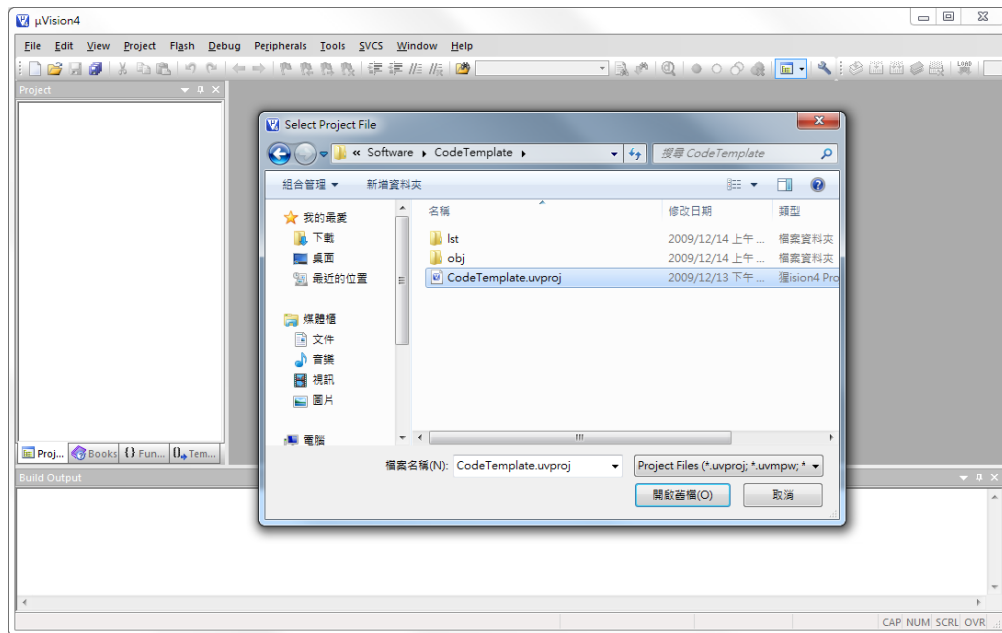


Figure 2-4

2.4.3 Device Support

A new window appears as shown below and you are now required to configure your setup to target the specific ARM device you wish to use (in this example we will be using the Cortex-M0) and the output file format generated after the compilation stage. This is achieved by completing the following steps.

- Open the ARM folder.
- Select the item on which you will be developing, in this tech note we will be using the Cortex-M0 as the target example.

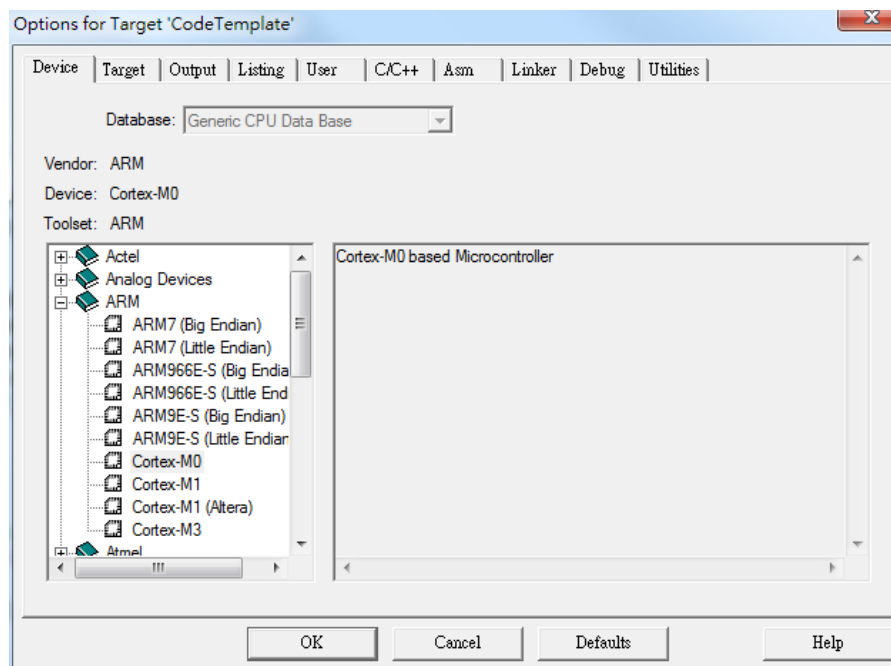


Figure 2-5

2.4.4 Project Management

Keil uVision4 ensures easy and consistent Project Management. A single project file stores source file names and saves configuration information for Compiler, Assembler, Linker, Debugger, Flash Loader, and other utilities. The Project menu provides access to project files and dialogs for project management.

Select the icon to open Management Project Component Setting

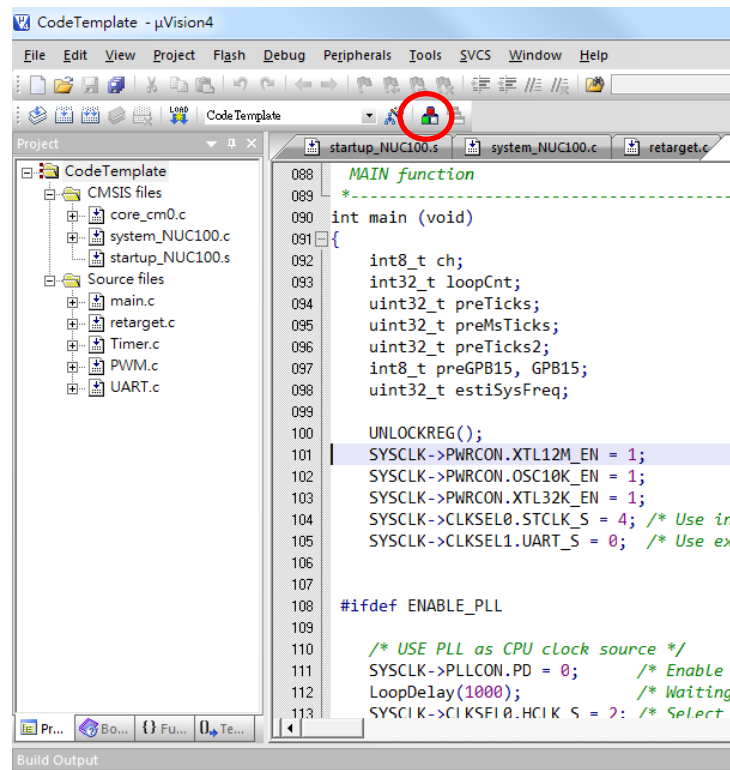


Figure 2-6

A Project Component Setting window appears as shown below. According to your assignment, create new group and link your source code.

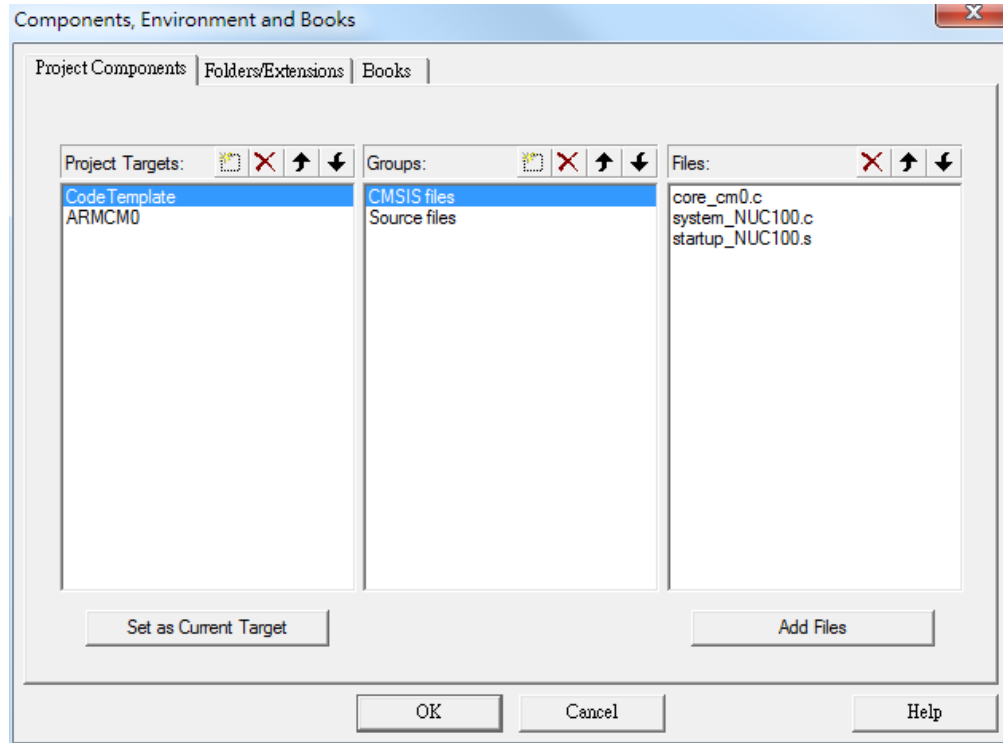


Figure 2-7

Finally, the project workspace will show as below:

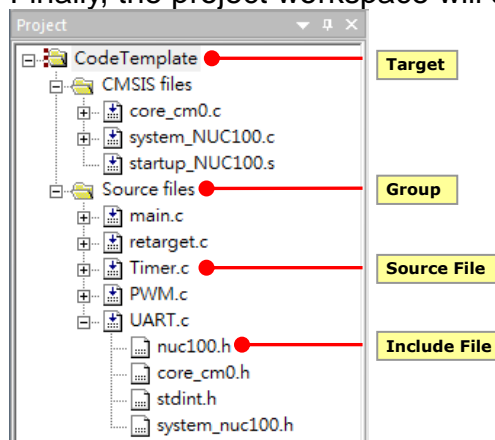


Figure 2-8

2.4.5 Creating a C program

Now you also can write your C program. In the main window, choose the File pull-down menu and select New. A new window named <text1> will appear on the screen and you can write your code to <text1>.

Once you've typed all the code, again choose the File pull-down menu and select Save. A new Save dialog window appears. Save your new file as “main.c” in the CodeTemplate folder you had created earlier. At this stage, before compiling the C-program, we need to include it in our project. To do this you must click with the right mouse button on ‘Source files’ and select Add Files to Group ‘Source Library’ as shown below. Select the file named “main.c” that is in the CodeTemplate folder and click on Add and then on Close.

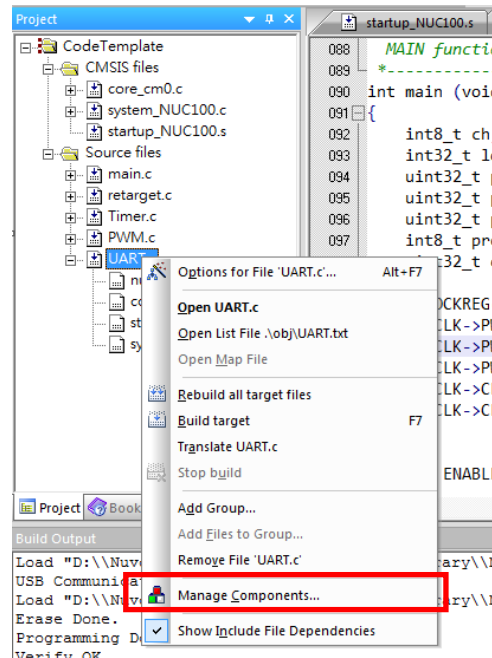


Figure 2-9

2.4.6 Compiling a C program

Select Rebuild all target files from the Project menu, or click on the Rebuild all button (icon).

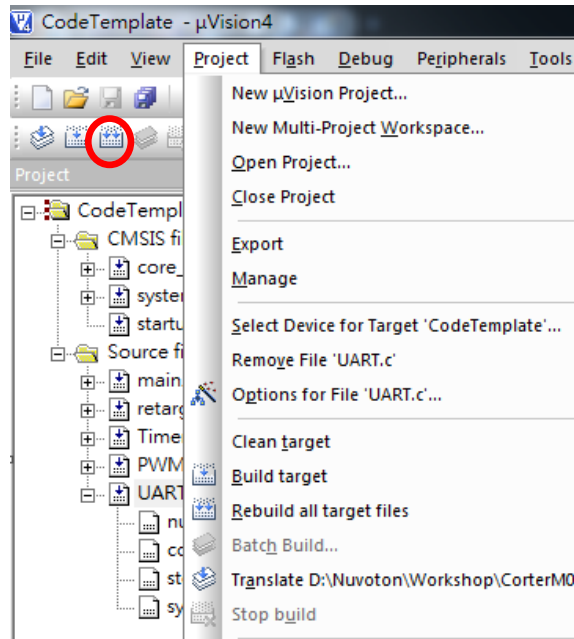


Figure 2-10

All of the source files are compiled and linked. The activity can be seen in the Build window at the bottom of the uVision4 IDE. (In this example, the process completes with an application named CodeTemplate.axf and CodeTemplate.bin built with no errors and no warnings.)

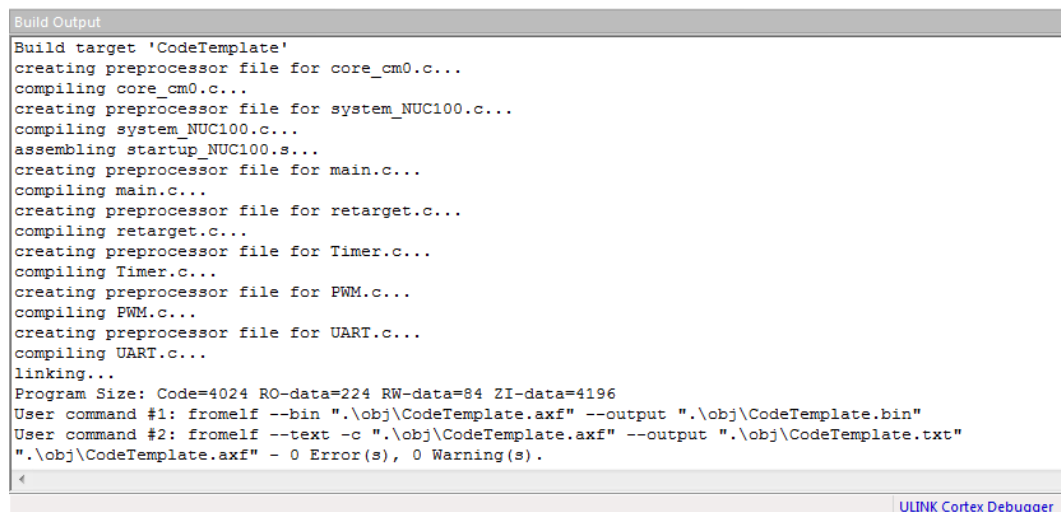


Figure 2-11

2.4.7 Connecting and Configuring the Hardware

Step-by-step to finished the section.

1. Click on Project => Options for Target => your_target_name. Or click on the Options for Target button (icon).

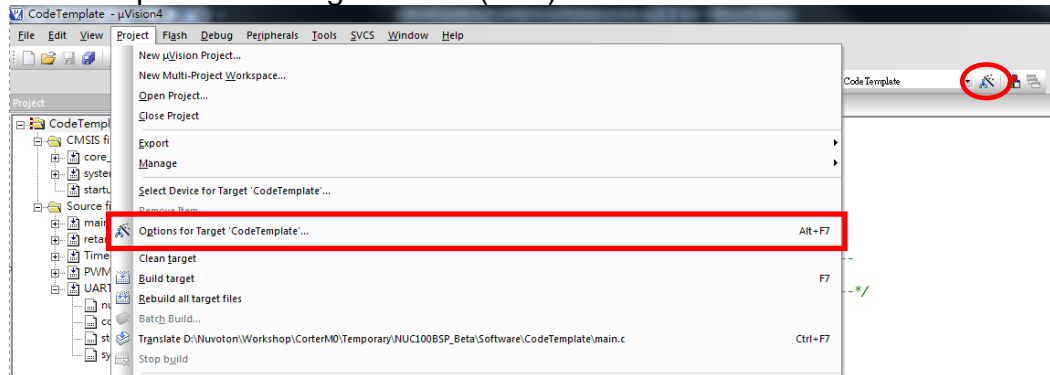


Figure 2-12

2. On Target tab, allows you to specify CPU and memory options. These are used to configure basic tool settings including those of the linker, debugger, and simulator.

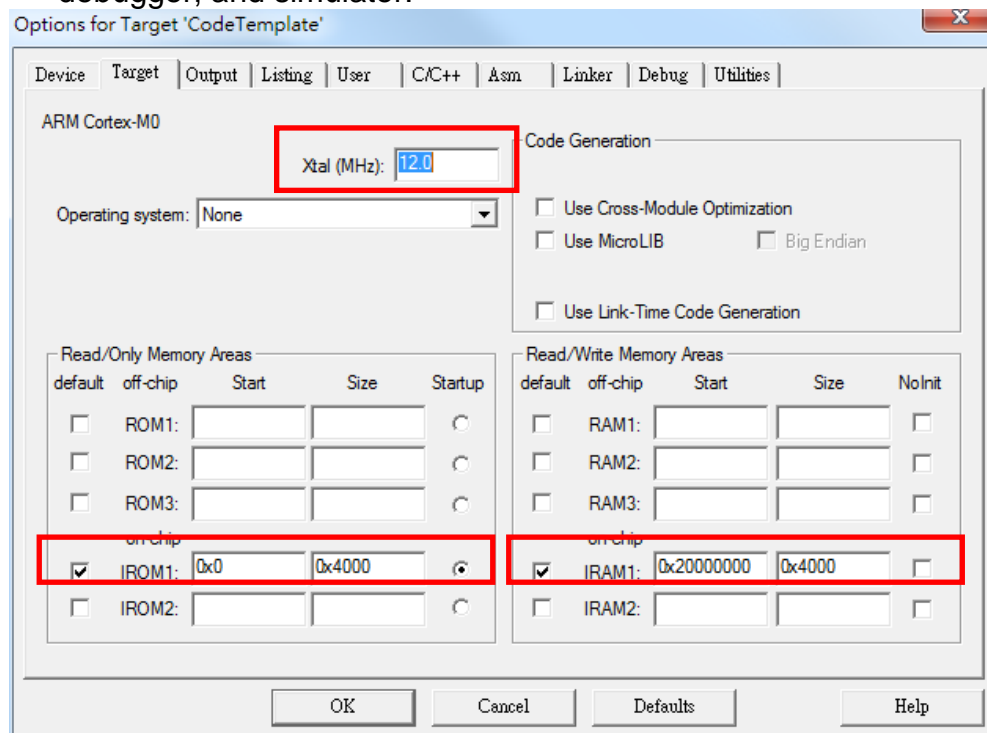


Figure 2-13

3. The setting dialog show up in a window on the center. Click Debug tab and Select ULINK Cortex Debugger. You might need to select this from the drop-down menu if it is not already selected. If you want to load the application when starting debug mode, please enable the check box of Load Application at Startup.

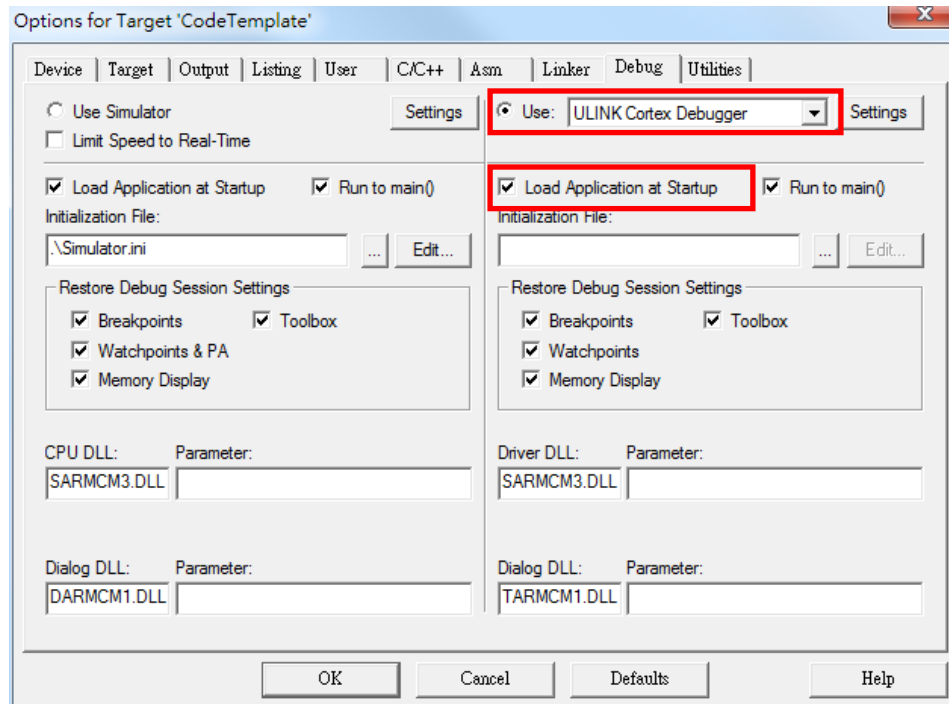


Figure 2-14

2.4.8 Simulating your source code

Another powerful feature of the uVision4 IDE is that it allows you to run your code. To start a simulation session you simply click on the **Start/Stop Debug Session** option available from the **Debug** pull-down menu. Alternatively you can press <Ctrl+F7> or the 'Debug' icon available in the 'File' toolbar as shown as below.

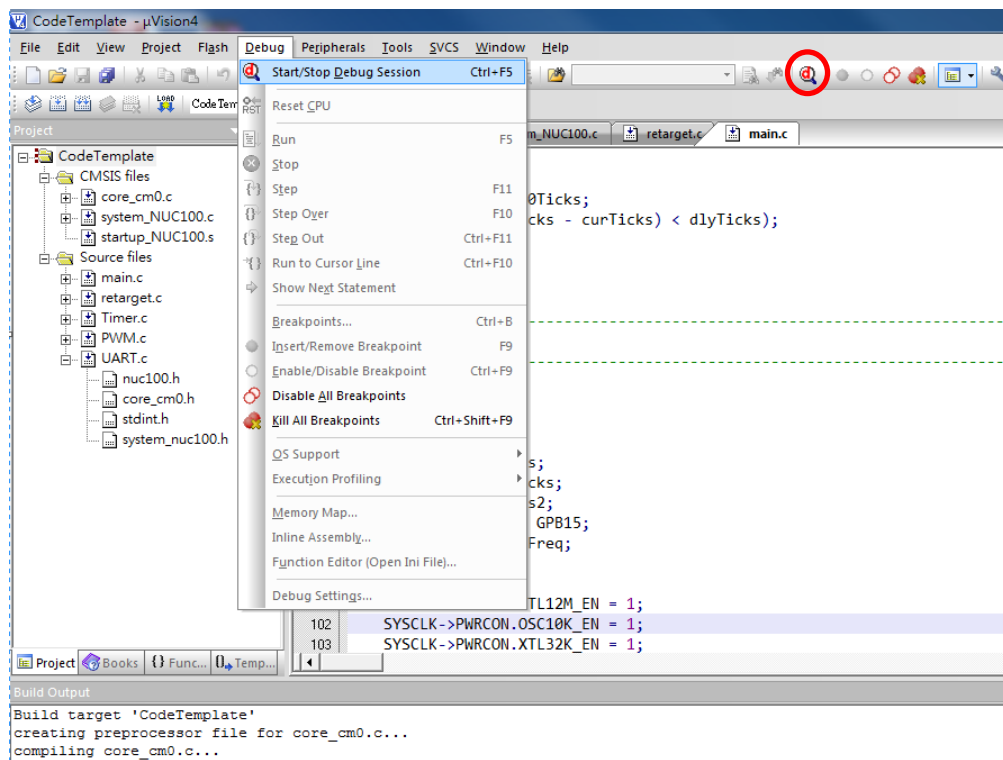


Figure 2-15

The IDE switches to debugging mode. The processor registers show up in a window on the left, the debugger command window is visible at the bottom, and the main window shows the source code being debugged.

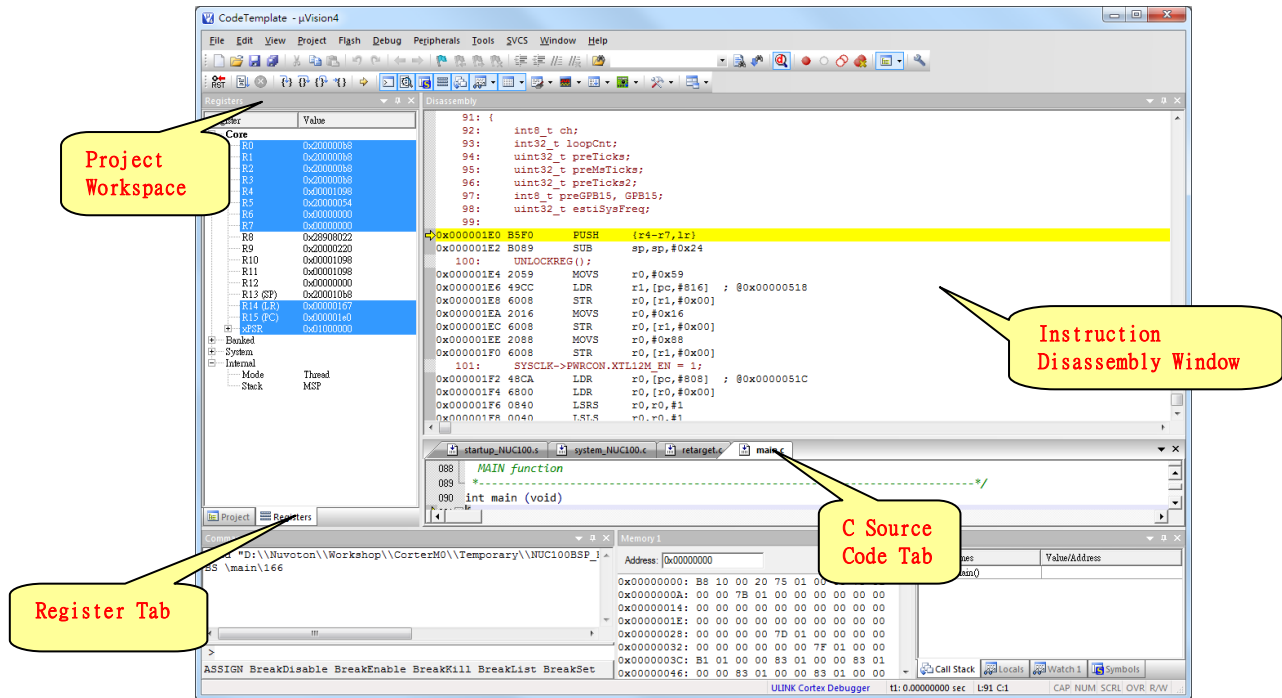


Figure 2-16

CodeTemplate - μVision4

File Edit View Project Flash Debug Peripherals Tools SVCS Window Help

Start/Stop Debug Session Ctrl+F5

Reset CPU

Run F5

Stop

Step F11

Step Over F10

Step Out Ctrl+F11

Run to Cursor Line Ctrl+F10

Show Next Statement

Breakpoints... Ctrl+B

Insert/Remove Breakpoint F9

Enable/Disable Breakpoint Ctrl+F9

Disable All Breakpoints

Kill All Breakpoints Ctrl+Shift+F9

QS Support

Execution Profiling

Memory Map...

Inline Assembly...

Function Editor (Open Ini File)...

Debug Settings...

Register Value

Core

Register	Value
R0	0x200000b8
R1	0x200000b8
R2	0x200000b8
R3	0x200000b8
R4	0x00001098
R5	0x20000054
R6	0x00000000
R7	0x00000000
R8	0x28980222
R9	0x20000220
R10	0x00001098
R11	0x00001098
R12	0x00000000
R13 (SP)	0x200010b8
R14 (LR)	0x00000167
R15 (PC)	0x00001e00
xPSR	0x01000000

Banked

System

Internal

Mode

Stack

Thread

MSP

opCnt;

reTicks;

reMsTicks;

reTicks2;

GPB15, GPB15;

stiSysFreq;

USH {r4-r7,lr}

UB sp,sp,#0x24

);

OVS r0,#0x59

DR r1,[pc,#816] ; @0x00000518

TR r0,[r1,#0x00]

OVS r0,#0x16

OVS r0,#0x88

TR r0,[r1,#0x00]

OVS r0,#0x88

TR r0,[r1,#0x00]

RCON.XTL12M_EN = 1;

DR r0,[pc,#808] ; @0x0000051C

DR r0,[r0,#0x00]

SRS r0,r0,#1

STS r0,r0,#1

Project Registers

startup_NUC100...

System_NUC100.c

retarget.c

main.c

2014年2月10日

2.4.9 Flash Tool

Keil tool chain supports to download image file to NUC1xx DEV Board through ICE interface. A specified flash tool file should be provided to let Keil flash download function work. For NUC1xx series IC, the flash tool is called NUC1xx_AP_128.FLM NUC1xx_AP_64.FLM NUC1xx_AP_32.FLM NUC1xx_LD_4.FLM for different flash size and this file could be found at \FlashTool. To install the NUC1xx_AP_128.FLM, we need to copy it to the install directory of Keil, i.e. C:\ARM\Flash. After copy NUC1xx_AP_128.FLM to the specified directory, we can go back to the Keil IDE, open the Options for Targets dialog, select Utilities tab and open the Settings dialog.

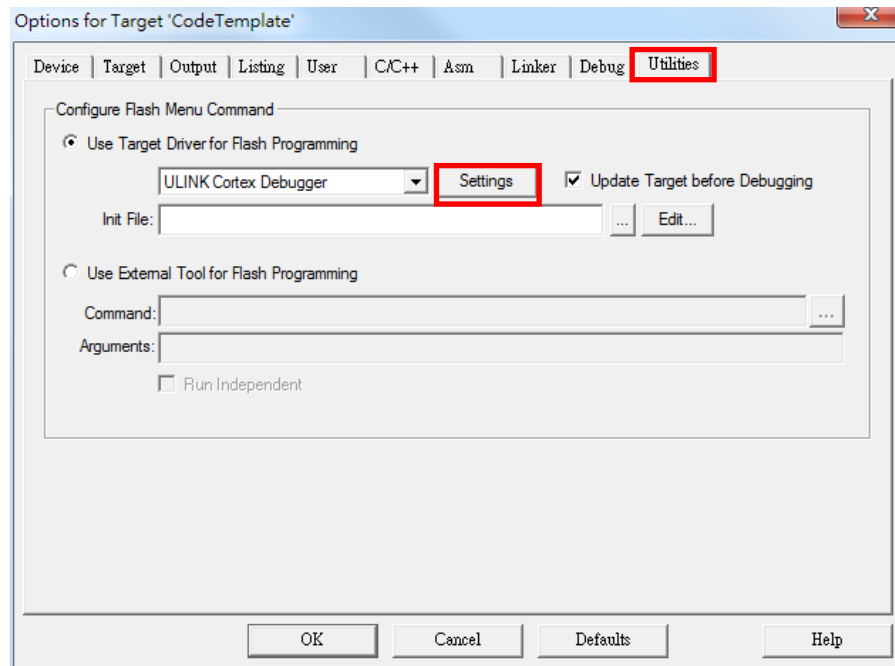


Figure 2-18

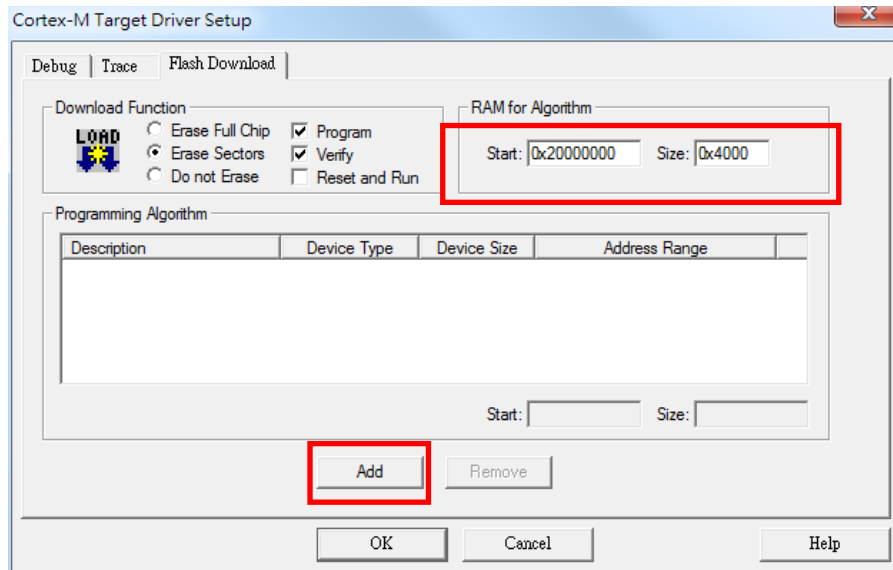


Figure 2-19

After the Settings button has been pressed, the Flash Download Setup dialog would be opened. Then we can set the start address to be 0x20000000, size to be 0x4000 in RAM for Algorithm setting. Finally, we press the Add button to add the flash tool. If the NUC1xx_AP_128.FLM was copy to \Keil\ARM\Flash, we should be able to find NUC1xx 128kB Flash AP item in Add dialog:

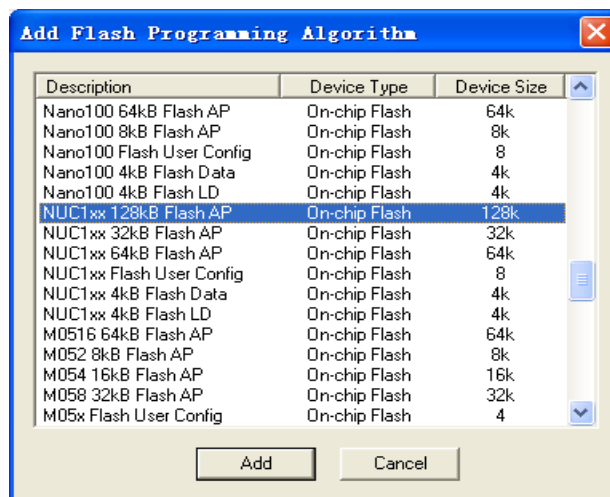


Figure 2-20

After find the NUC1xx 128kB Flash AP item, press Add button to add it to Flash Download Setup dialog.

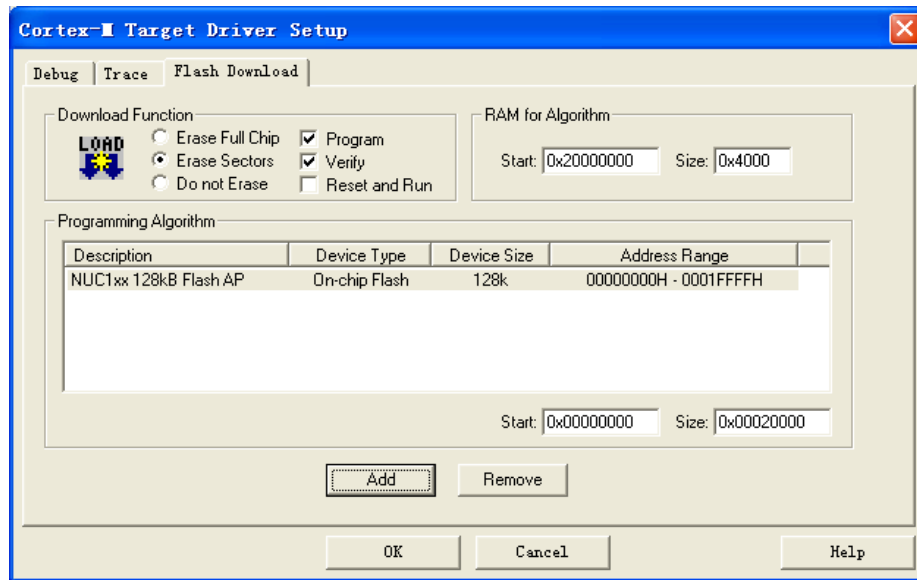


Figure 2-21

Now, we can press ok to finish the setting.

Note: Due to the flash base address is 0x00000000, we should set the RO base of linker according to the flash base address.

2.4.10 Conclusion

You have now installed the Keil™ RealView® Microcontroller Development Kit, and used it to build, load, and run a demonstration application on your Nuvoton® Development Board. From here, you can experiment with the debugger or start creating your own application using the CodeTemplate program as an example. If CodeTemplate project sample starts running, and you should see some text output to the hyper terminal display as shown as below:

```

SysTick:101 TMR0:2900 TMR1:2900 TMR2:2900 TMR3:2956 CNT:130826 CPU:22220000
SysTick:101 TMR0:3000 TMR1:3000 TMR2:3000 TMR3:3058 CNT:130827 CPU:22220000
SysTick:102 TMR0:3100 TMR1:3100 TMR2:3100 TMR3:3160 CNT:130833 CPU:22440000
SysTick:102 TMR0:3200 TMR1:3200 TMR2:3200 TMR3:3262 CNT:130818 CPU:22440000
SysTick:102 TMR0:3300 TMR1:3300 TMR2:3300 TMR3:3364 CNT:130827 CPU:22440000
SysTick:102 TMR0:3400 TMR1:3400 TMR2:3400 TMR3:3466 CNT:130824 CPU:22440000
SysTick:102 TMR0:3500 TMR1:3500 TMR2:3500 TMR3:3568 CNT:130821 CPU:22440000
SysTick:102 TMR0:3600 TMR1:3600 TMR2:3600 TMR3:3670 CNT:130825 CPU:22440000
SysTick:102 TMR0:3700 TMR1:3700 TMR2:3700 TMR3:3772 CNT:130825 CPU:22440000
SysTick:102 TMR0:3800 TMR1:3800 TMR2:3800 TMR3:3874 CNT:130833 CPU:22440000
SysTick:102 TMR0:3900 TMR1:3900 TMR2:3900 TMR3:3976 CNT:130835 CPU:22440000
SysTick:102 TMR0:4000 TMR1:4000 TMR2:4000 TMR3:4078 CNT:130838 CPU:22440000
SysTick:102 TMR0:4100 TMR1:4100 TMR2:4100 TMR3:4180 CNT:130838 CPU:22440000
SysTick:102 TMR0:4200 TMR1:4200 TMR2:4200 TMR3:4282 CNT:130836 CPU:22440000
SysTick:102 TMR0:4300 TMR1:4300 TMR2:4300 TMR3:4384 CNT:130832 CPU:22440000
SysTick:102 TMR0:4400 TMR1:4400 TMR2:4400 TMR3:4486 CNT:130826 CPU:22440000
SysTick:102 TMR0:4500 TMR1:4500 TMR2:4500 TMR3:4588 CNT:130827 CPU:22440000
SysTick:102 TMR0:4600 TMR1:4600 TMR2:4600 TMR3:4690 CNT:130832 CPU:22440000
SysTick:102 TMR0:4700 TMR1:4700 TMR2:4700 TMR3:4792 CNT:130830 CPU:22440000
SysTick:102 TMR0:4800 TMR1:4800 TMR2:4800 TMR3:4894 CNT:130817 CPU:22440000
SysTick:102 TMR0:4900 TMR1:4900 TMR2:4900 TMR3:4996 CNT:130827 CPU:22440000
SysTick:102 TMR0:5000 TMR1:5000 TMR2:5000 TMR3:5098 CNT:130829 CPU:22440000
SysTick:102 TMR0:5100 TMR1:5100 TMR2:5100 TMR3:5200 CNT:130823 CPU:22440000

```

Figure 2-22

3 Revision History

Revision	Date	Description
V1.01	Jan. 28, 2010	● Created.
V1.02	Jun. 22, 2010	● To change NUC100.FLM file which depends on flash size by using NUC1xx_128kB.FLM NUC1xx_64kB.FLM NUC1xx_32kB
V1.03	Jul. 22, 2010	● BTo change NUC1xx_128kB.FLM NUC1xx_64kB.FLM NUC1xx_32kB.FLM to NUC1xx_AP_128.FLM NUC1xx_AP_64.FLM NUC1xx_AP_32.FLM and Add NUC1xx_LD_4.FLM
V1.04	Feb. 10, 2014	● Document format modified

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