

Pretend Synchrony

Klaus von Gleissenthall



UC San Diego

Software Systems
Shouldn't Fail

Ariane 5 Crash

Crashed due to float to int conversion bug



1996

Marriott Breach

Data from 500 Mio customers (2018)

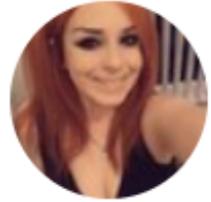


Name, Passport No., Credit Card Numbers

The Nightmare



The Nightmare



Katie Warren @KayEllDoubleYoo · 5 Oct 2017

@NetflixUK is down! What do I do now? 😩 What kind of life is this?
#netflixdown



The Nightmare



↑ Nomnipotent 69 points · 5 years ago

↓ WHO IS RESPONSIBLE FOR THIS?

Share Report Save

↑ Derburnley 22 points · 5 years ago

↓ UNACCEPTABLE!!!!

Share Report Save

The Nightmare



↑ elguitarro 49 points · 5 years ago

↓ *Hello darkness, my old friend..*

Share Report Save

↑ ToxicSandwich 2 points · 4 years ago

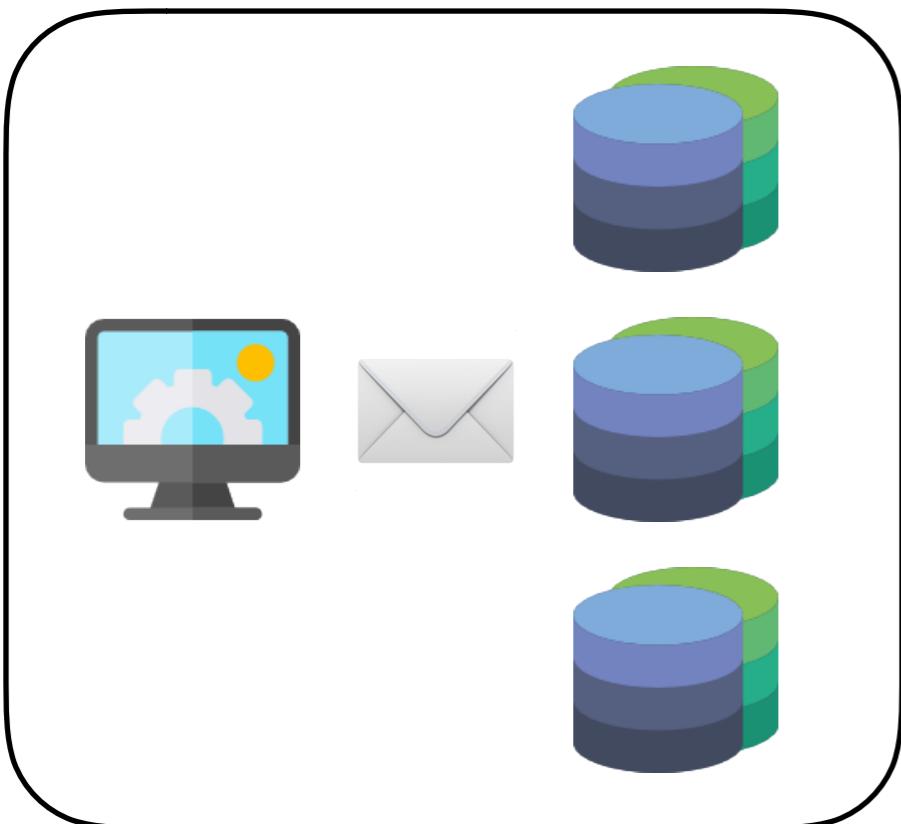
↓ I feel so damn lost.

Share Report Save

Software Systems
Shouldn't Fail

How to Make Sure *Distributed* Systems Don't Crash?

Distributed Systems



Nodes run
protocol

Send & receive
asynchronously

Testing?

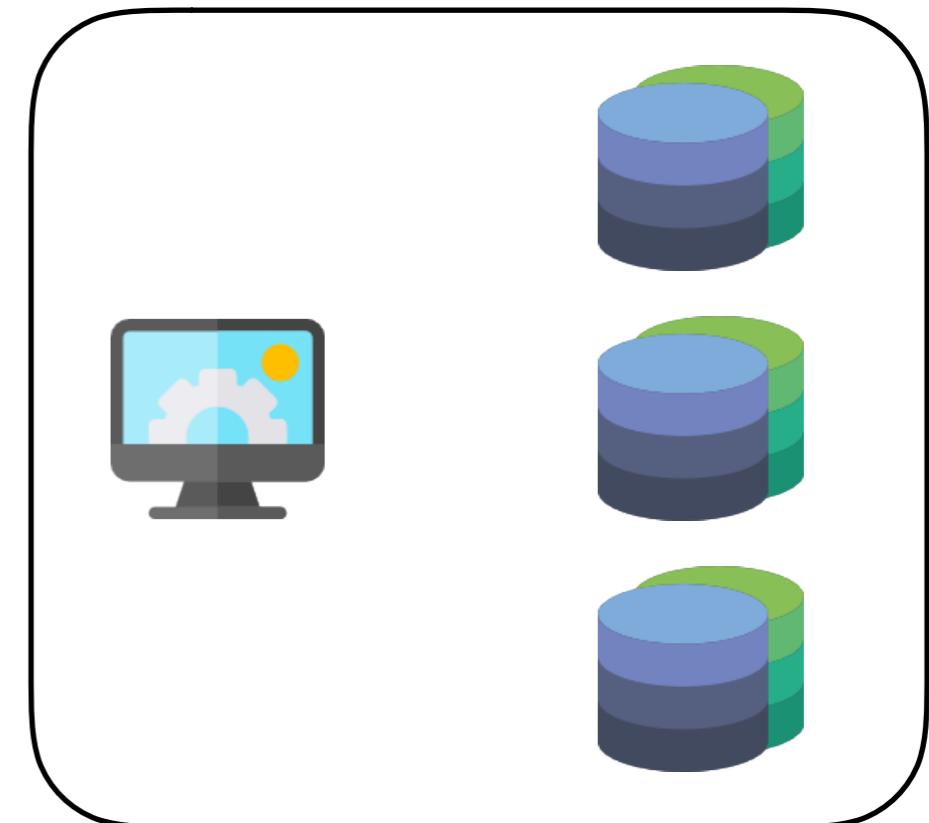
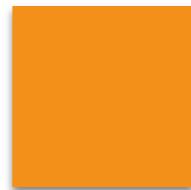
Testing?

To fix a run, we need

Input

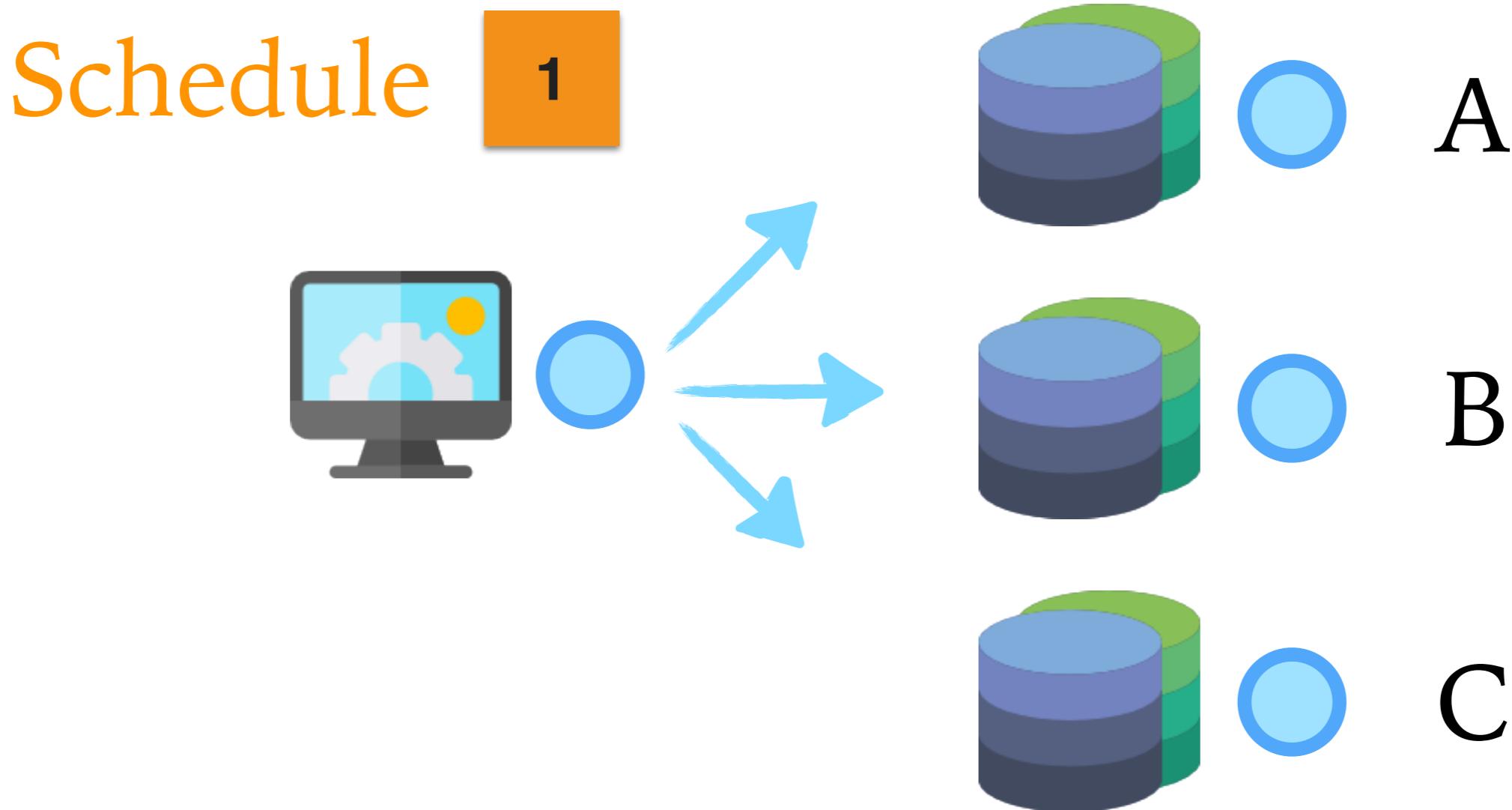


Schedule



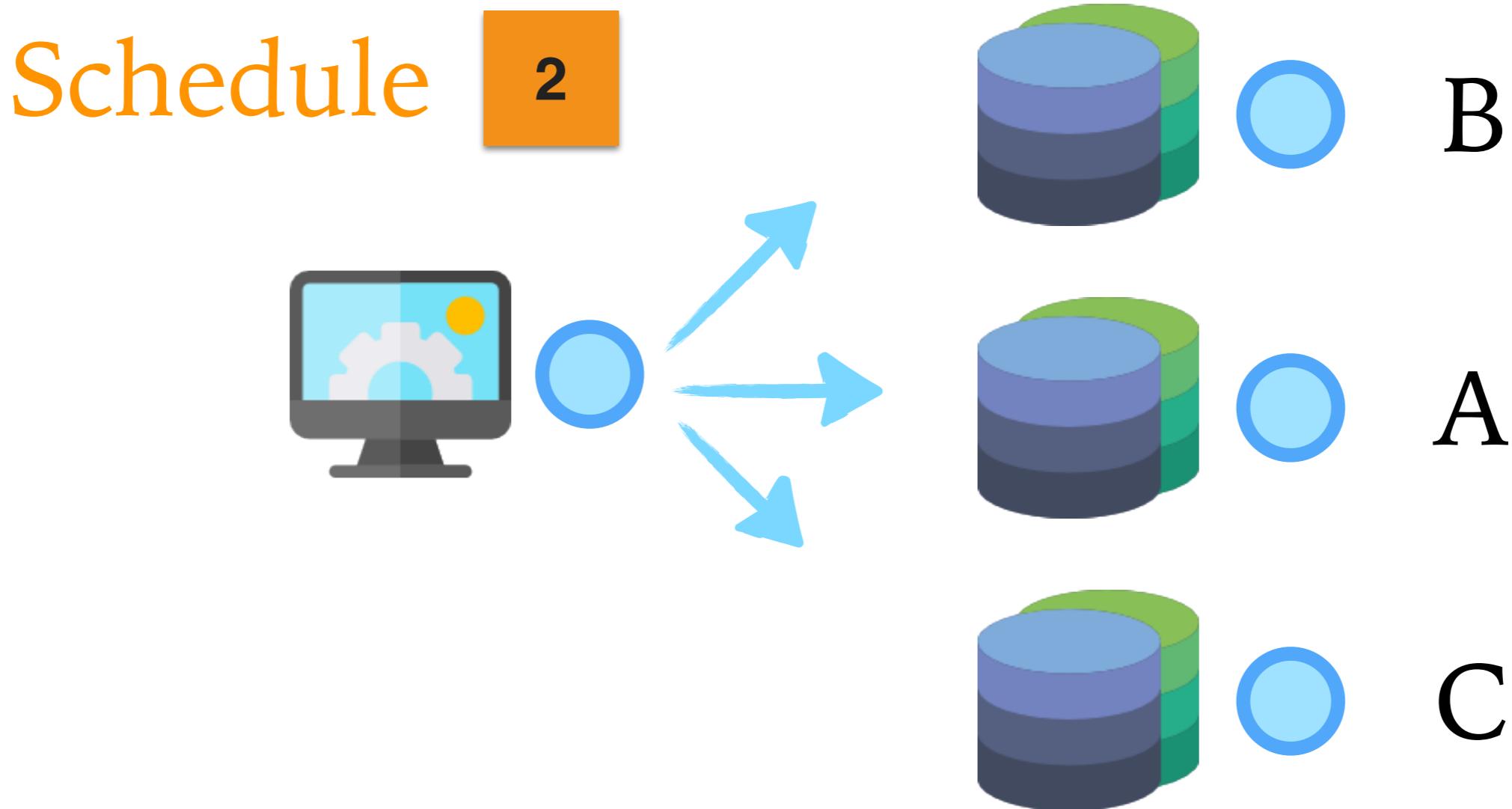
Testing?

Schedule: order on message delivery



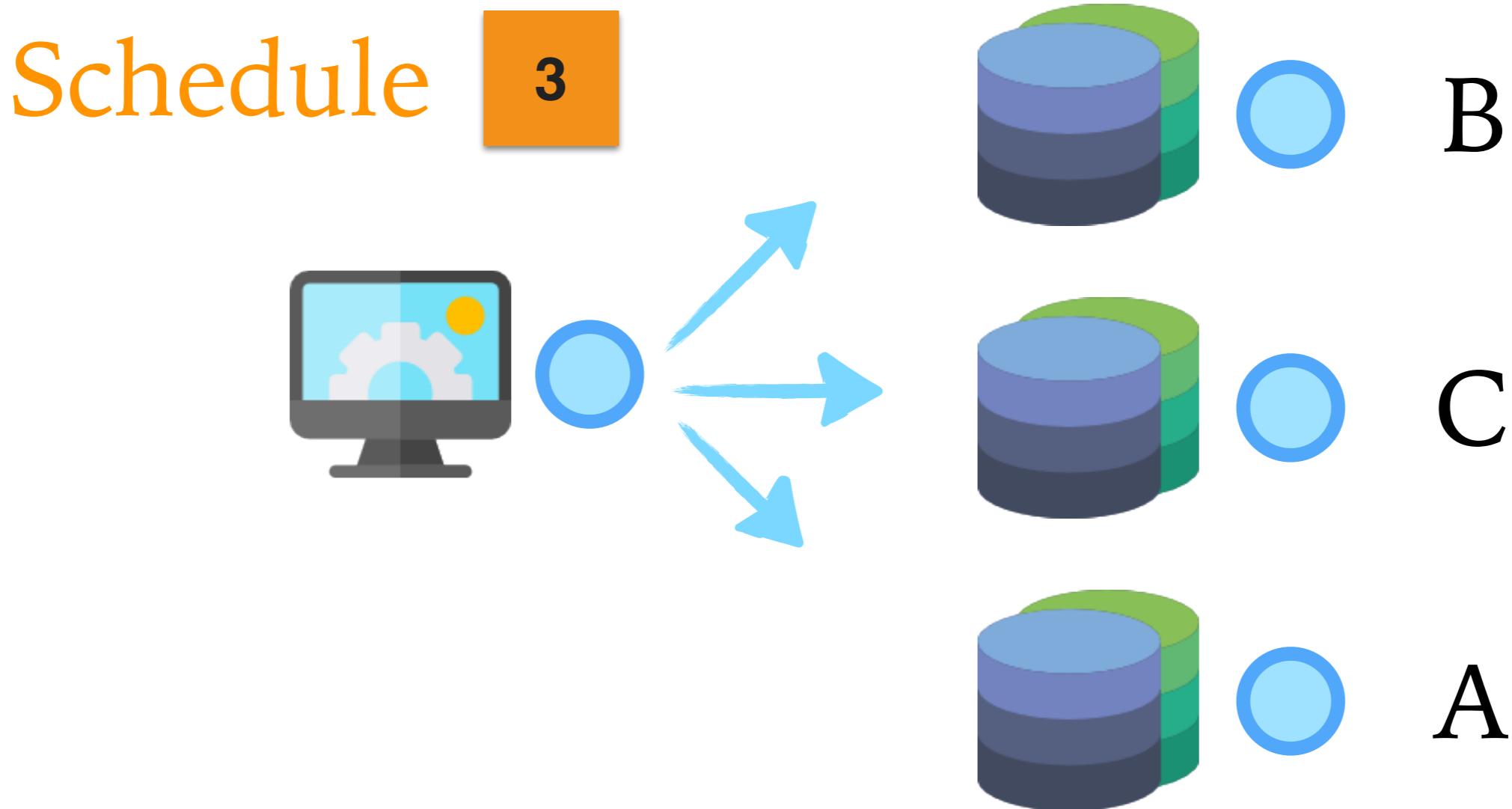
Testing?

Schedule: order on message delivery



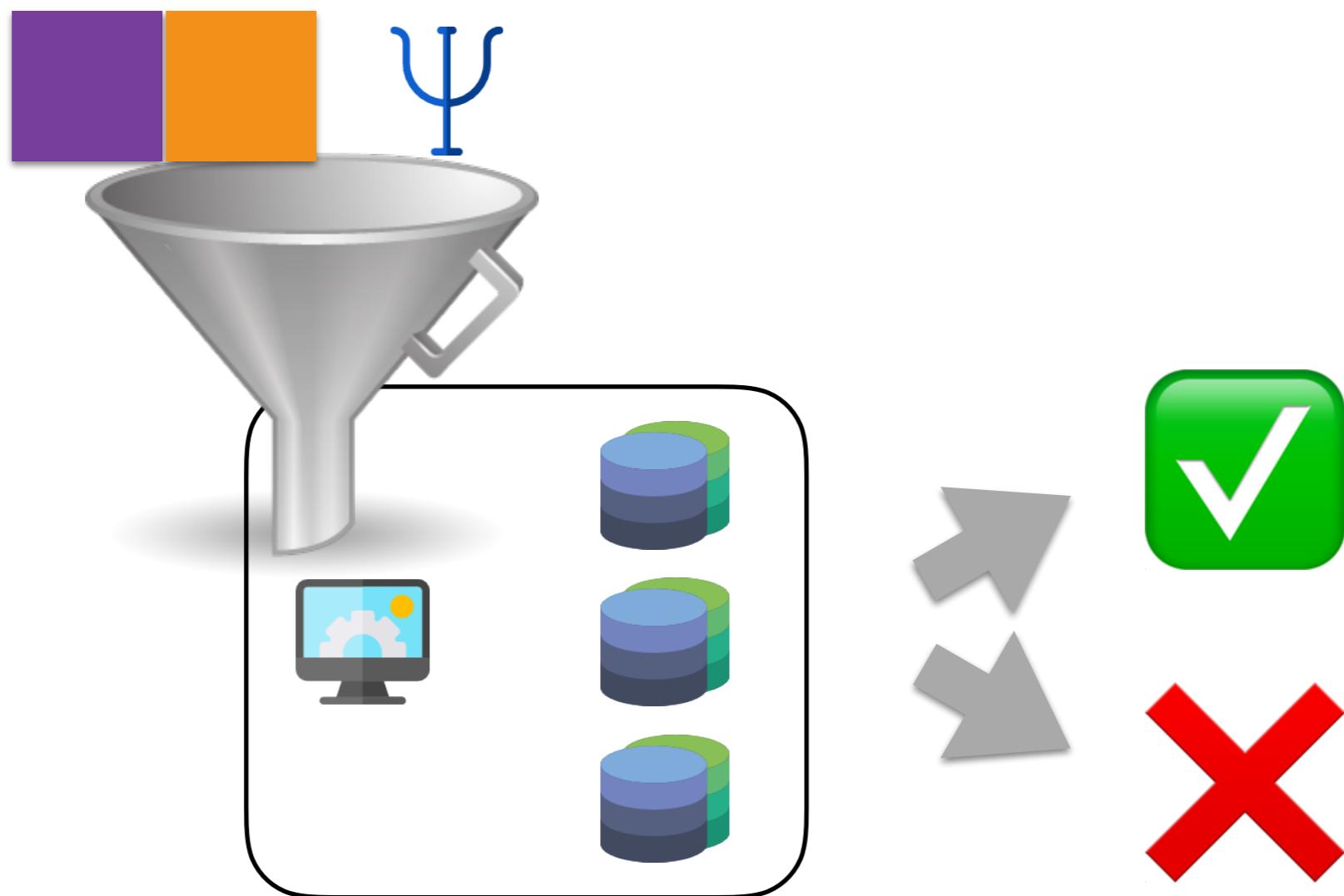
Testing?

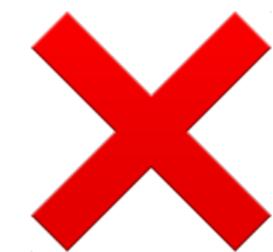
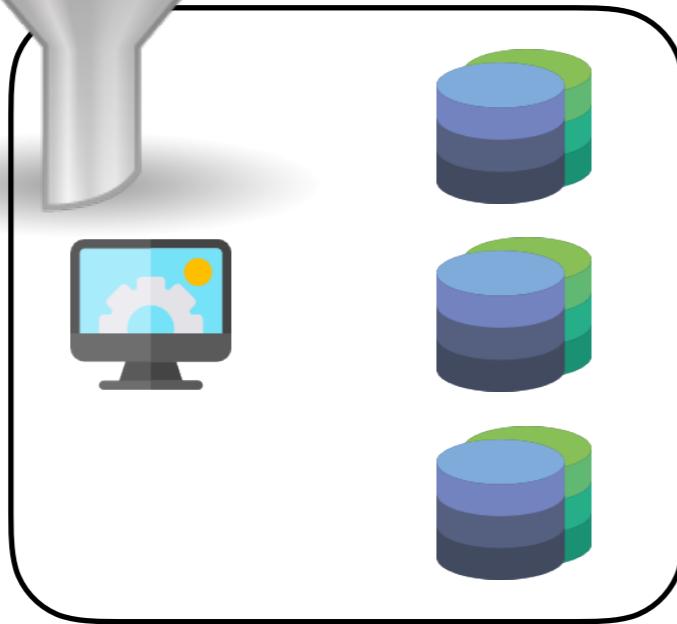
Schedule: order on message delivery



Testing?

Given *input* & *schedule* check property Ψ





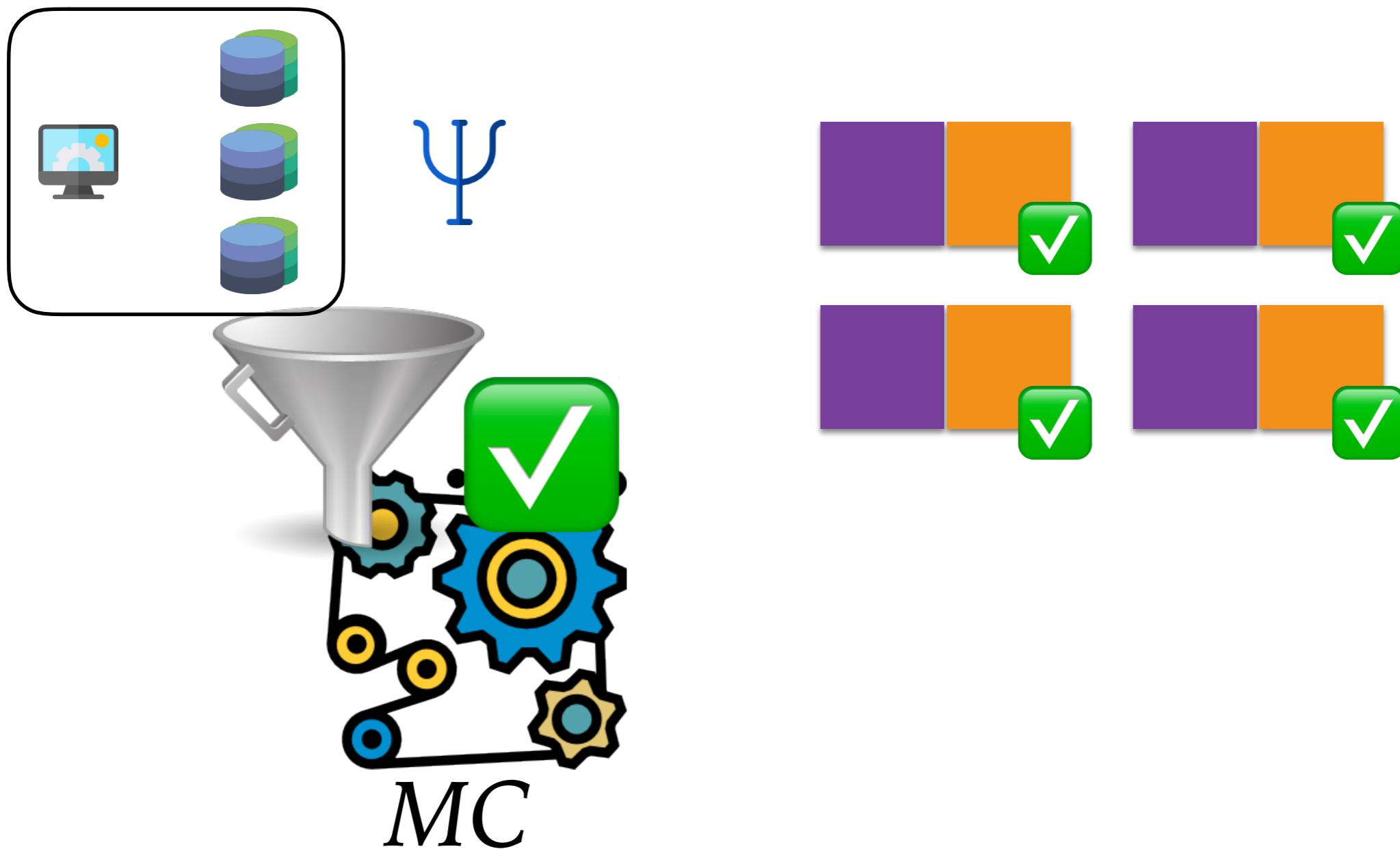
Testing?

Asynchrony: too many schedules

Model Checking?

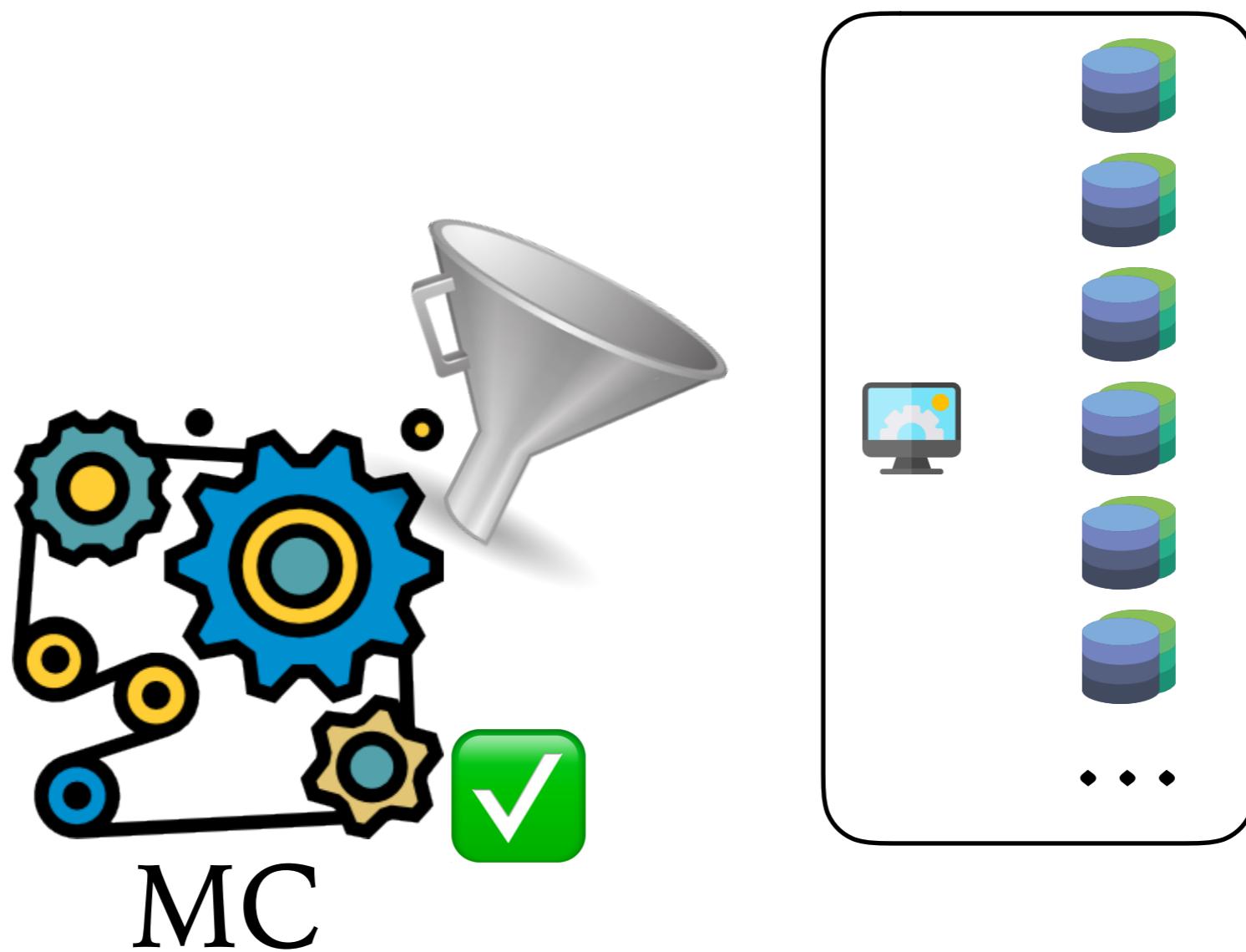
Model Checking?

Enumerate all *inputs* & *schedules*



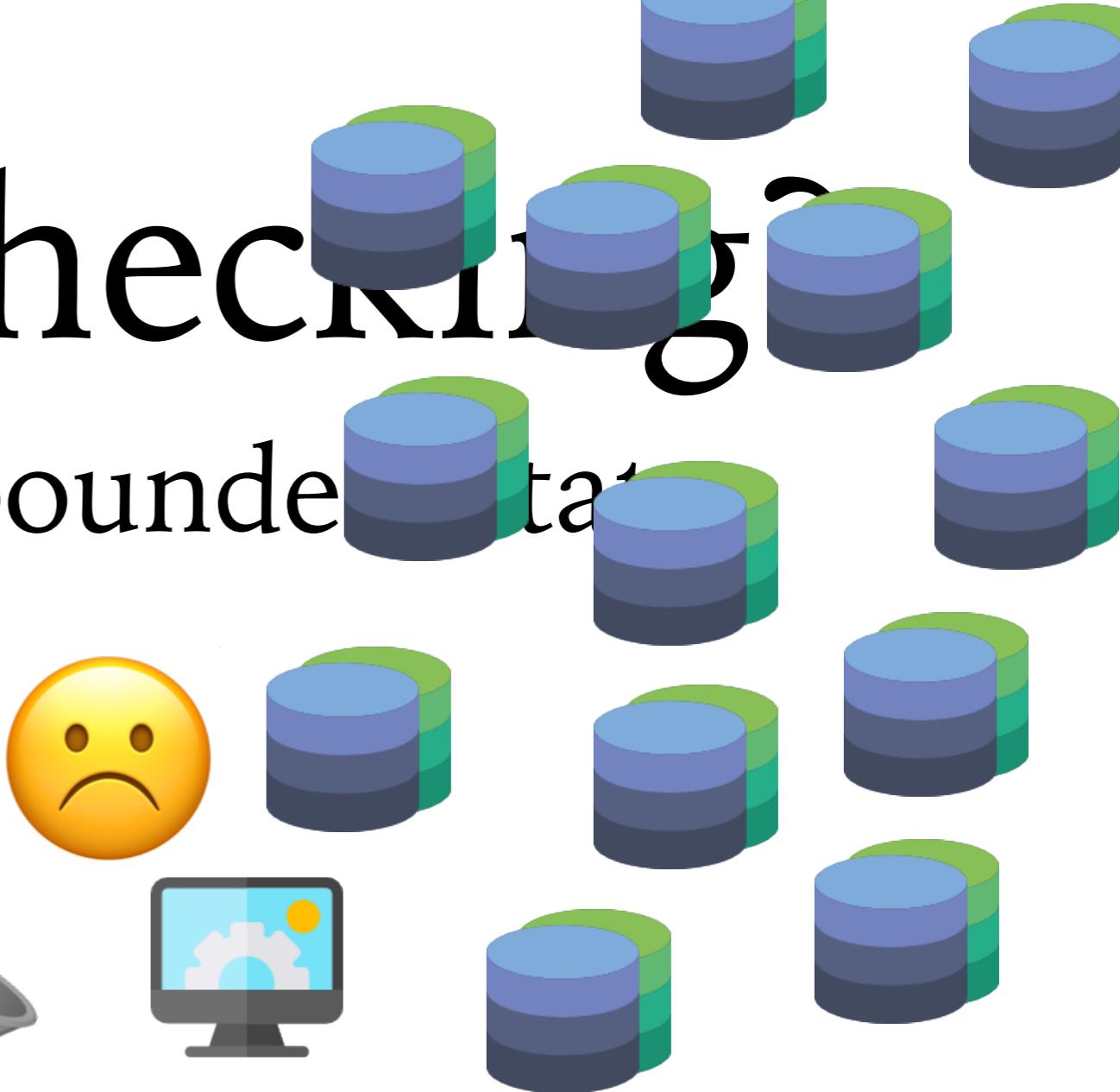
Model Checking?

Problem: Unbounded State



Model Checking

Problem: Unbounded state space



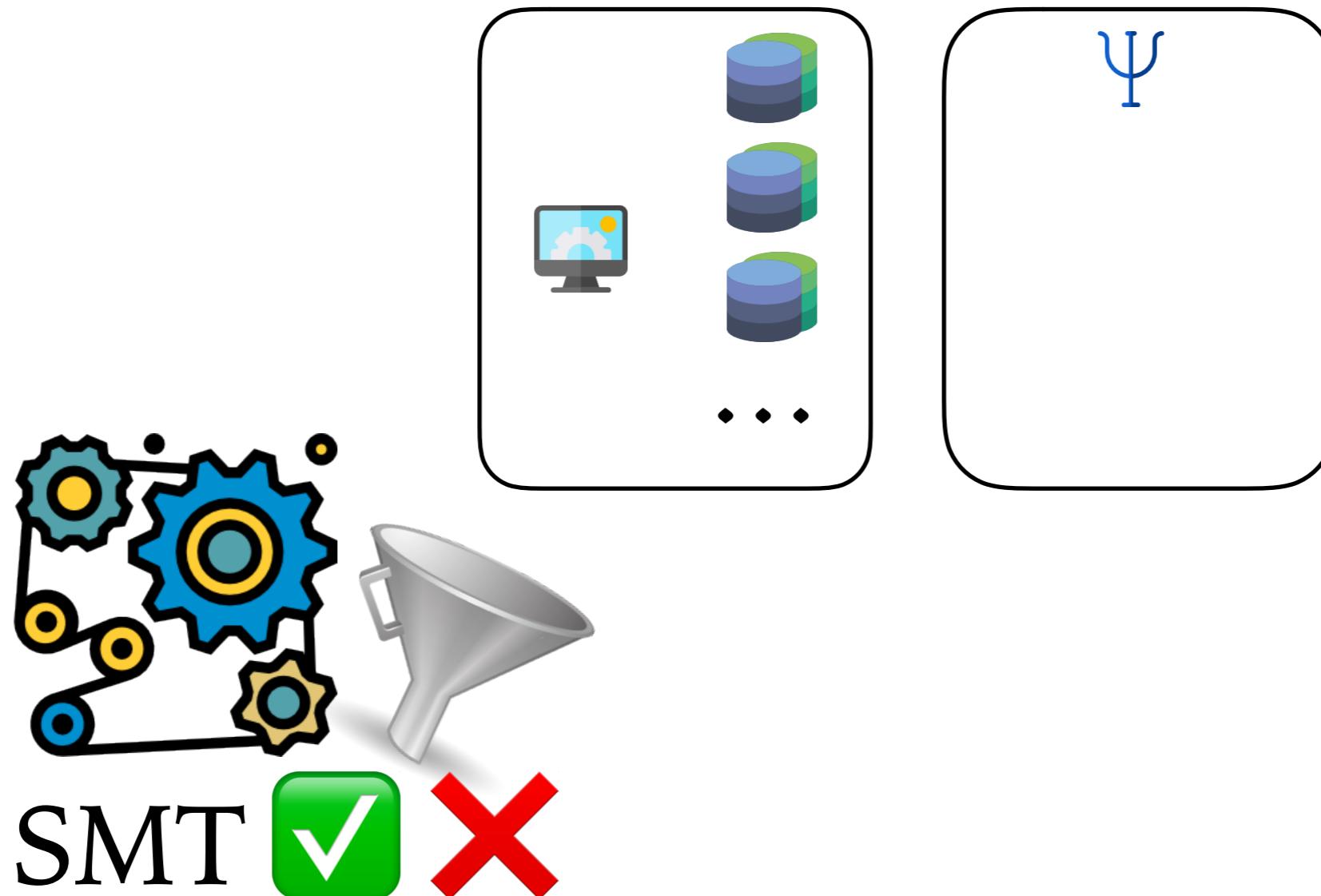
Model Checking?

Problem: Unbounded State

Deductive Verification?

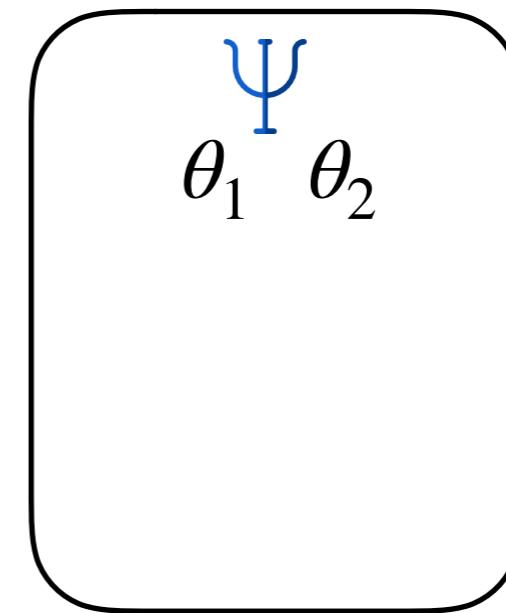
Deductive Verification?

Prove Protocol Correctness



Deductive Verification?

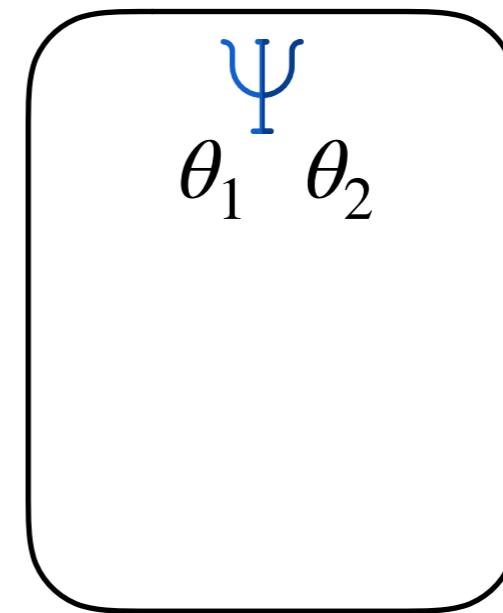
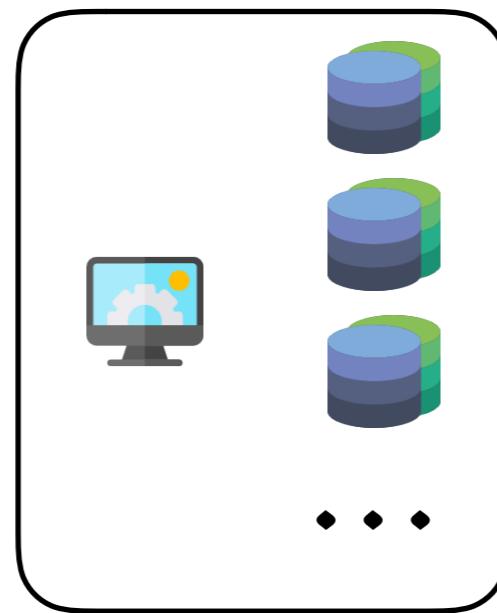
Can handle Unbounded State



... but needs Auxiliary Invariants

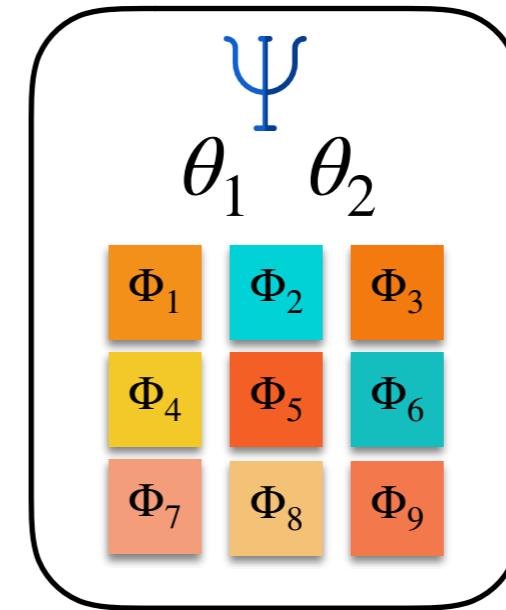
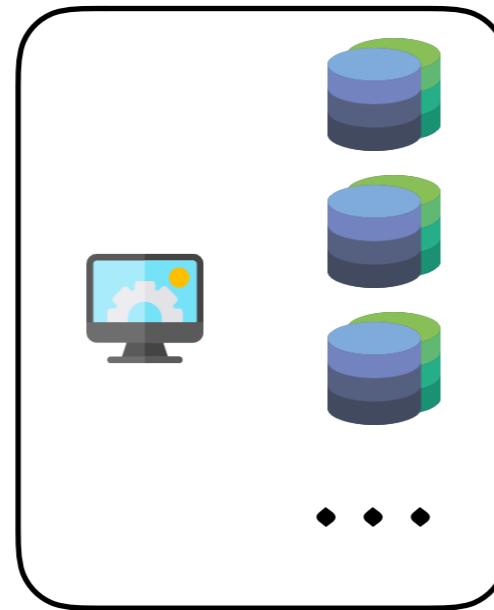
Deductive Verification?

... but needs Auxiliary Invariants



Deductive Verification?

... but needs Auxiliary Invariants



that enumerate
schedules and network state

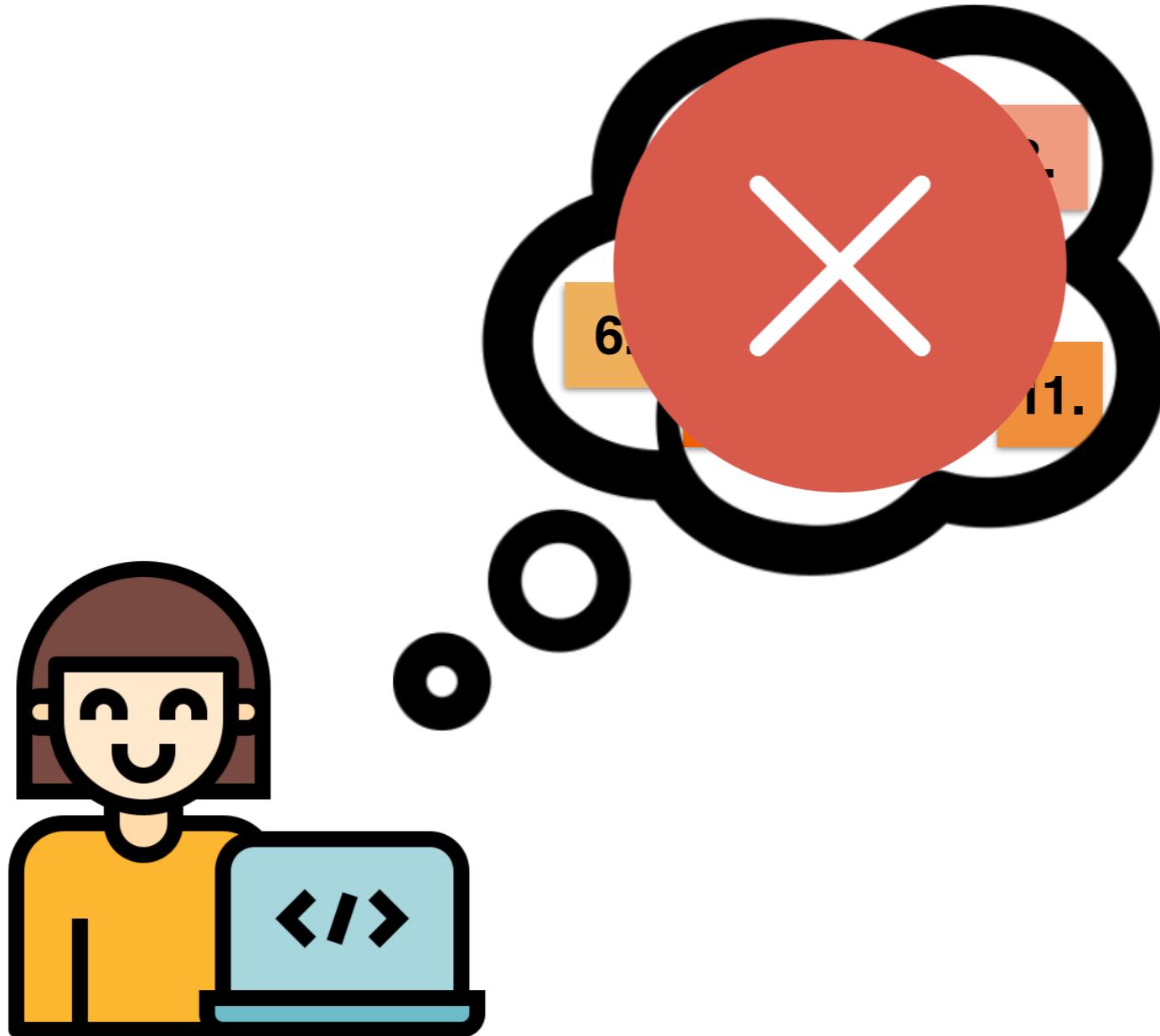
Deductive Verification?

Too many Invariants!

Pretend Synchrony:
Make Proofs Easier!

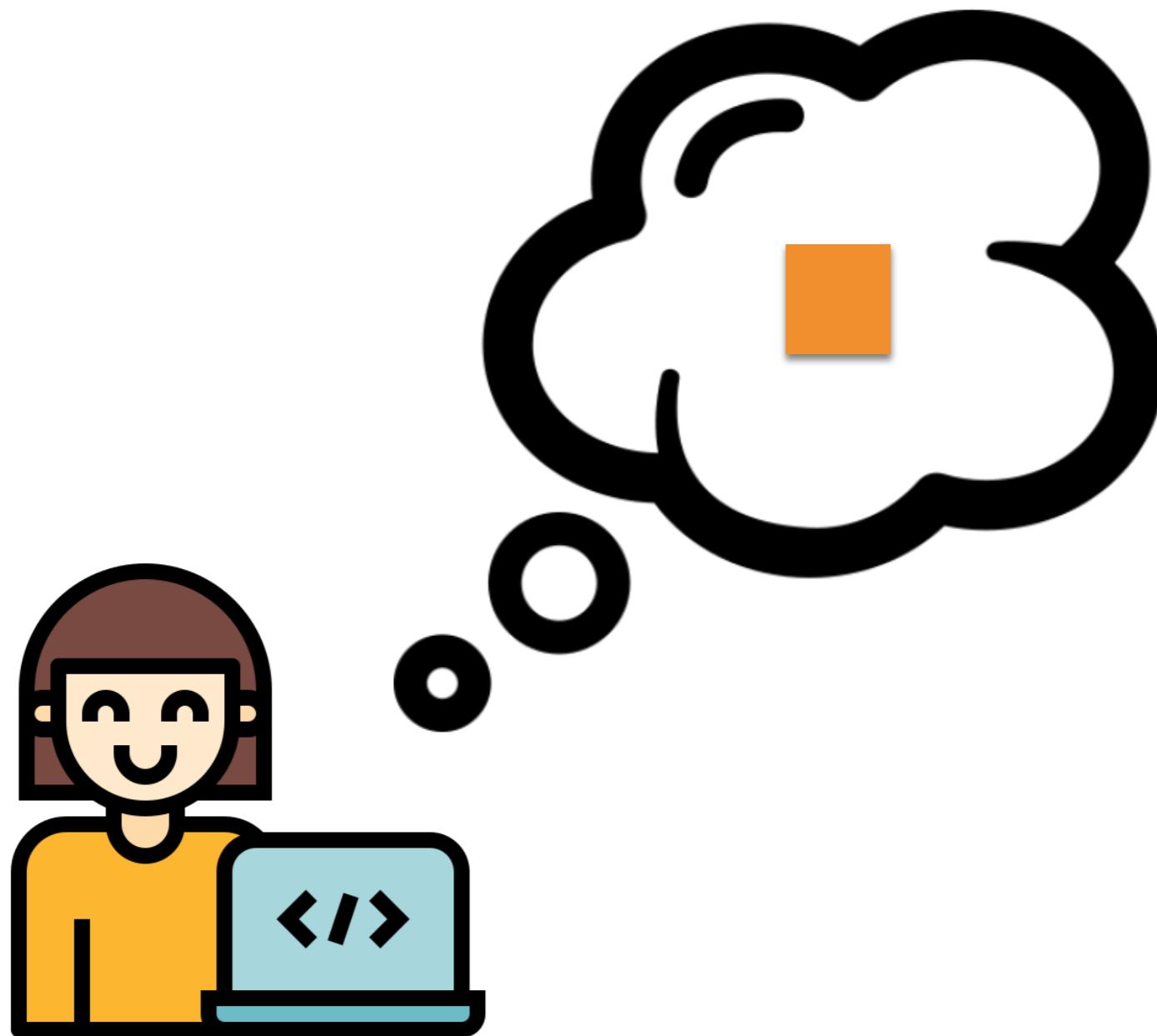
Pretend Synchrony

Programmers *don't case-split on schedules & network*



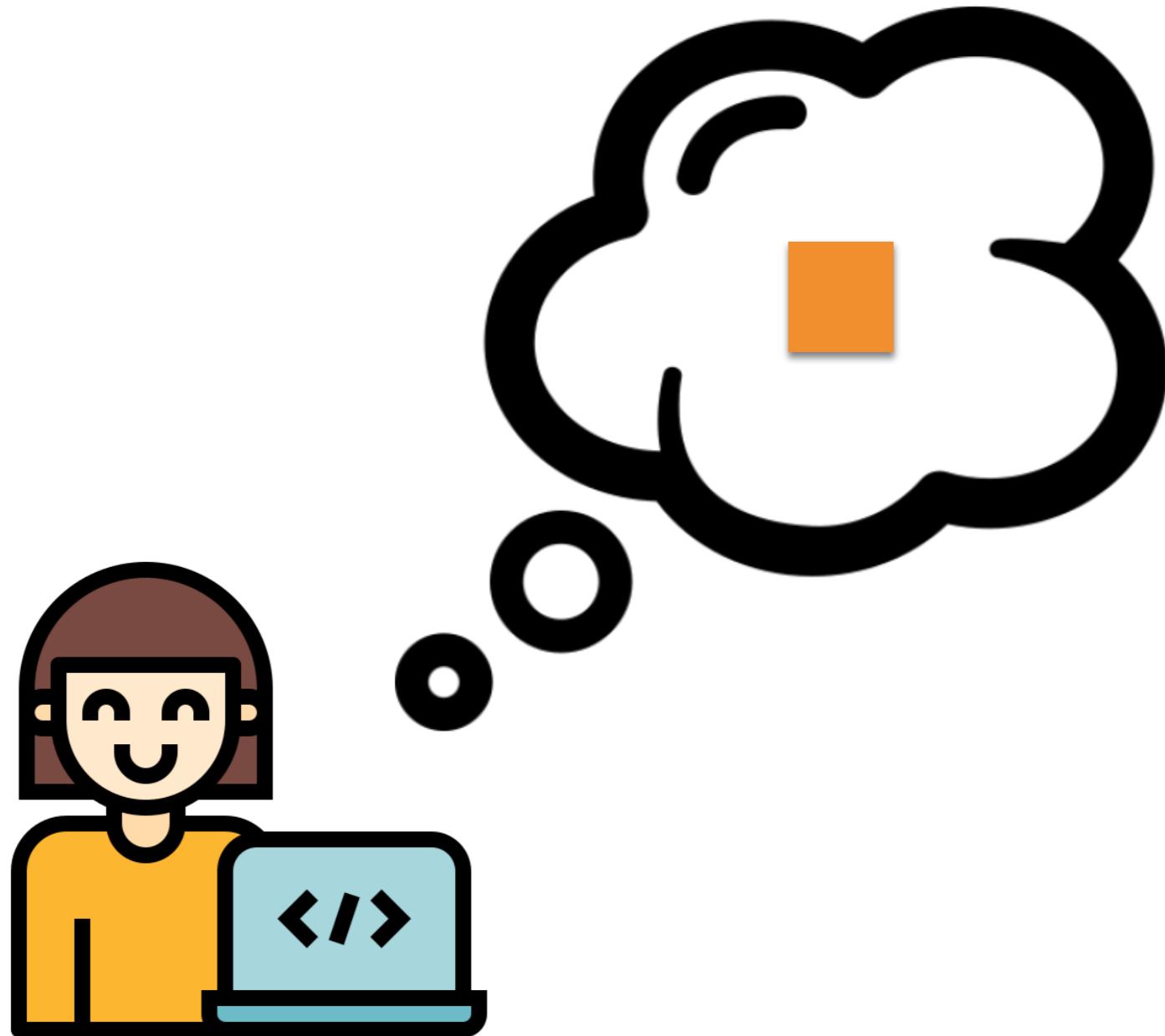
Pretend Synchrony

... they think about a *representative* schedule



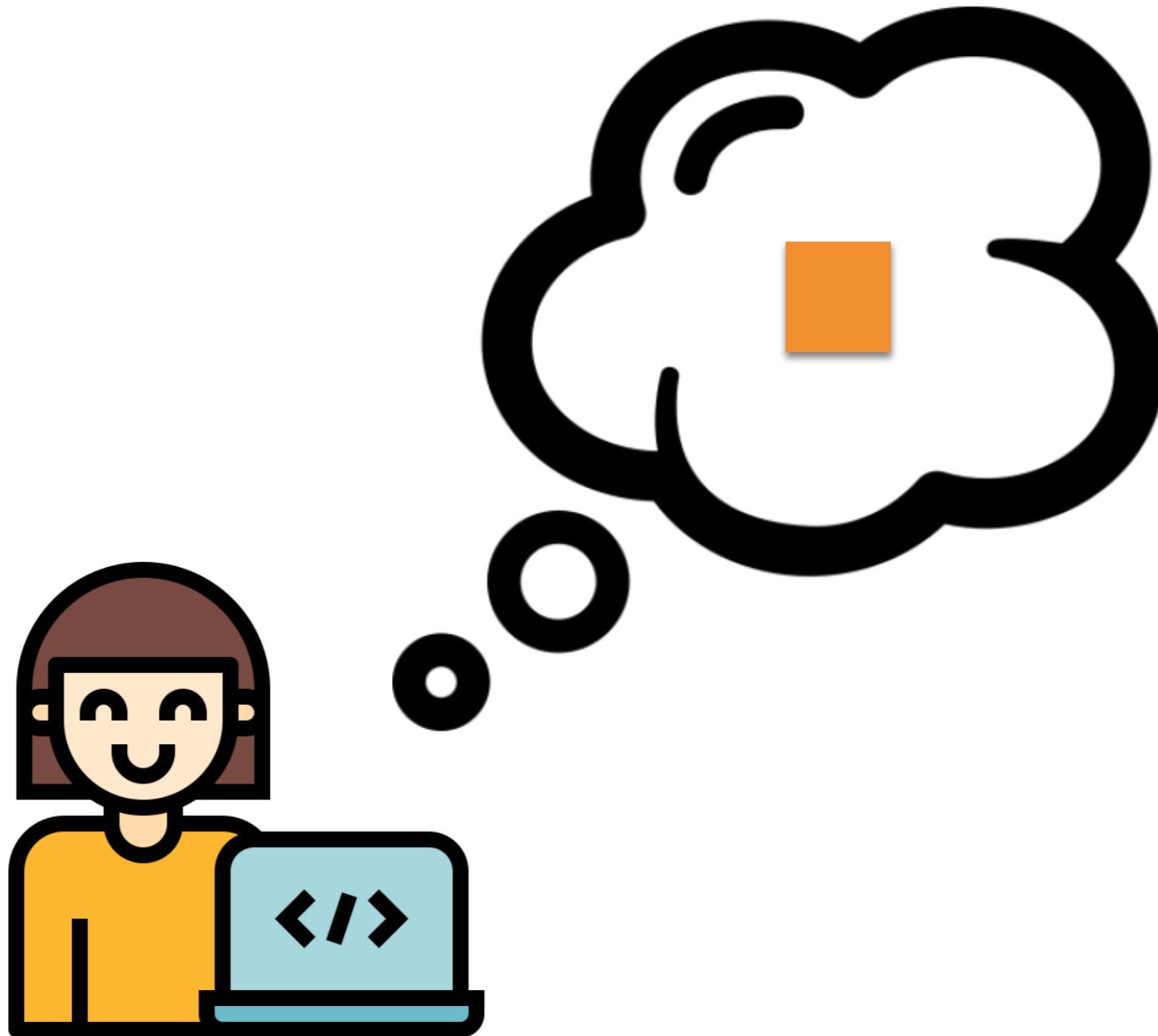
Pretend Synchrony

... where **messages** are delivered *instantaneously*



Pretend Synchrony

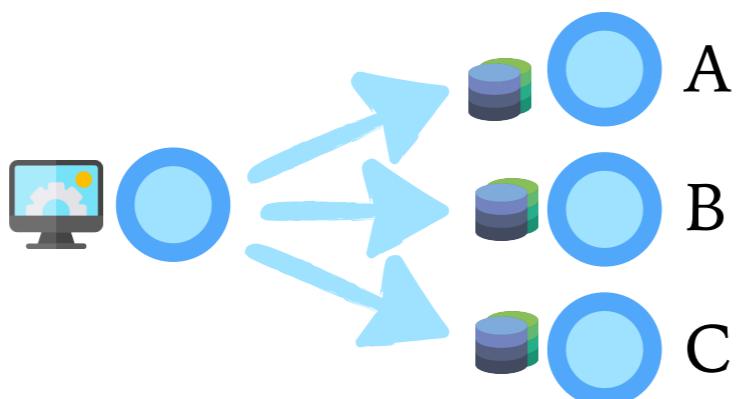
we call this schedule synchronization



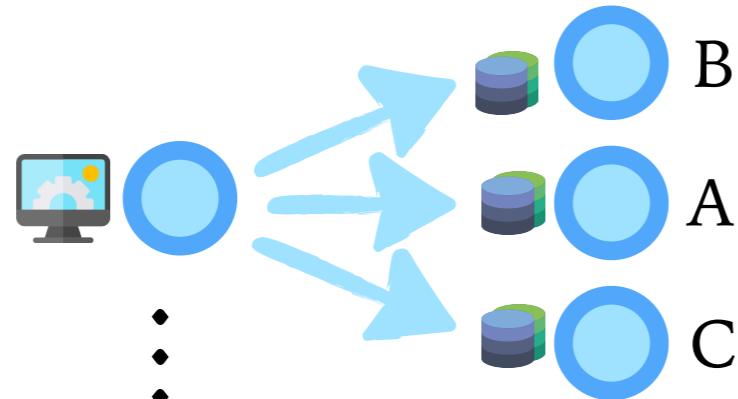
Pretend Synchrony

To verify a protocol

1.

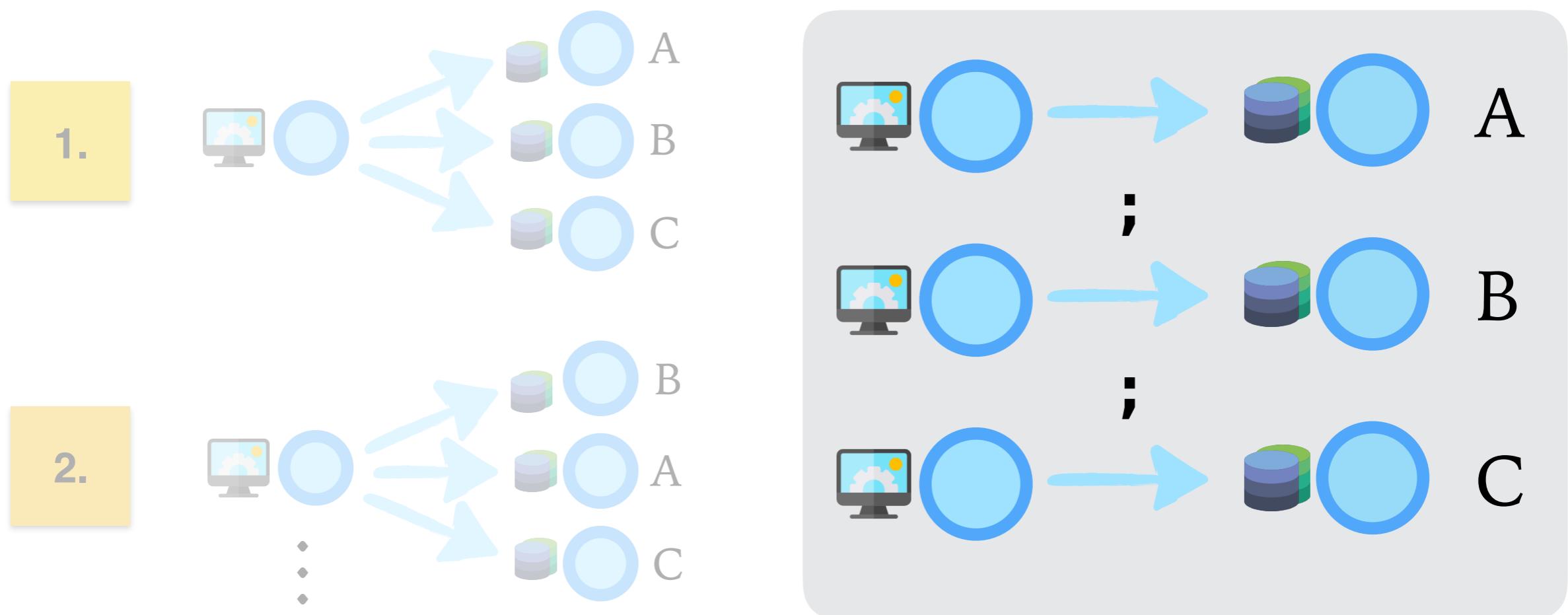


2.



Pretend Synchrony

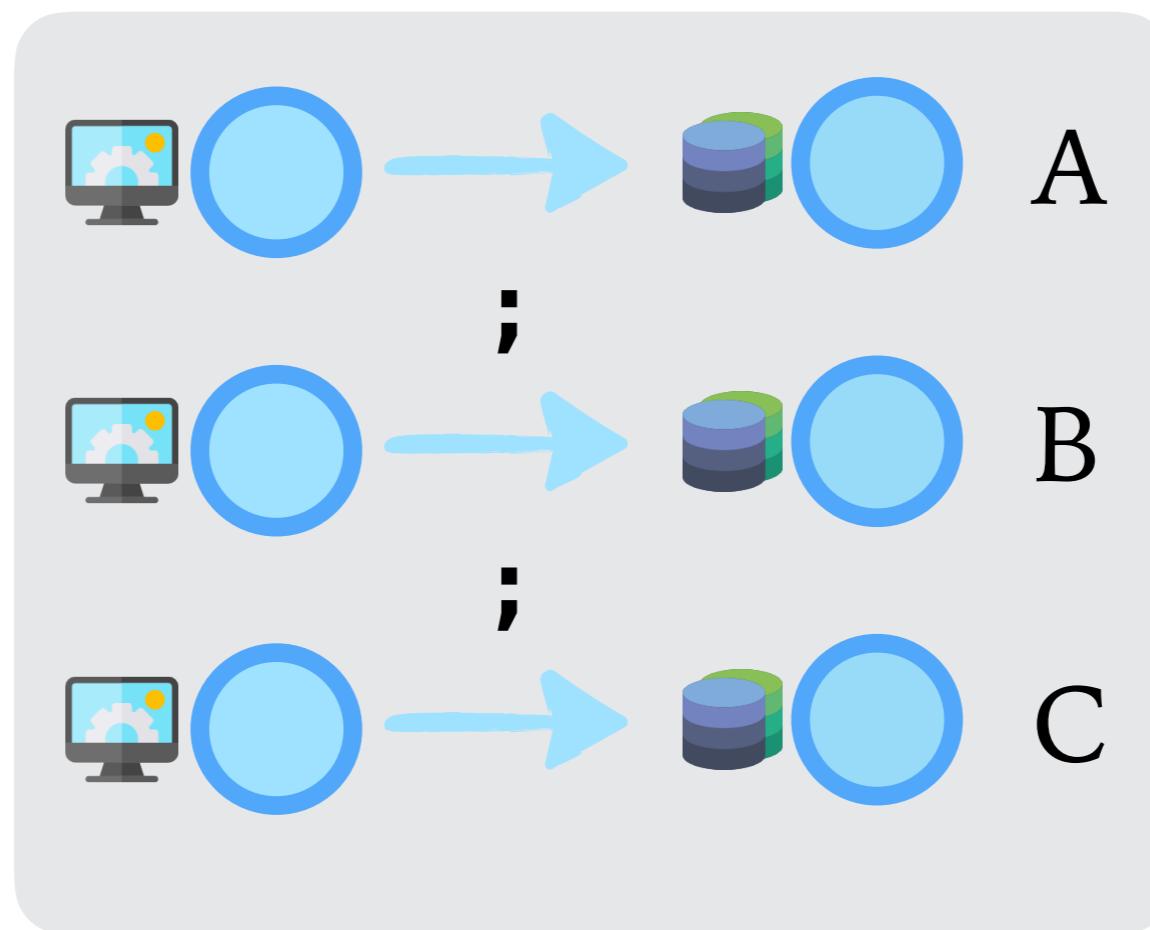
To verify a protocol



... 1. compute its synchronization

Pretend Synchrony

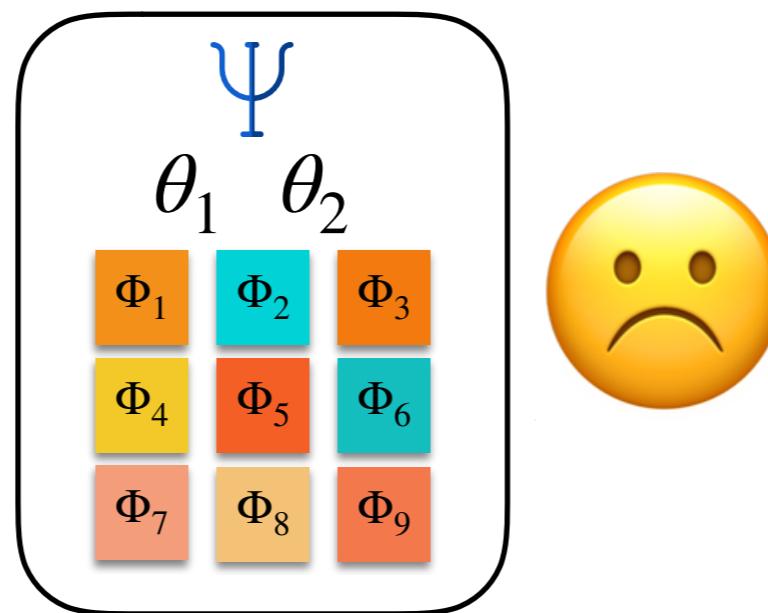
To verify a protocol



... 2. verify synchronization

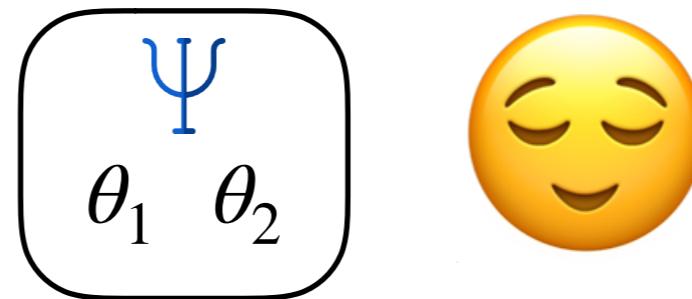
Pretend Synchrony

Synchronizations *don't case-split on
schedules & network*



Pretend Synchrony

Synchronizations *don't case-split on*
schedules & network



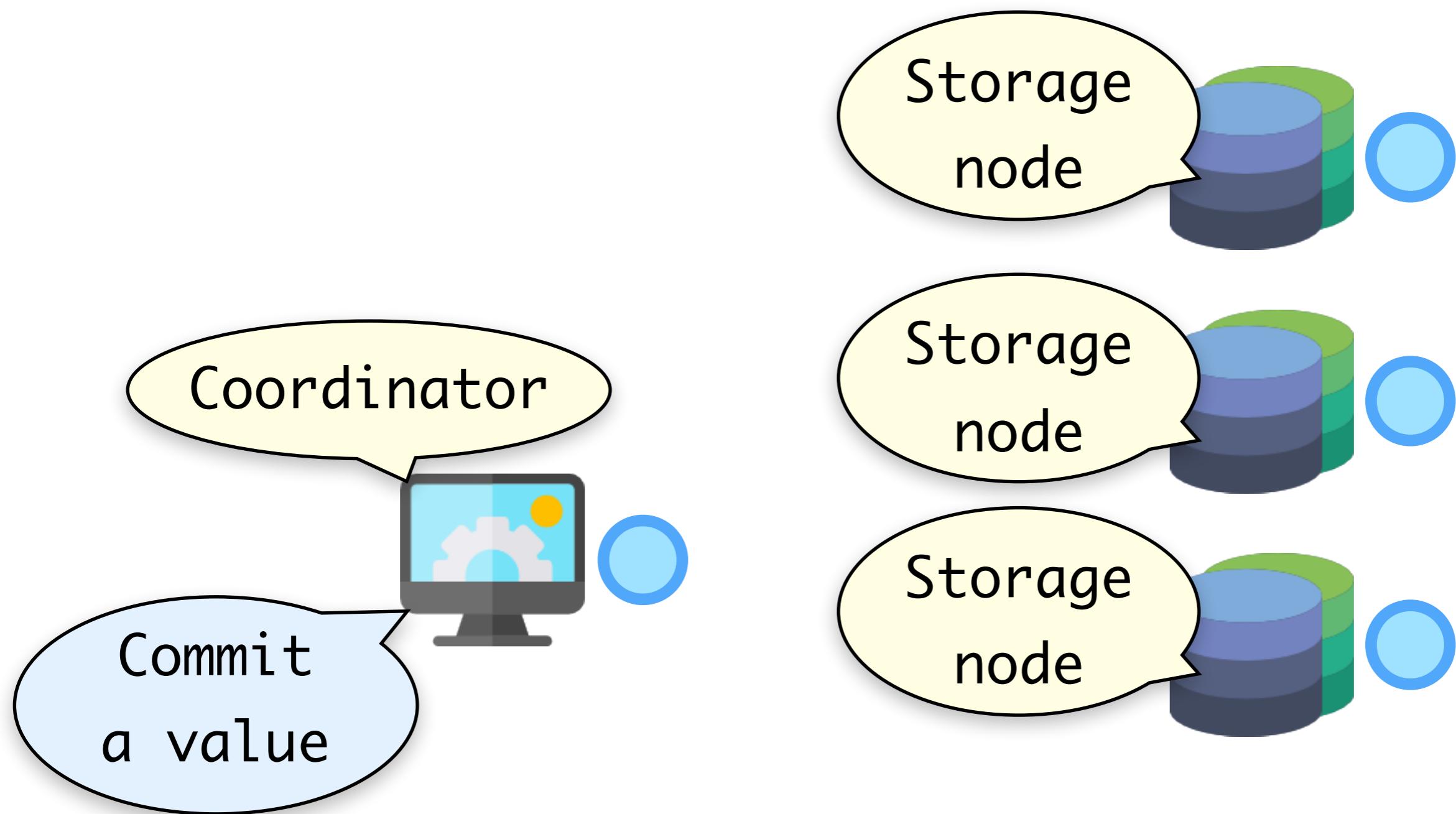
... which significantly *reduces* invariants

Synchronizations

Example 1: Two-Phase Commit

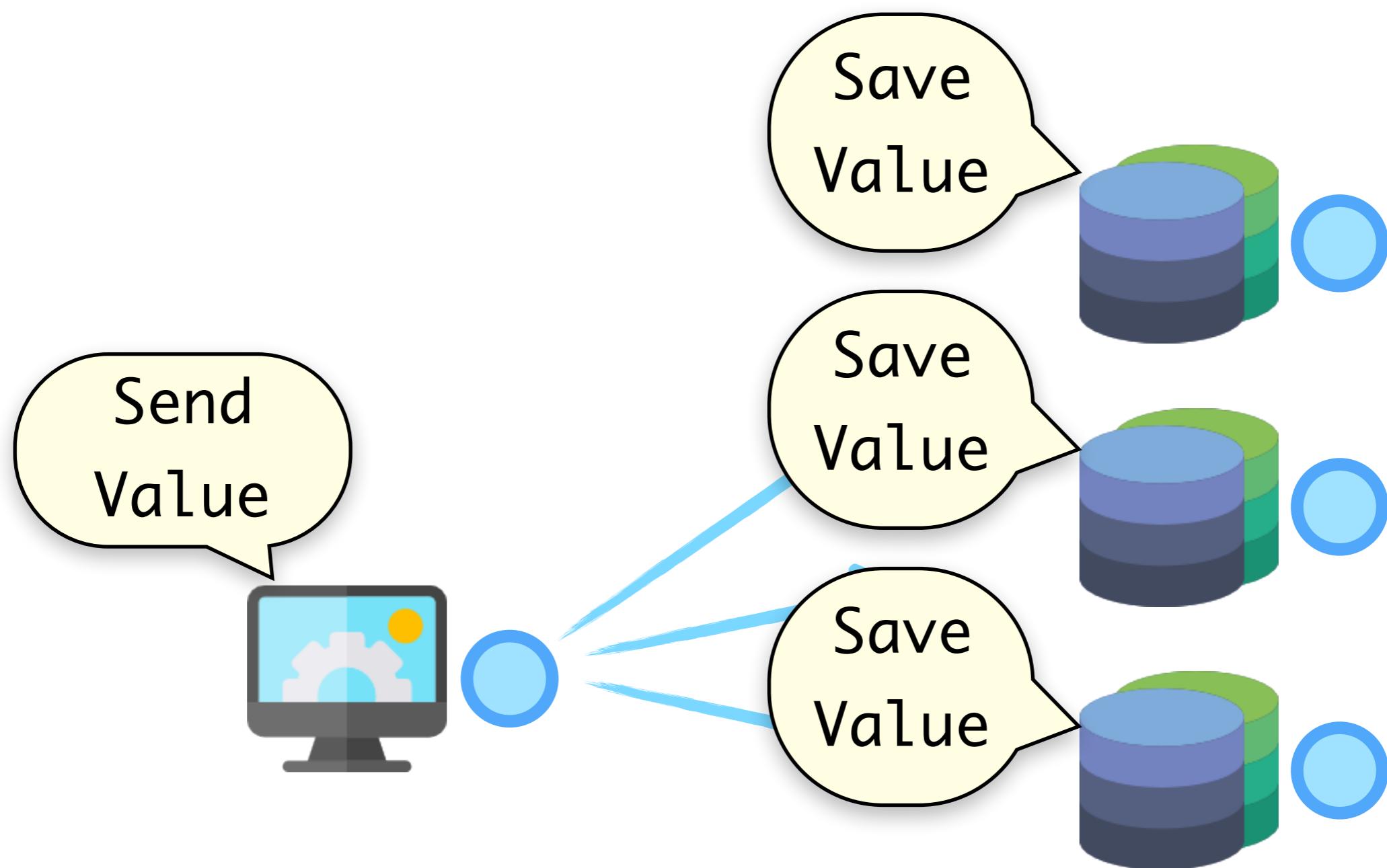
Two-Phase Commit

Phase 1



Two-Phase Commit

Phase 1

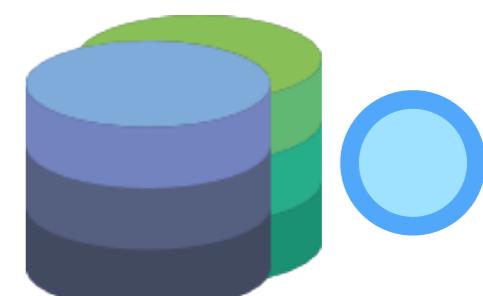
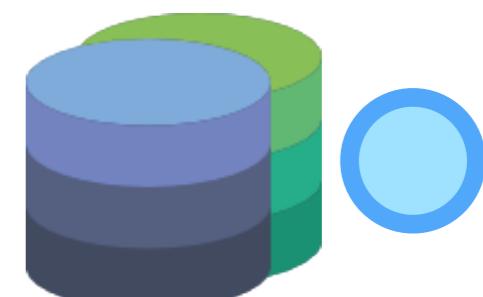
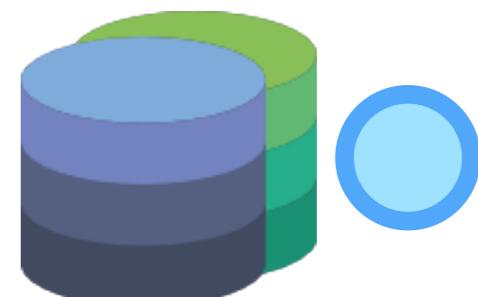
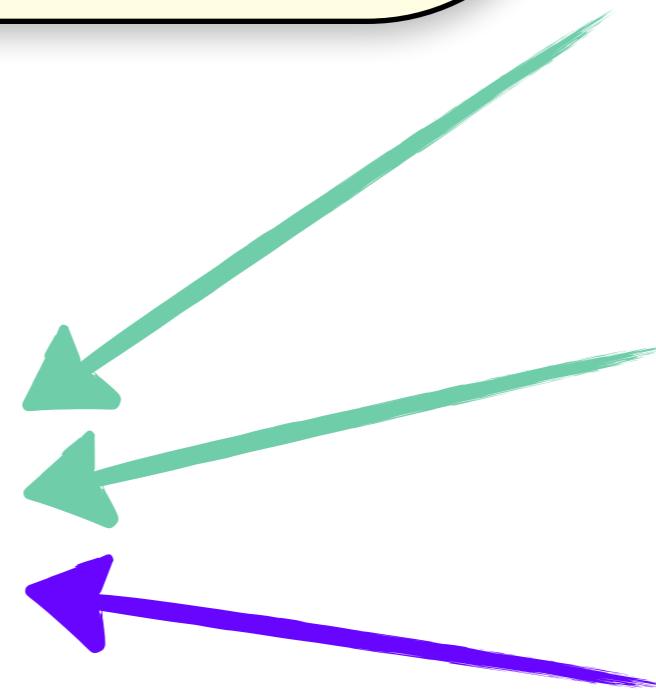
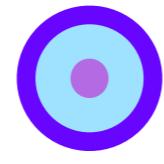


Two-Phase Commit

Phase 1

Use
value to **commit**
or **abort**

Abort if any
node aborts

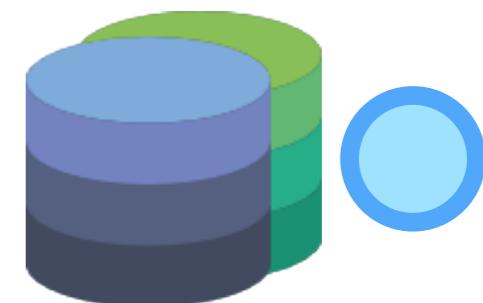
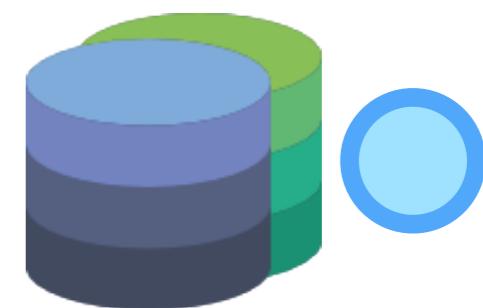
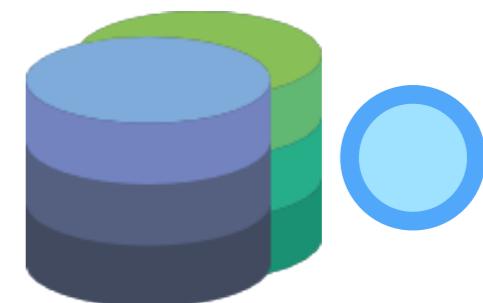
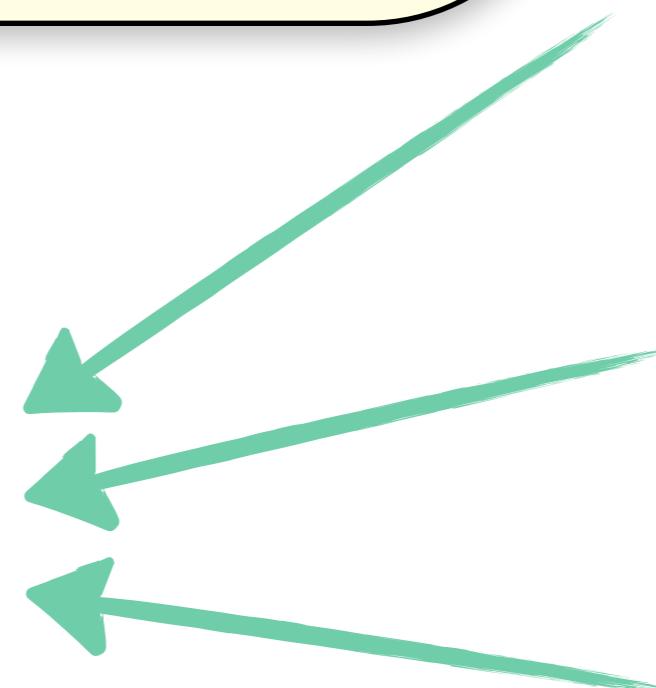


Two-Phase Commit

Phase 1

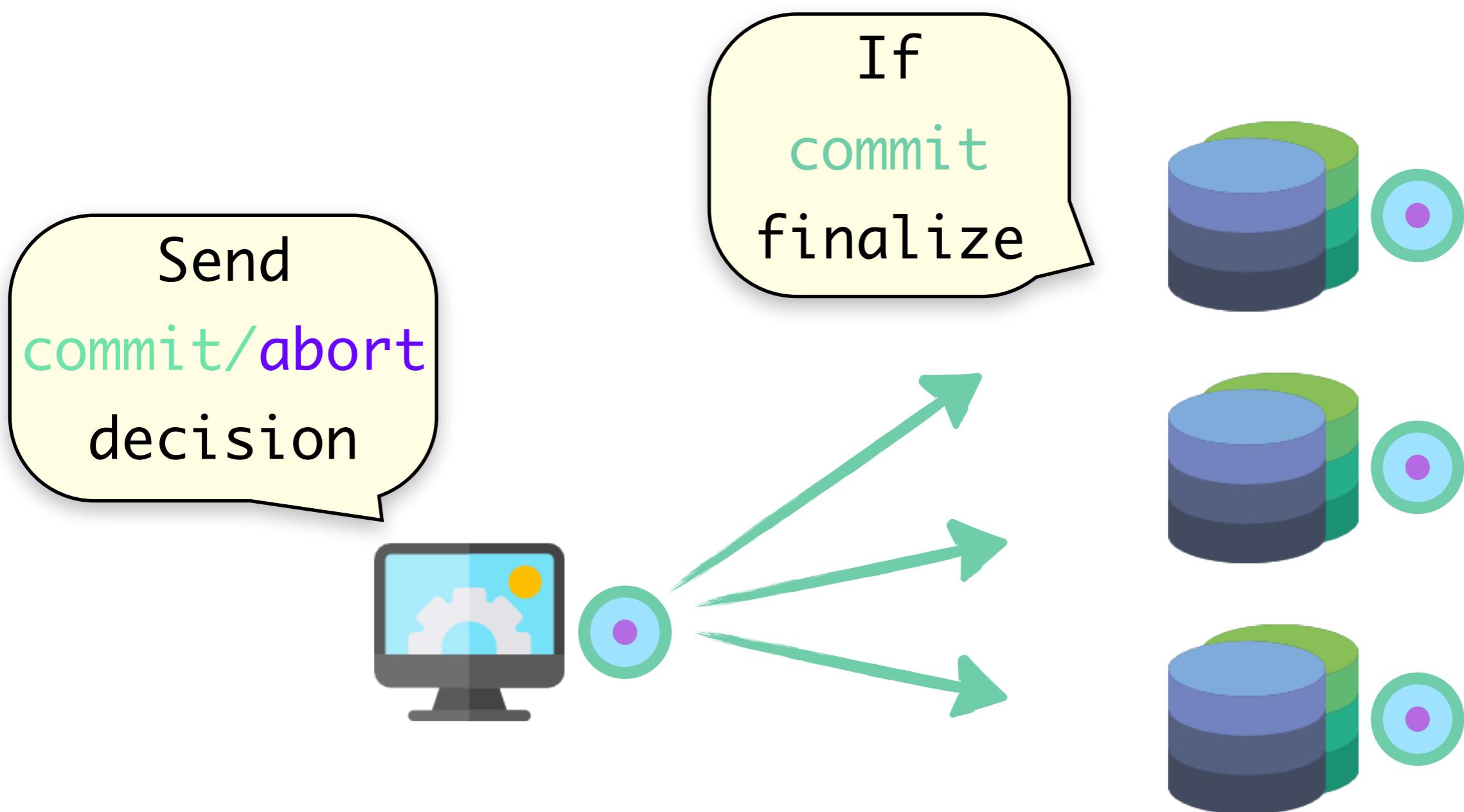
Use
value to **commit**
or **abort**

Commit if
all nodes
commit



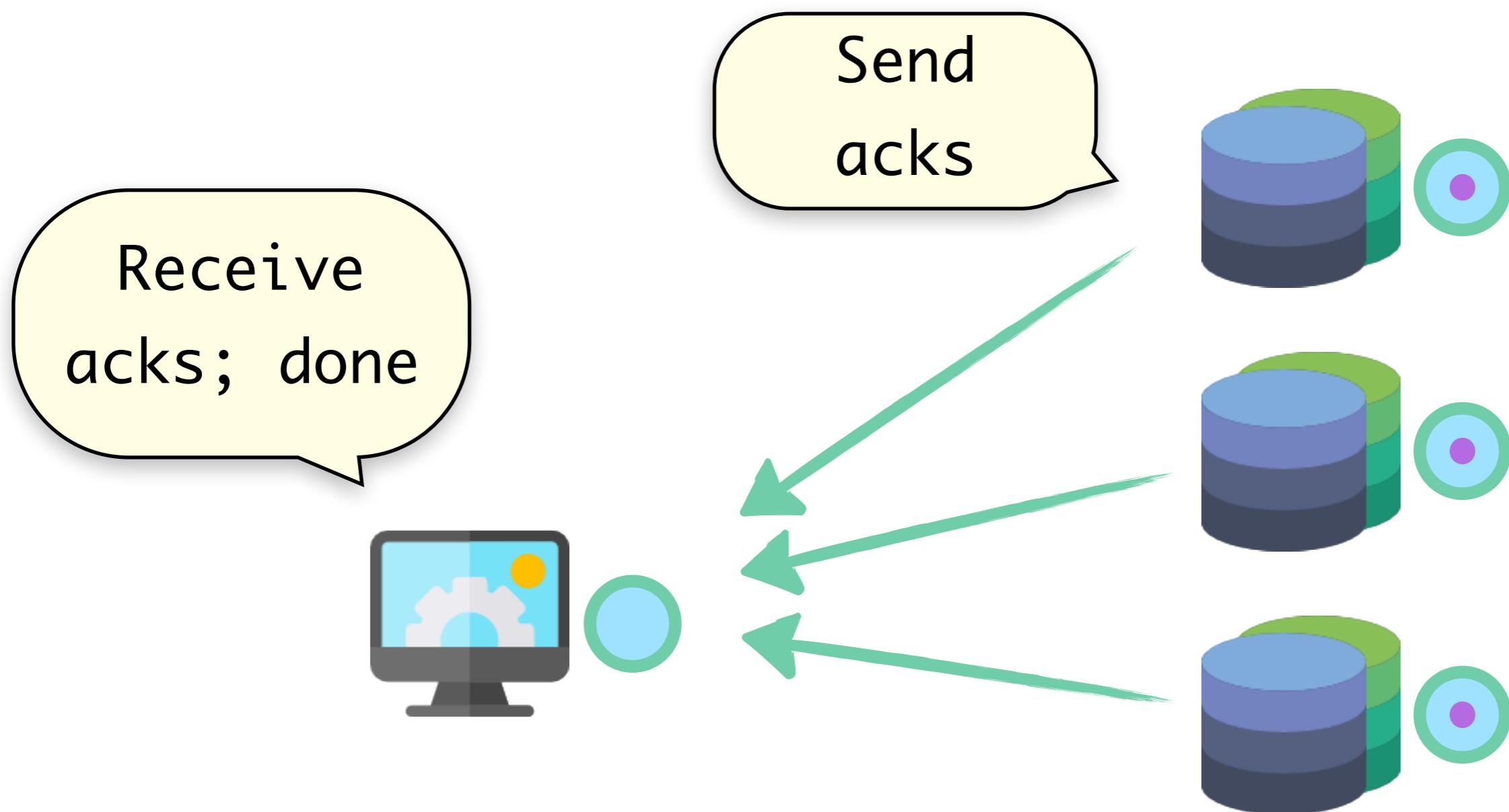
Two-Phase Commit

Phase 2



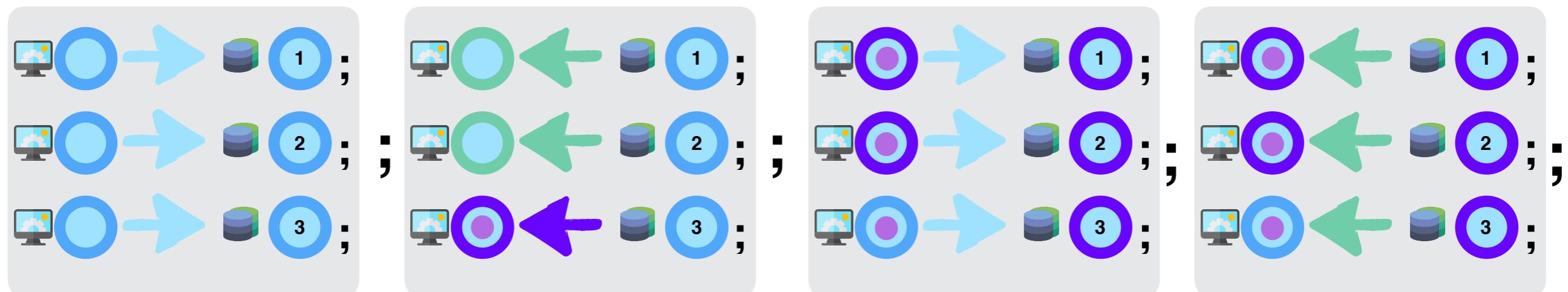
Two-Phase Commit

Phase 2



Two-Phase Commit

Synchronization



1.

Send **value**
to nodes

2.

Respond
commit/abort

3.

Relay **decision**

4.

Gather **acks**

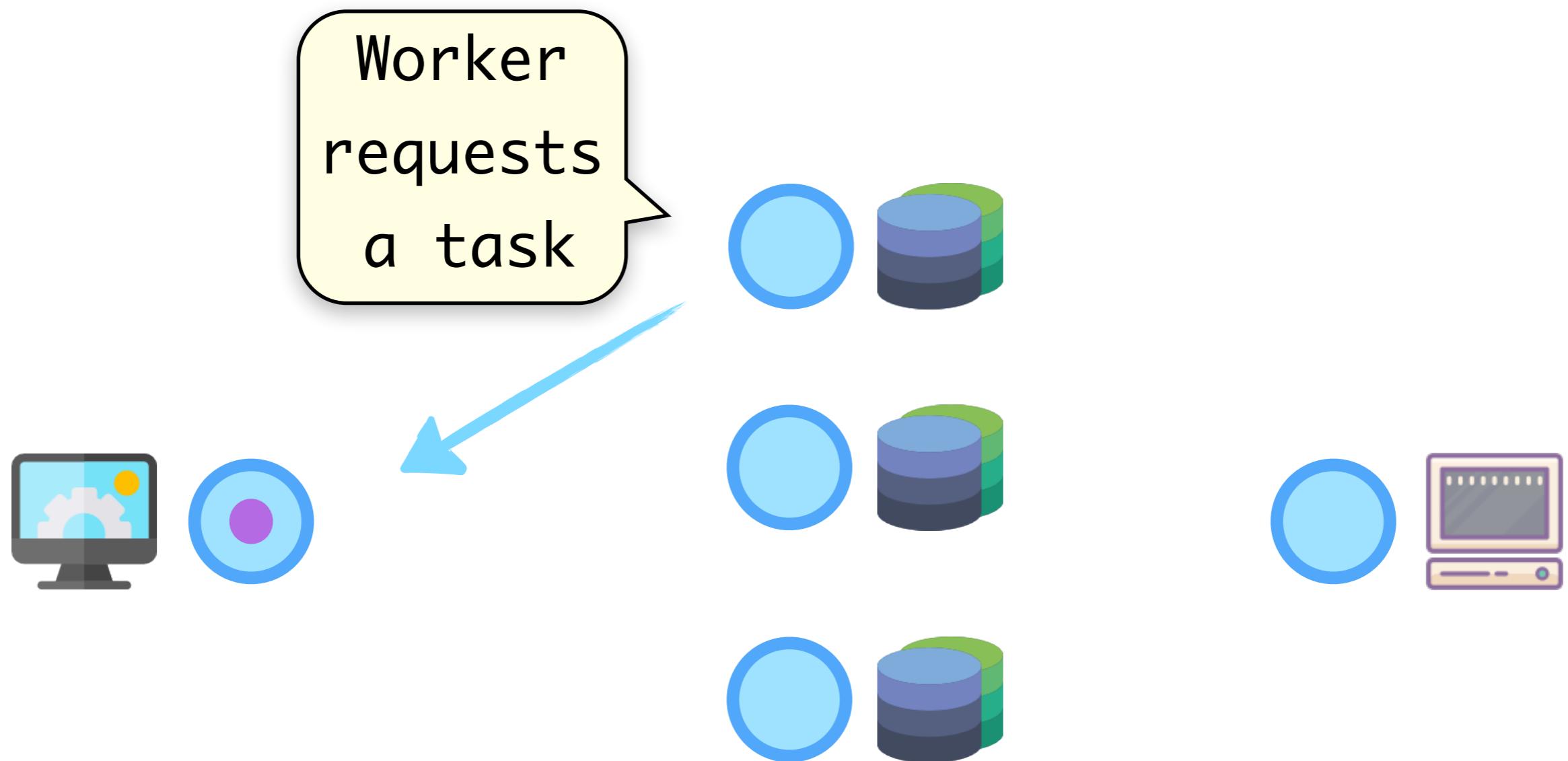
Synchronizations

Example 2: Work stealing Queue

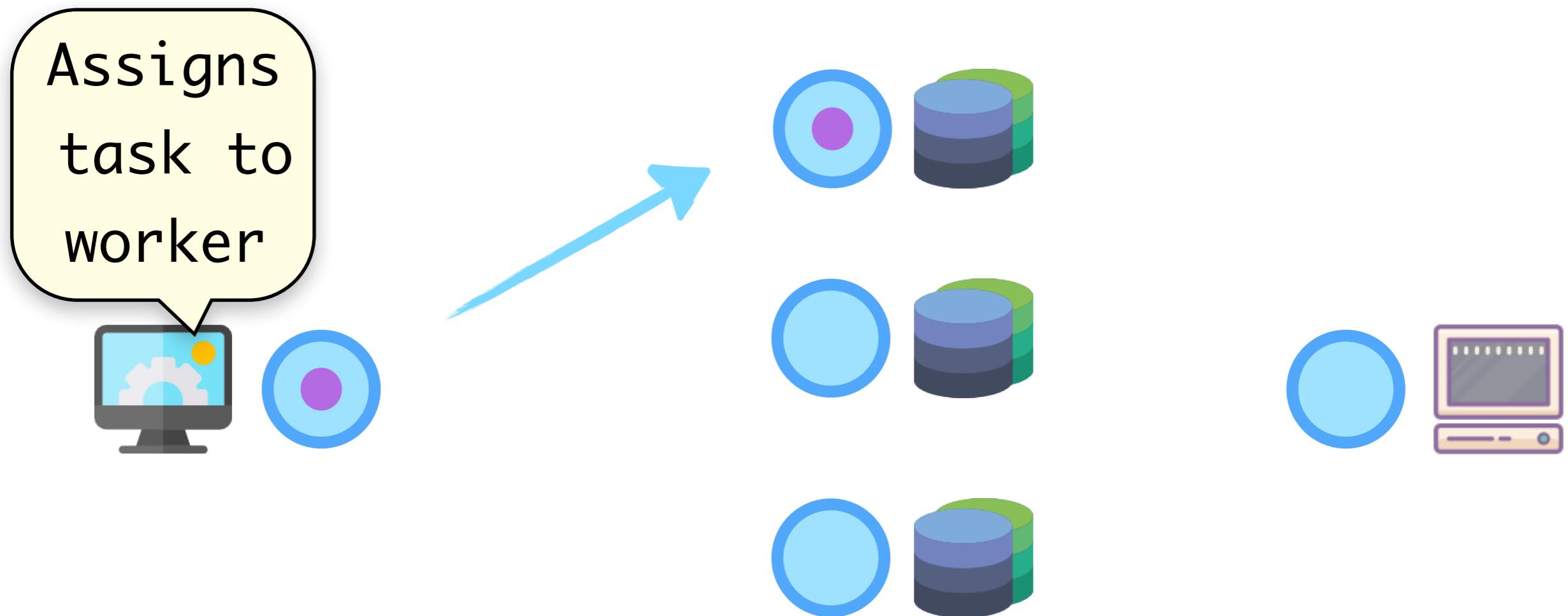
Work stealing Queue



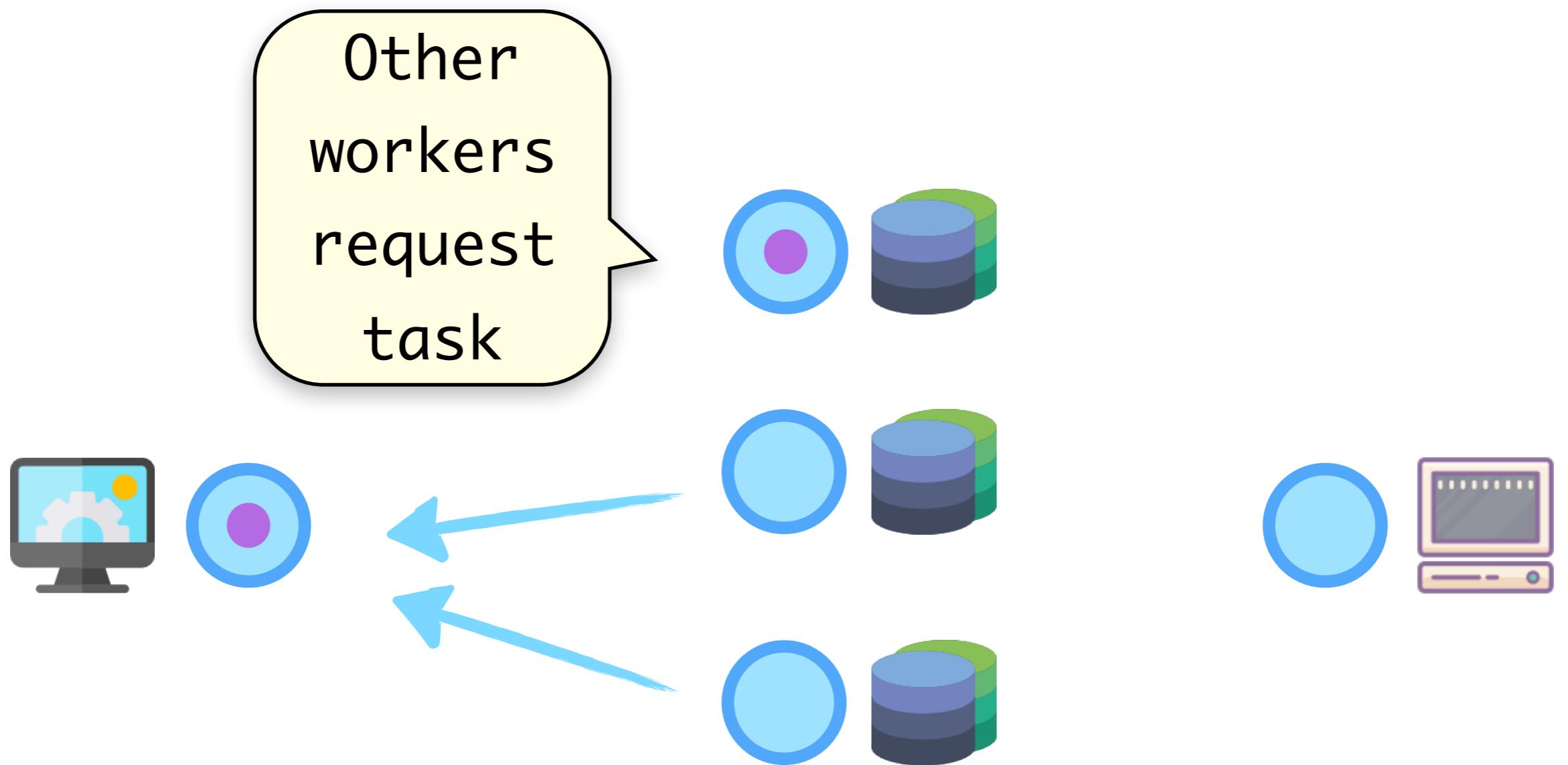
Work stealing Queue



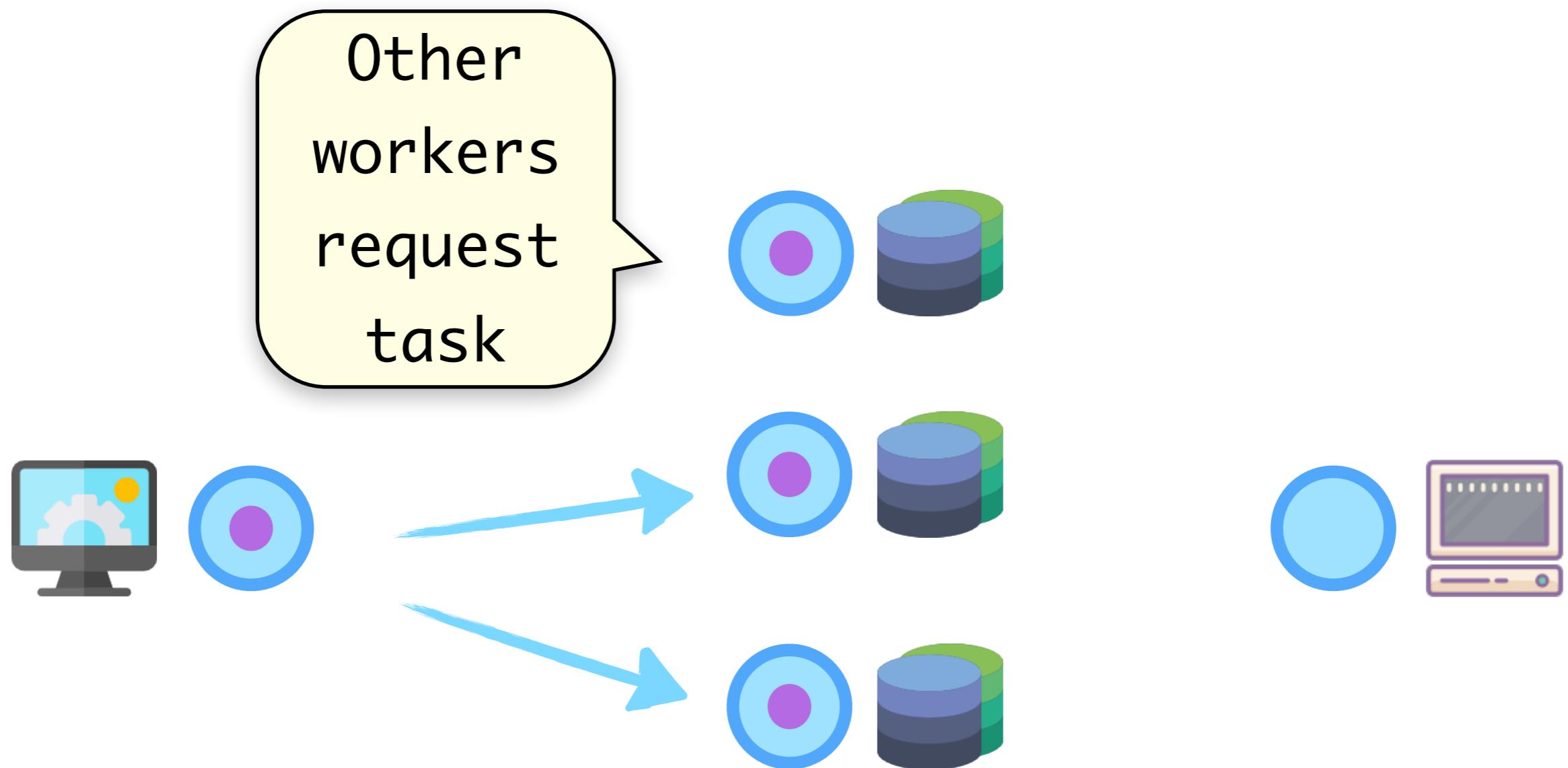
Work stealing Queue



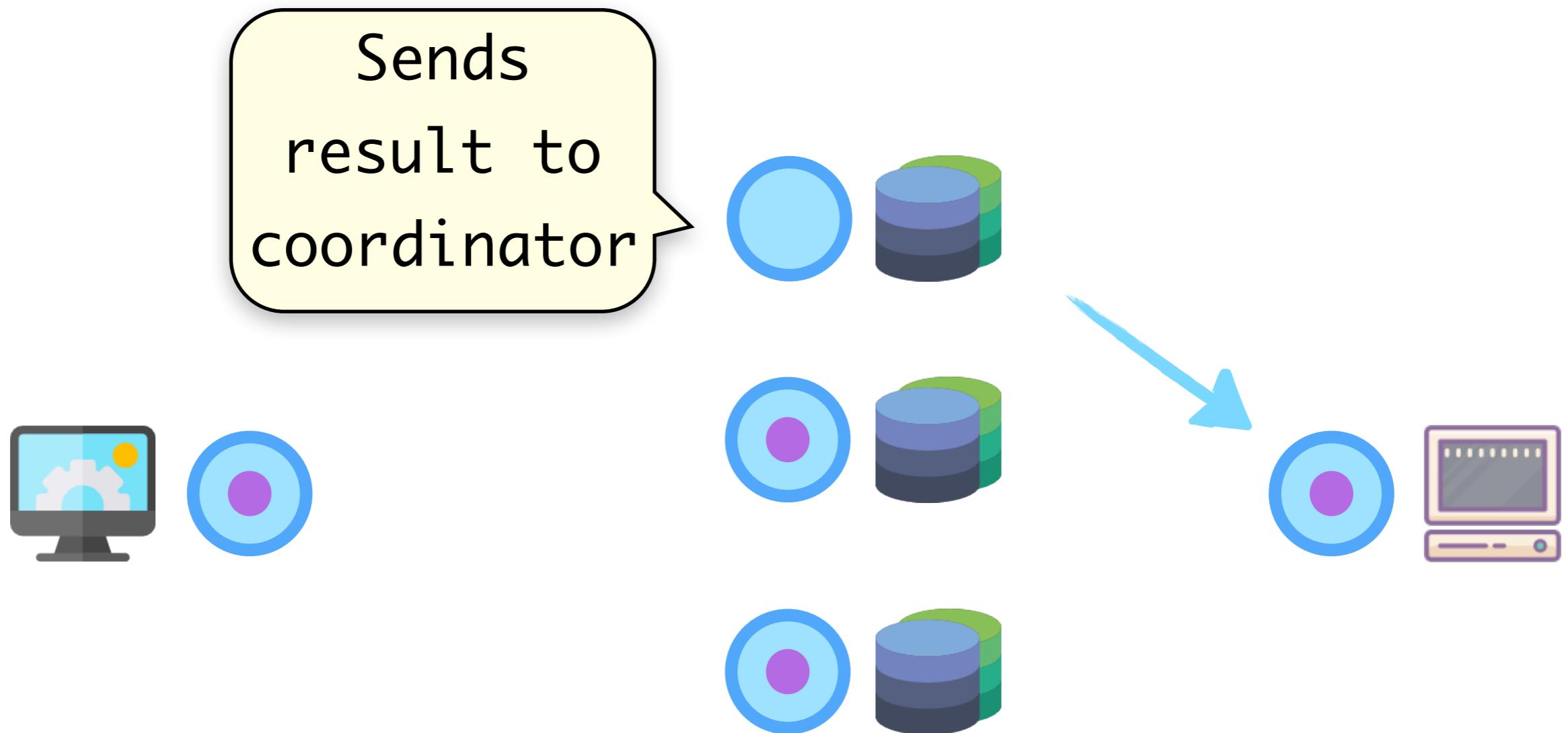
Work stealing Queue



Work stealing Queue

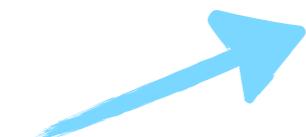
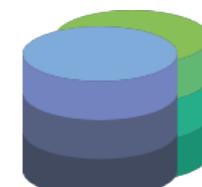
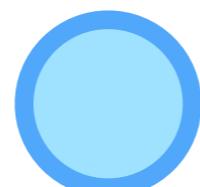
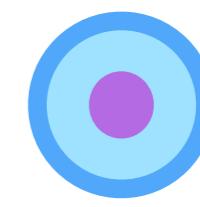
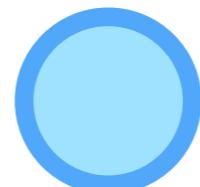
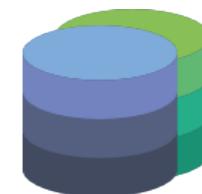
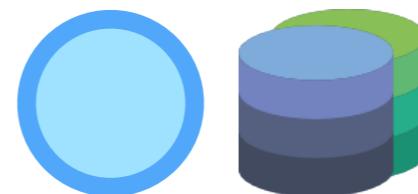


Work stealing Queue



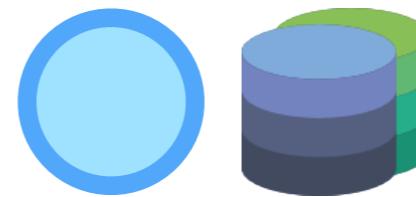
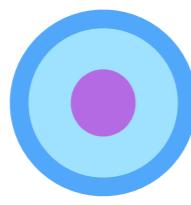
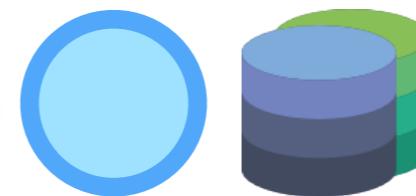
Work stealing Queue

The other
workers
finish



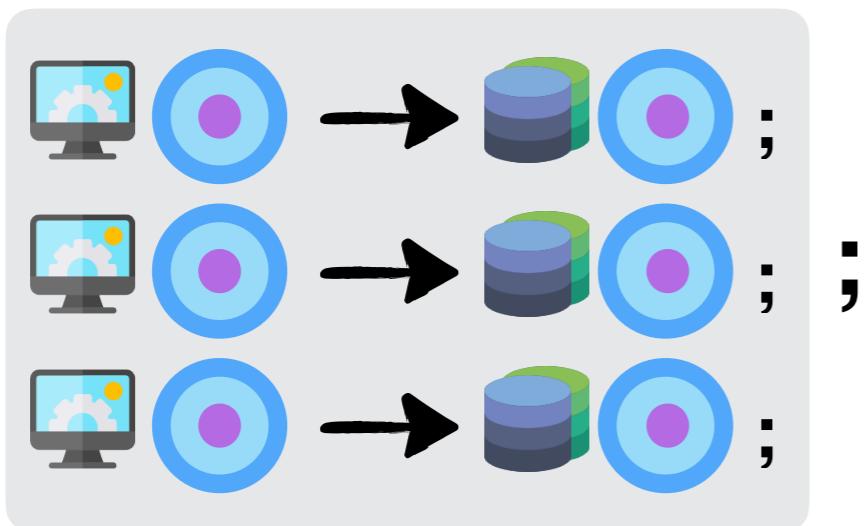
Work stealing Queue

Done!

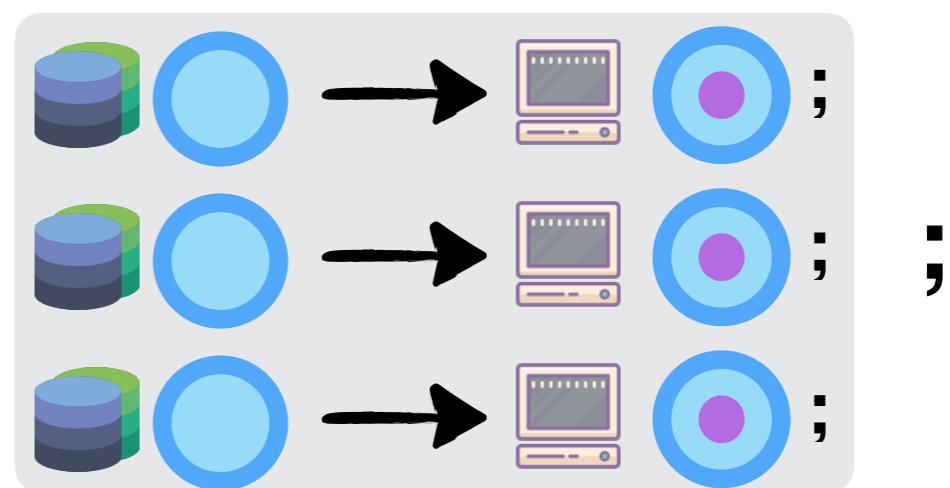


Work stealing Queue

Synchronization



1.
Queue assigns
tasks to workers
that write result to set



2.
Workers pick results
from set and send
to collector

Outline

Key Idea: Pretend Synchrony

1. Computing Synchronizations
2. Verifying the Synchronization

Extensions

Evaluation

1. Computing Synchronizations

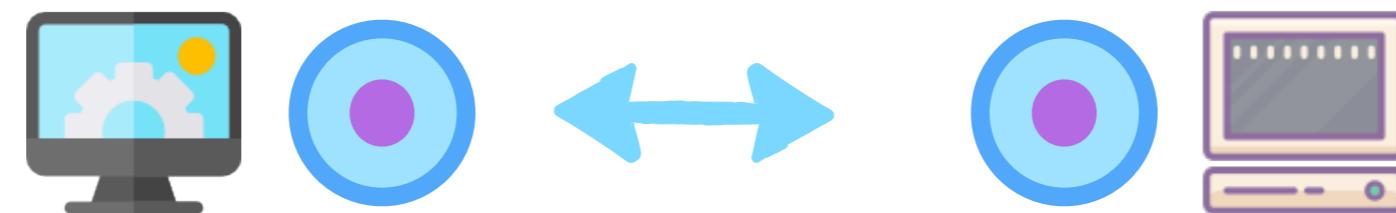
(By Rewriting and By Example)

Synchronize by Rewriting

Example 1: Loop Free

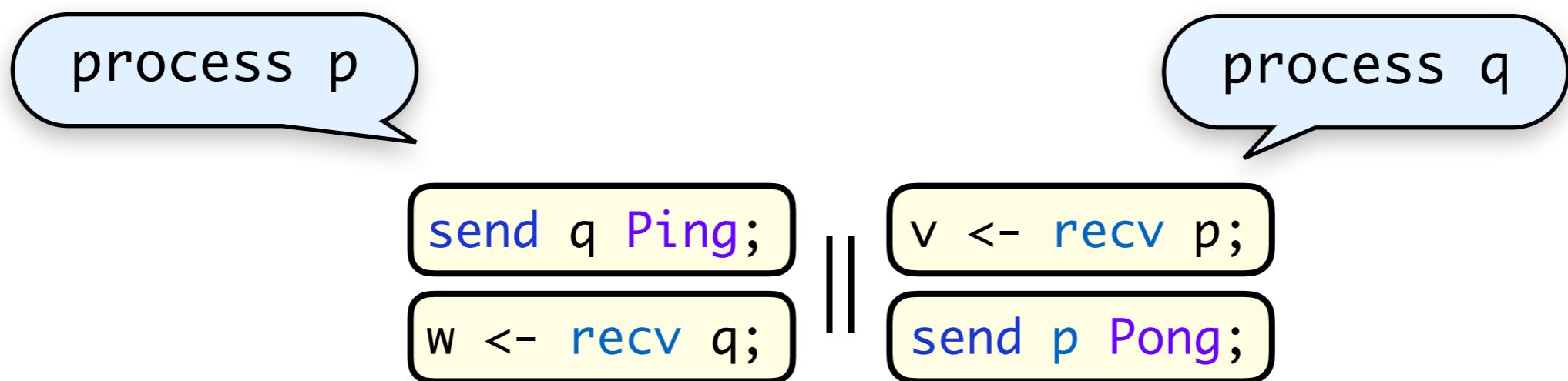
Synchronize by Rewriting

Example 1



Synchronize by Rewriting

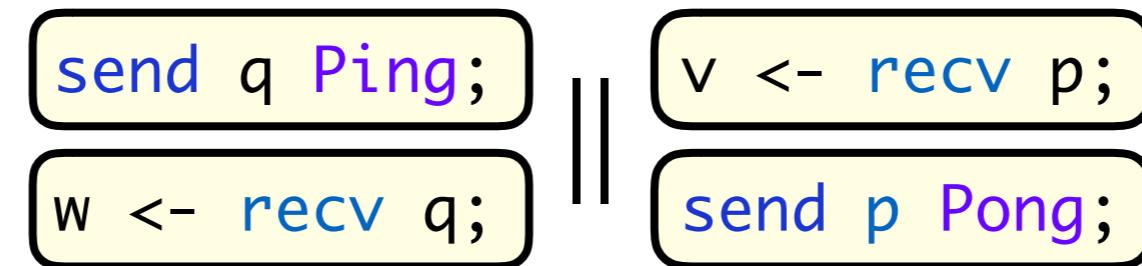
Example 1



Synchronize by Rewriting

Example 1

Since there is only a *single order*



Synchronize by Rewriting

Example 1

Since there is only a *single order*

send q Ping;

v <- recv p;

w <- recv q; || send p Pong;

... we can *sequentialize the send & receive (Lipton'75)*

Synchronize by Rewriting

Example 1

... we can *sequentialize the send & receive (Lipton'75)*

send q Ping;

v <- recv p;

w <- recv q; || send p Pong;

Synchronize by Rewriting

Example 1

... we can *sequentialize the send & receive (Lipton'75)*

```
q.v <- Ping;
```

```
w <- recv q; || send p Pong;
```

... and replace them by an *assignments*

Synchronize by Rewriting

Example 1

... we can *sequentialize the send & receive* (Lipton'75)

```
q.v <- Ping;  
send p Pong;  
w <- recv q;
```

... and replace them by an *assignments*

Synchronize by Rewriting

Example 1

... we can *sequentialize the send & receive (Lipton'75)*

```
q.v <- Ping;  
p.w <- Pong;
```

... and replace them by an *assignments*

Synchronize by Rewriting

Example 1

Synchronization

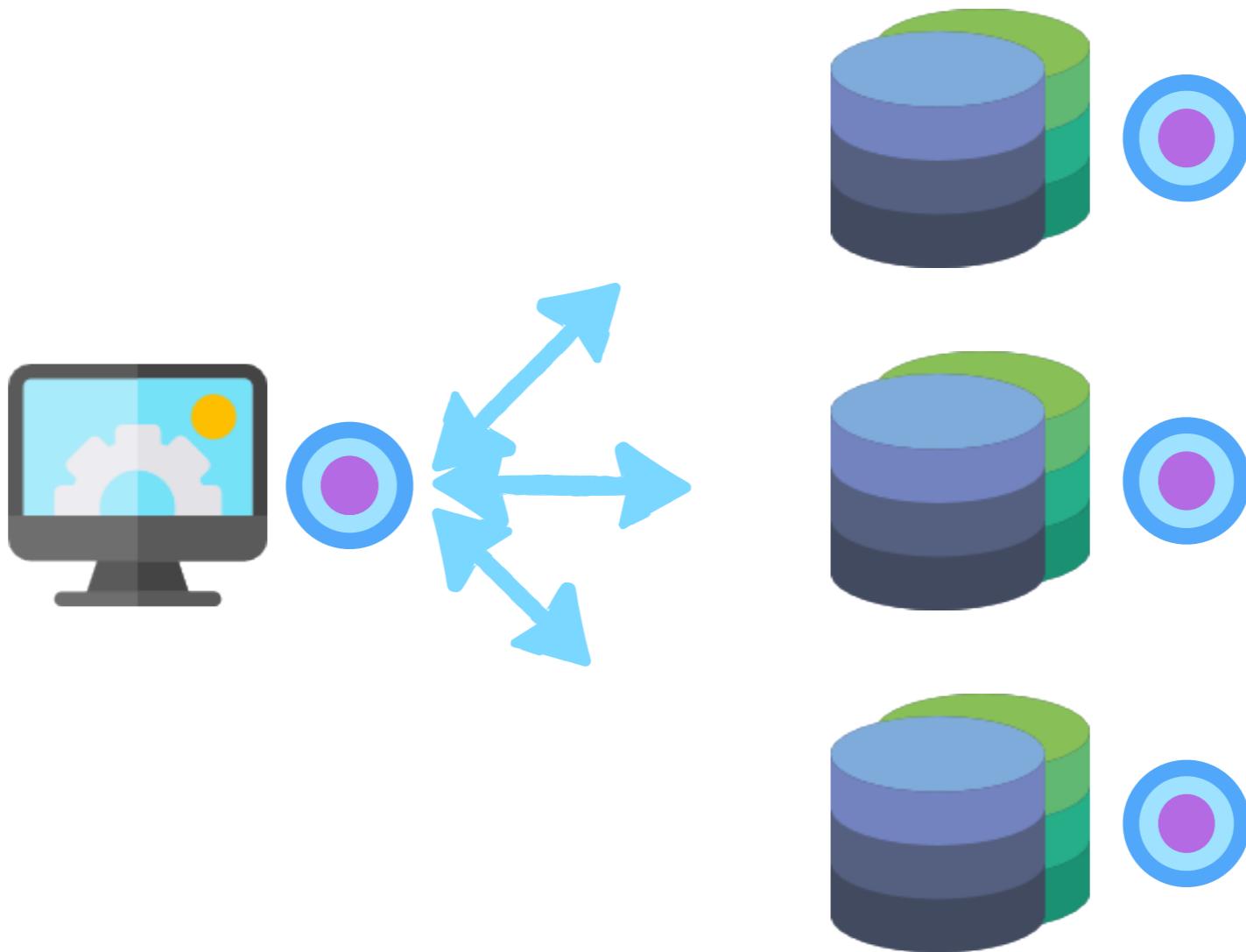
```
q.v <- Ping;  
p.w <- Pong;
```

Synchronize by Rewriting

Example 2 : Loop over Processes

Synchronize by Rewriting

Example 2



Synchronize by Rewriting

Example 2



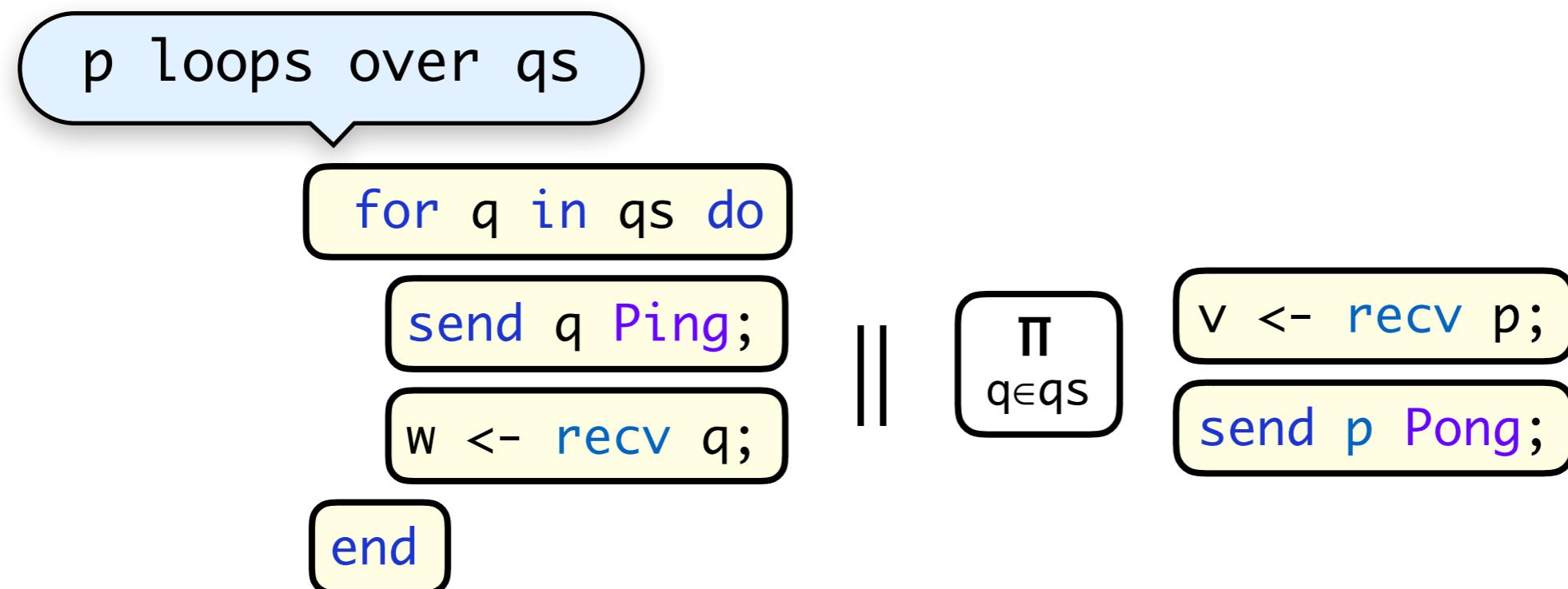
||

$\prod_{q \in qs}$



Synchronize by Rewriting

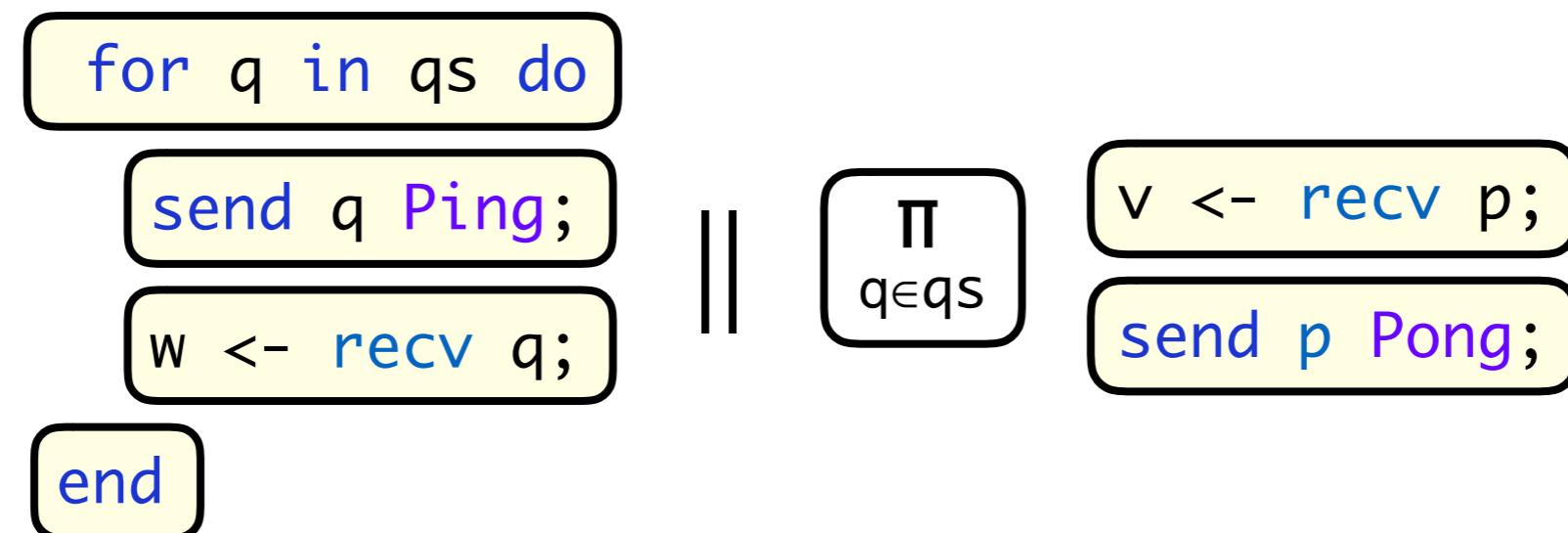
Example 2



Synchronize by Rewriting

Example 2

Since iterations are *sequential*

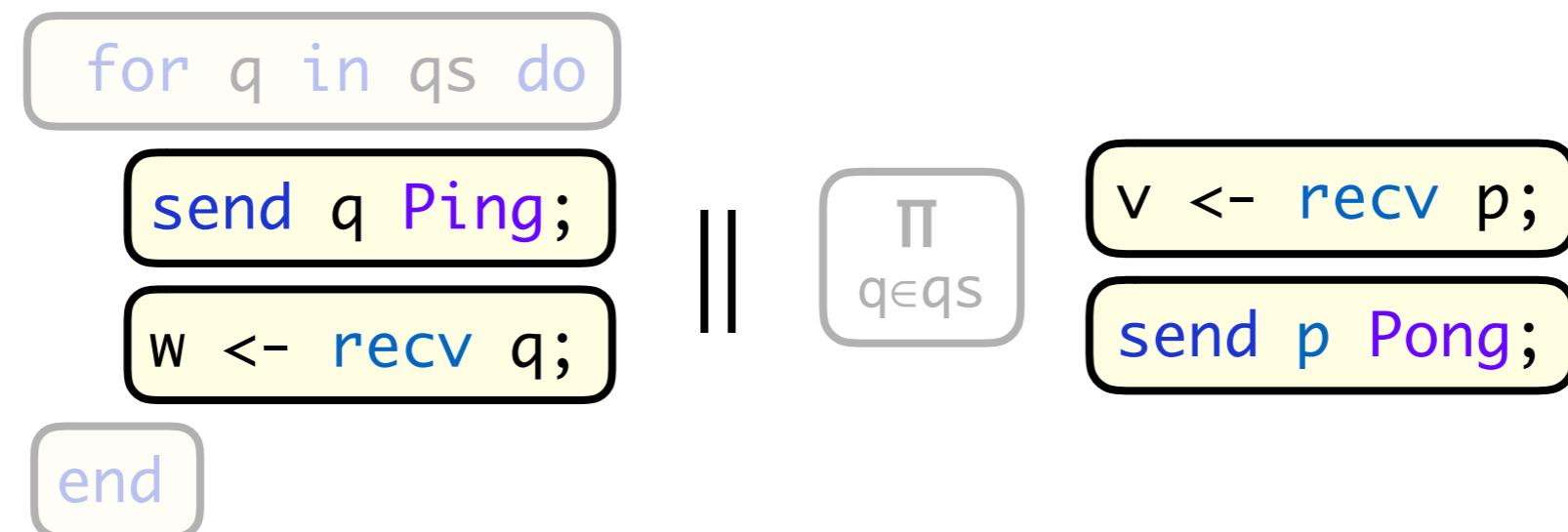


... and each iteration talks to a *single process*

Synchronize by Rewriting

Example 2

... focus on arbitrary iteration



Synchronize by Rewriting

Example 2

... focus on arbitrary iteration, synchronize

```
for q in qs do
```

```
    q.v <- Ping;
```

```
    p.w <- Pong;
```

```
end
```

Synchronize by Rewriting

Example 2

... focus on arbitrary iteration, synchronize

```
for q in qs do
    q.v <- Ping;
    p.w <- Pong;
end
```

... and generalize (Materialization, Sagiv'99)

Synchronize by Rewriting

Example 2

```
for q in qs do
    q.v <- Ping;
    p.w <- Pong;
end
```

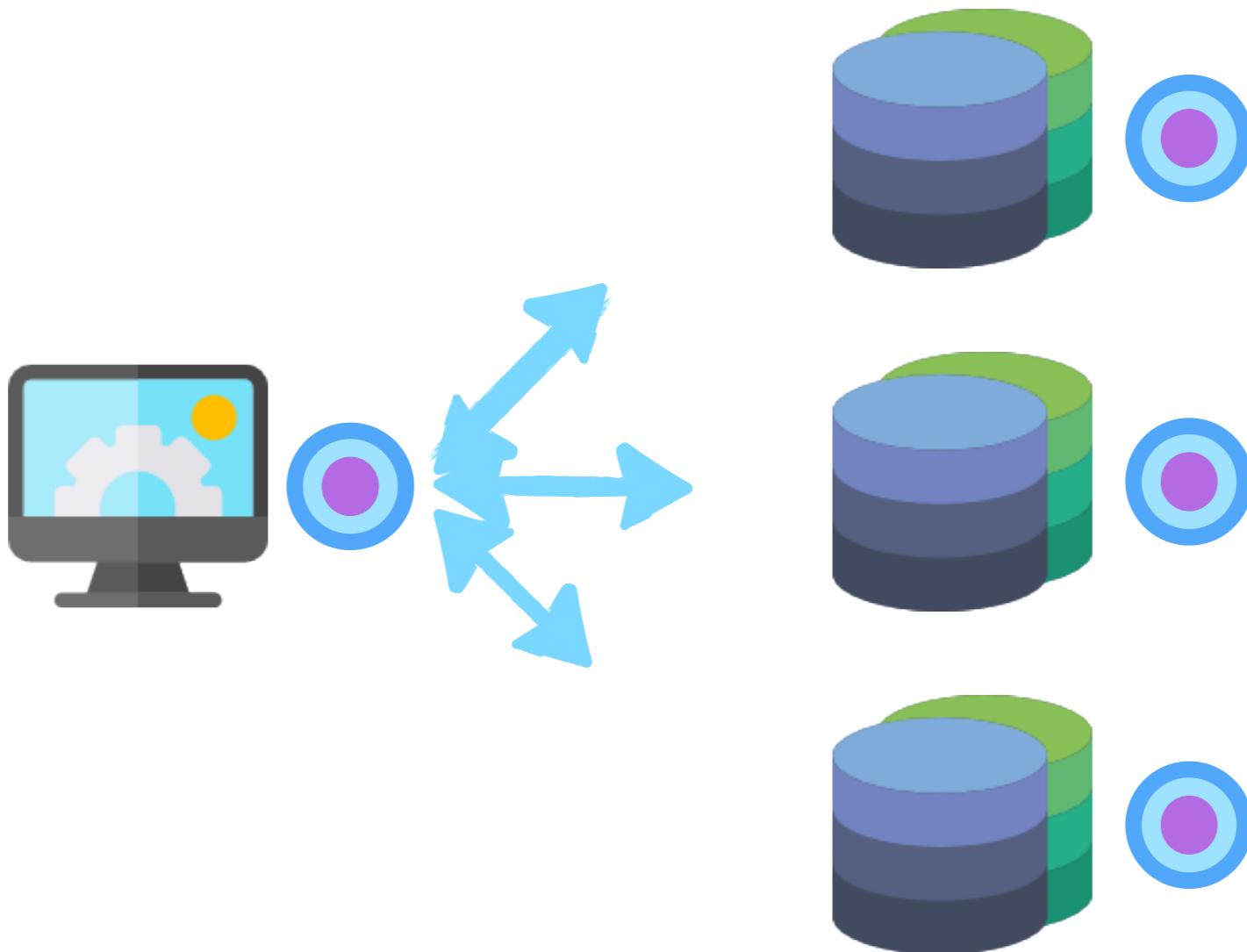
Synchronization

Synchronize by Rewriting

Example 3 : Symmetric Races

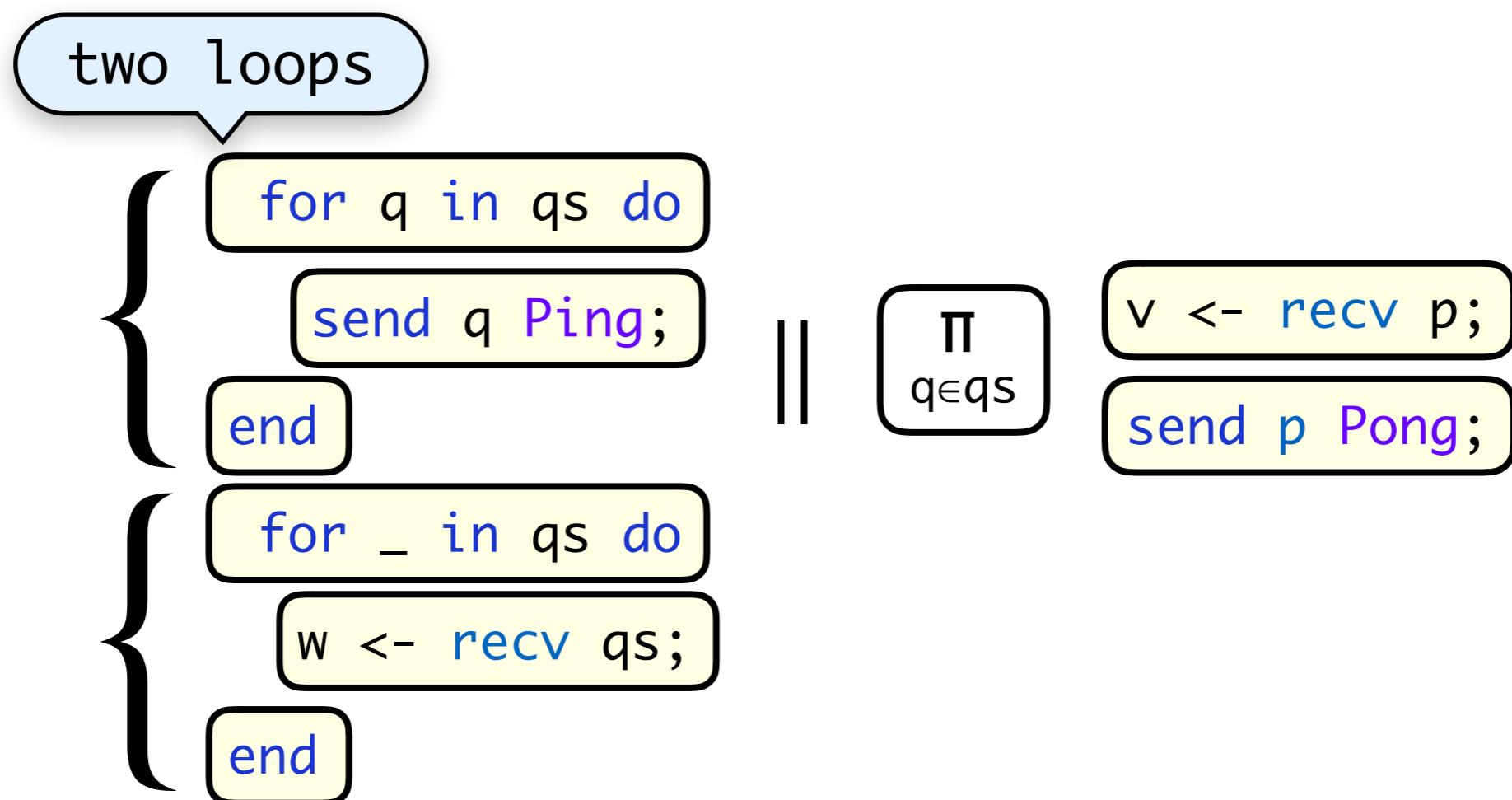
Synchronize by Rewriting

Example 3



Synchronize by Rewriting

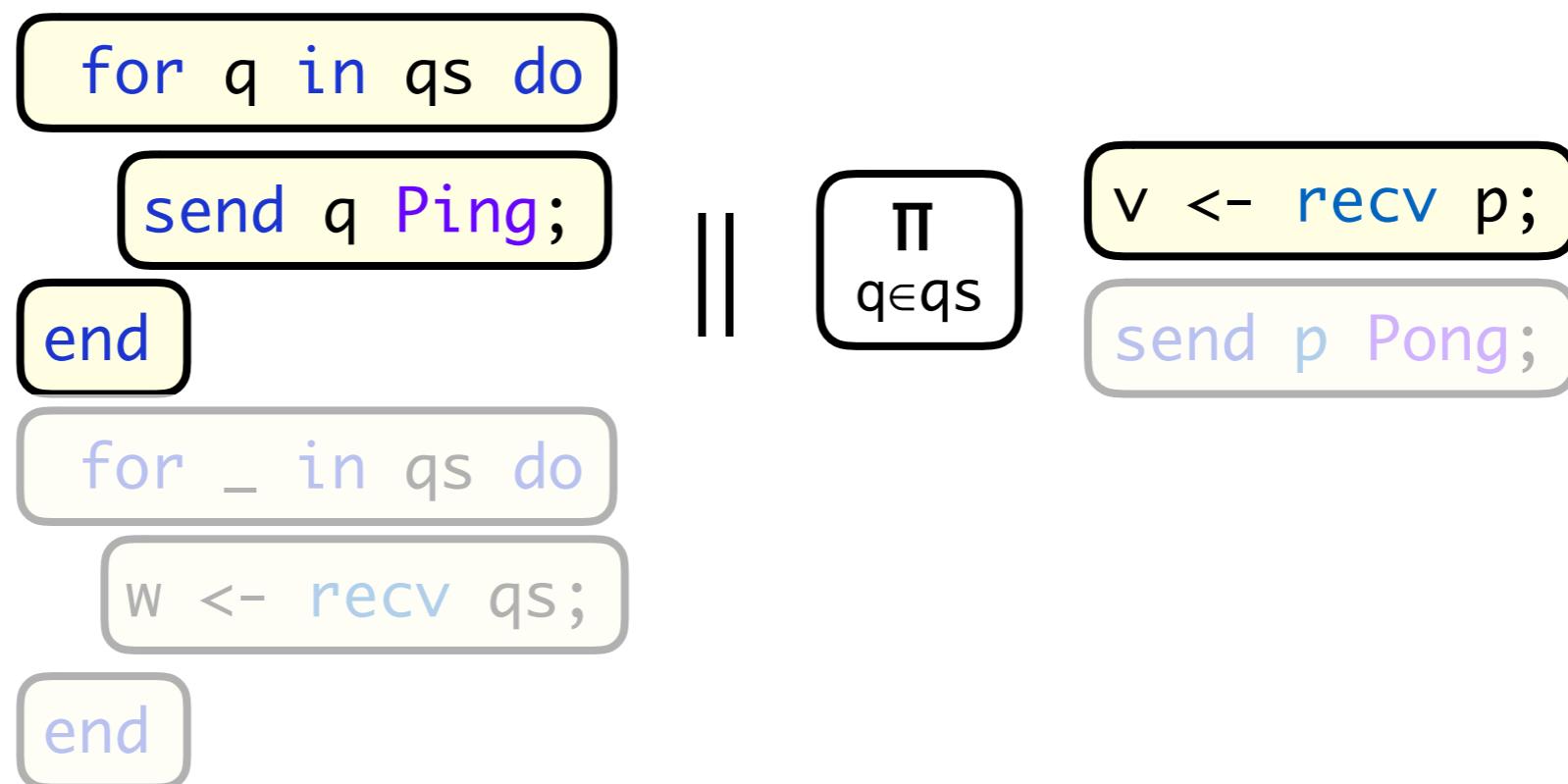
Example 3



Synchronize by Rewriting

Example 3

Split the rewrite into two *sequential steps*

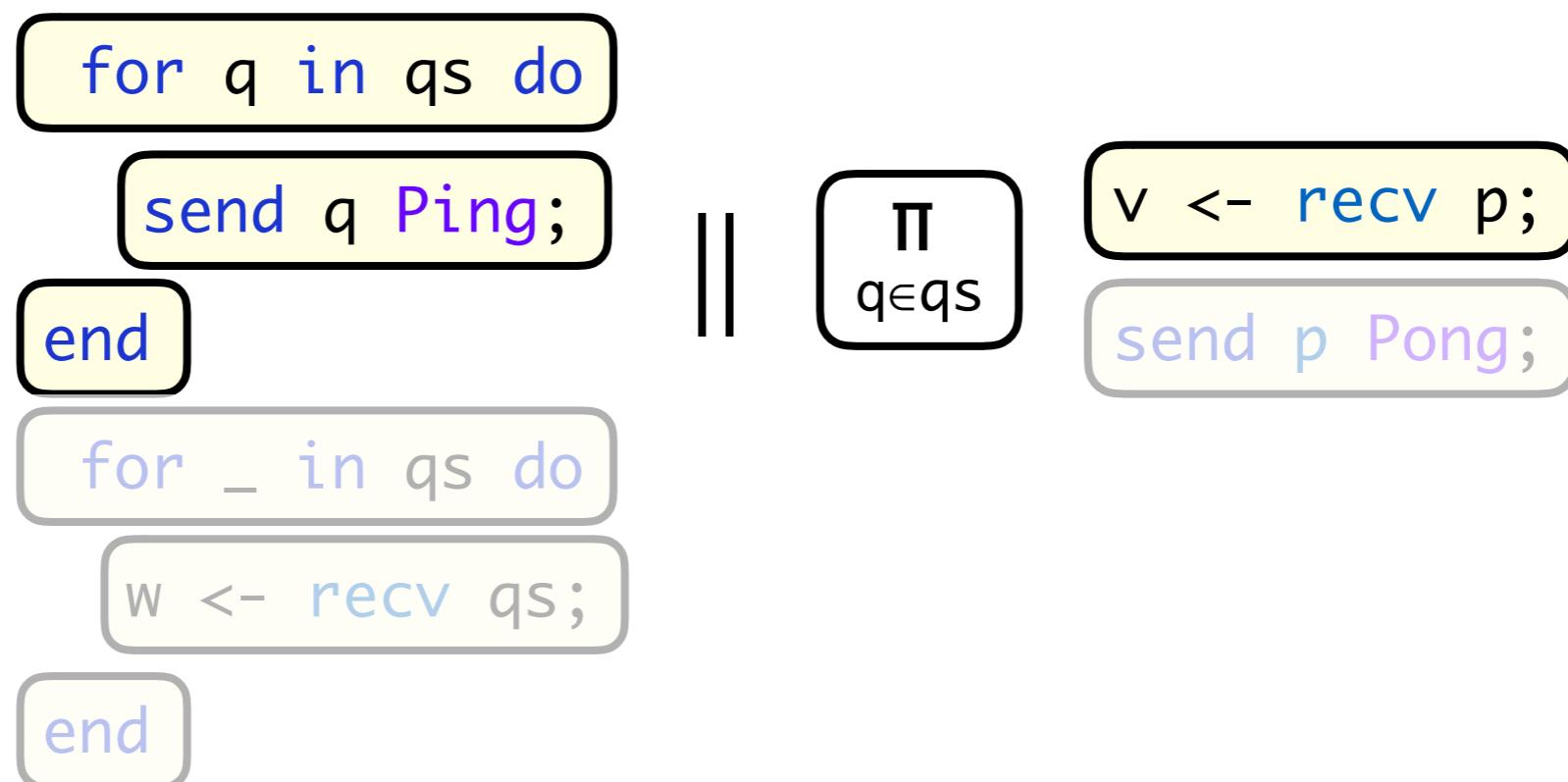


... the first loop and its *receive*, then the rest

Synchronize by Rewriting

Example 3

... some qs might send during the first loop

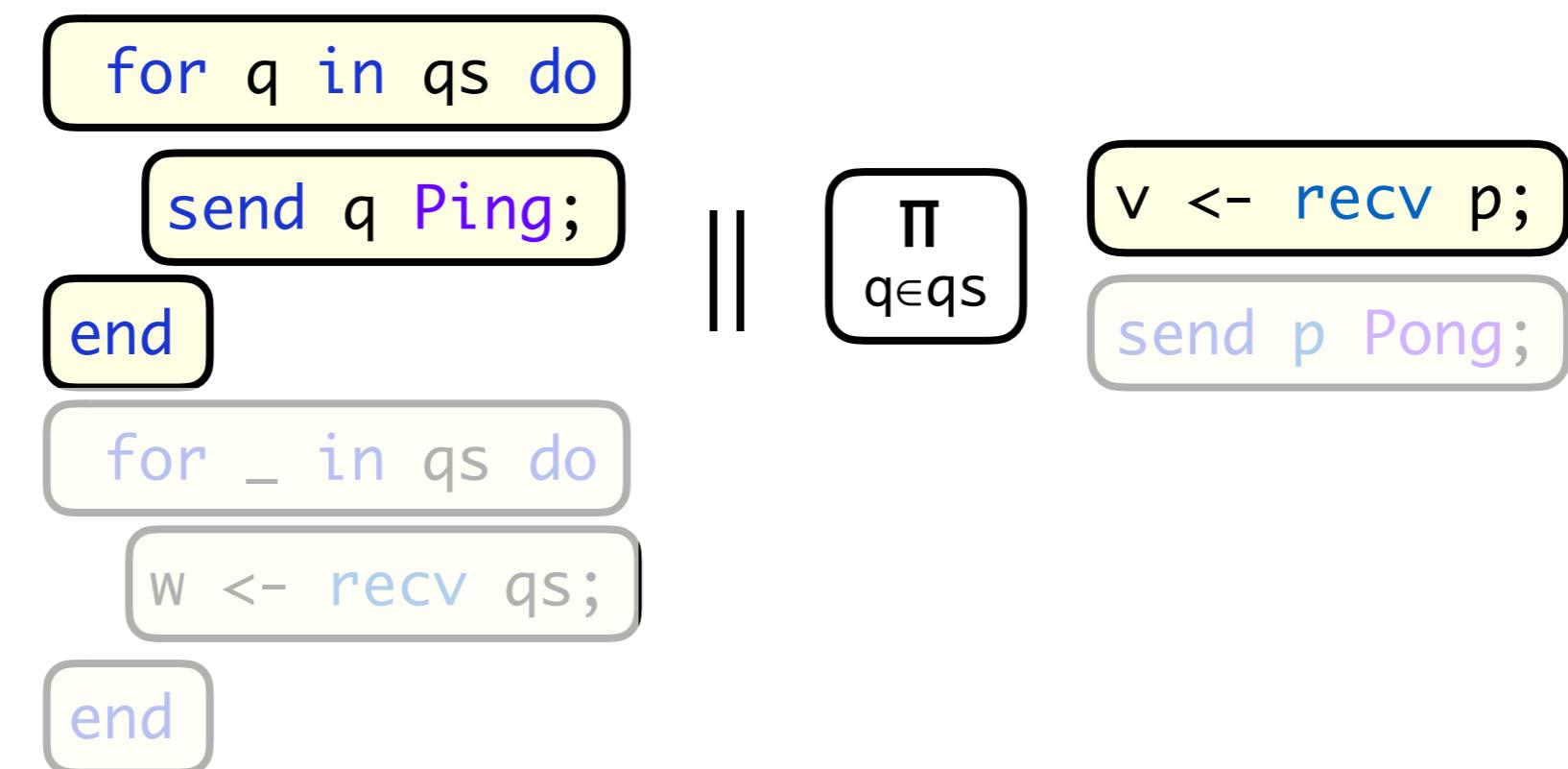


we can *pretend* the sends happen later (*Lipton'75*)

Synchronize by Rewriting

Example 3

Since the loop is *sequential*



Synchronize by Rewriting

Example 3

Since the loop is *sequential*

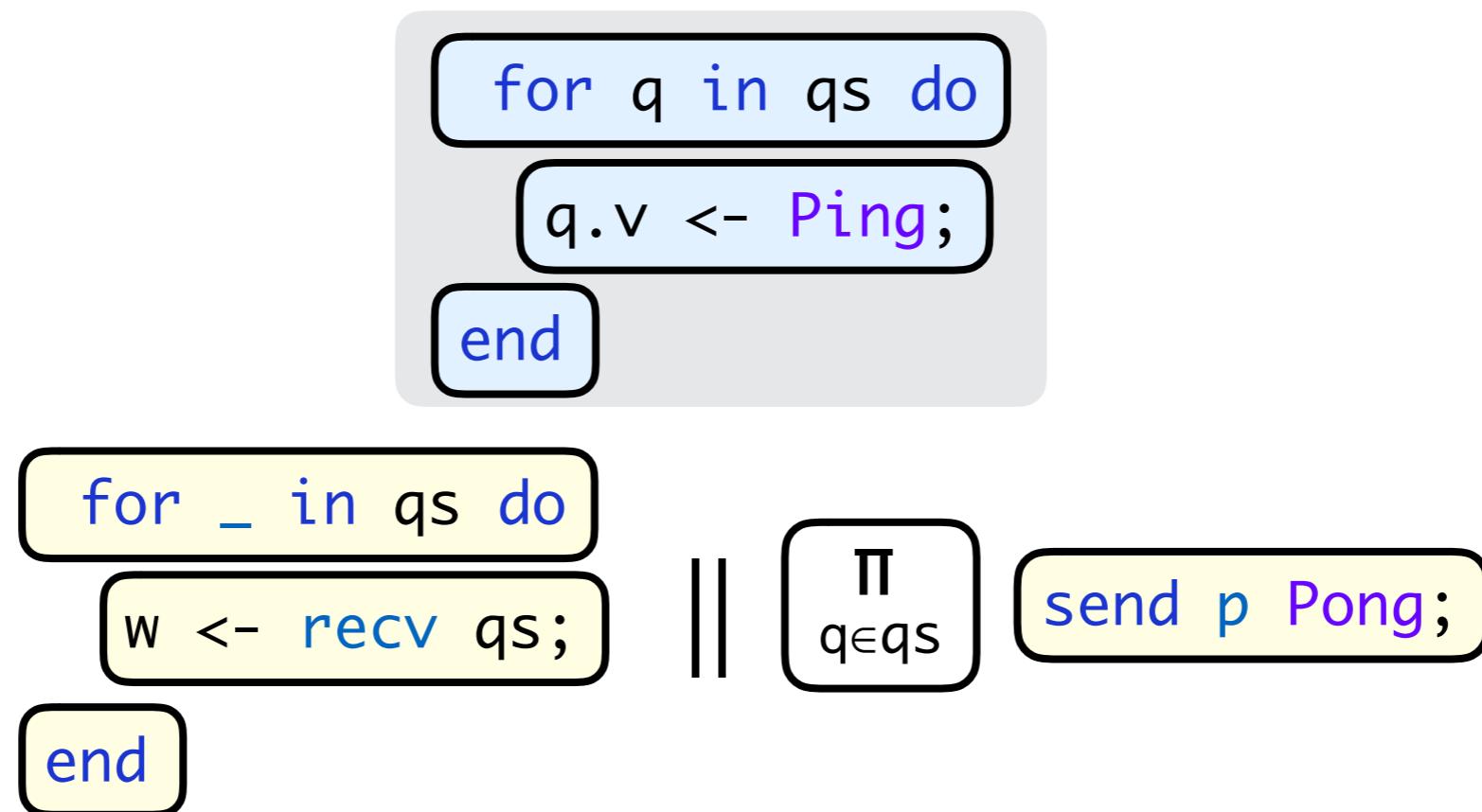


... synchronize arbitrary an iteration

Synchronize by Rewriting

Example 3

Since the loop is *sequential*



... and generalize

Synchronize by Rewriting

Example 3

Problem: Iterations are no longer *sequential*

```
for _ in qs do
    w <- recv qs;
    end
||  $\prod_{q \in qs}$ 
   send p Pong;
```

... there is a *race* between the processes in *qs*

Synchronize by Rewriting

Example 3

Problem: Iterations are no longer *sequential*

```
for _ in qs do
    w <- recv qs;    ||  $\prod_{q \in qs}$ 
    send p Pong;
end
```

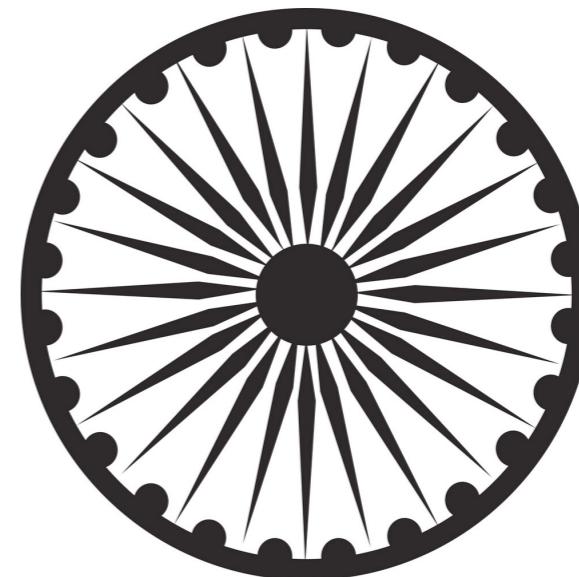
... how can we synchronize?

Synchronize by Rewriting

Exploit



Sequence



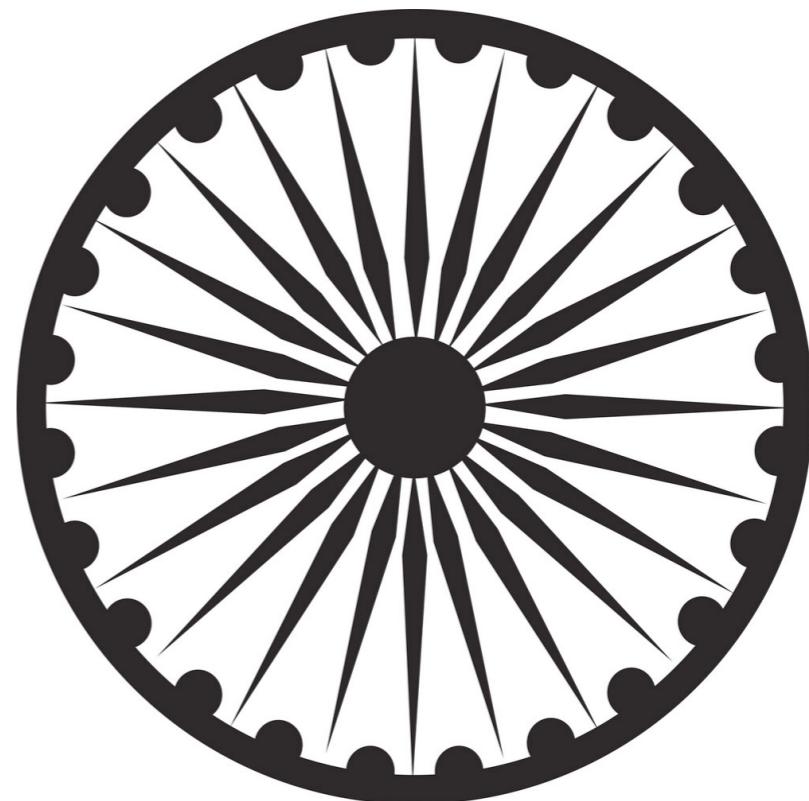
Symmetry

Synchronize by Rewriting

Exploit



Sequence

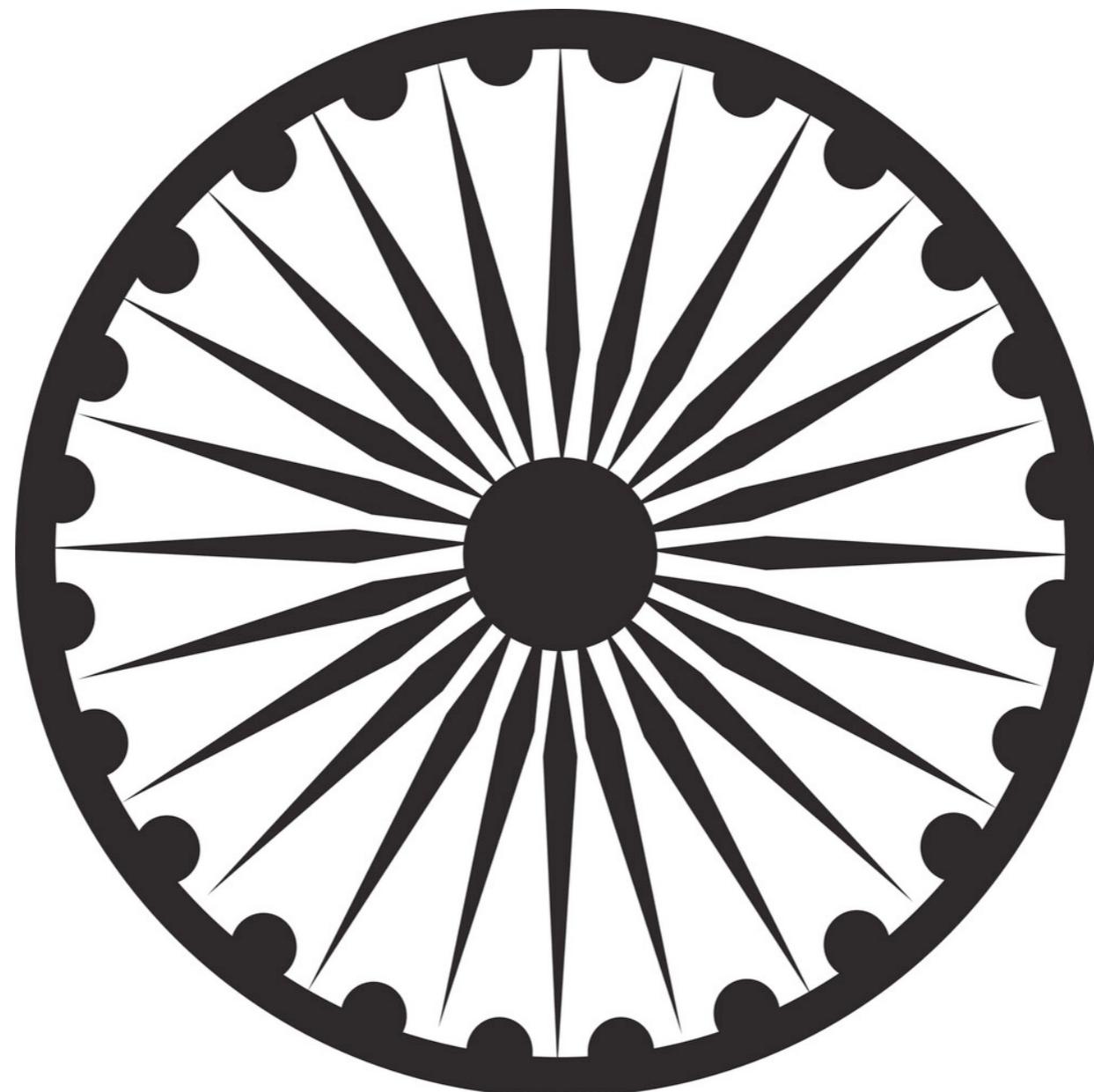


Symmetry

Symmetry

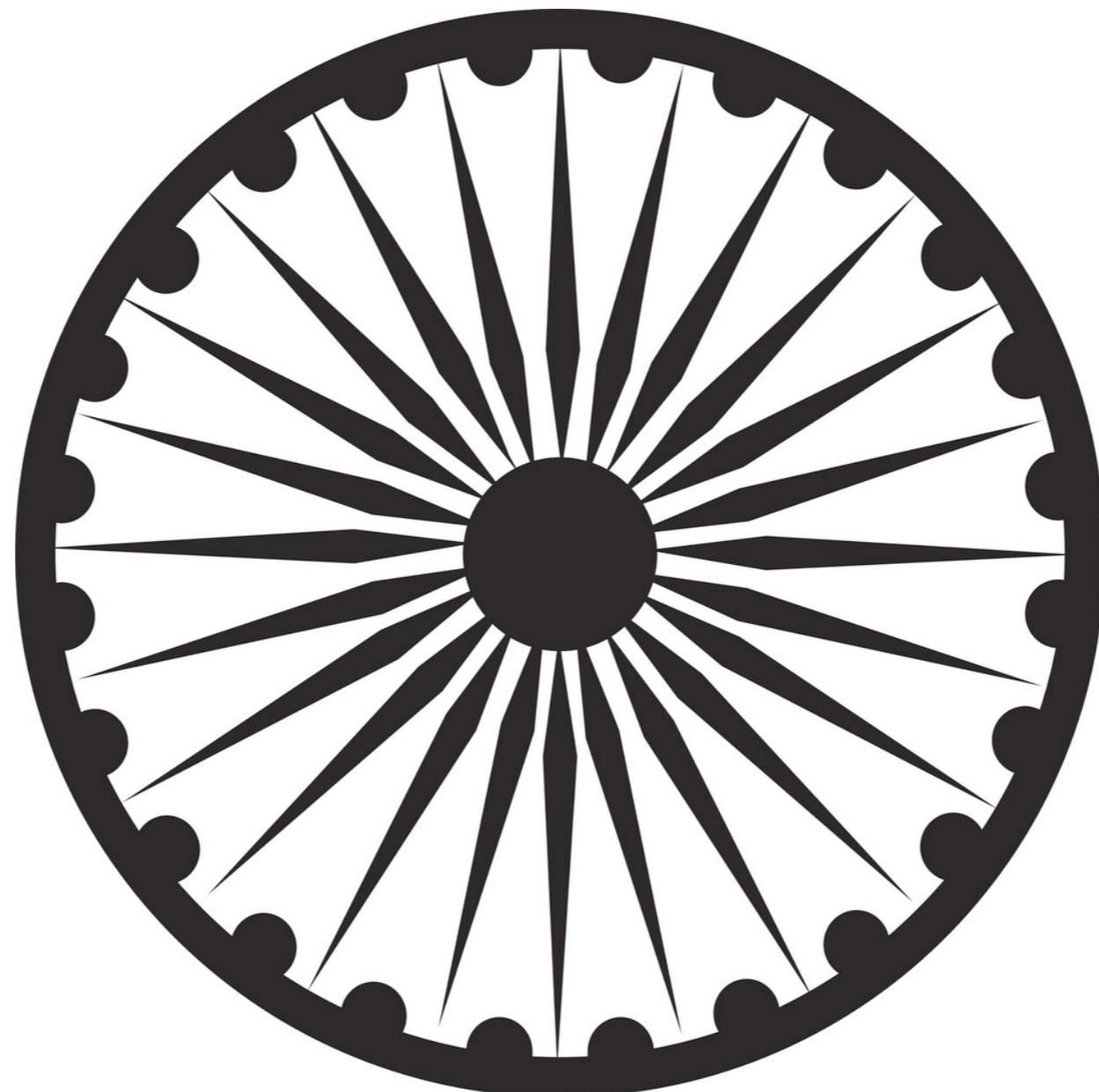
Invariance under *transformation*

Symmetry



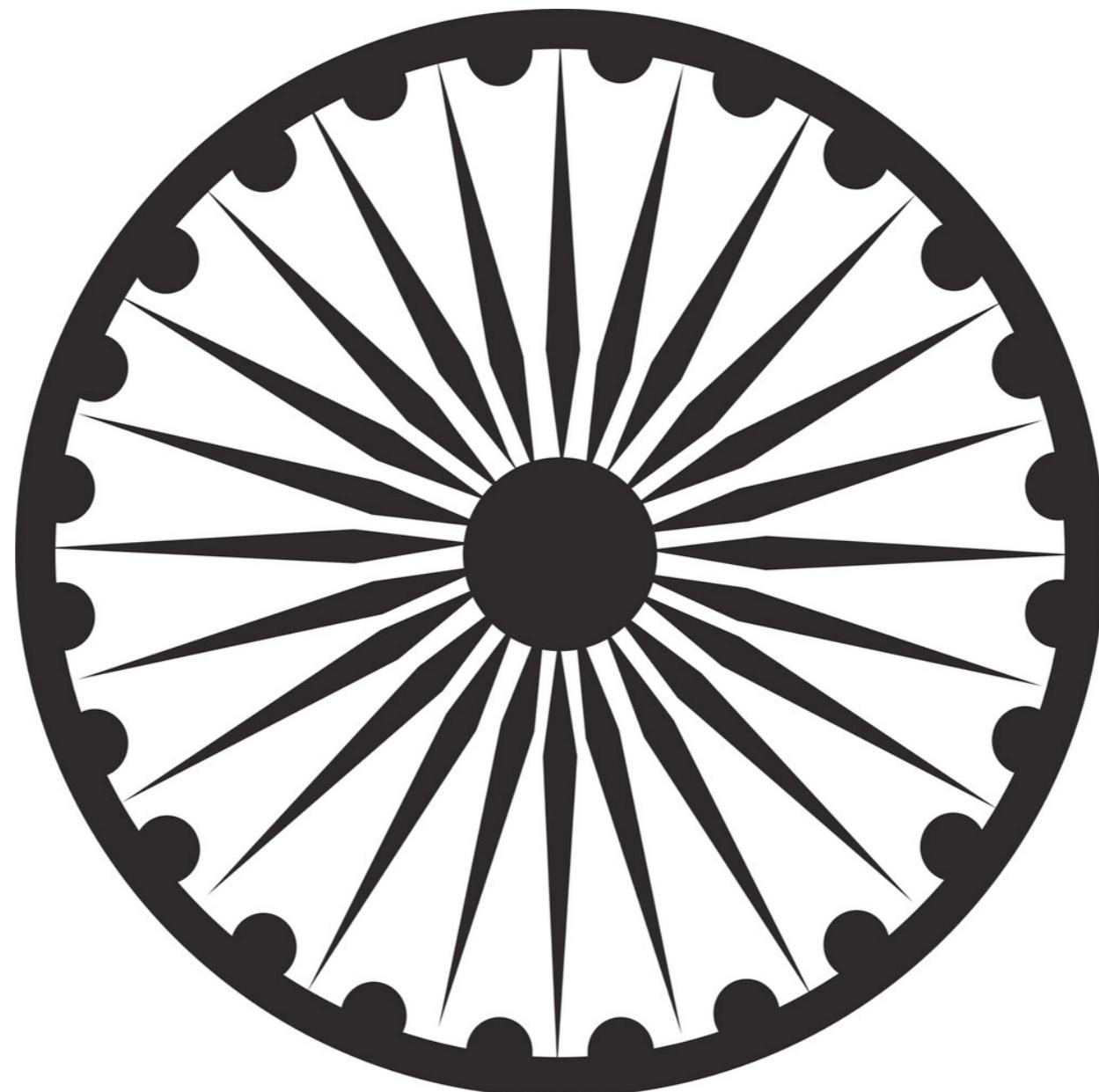
Invariance under *transformation*

Symmetry



Invariance under *rotation*

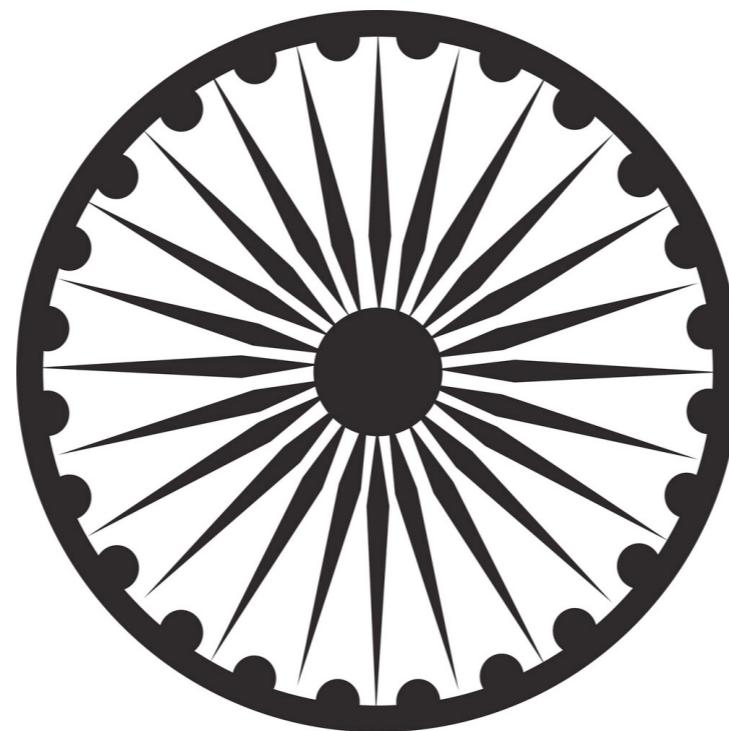
Symmetry



Invariance under *rotation*

Symmetry in Distributed Systems

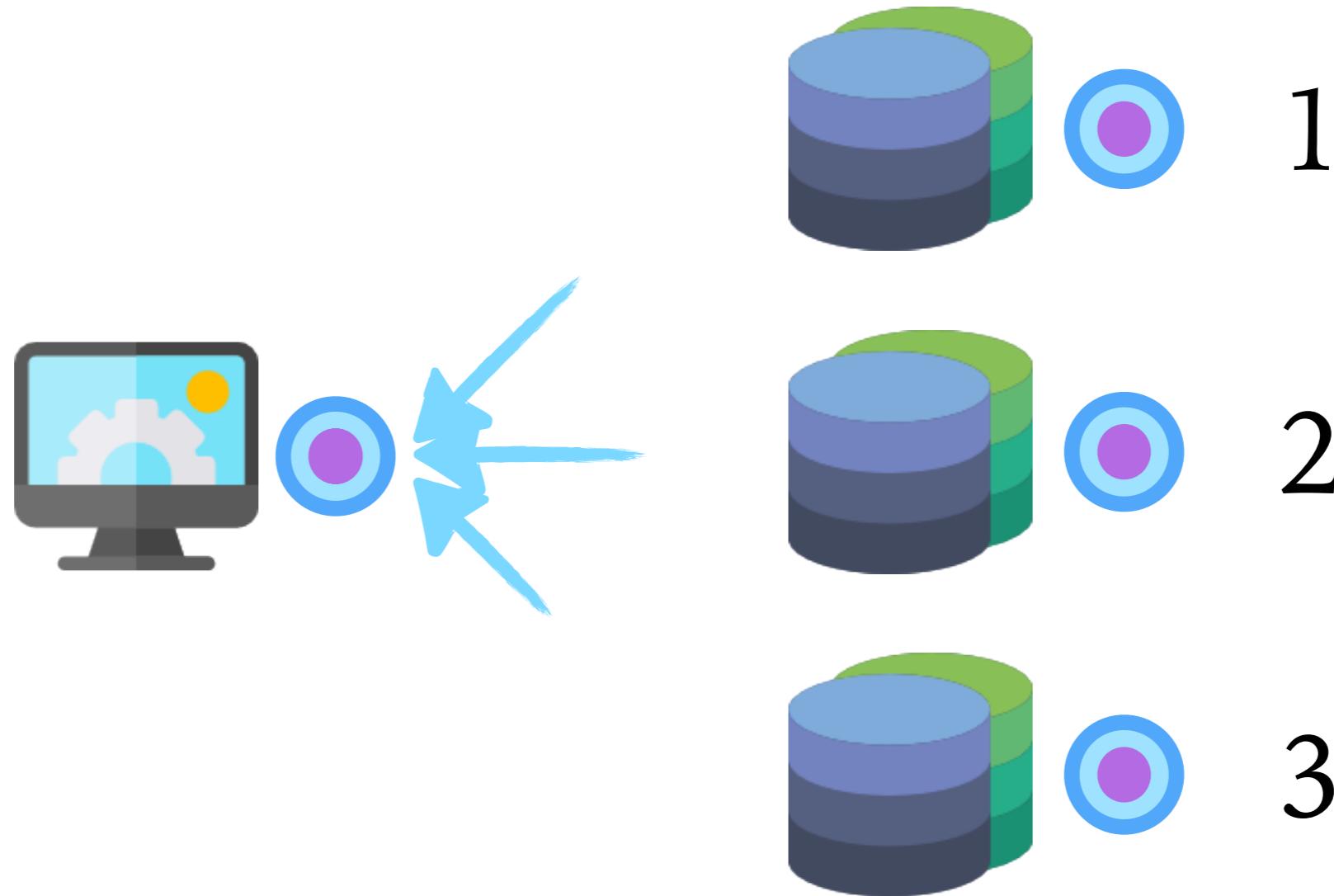
Invariance under process-id permutation



... doesn't affect halting states (Ip & Dill 1996)

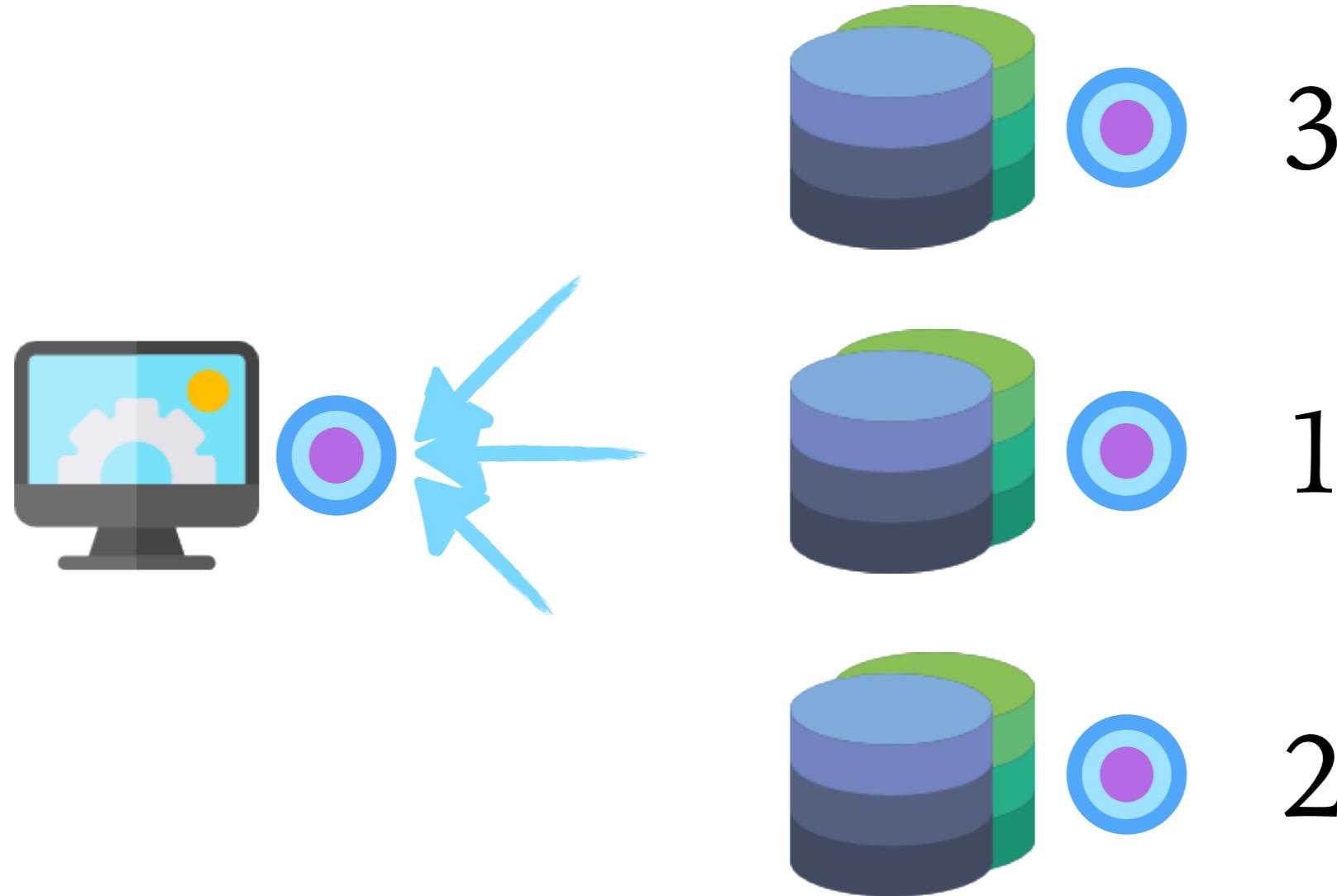
Symmetry in Distributed Systems

Name nodes ...



Symmetry in Distributed Systems

permuting process names



... does not affect halting states

Use Symmetry to
Synchronize!

Synchronize by Rewriting

Example 3

Since the processes in qs are *symmetric*

```
for _ in qs do
    w <- recv qs;
    ||  $\prod_{q \in qs}$ 
    send p Pong;
end
```

... we can receive the messages in *any* order

Synchronize by Rewriting

Example 3

... we can receive the messages in *any* order

```
for _ in qs do
    w <- recv qs;
    end
||  $\prod_{q \in qs}$  send p Pong;
```

Synchronize by Rewriting

Example 3

... we can receive the messages in *any* order

```
for q in qs do
    w <- recv q;
    end
||  $\prod_{q \in qs}$  send p Pong;
```

... in particular, we the *iteration order* of the loop

Synchronize by Rewriting

Example 3

... in particular, we the *iteration order* of the loop

```
for q in qs do
    w <- recv q;
    send p Pong;
end
```

$\parallel \prod_{q \in qs}$

... since the loop is now *sequential*

Synchronize by Rewriting

Example 3

... since the loop is now *sequential*

```
for q in qs do
    w <- recv q;
    send p Pong;
end
```

$$\parallel \prod_{q \in qs} \text{send } p \text{ Pong};$$

Synchronize by Rewriting

Example 3

... since the loop is now *sequential*

```
for _ in qs do  
    p.w <- Pong;
```

```
end
```

... we can synchronize, as before

Synchronize by Rewriting

Example 3

... since the loop is now *sequential*

```
for _ in qs do
```

```
    p.w <- Pong;
```

```
end
```

... we can synchronize, as before

Synchronize by Rewriting

Example 3

... we can synchronize, as before

```
for _ in qs do
```

```
    p.w <- Pong;
```

```
end
```

Synchronize by Rewriting

Example 3

... we can synchronize, as before

```
for q in qs do
```

```
    q.v <- Ping;
```

```
end
```

```
for _ in qs do
```

```
    p.w <- Pong;
```

```
end
```

... and get the overall synchronization

Synchronize by Rewriting

Example 3

```
for q in qs do  
    q.v <- Ping;
```

```
end
```

```
for _ in qs do  
    p.w <- Pong;
```

```
end
```

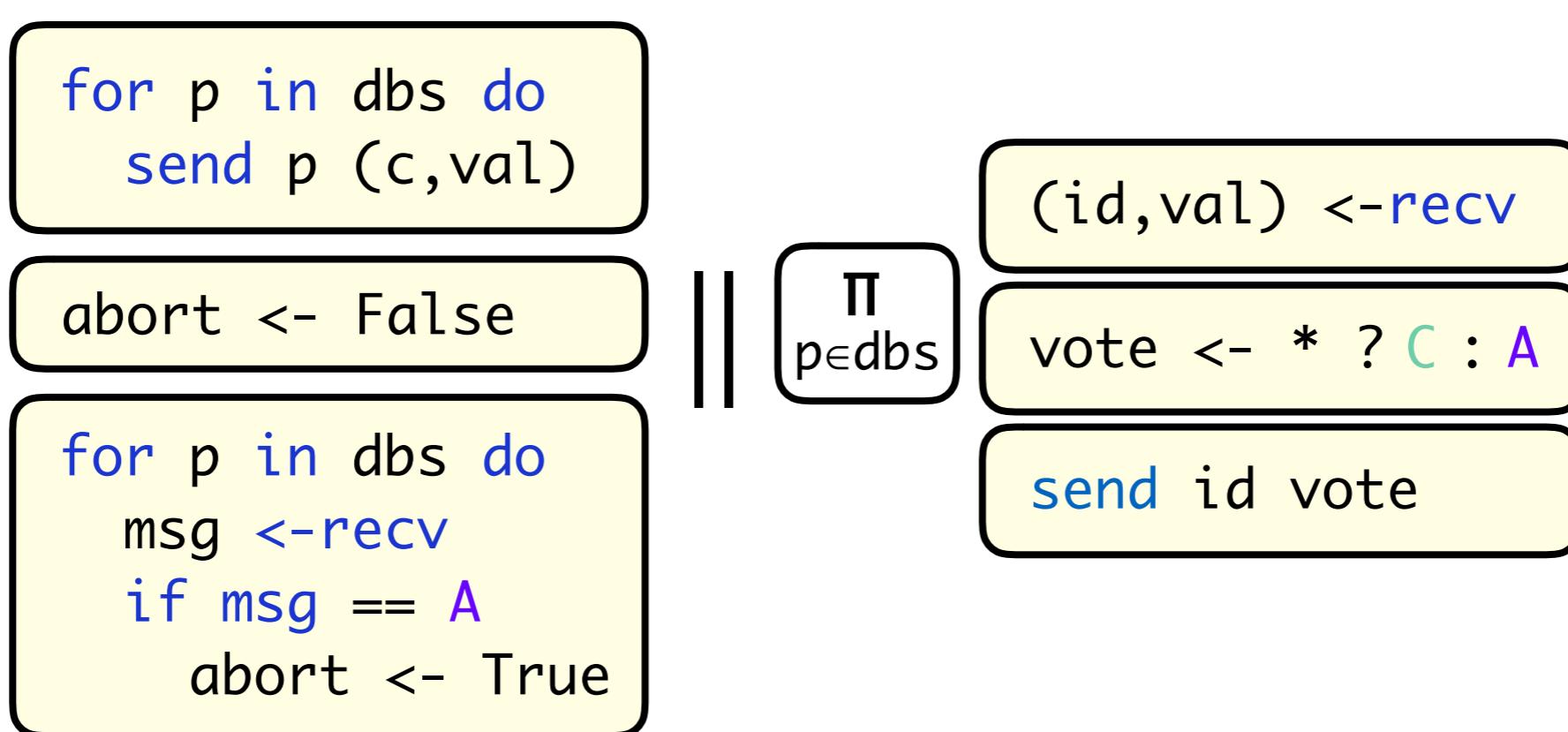
Synchronization

Synchronize by Rewriting

Example 4: Two Phase Commit

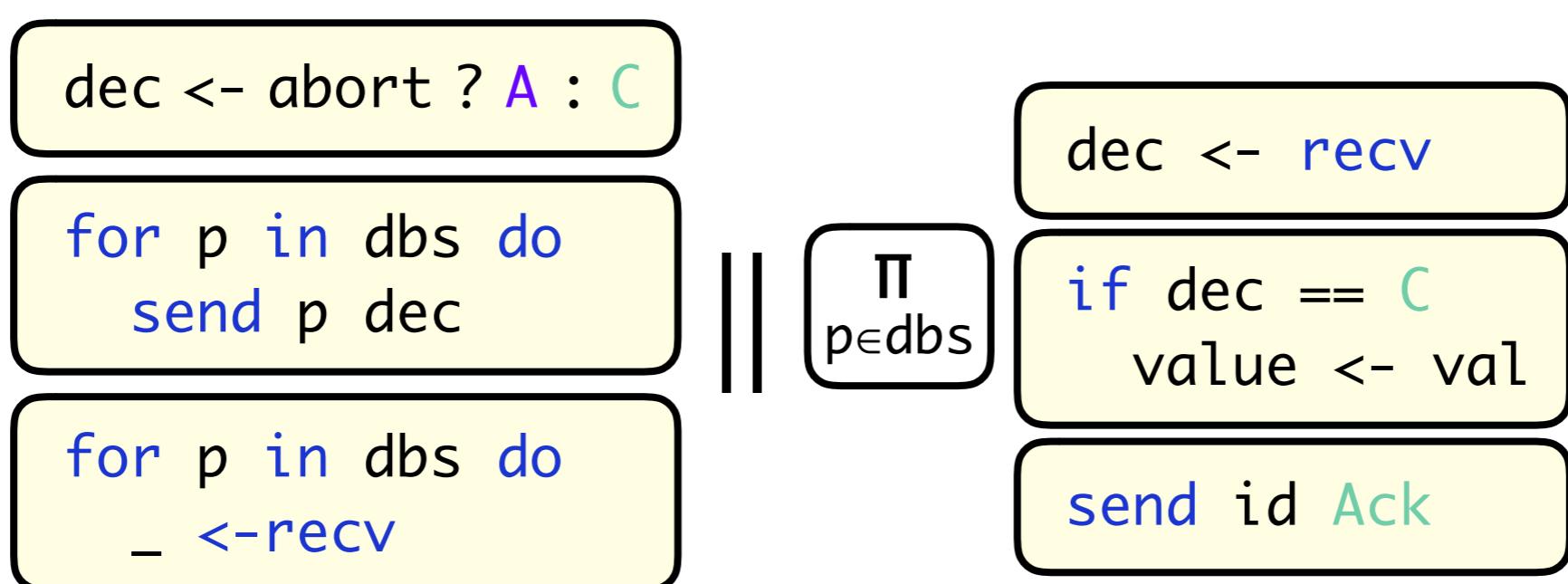
Synchronize by Rewriting

Two Phase Commit: Phase 1



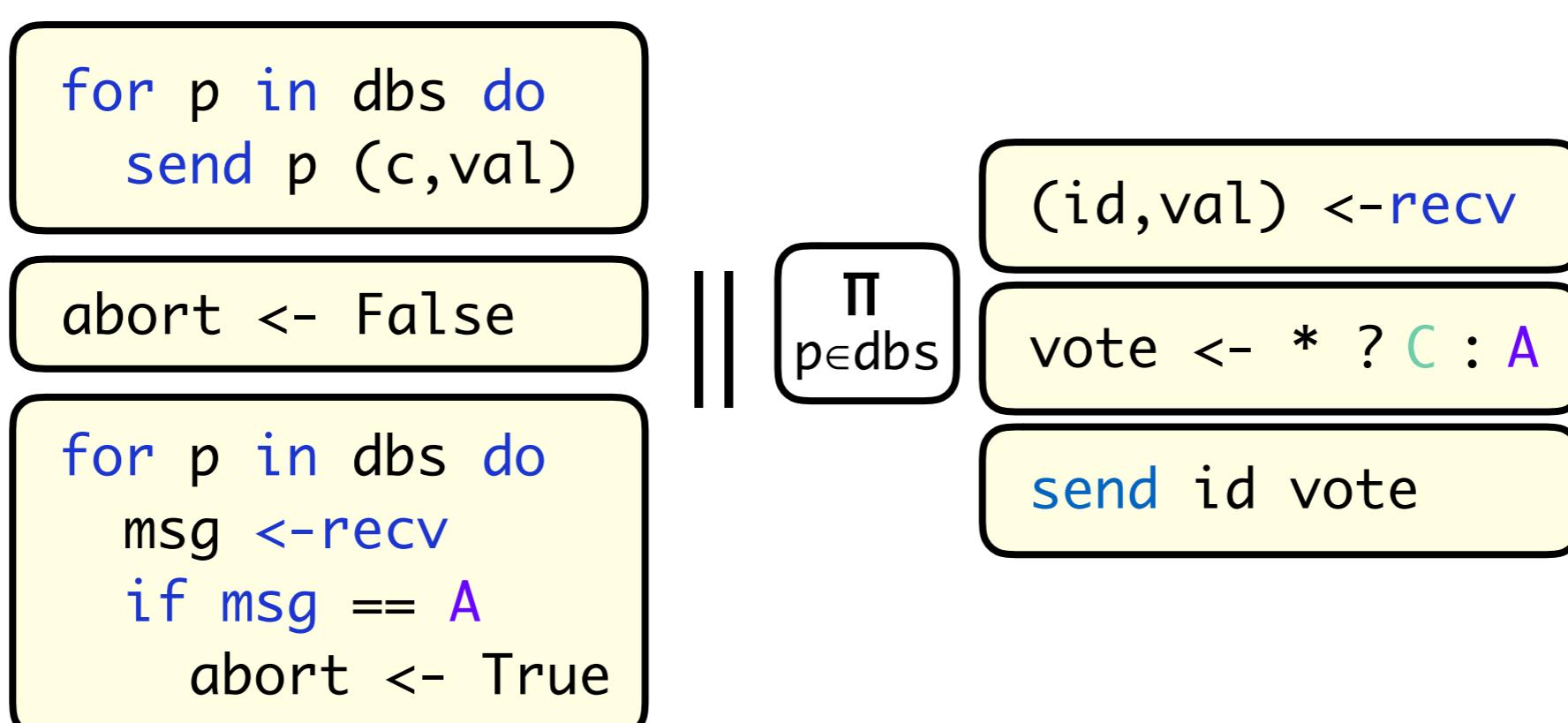
Synchronize by Rewriting

Two Phase Commit: Phase 2



Synchronize by Rewriting

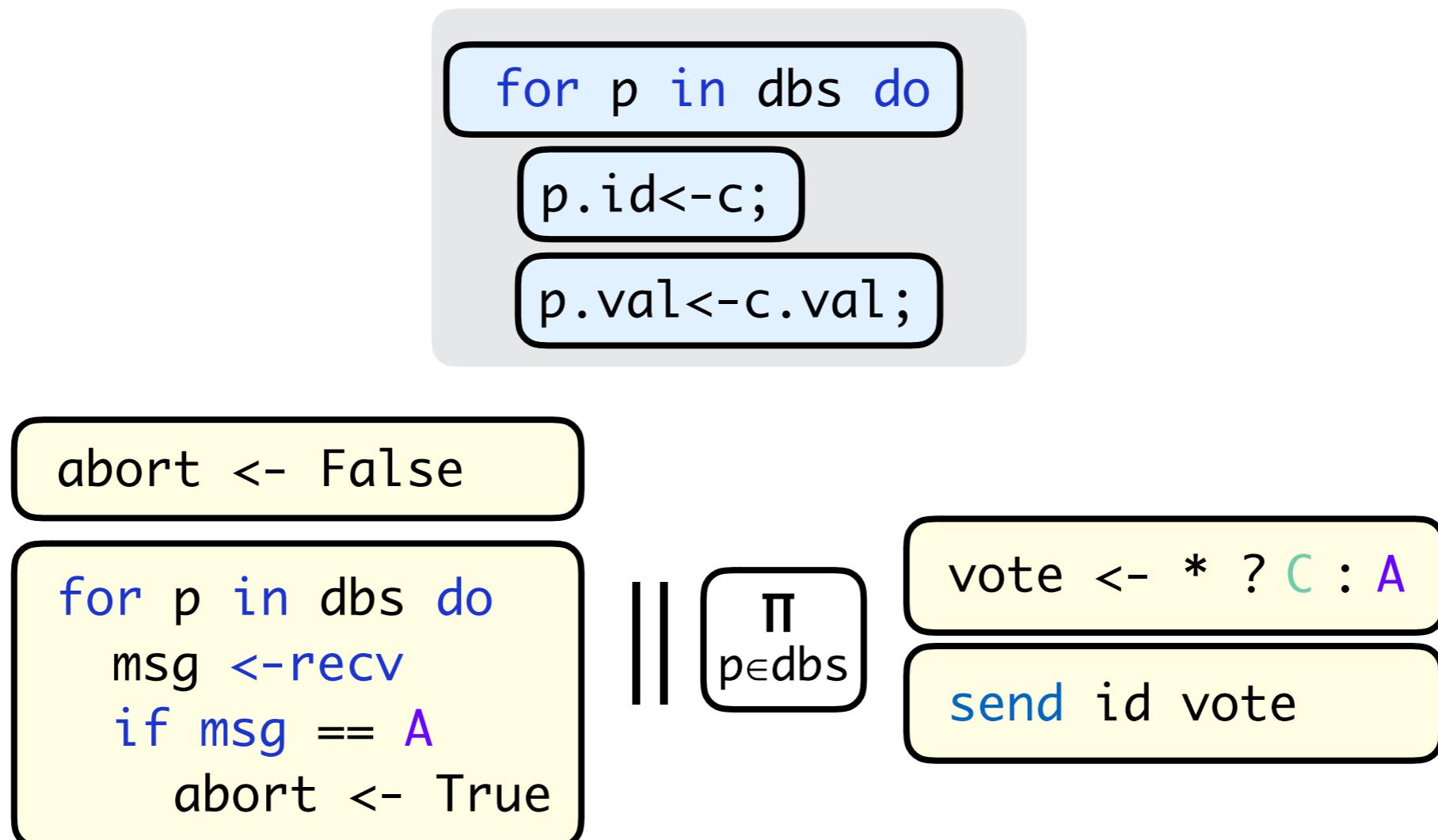
Two Phase Commit : Synchronizing Phase 1



... by example 3

Synchronize by Rewriting

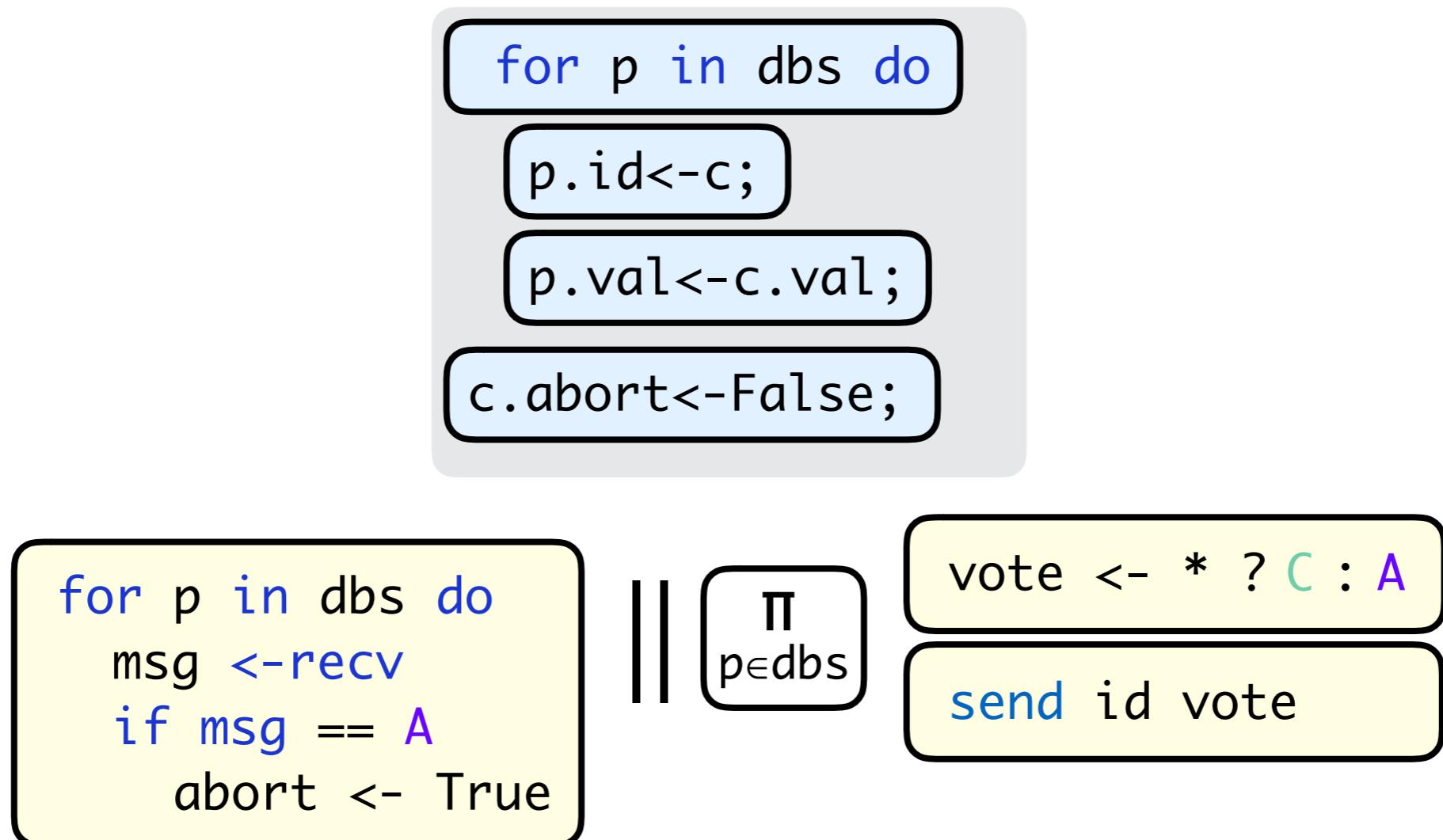
Two Phase Commit : Synchronizing Phase 1



... by example 3

Synchronize by Rewriting

Two Phase Commit : Synchronizing Phase 1



... by example 3

Synchronize by Rewriting

```
for p in dbs do
    p.id<-c;
    p.val<-c.val;
    c.abort<-False;

for p in dbs do
    vote <-* ? C : A
    c.msg<-p.vote;
    if msg == A
        abort <- True
```

Synchronized Phase 1

Outline

Key Idea: Pretend Synchrony

1. Computing Synchronizations
2. Verifying the Synchronization

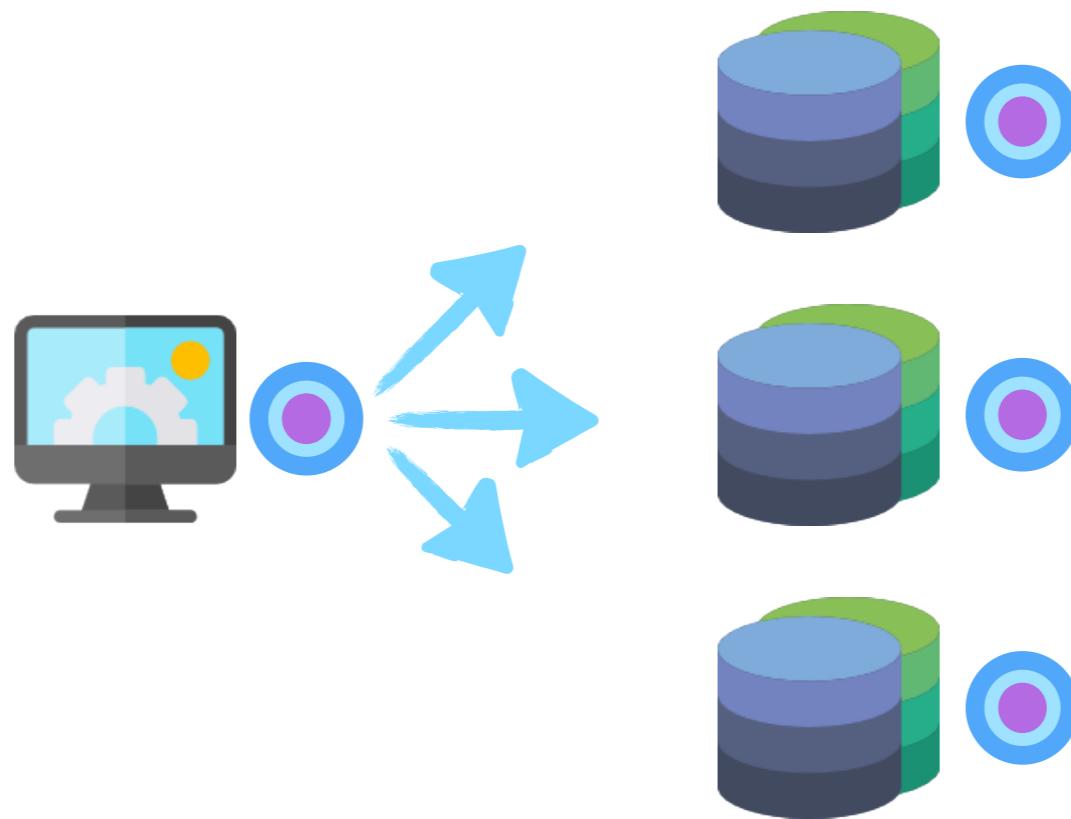
Extensions

Evaluation

2. Verifying the Synchronization

Synchronous Proofs Are Easy!

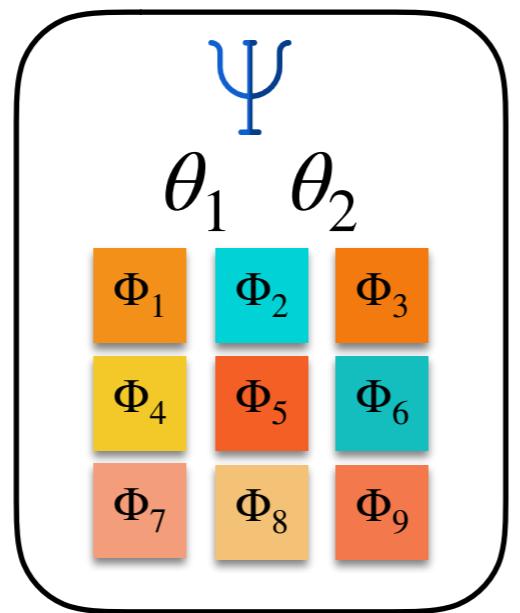
2PC: Correctness



Ψ = Nodes *agree on same value*

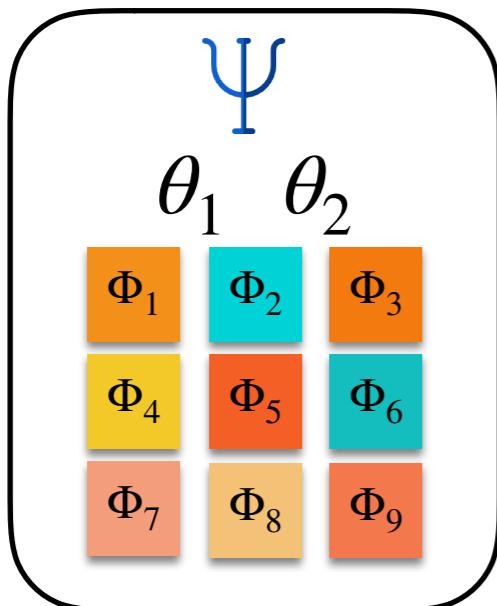
Asynchronous
Proofs are *Ugly*!

Asynchronous Proofs



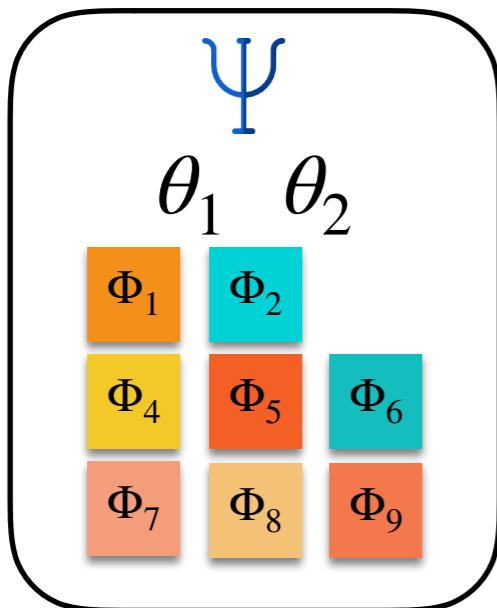
Asynchronous Proofs

Enumerate **schedules** and **network state**



Asynchronous Proofs

Enumerate **schedules** and **network state**

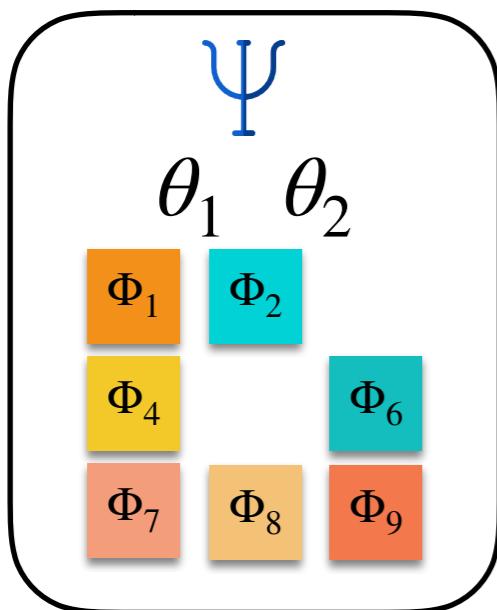


Φ_3 did send to



Asynchronous Proofs

Enumerate **schedules** and **network state**



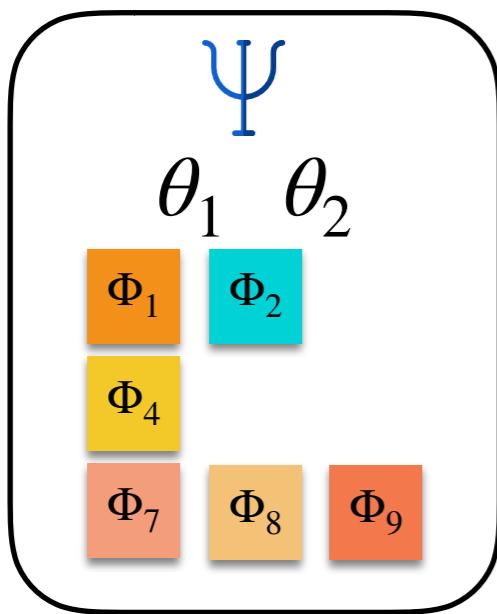
\vee
 Φ_3 did send to

\vee
 Φ_5 didn't execute its receive



Asynchronous Proofs

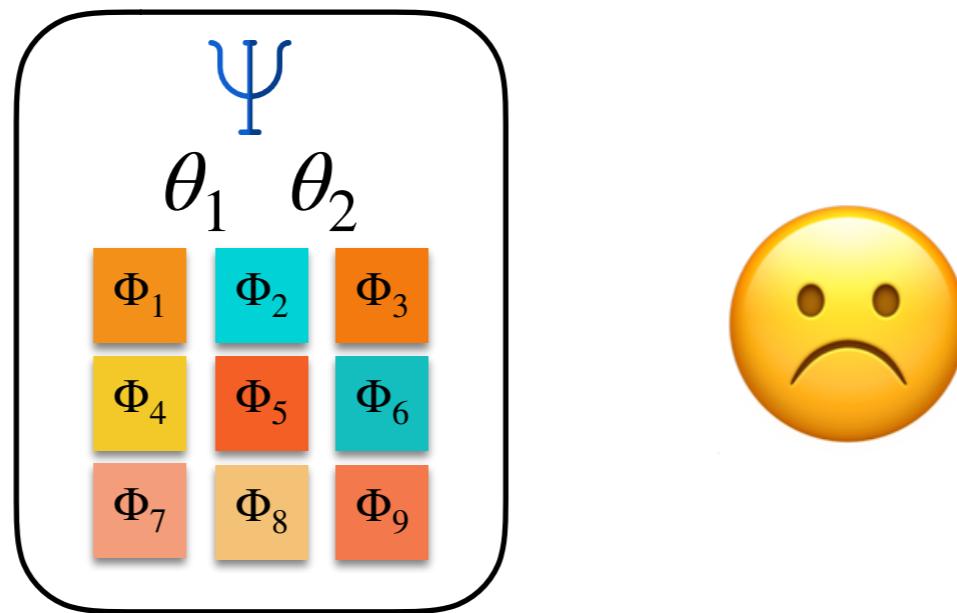
Enumerate **schedules** and **network state**



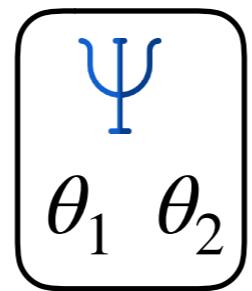
- Φ_3 did send to
- \vee Φ_5 didn't execute its receive
- \vee Φ_6 there is messages from to
containing 's ID and value

Synchronous
Proofs are *Nice!*

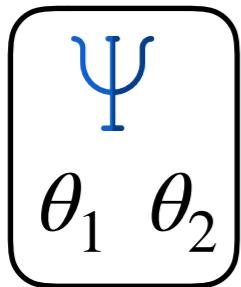
Asynchronous Proofs



Synchronous Proofs



Synchronous Proofs



Synchronous Proofs

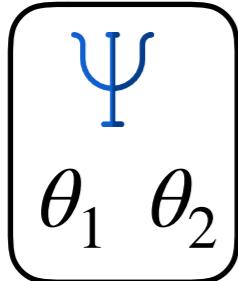
$$\begin{array}{c} \Psi \\ \theta_1 \ \theta_2 \end{array}$$



```
for p in dbs do  
    p.id<-c;  
    p.val<-c.val;
```

$$\theta_1 = \forall p \in dbs . p \in done \rightarrow p.val = c.val$$

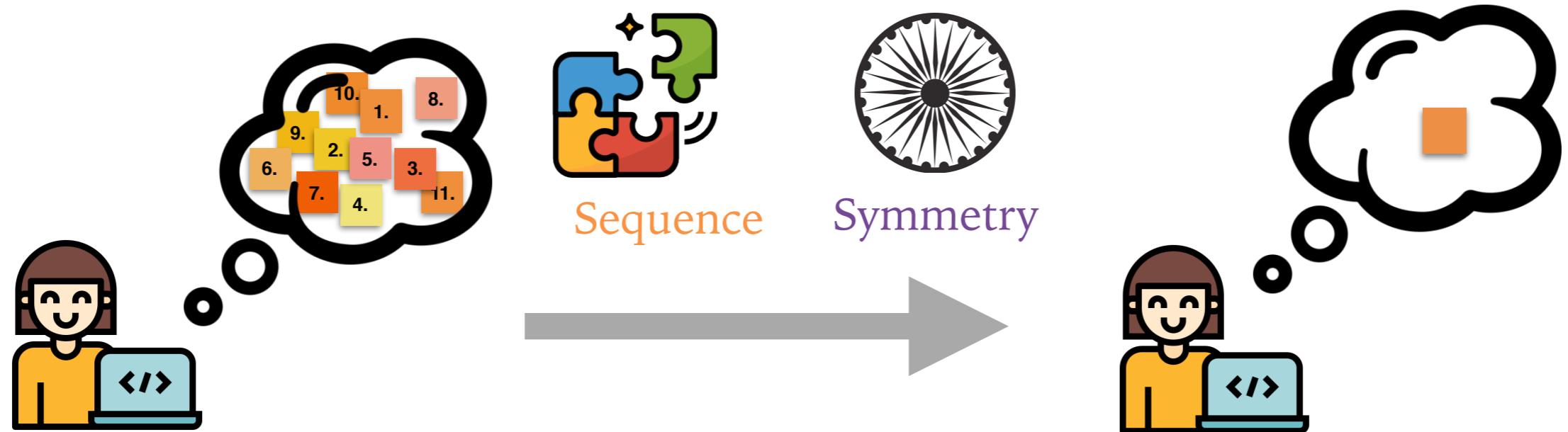
Synchronous Proofs



```
for p in dbs do
    if msg == C
        p.value <- p.val;
    _<-Ack;
```

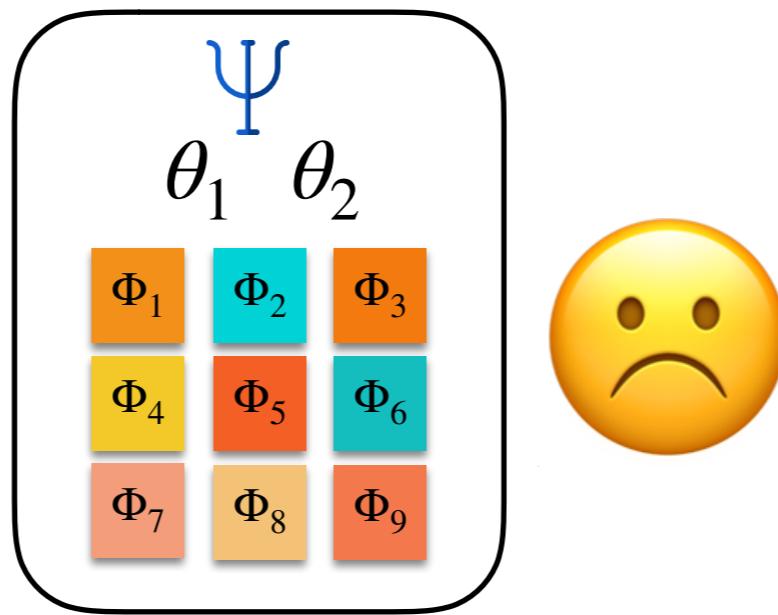
$$\theta_2 = \forall p \in dbs . p \in done \wedge c . dec = C \Rightarrow p . value = c . val$$

Recap



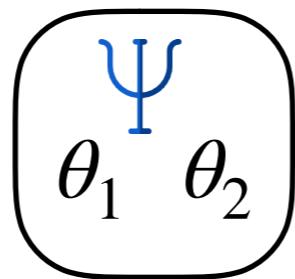
Compute synchronizations using
sequence and *symmetry*

Recap



Makes Deductive Proofs Easier

Recap



Makes Deductive Proofs Easier

Outline

Key Idea: Pretend Synchrony

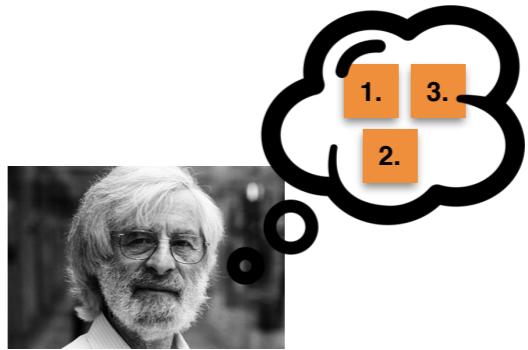
1. Computing Synchronizations
2. Verifying the Synchronization

Extensions

Evaluation

Extensions

Multicasts



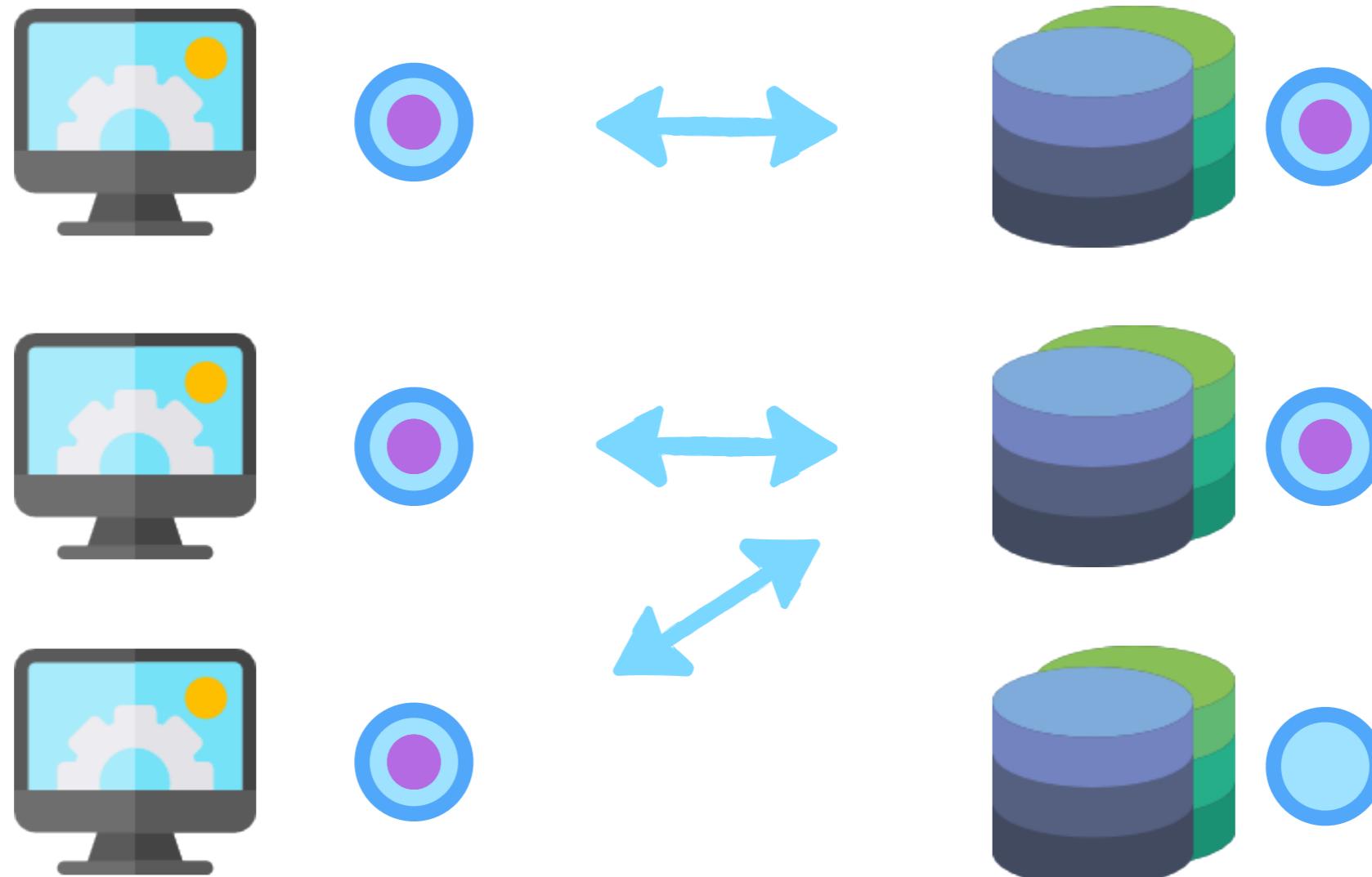
Message Drops



Rounds



Multicasts

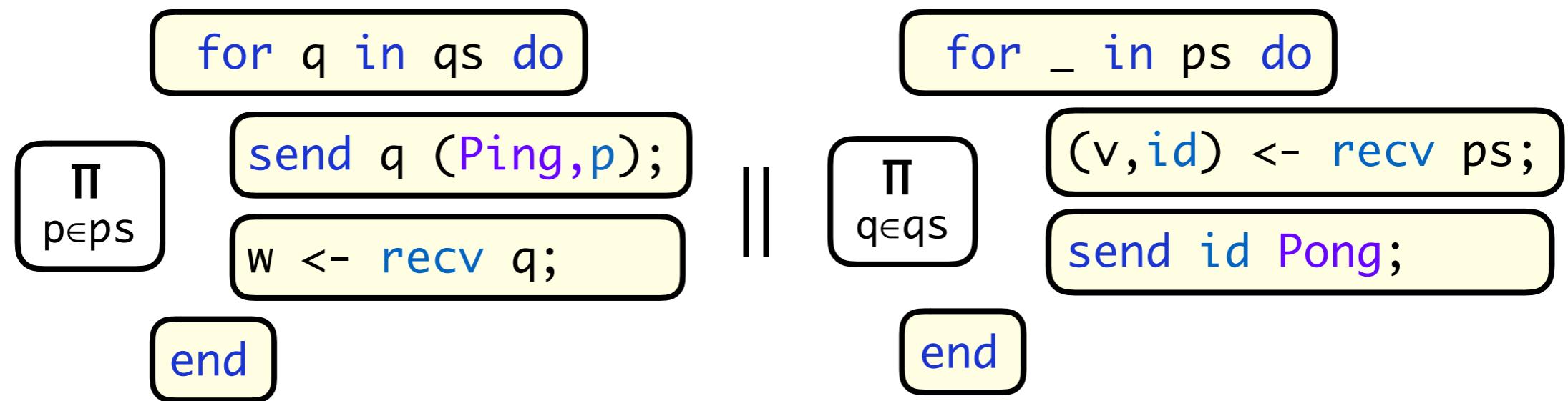


Multicasts



Multicasts

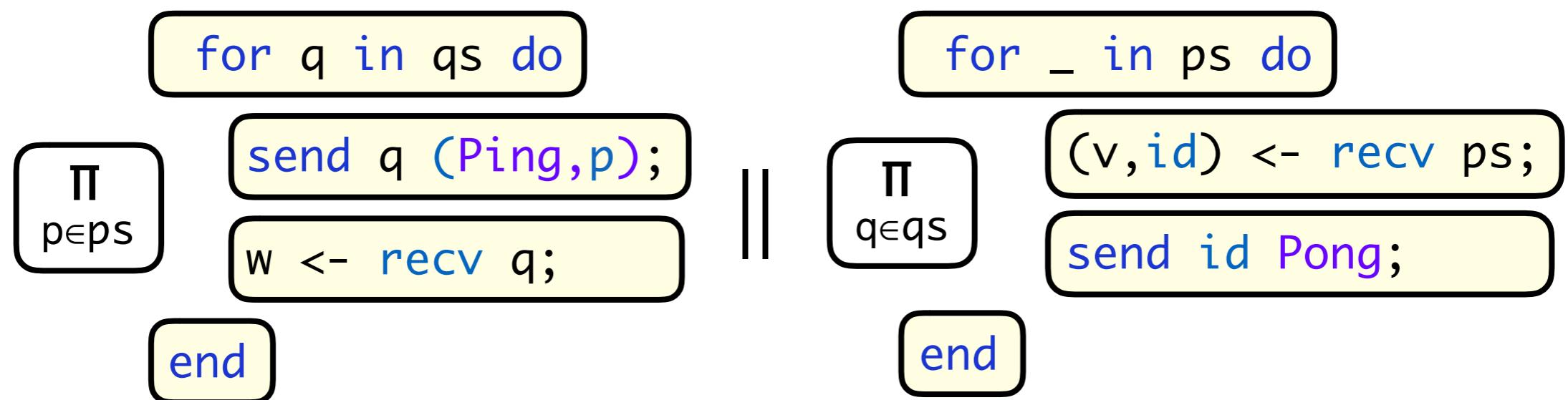
Problem: neither **sequential** nor **symmetric**



... can't compose *in sequence*? Compose *in parallel*!

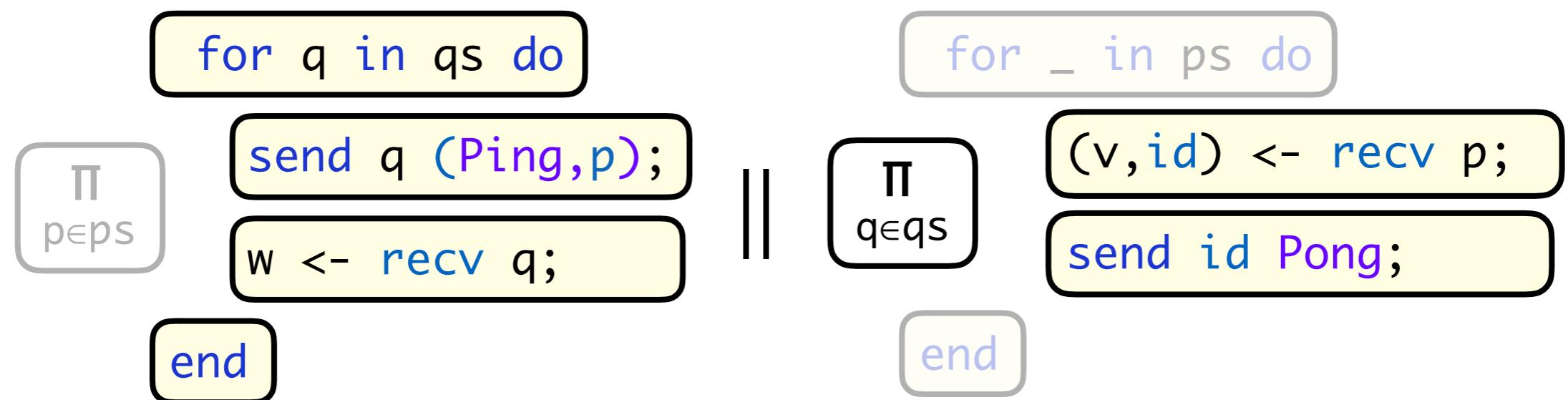
Multicasts

... can't compose *in sequence?* Compose *in parallel!*



Multicasts

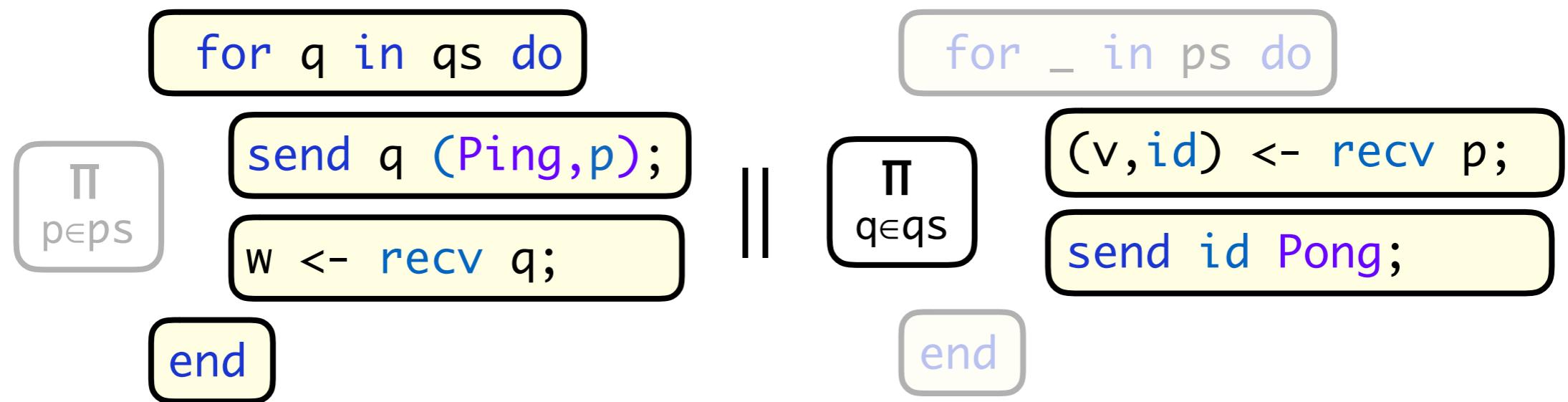
... can't compose *in sequence?* Compose *in parallel!*



... focus on arbitrary process p

Multicasts

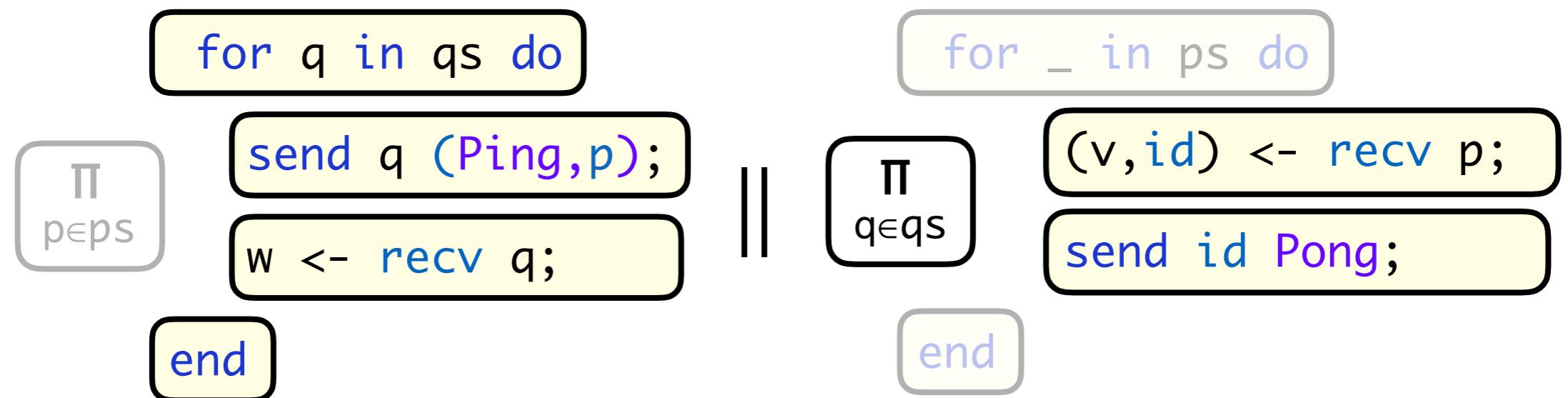
... focus on arbitrary process p



... the interaction is *sequential!*

Multicasts

... the interaction is *sequential!*



Multicasts

... the interaction is *sequential!*

```
for q in qs do
     $\Pi_{p \in ps}$ 
        (q.v, q.id) <- (Ping, p);
        p.w <- Pong;
    end
```

... synchronize (by example 2)

Multicasts

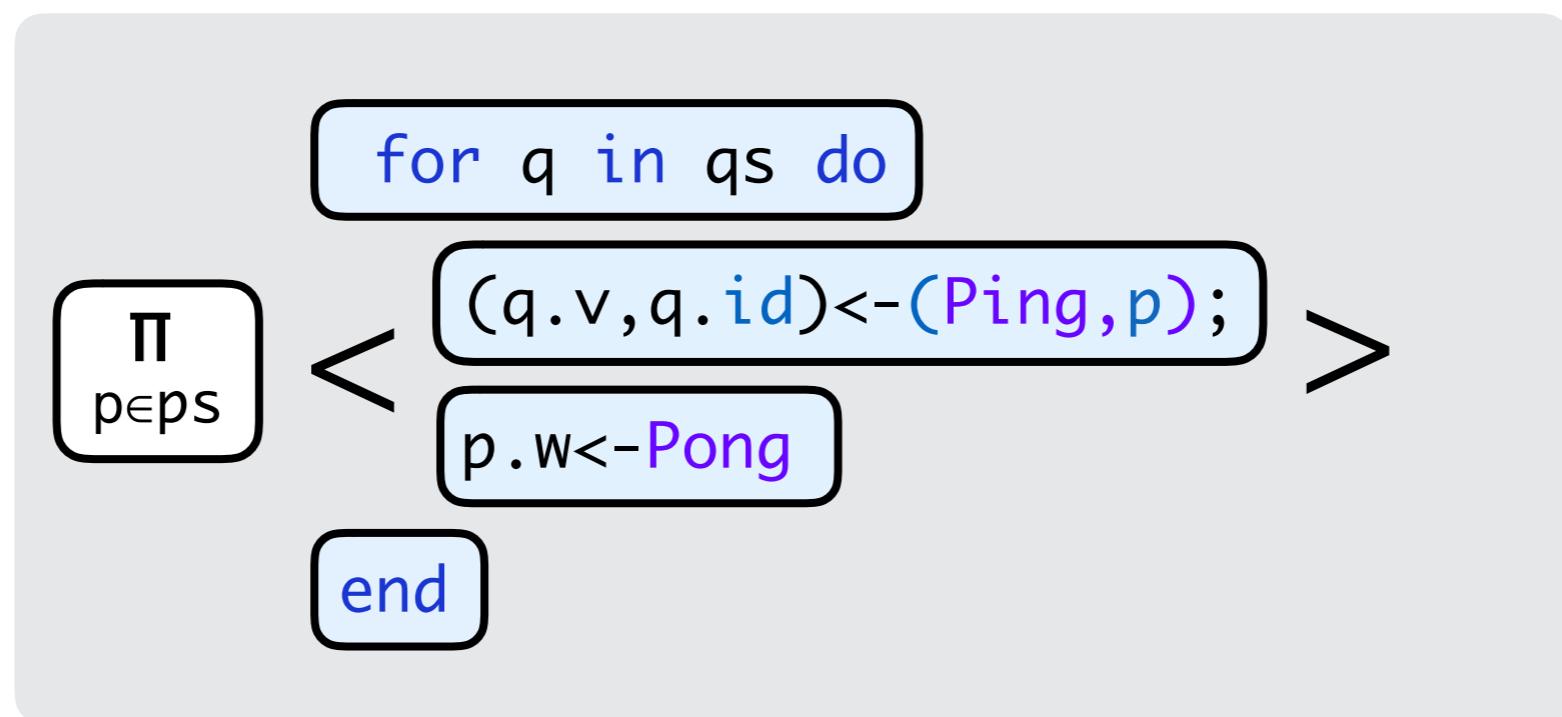
... synchronize (by example 2)

```
for q in qs do
    (q.v,q.id)<-(Ping,p);
    p.w<-Pong
end
```

$\prod_{p \in ps}$

Multicasts

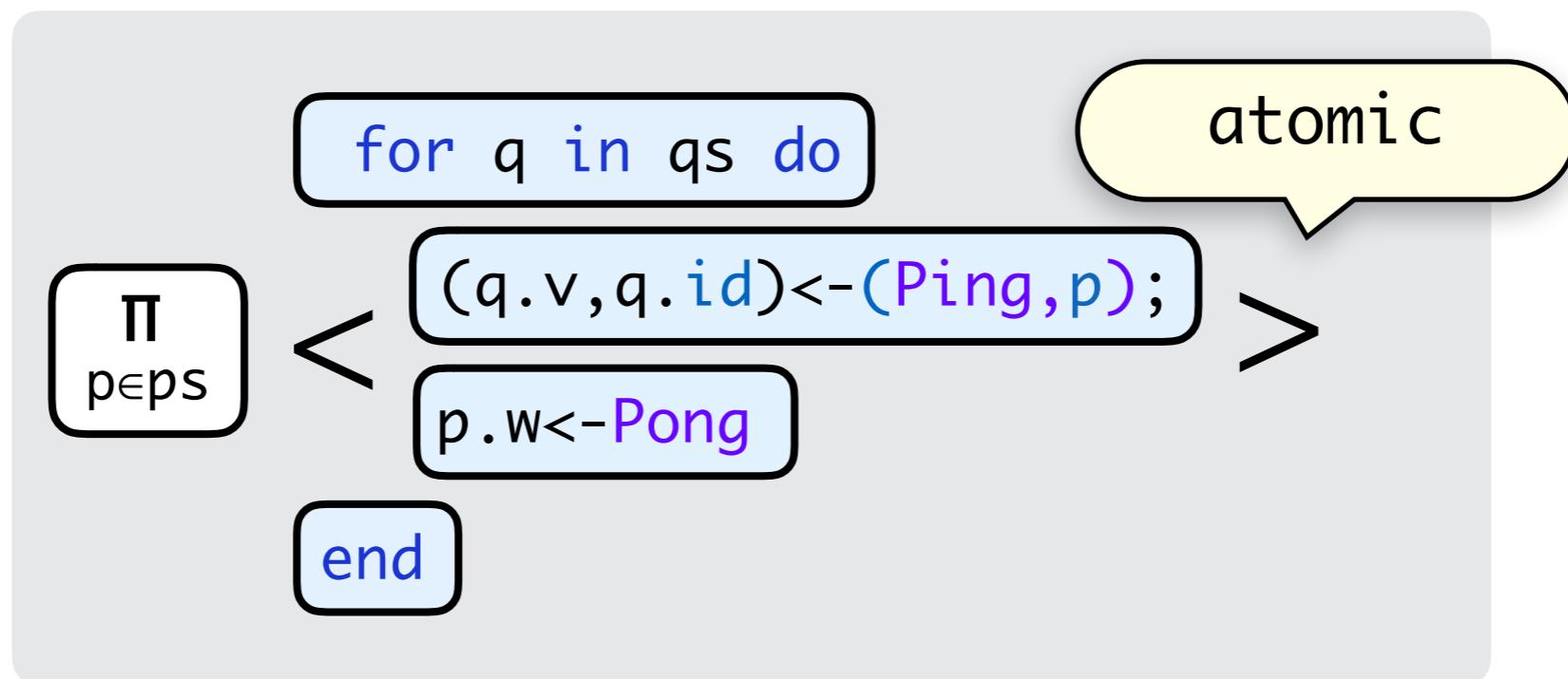
... synchronize (by example 2)



... and *generalize!*

Multicasts

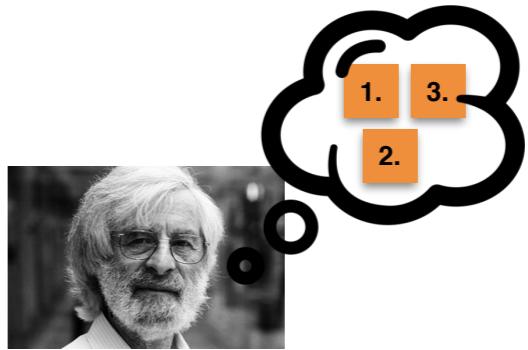
... and *generalize!*



results in a *concurrent, shared memory* program

Extensions

Multicasts



Message Drops

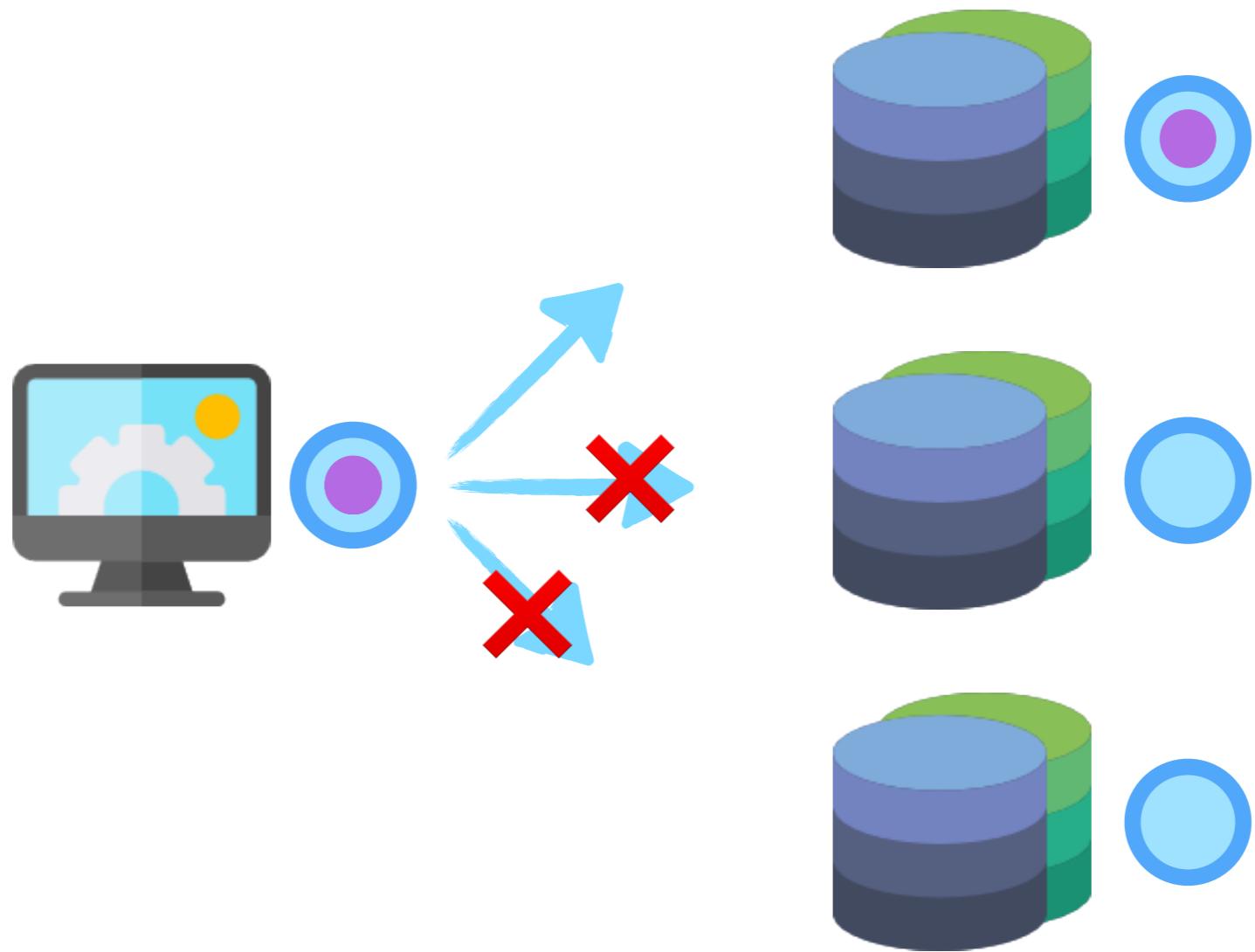


Rounds



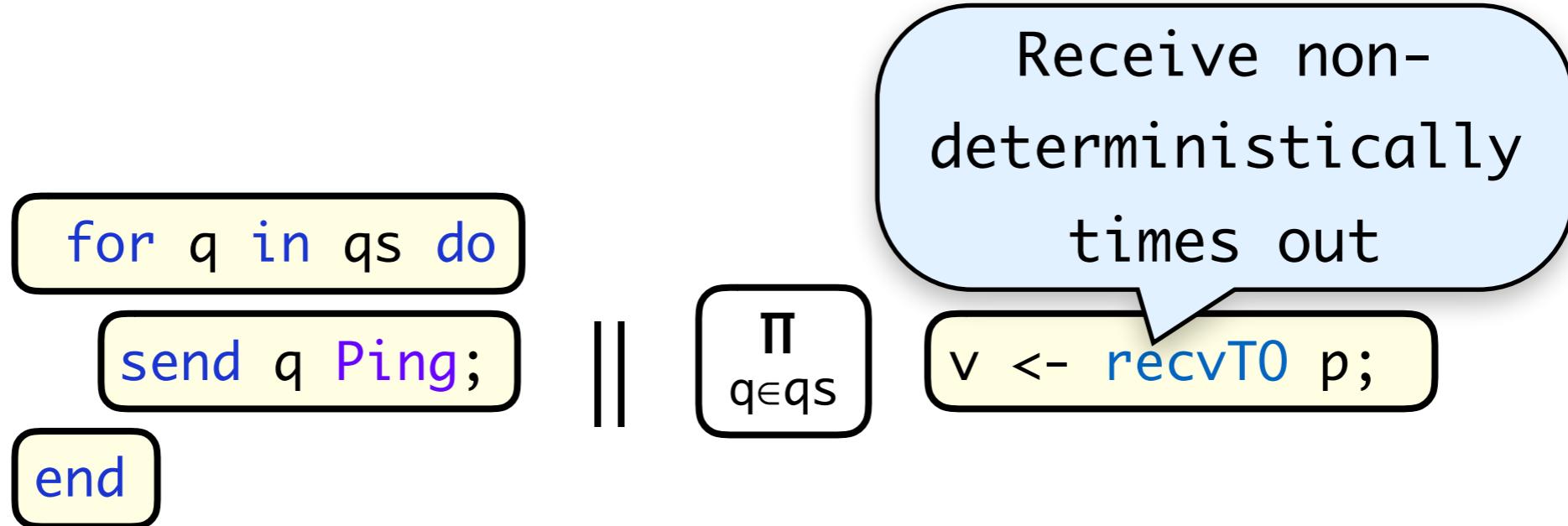
Extensions

Message Drops



Extensions

Message Drops



Extensions

Message Drops

The interaction is *sequential*

```
for q in qs do
    send q Ping;
    ||  $\prod_{q \in qs}$ 
    v <- recvTO p;
end
```

Extensions

Message Drops

The interaction is *sequential*

```
for q in qs do
    send q Ping; ||  $\prod_{q \in qs}$  v <- recvTO p;
end
```

... focus on a single iteration

Extensions

Message Drops

... focus on a single iteration

```
for q in qs do
    send q Ping; ||  $\prod_{q \in qs}$  v <- recvTO p;
end
```

Extensions

Message Drops

... focus on a single iteration

```
for q in qs do
```

```
    send q Ping;
```

```
    v <- recvTO p;
```

```
end
```

... match up the send and receive

Extensions

Message Drops

... match up the send and receive

```
for q in qs do
```

```
    send q Ping;
```

```
    v <- recvTO p;
```

```
end
```

Extensions

Message Drops

... match up the send and receive

```
for q in qs do
```

```
    q.v <-* ?
```

```
    Just Ping:
```

```
    None
```

```
end
```

... *case-split* whether the message was received

Extensions

Message Drops

... *case-split* whether the message was received

```
for q in qs do
    q.v <-* ?
        Just Ping:
        None
end
```

Extensions

Message Drops

... *case-split* whether the message was received

```
for q in qs do
```

```
    q.v <-* ?
```

Just **Ping**:

None

```
end
```

... *and generalize*

Extensions

Message Drops

```
for q in qs do
```

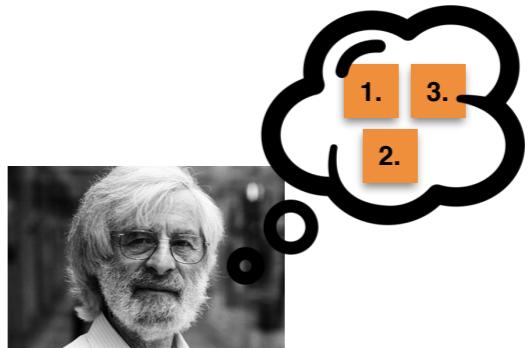
```
    q.v <-* ?  
    Just Ping:  
    None
```

```
end
```

Synchronization

Extensions

Multicasts



Message Drops

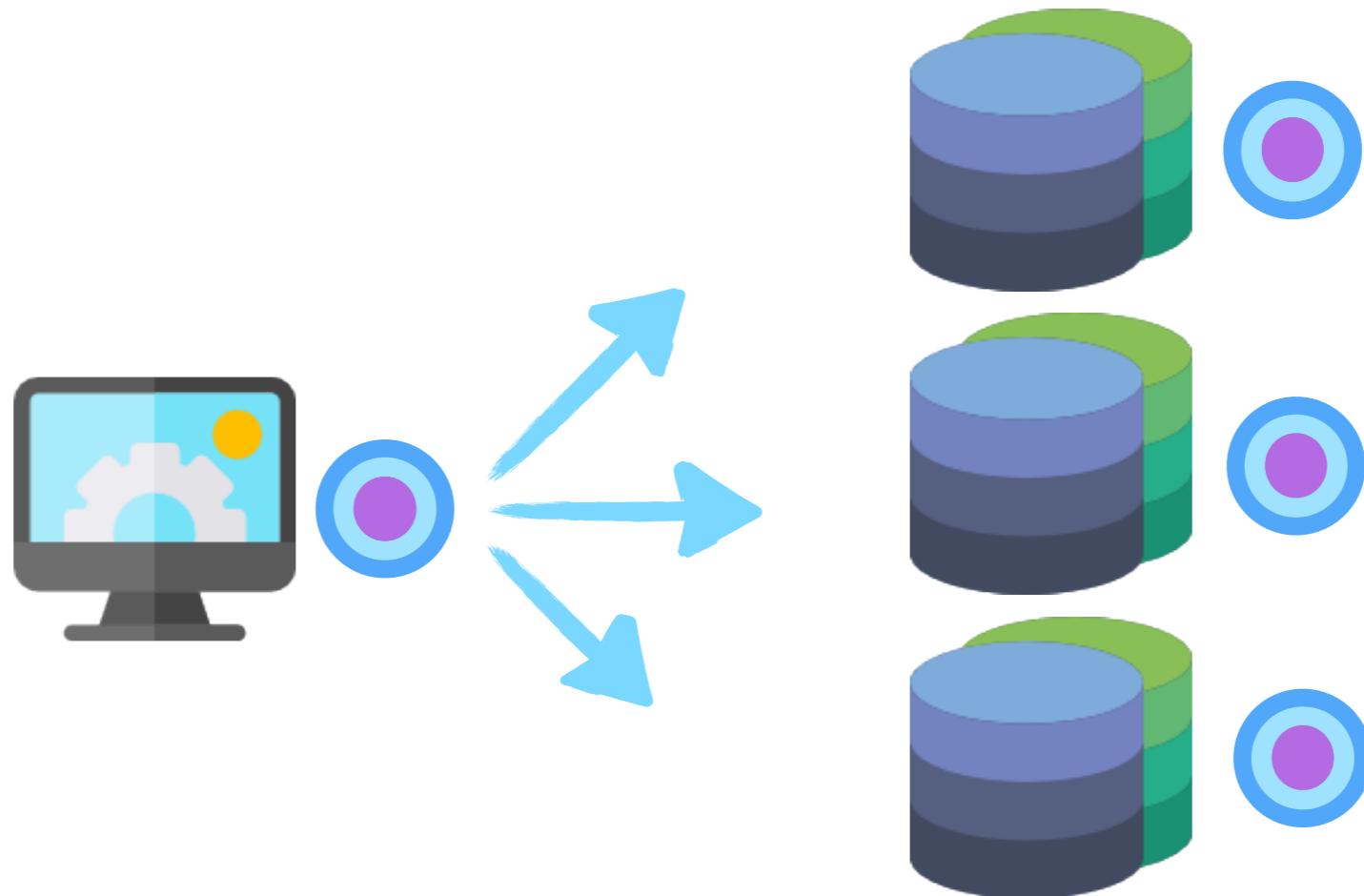


Rounds



Extensions

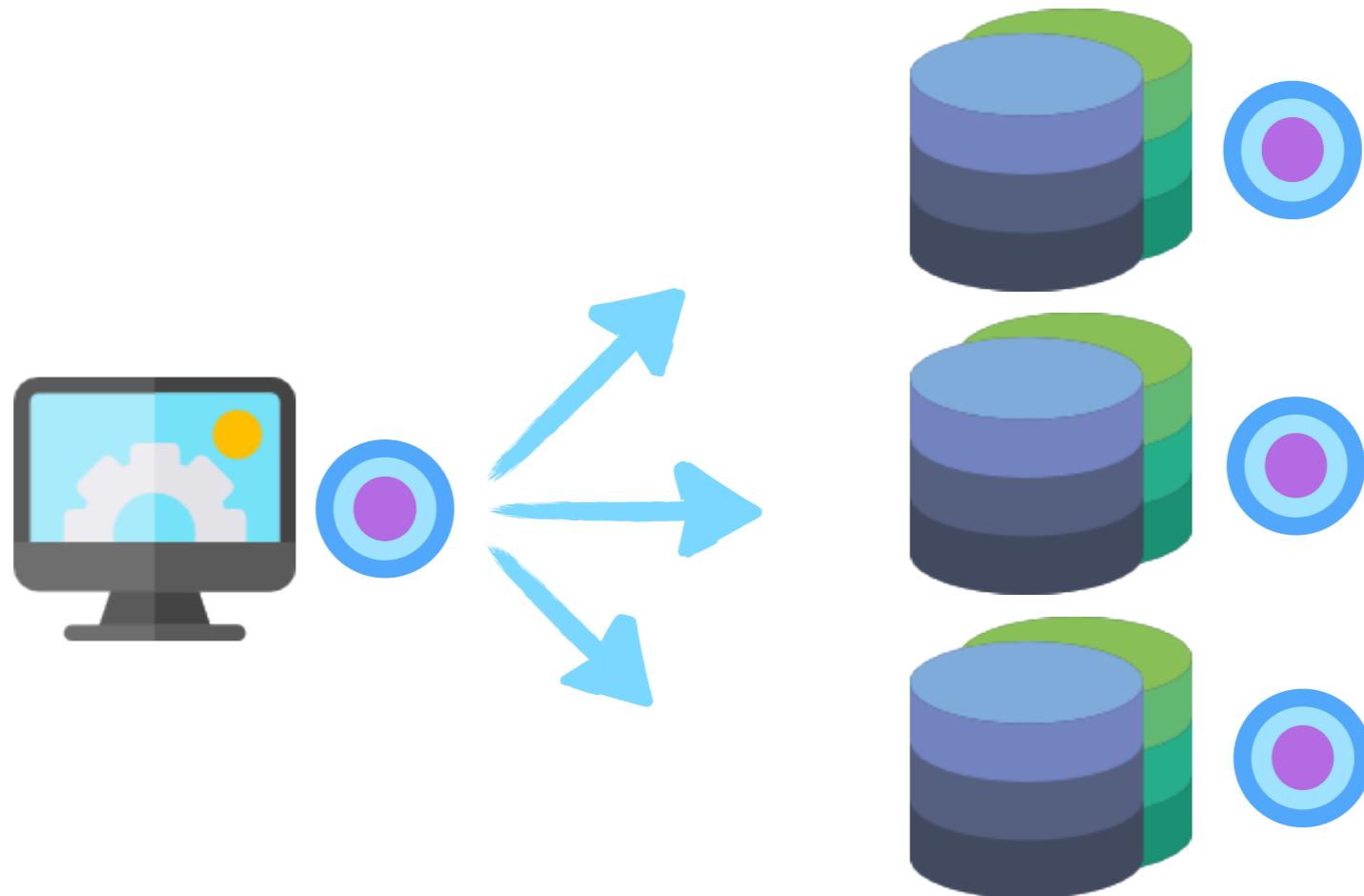
Rounds



Instead of running only *once*

Extensions

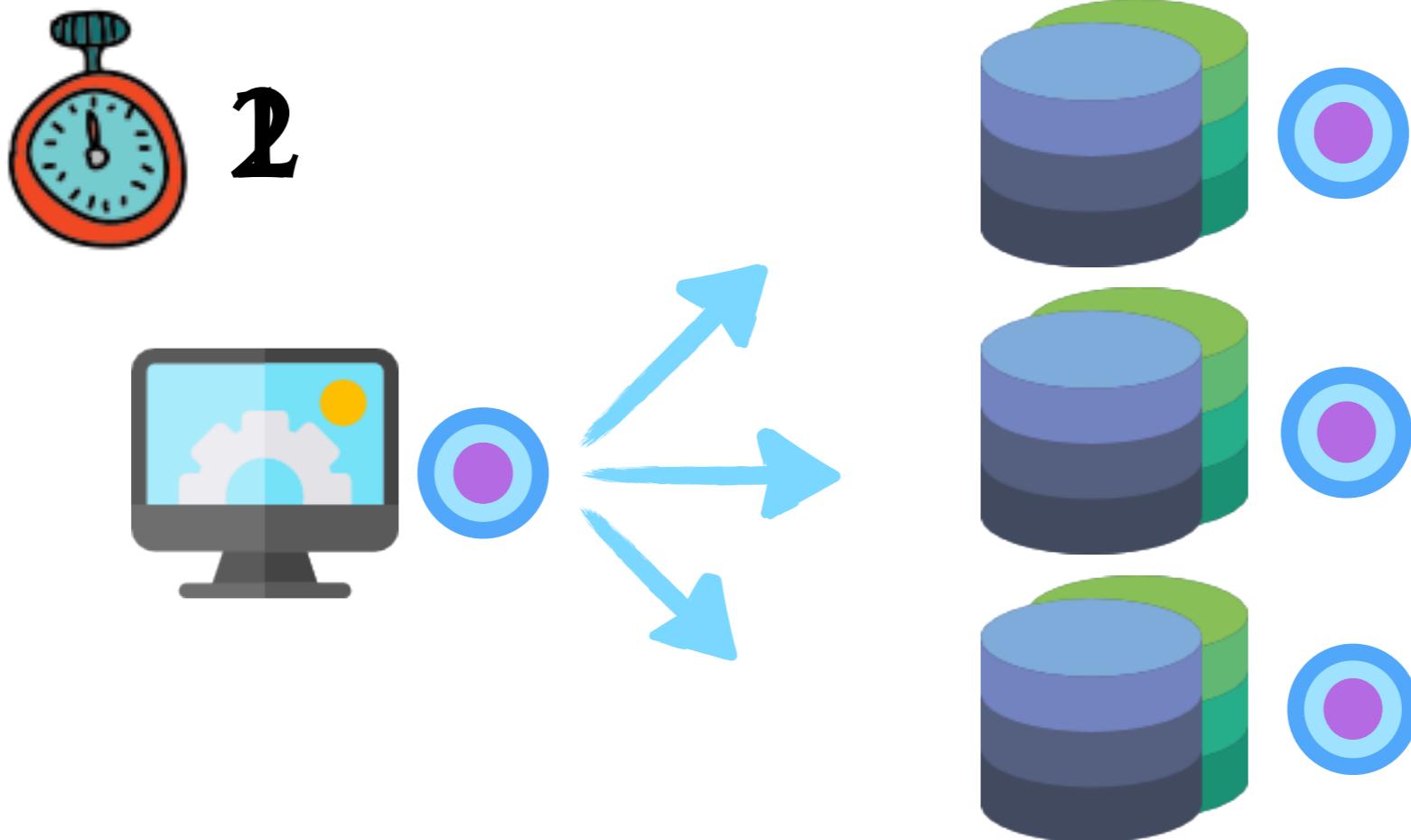
Rounds



... repeat protocol in multiple rounds

Extensions

Rounds

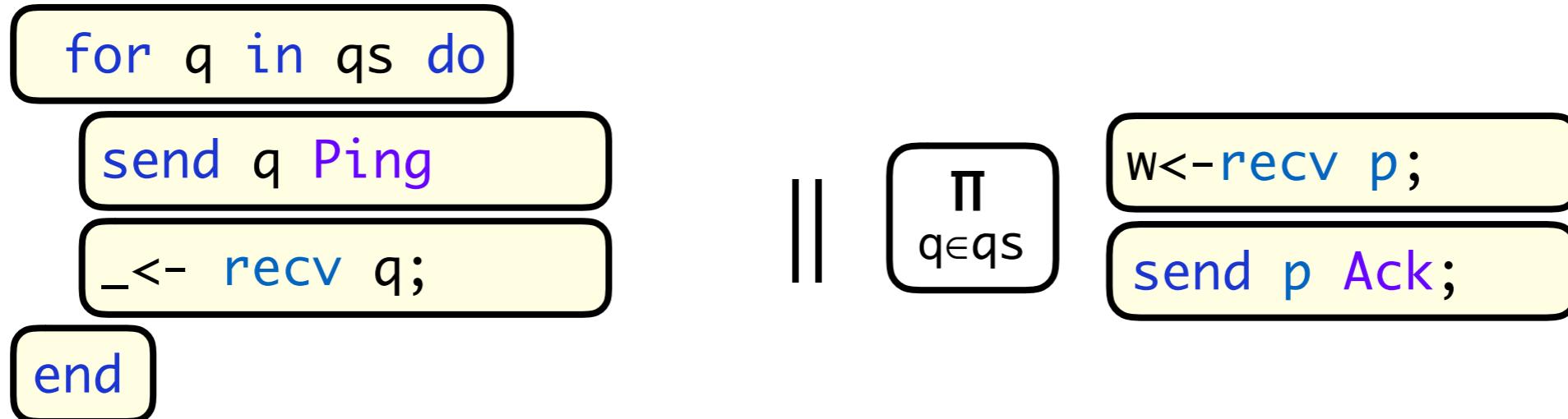


... repeat protocol in multiple rounds

Extensions

Rounds

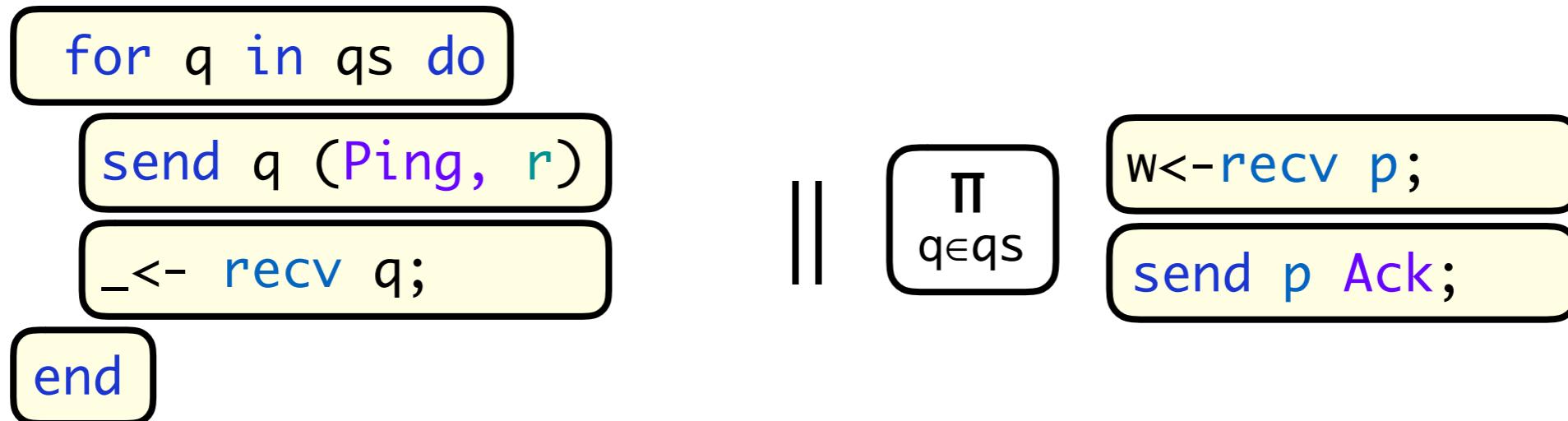
To repeat the protocol (from *Ex. 2*)



Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

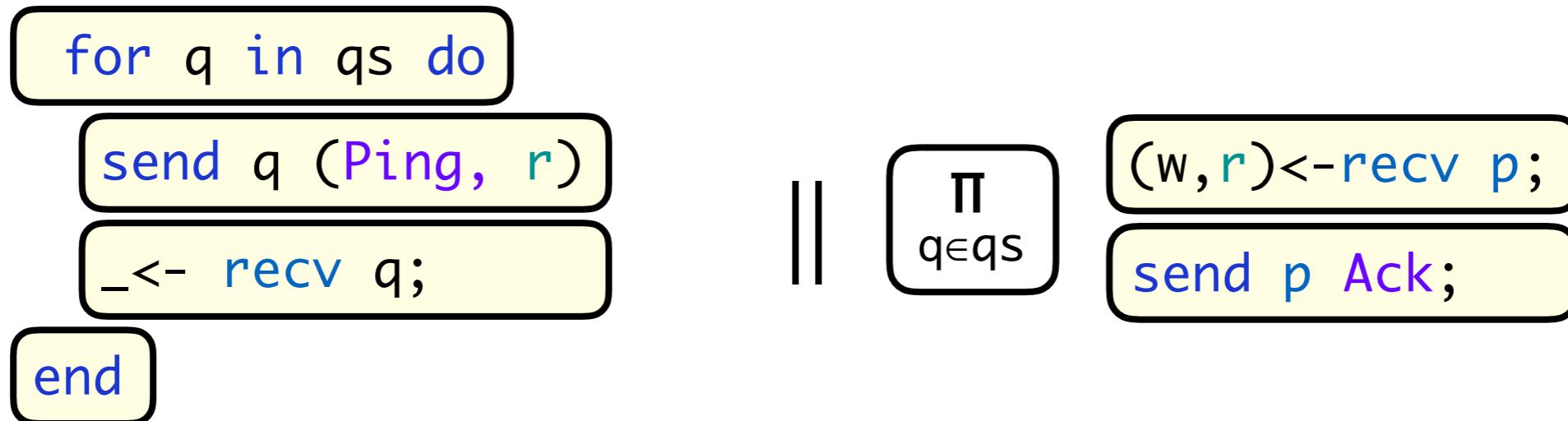


... we send a round number *r*

Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

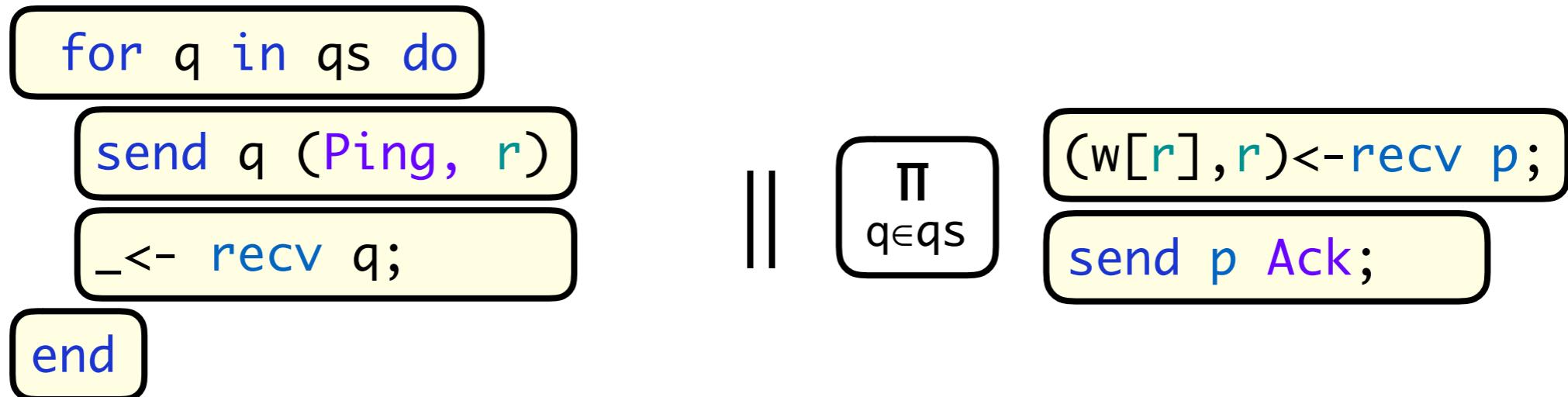


... bind the round number at the receive

Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

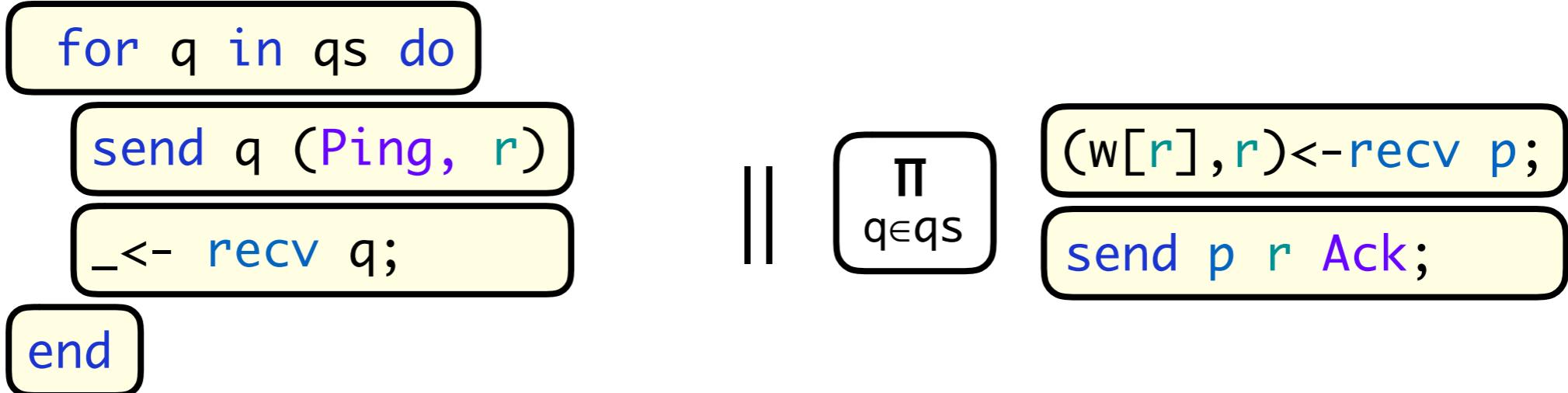


... and turn w into an *array*

Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

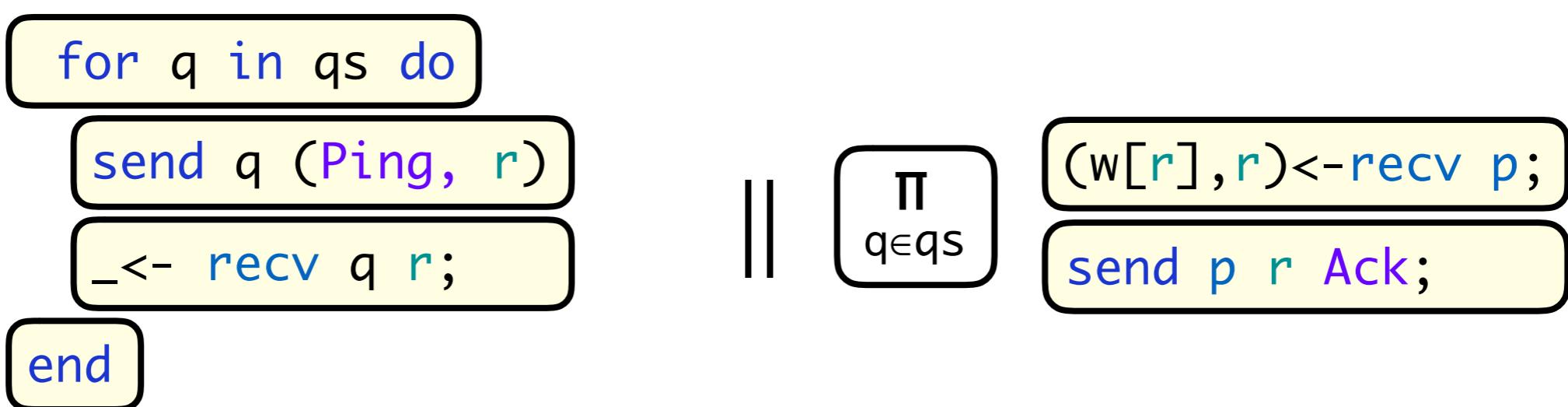


... reply with a message for round number r

Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

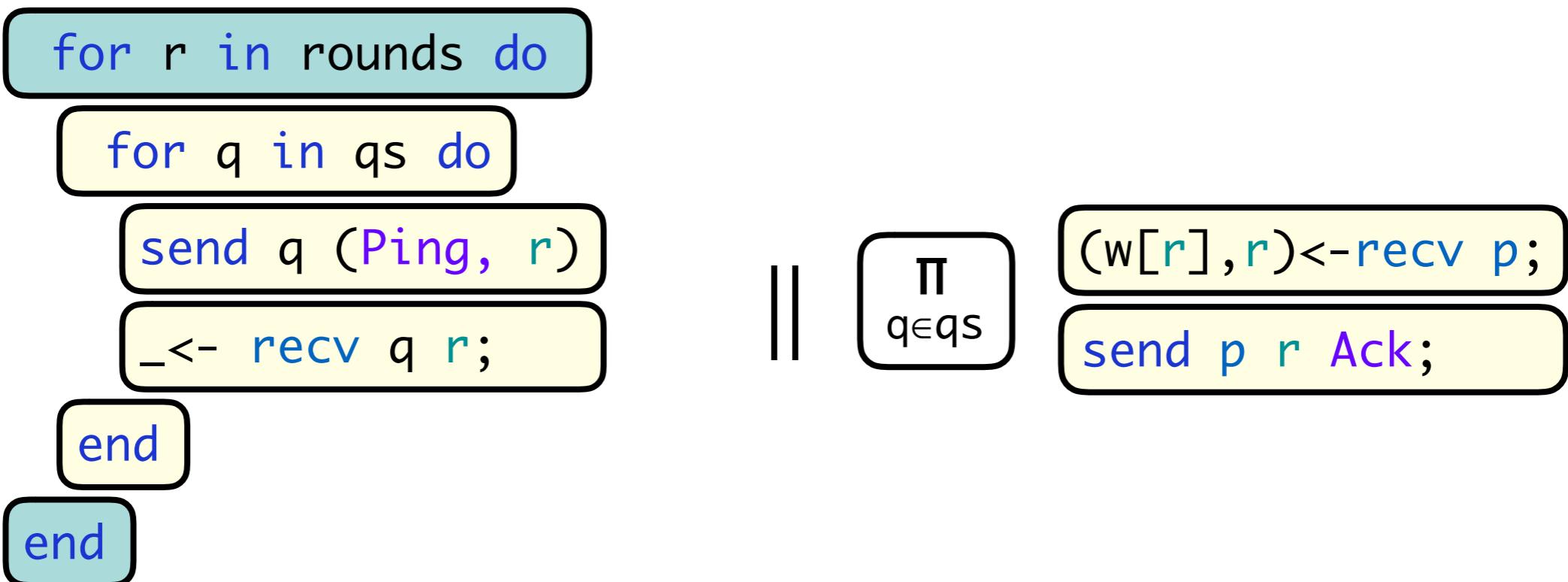


... receive for round r

Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

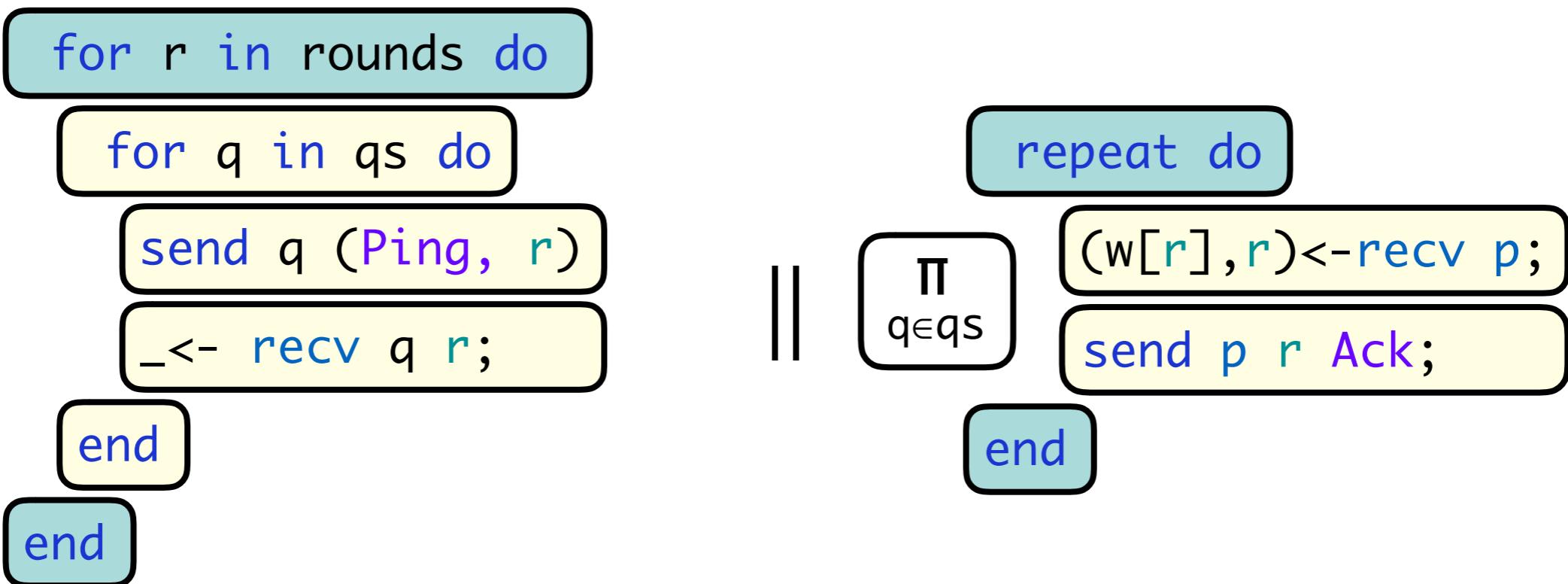


... repeat the for-loop, *once for each round*

Extensions

Rounds

To repeat the protocol (from *Ex. 2*)

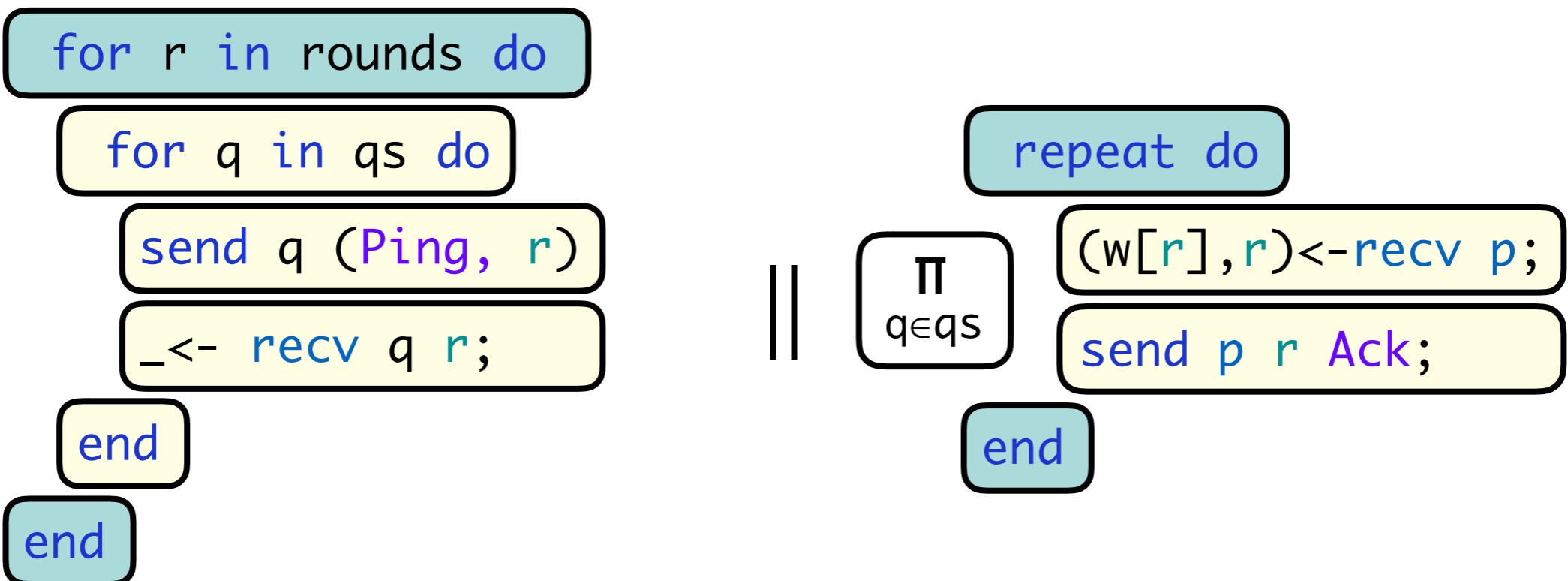


... and *repeat* each process *q*, *indefinitely*

Extensions

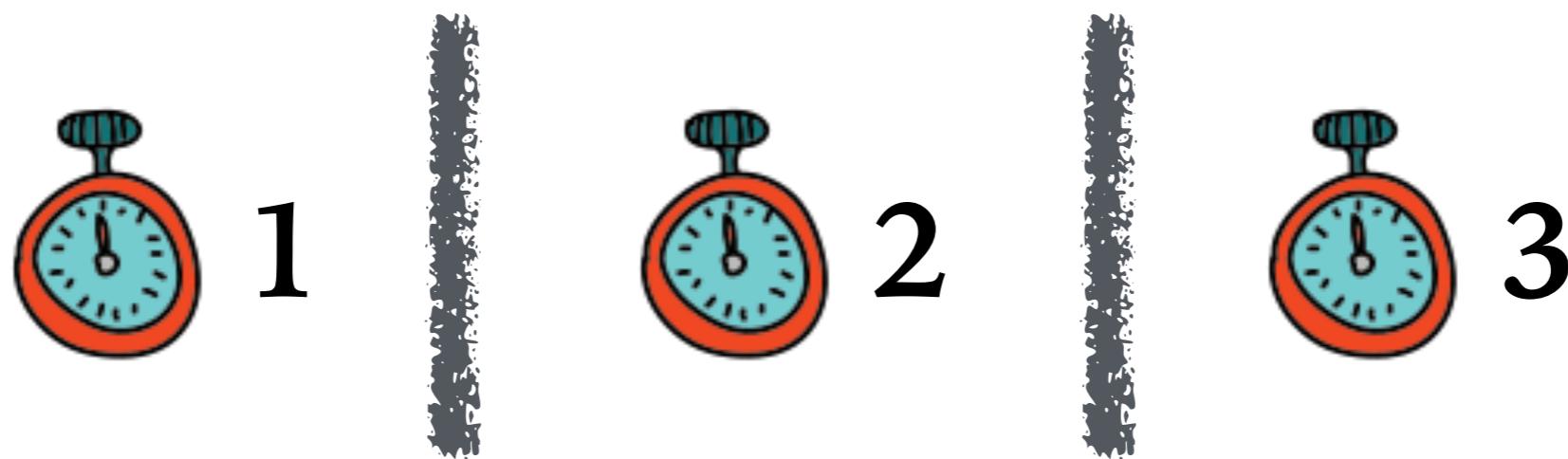
Rounds

Show $\forall r \in rounds, \forall q \in qs : q . w[r] = Ping$



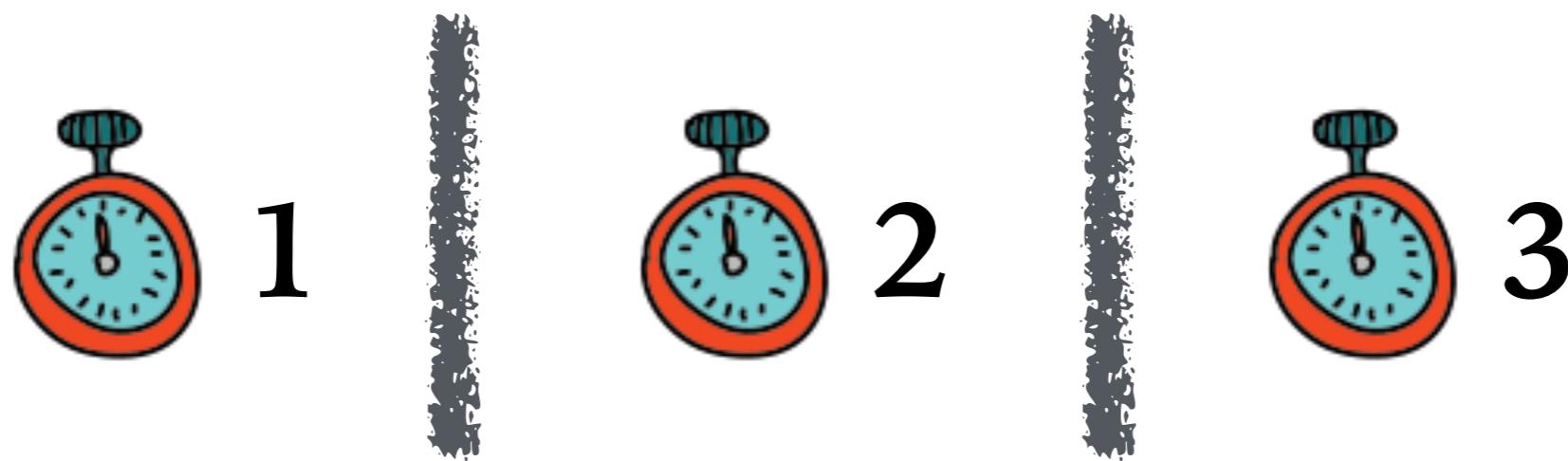
Idea: Round
Non-Interference

Idea: Round Non-Interference



No shared state or communication
between rounds

Idea: Round Non-Interference

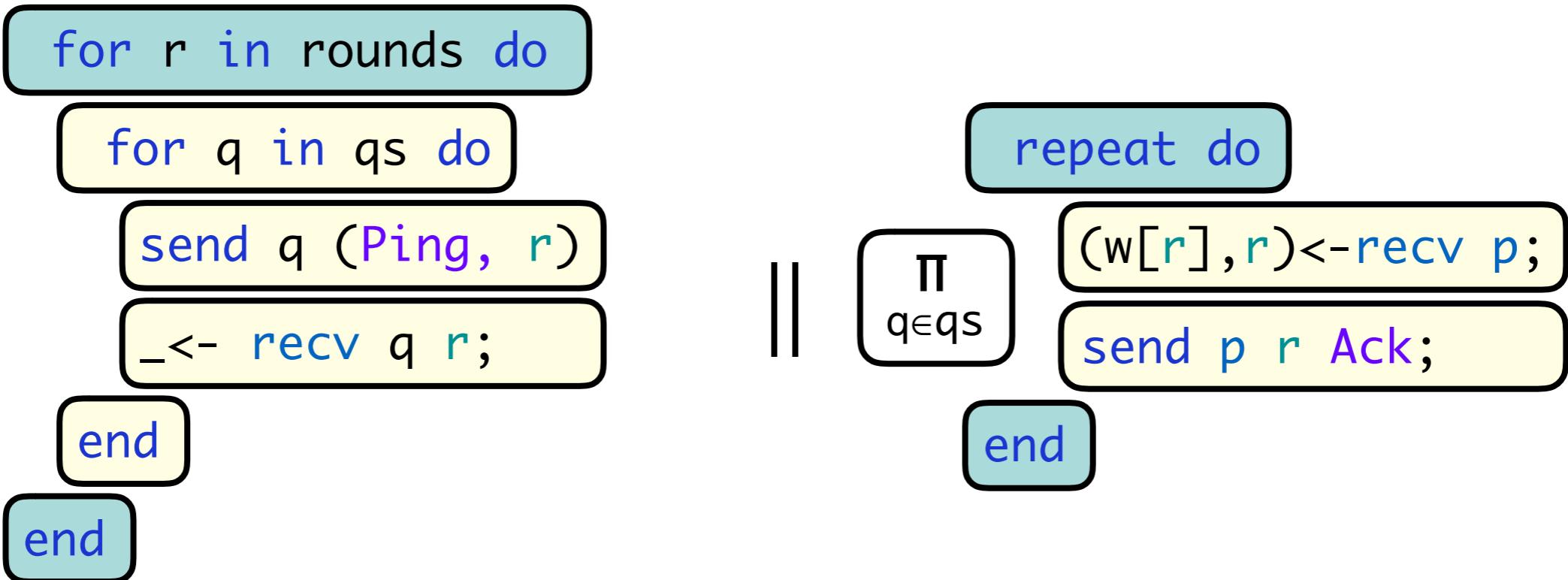


Show $\forall r \in \text{rounds} : \varphi(r)$ by showing
 $\varphi(r^*)$ for an arbitrary round r^*

Extensions

Rounds

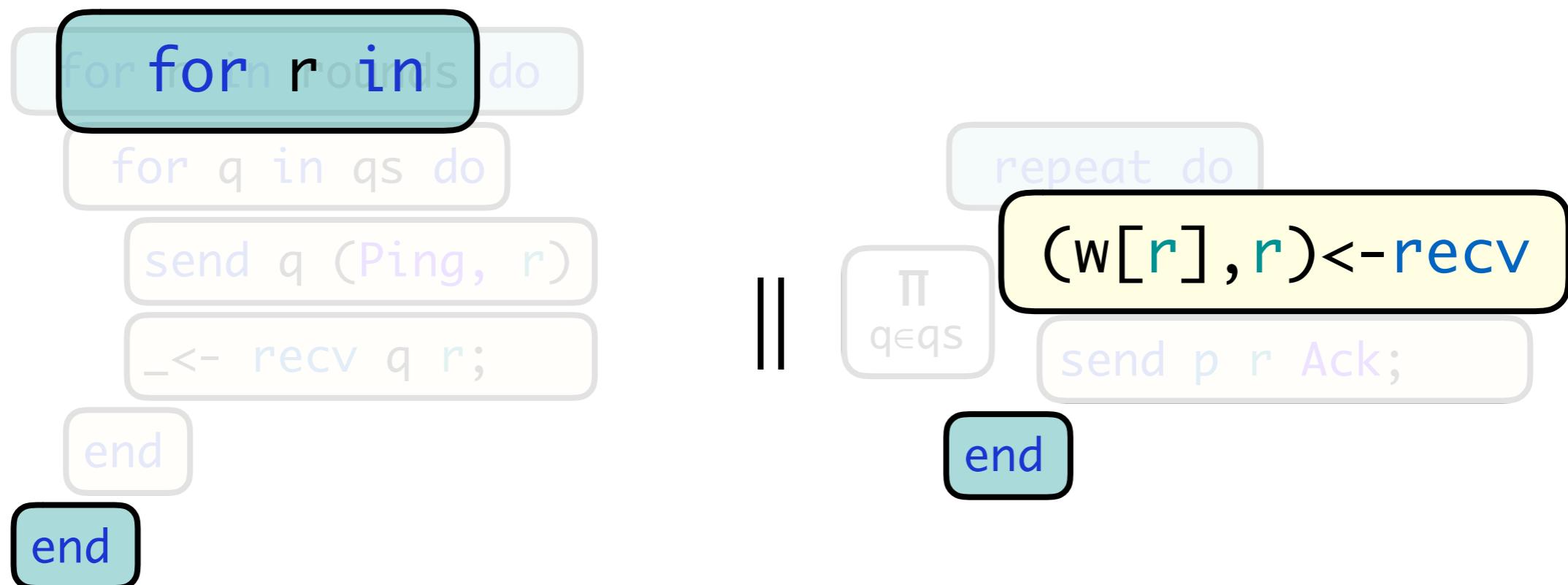
Check Round Non-interference via Syntax



Extensions

Rounds

Check Round Non-interference via Syntax

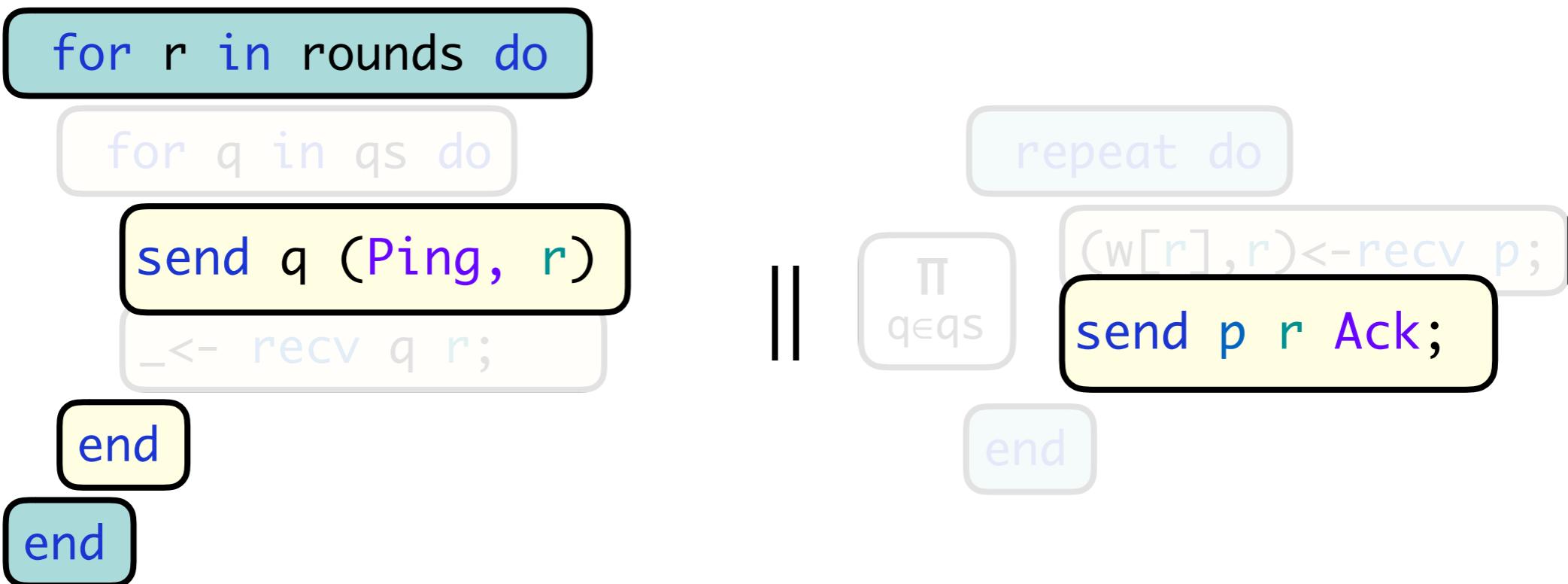


... round id only bound *once*

Extensions

Rounds

Check Round Non-interference via Syntax

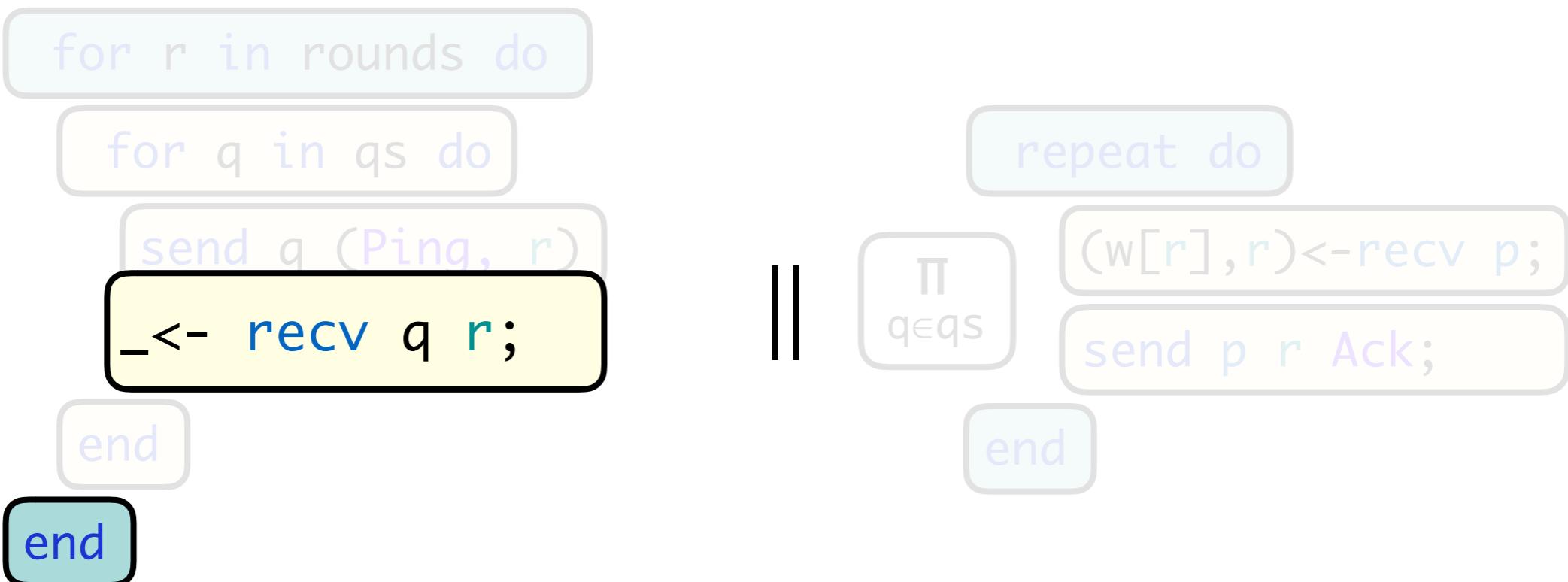


... send only for bound round

Extensions

Rounds

Check Round Non-interference via Syntax

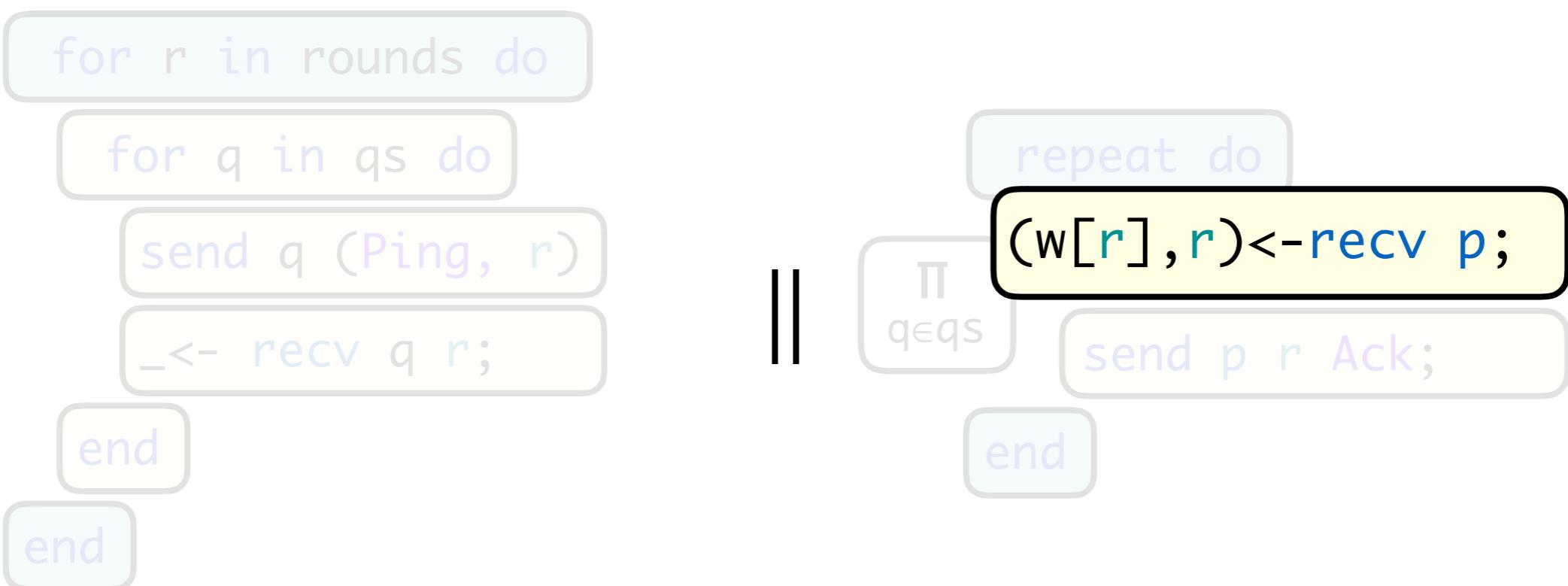


... receive *only for bound round*

Extensions

Rounds

Check Round Non-interference via Syntax

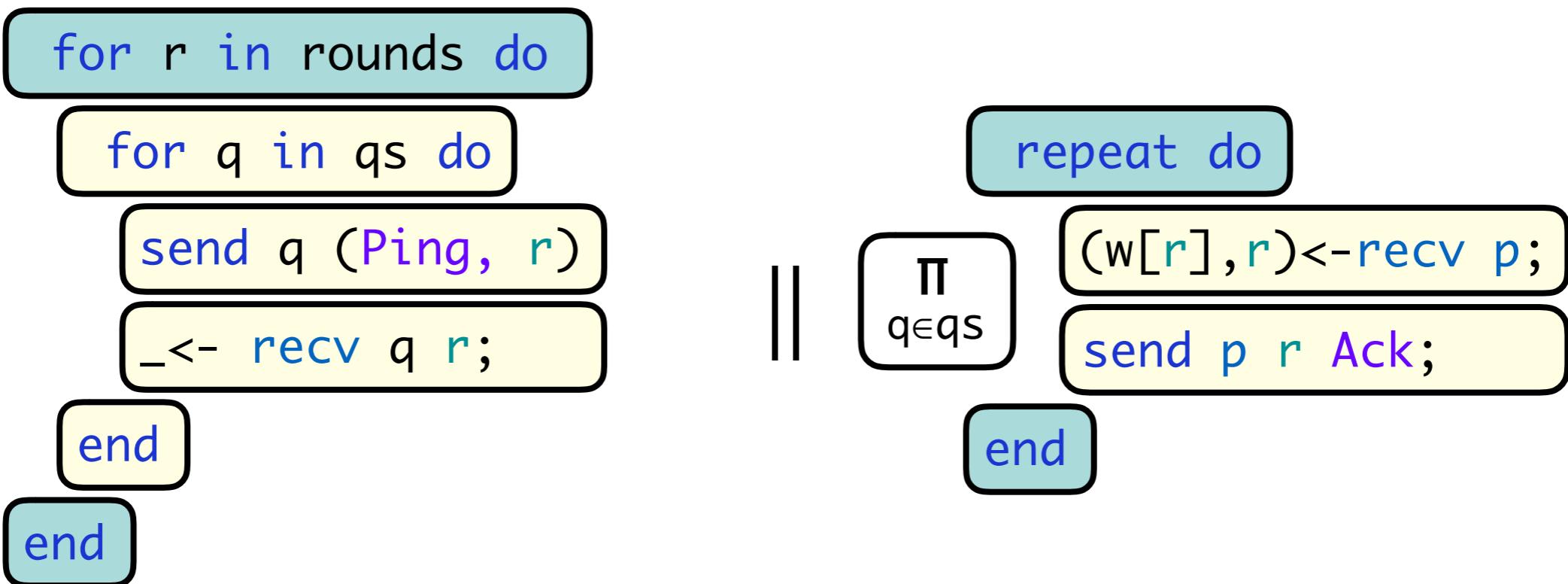


... array indexed *only for bound round*

Extensions

Rounds

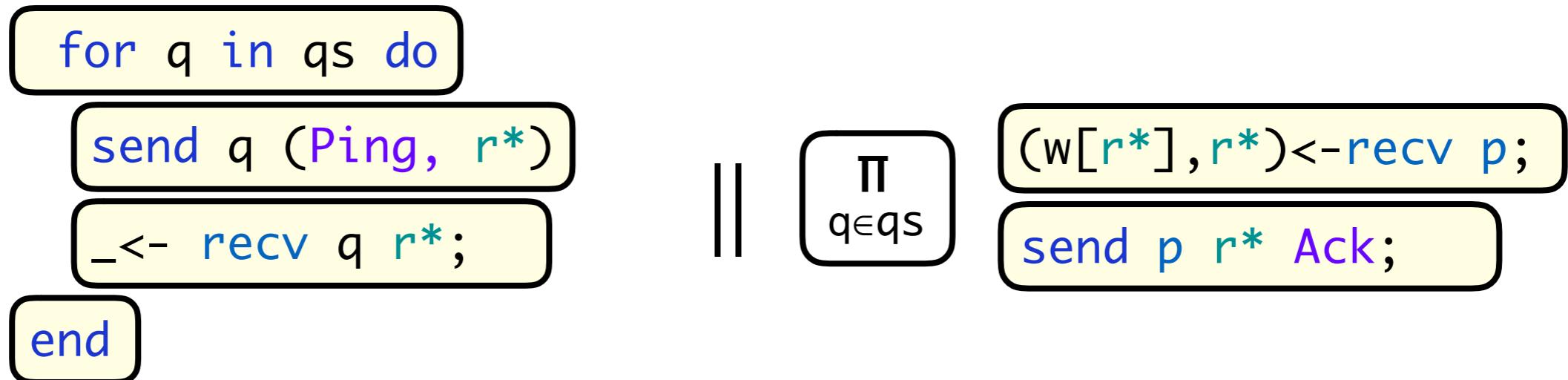
To show $\forall r \in rounds : q.w[r] = Ping$



Extensions

Rounds

To show $\forall r \in rounds : q . w[r] = Ping$



... show $\forall q \in qs : q . w[r^*] = Ping$ as before

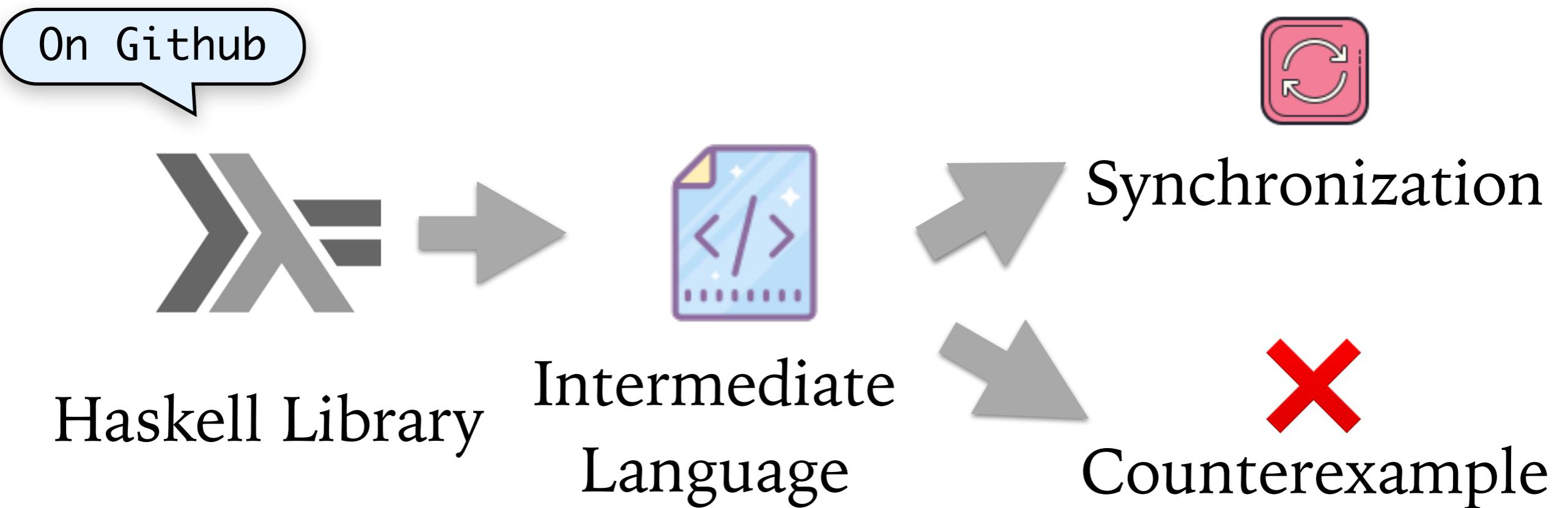
Evaluation

Evaluation

Brisk

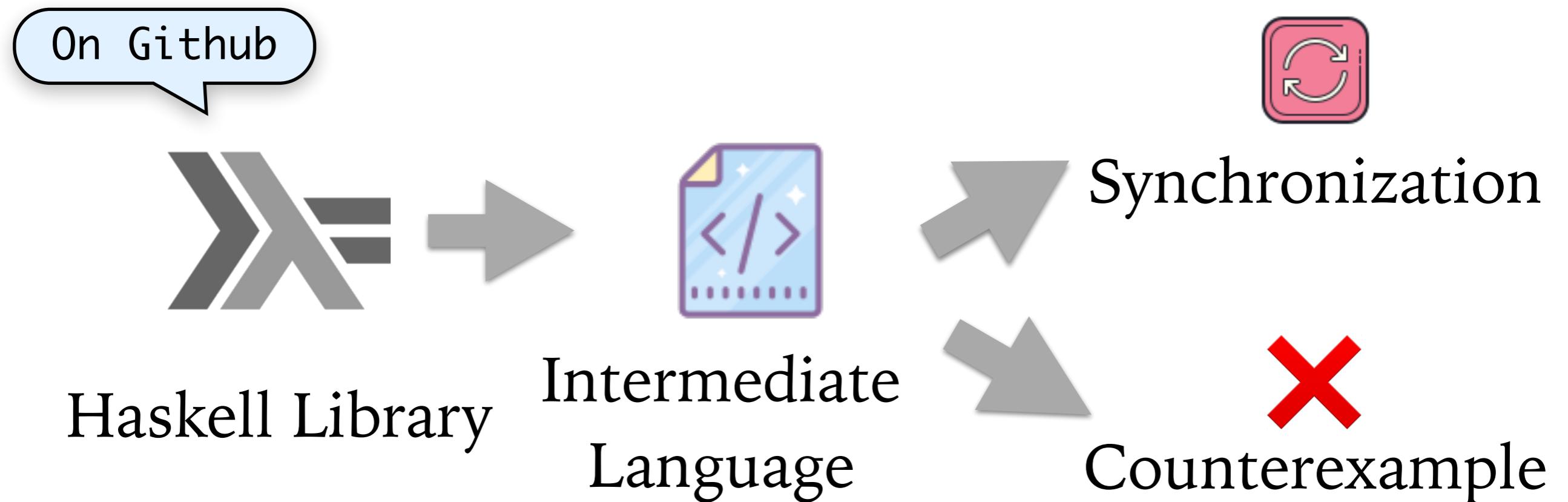
Goolong

Brisk: OOPSLA'17



checks if a *synchronization* exists

Brisk: OOPSLA'17



...through *rewrites* implemented in Prolog

Brisk: OOPSLA'17

On Github



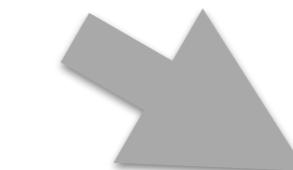
Haskell Library



Intermediate
Language



Synchronization



Counterexample

... no *deadlocks*, spurious sends, etc.

Brisk: OOPSLA'17

Name	Time	Brisk	Spin	T0	#n
ConcDB	20 ms			6	
DistDB	20 ms			2	
Firewall	30 ms			2	
LockServer	30 ms			12	
MapReduce	30 ms			4	
Parikh	20 ms			-	
Registry	30 ms			10	
TwoBuyers	20 ms			-	
2PC	50 ms			6	

From
the
literature

Use
interactively

Really
fast

Brisk: OOPSLA'17

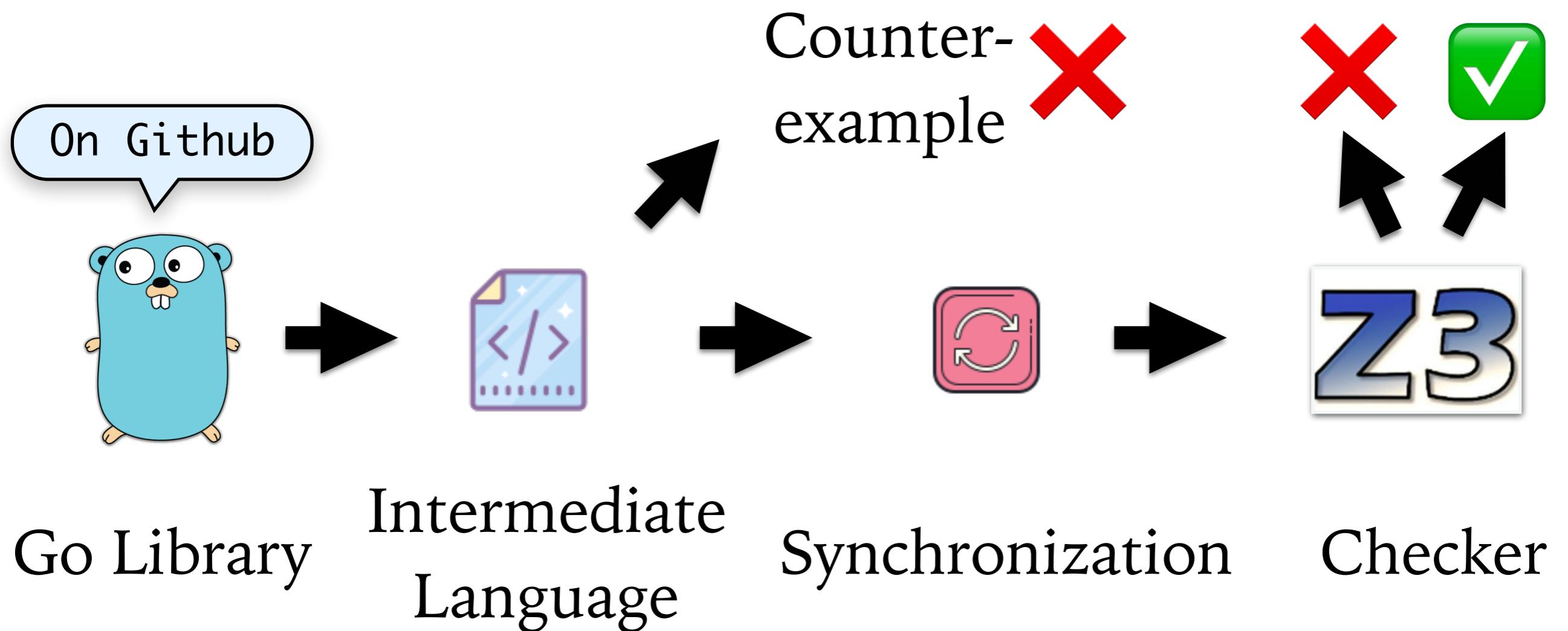
Name	Time	Brisk	Spin	T0	#n	Case studies
Map/Reduce		40	ms		5	
Theque						
Filesystem		100	ms		3	

Evaluation

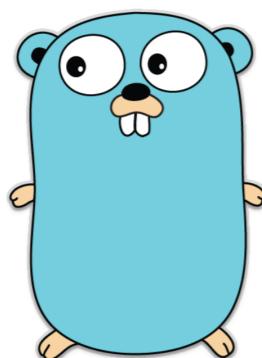
Brisk

Goolong

Goolong: POPL'19



Goolong: POPL'19



Go Library

Declare
protocol
variables

Iteration
over sets/
Invariants

Communication

x<-**NewVar()**

v<-x.**Get()**

x.**Set(v)**

for q **in** qs **@Inv** **do**

send q v

w<- **recv** q;

Goolong: 2PC Phase 1

```
proposal := gochai.NewVar()  
vote := gochai.NewVar()  
reply := gochai.NewVar()  
abort := gochai.NewVar()  
committed := gochai.NewVar()  
ack := gochai.NewVar()
```

Declarations

```
committed.Assign(0)  
abort.Assign(0)
```

Send proposals

```
/*{-@ invariant: forall([decl(i,int)], implies( and([ elem(i,done) ]), ref(val,i)=proposal) ) -@}*/  
for ID := range n.PeerIds {  
    n.Send(ID, proposal)  
}  
  
for ID := range n.PeerIds {  
    vote = n.RecvAll()  
    if vote.Get() == 0 {  
        abort.Assign(1)  
    }  
}
```

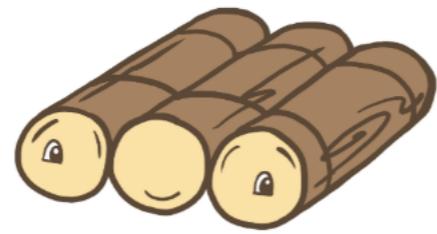
Receive Votes

Goolong: POPL'19

Case-Studies



2PC



Raft
Leader
Election



Single Decree
Paxos



Multi-Paxos
KV Store

Goolong: POPL'19

Case-Studies

If committed, all nodes have same value



2PC

At most one candidate elected leader, per term



Raft Leader Election

Proposers agree on same value



Single Decree Paxos

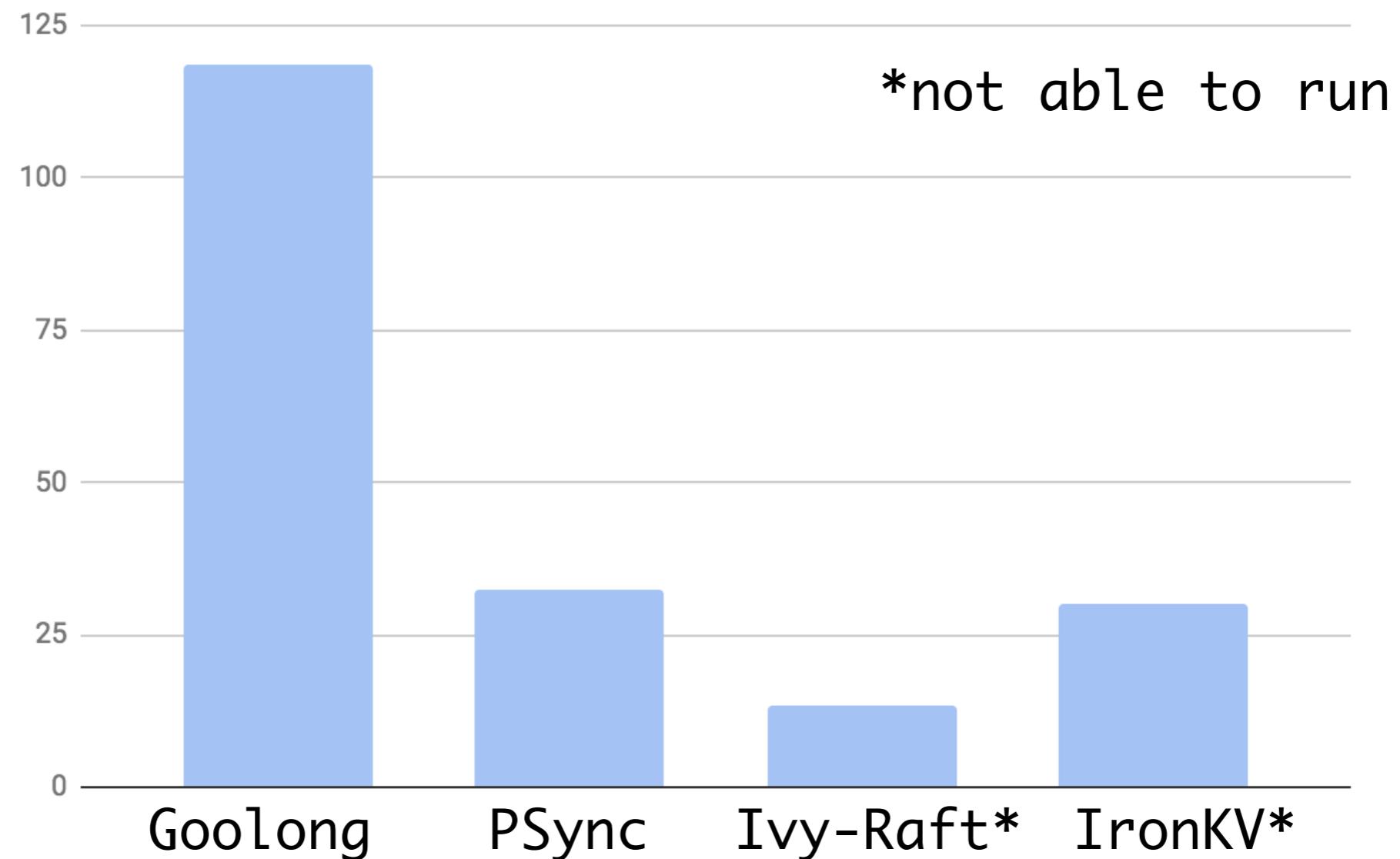
Proposers agree on same value, per instance



Multi-Paxos KV Store

Goolong: POPL'19

Throughput (req/ms)

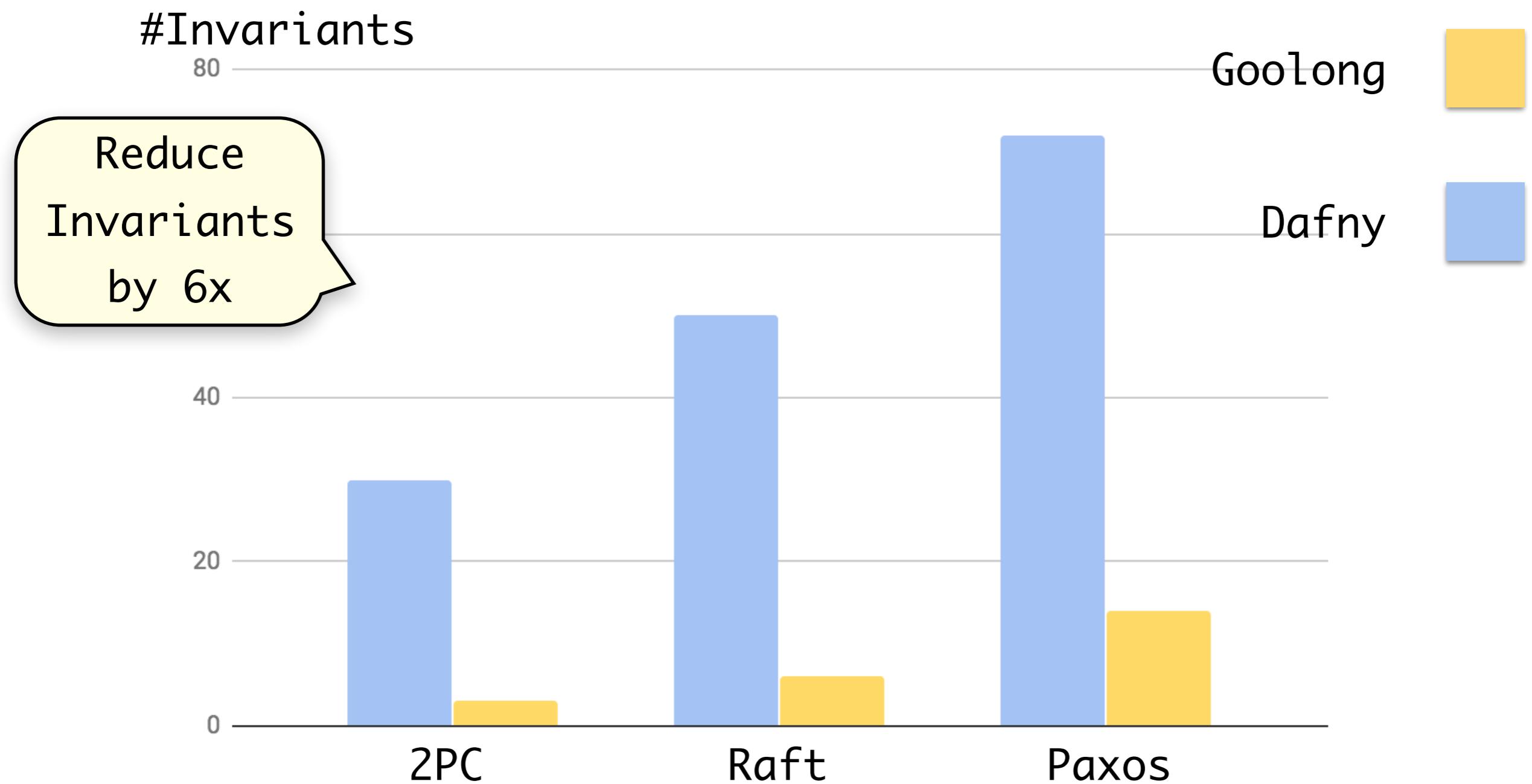


Multi-Paxos
KV Store

Goolong vs. other verified KVstores

Does Synchrony
Simplify Proofs?

Goolong: POPL'19



Number of Invariants Dafny vs. Goolong

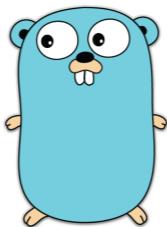
Goolong: POPL'19

Name	Time Dafny	Time Goolong	Reduce checking time by 3 orders of magnitude
2PC	12.8s	0.04s	
Raft	301.6s	0.18s	
Paxos	1141.3s	1.51s	
Total	1455.8s	1.73s	

Recap: Evaluation



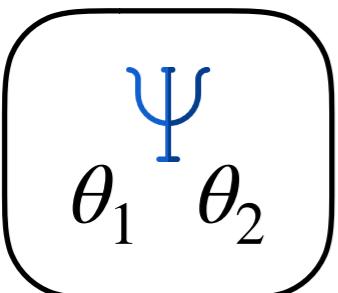
Brisk: Synchronization
in ms



Goolong: Go Library



Competitive with
verified KV-stores



Reduces Invariants
and Checking Time

?

Inference Rules

R-SEND

$$\frac{\Delta, \Sigma \models x = q \quad m \text{ fresh} \quad q \text{ is a PID}}{\Gamma, \Delta, \Sigma, [\text{send}(x, n, t, r)]_p \rightsquigarrow \Gamma', (\Delta; [m \leftarrow n]_p), \Sigma, \text{skip}}$$

R-RECV

$$\frac{\Delta, \Sigma \models x = p \quad (p, q, t, r, m) \in \Gamma \quad p \text{ is a PID}}{\Gamma, \Delta, \Sigma, [y \leftarrow \text{recv}(t, x, r)]_q \rightsquigarrow \Gamma', (\Delta; [y \leftarrow p.m]_q), \Sigma, \text{skip}}$$

R-RECVTO

$$\frac{\Gamma, \Delta, \Sigma, [y \leftarrow \text{recv}(t, x, r)]_q \rightsquigarrow \Gamma', (\Delta; [y \leftarrow p.m]_q), \Sigma, \text{skip}}{\Gamma, \Delta, \Sigma, [y \leftarrow \text{recvTO}(t, x, r)]_q \rightsquigarrow \Gamma', (\Delta; [y \leftarrow \text{Just } p.m]_q \oplus [y \leftarrow \text{None}]_q), \Sigma, \text{skip}}$$

R-CHOICE

$$\frac{\Gamma, \Delta, \Sigma, A \rightsquigarrow \Gamma, (\Delta; \Delta_A), \Sigma, \text{skip} \quad \Gamma, \Delta, \Sigma, B \rightsquigarrow \Gamma, (\Delta; \Delta_B), \Sigma, \text{skip}}{\Gamma, \Delta, \Sigma, A \oplus B \rightsquigarrow \Gamma, (\Delta; \Delta_A \oplus \Delta_B), \Sigma, \text{skip}}$$

$$\frac{\Delta \models \text{false}}{\Gamma, \Delta, \Sigma, A \rightsquigarrow \Gamma, \Delta, \Sigma, \text{skip}}$$

R-CONTEXT

$$\frac{\Gamma, \Delta, \Sigma, A \rightsquigarrow \Gamma', \Delta', \Sigma', A'}{\Gamma, \Delta, \Sigma, A \circ B \rightsquigarrow \Gamma', \Delta', \Sigma', A' \circ B}$$

R-SEND-UNFOLD

$$\frac{\Gamma \vdash \text{unfold}(u, x, ps) \quad \Gamma' \triangleq \Gamma - \{\text{unfold}(u, x, ps)\}}{\Gamma, \Delta, \Sigma, \text{send}(t, x, n) \rightsquigarrow \Gamma', (\Delta ; \text{assume}(x = u)), \Sigma, \text{send}(t, x, n)}$$

R-RECV-UNFOLD

$$\frac{\Gamma \vdash \text{unfold}(u, x, ps) \quad \Gamma' \triangleq \Gamma - \{\text{unfold}(u, x, ps)\}}{\Gamma, \Delta, \Sigma, y \leftarrow \text{recv}(ps, t) \rightsquigarrow \Gamma', (\Delta ; x \leftarrow \text{pick}(ps)), \Sigma', y \leftarrow \text{recv}(u, t)}$$

R-LOOP

- (1) u, x fresh
 - (2) $\Gamma_0 \triangleq \Gamma \cup \{\text{unfold}(u, x, ps)\}$ and $\Delta_0 \triangleq \text{assume}(I_C)$
 - (3) $\Delta, \Sigma \models I_C$ and $(\Delta_0; \langle \Delta^u \rangle), \Sigma \models I_C$
- $$\Gamma_0, \Delta_0, \Sigma, [A]_u \parallel B[x/p] \rightsquigarrow \Gamma, (\Delta_0; \Delta^u), \Sigma, \text{skip}$$

$$\Gamma, \Delta, \Sigma, \prod (p \in ps). [A]_p \parallel [\text{for } p \in ps \{I_S\} \text{ do } B \text{ end}]_q \rightsquigarrow \Gamma, [\text{for } p \in ps \text{ do } \langle I_S \triangleright \Delta^u[p/u] \rangle \text{ end}], \Sigma, \text{skip}$$

R-FOCUS

- (1) u fresh
 - (2) $\Gamma_0 \triangleq \Gamma \cup \{\text{unfold}(u, _, ps)\}$ and $\Delta_0 \triangleq \text{assume}(I_C)$
 - (3) $\Sigma' = (\Sigma \parallel \prod (p \in ps). \Delta^u[p/u])$
 - (4) $\Delta, \Sigma' \models I_C$ and $(\Delta_0; \Delta^u), \Sigma' \models I_C$
- $$\Gamma_0, \Delta_0, \text{skip}, [A]_u \parallel \prod (q \in qs). [B]_q \rightsquigarrow \Gamma_0, (\Delta_0; \Delta^u), \text{skip}, \text{skip}$$

$$\Gamma, \Delta, \Sigma, \prod (p \in ps). [A]_p \parallel \prod (q \in qs). [\text{foreach } ps \text{ do } B]_q \rightsquigarrow \Gamma, \Delta, \Sigma', \text{skip}$$

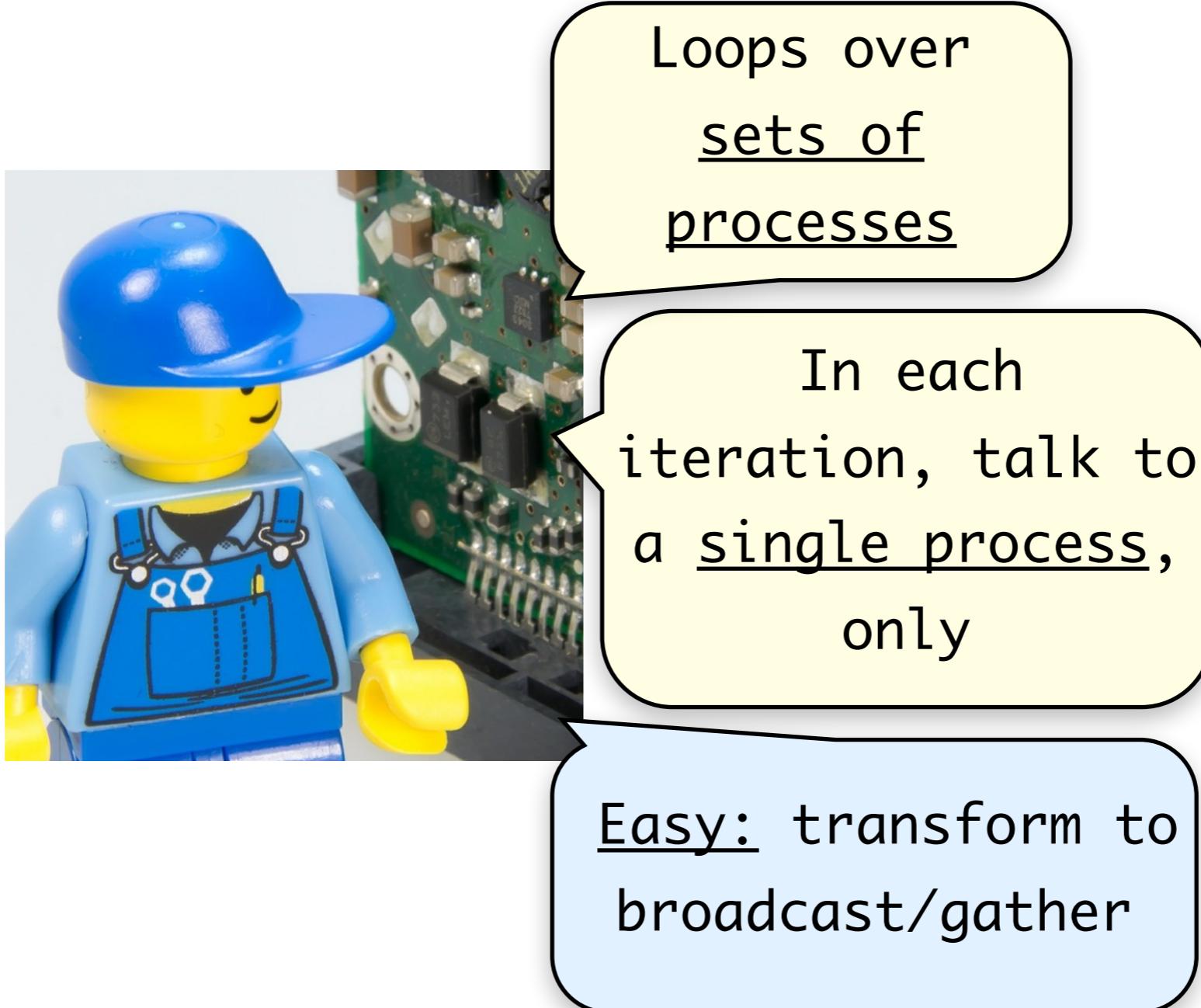
Limitations

Structured Loops

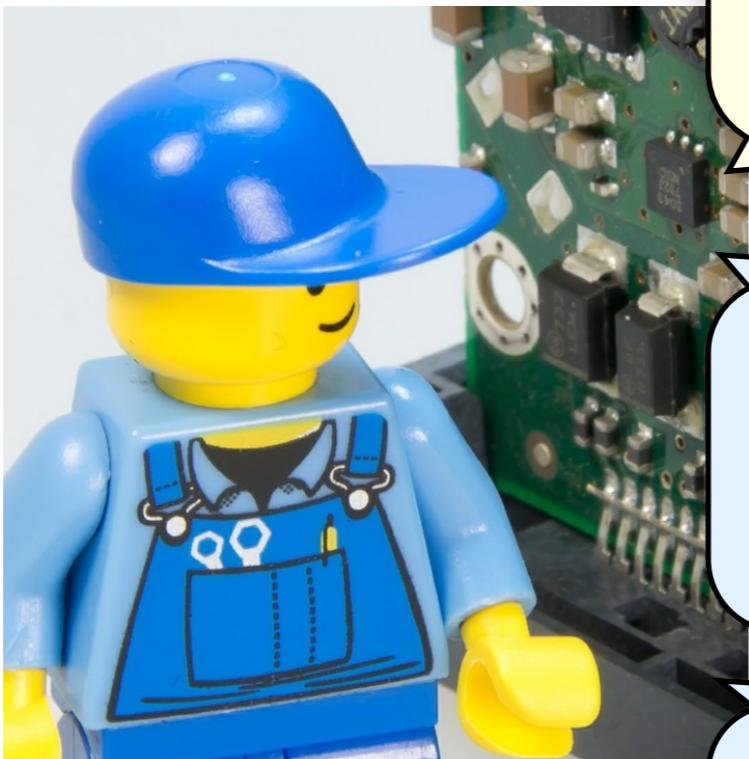
Symmetric Non-Determinism

Round Non-Interference

Limitations: Structured Loops



Limitations: Structured Loops



Loops over
sets of
processes

Hard: arbitrary
loop carried state

Encode as
rounds

Limitations

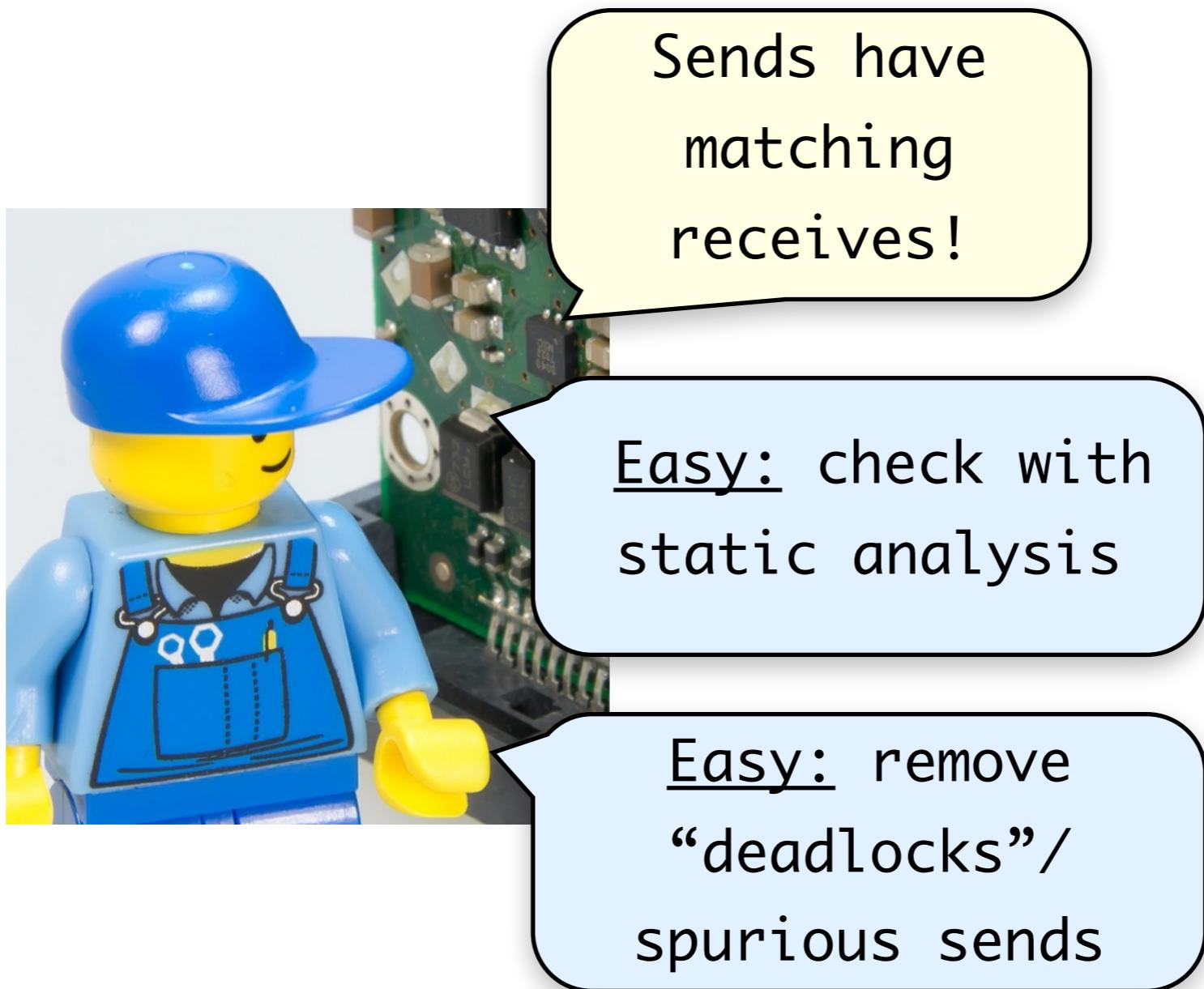
Structured Loops

Symmetric Non-Determinism

Round Non-Interference

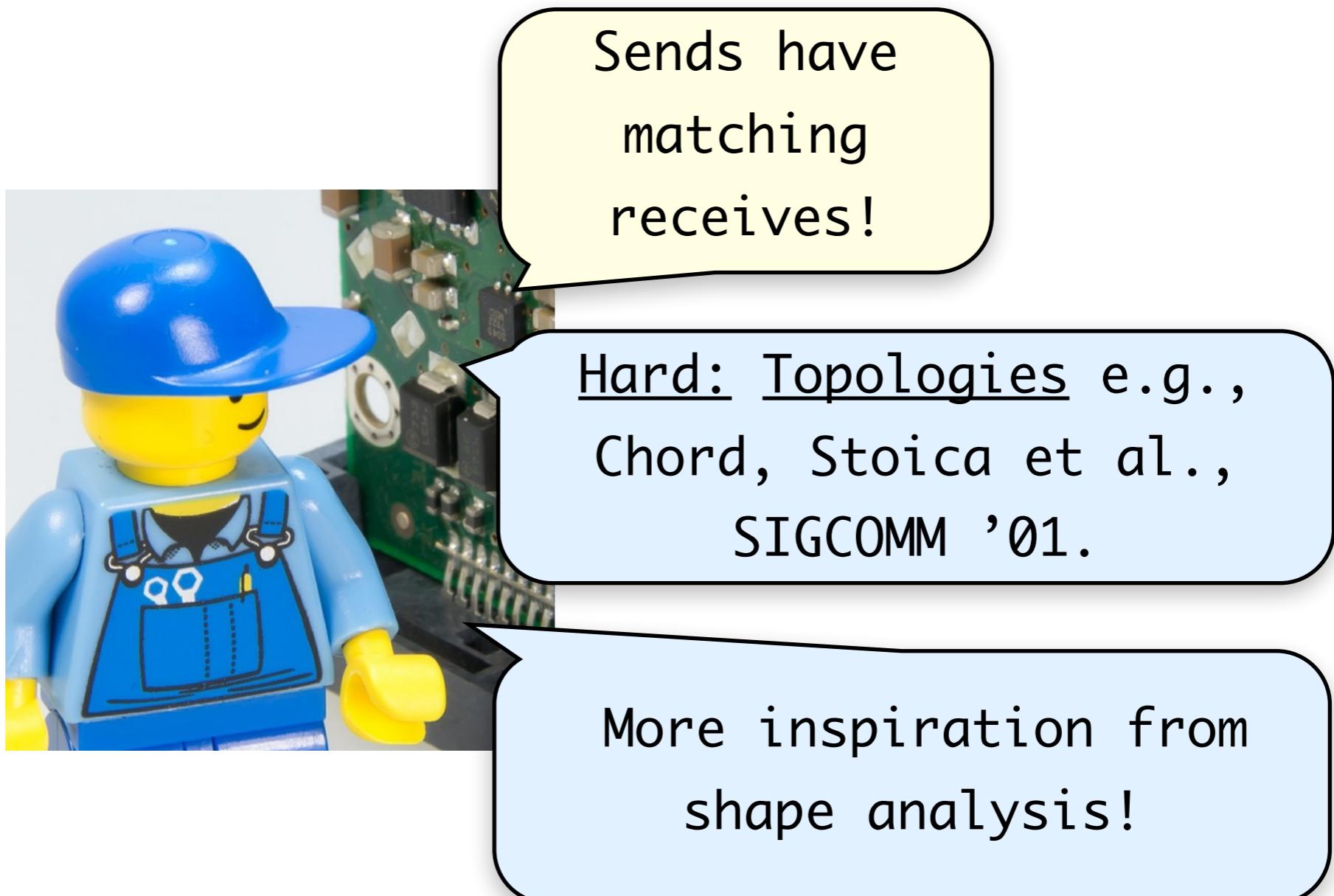
Limitations:

Symmetric Non-Determinism



Limitations:

Symmetric Non-Determinism



Limitations

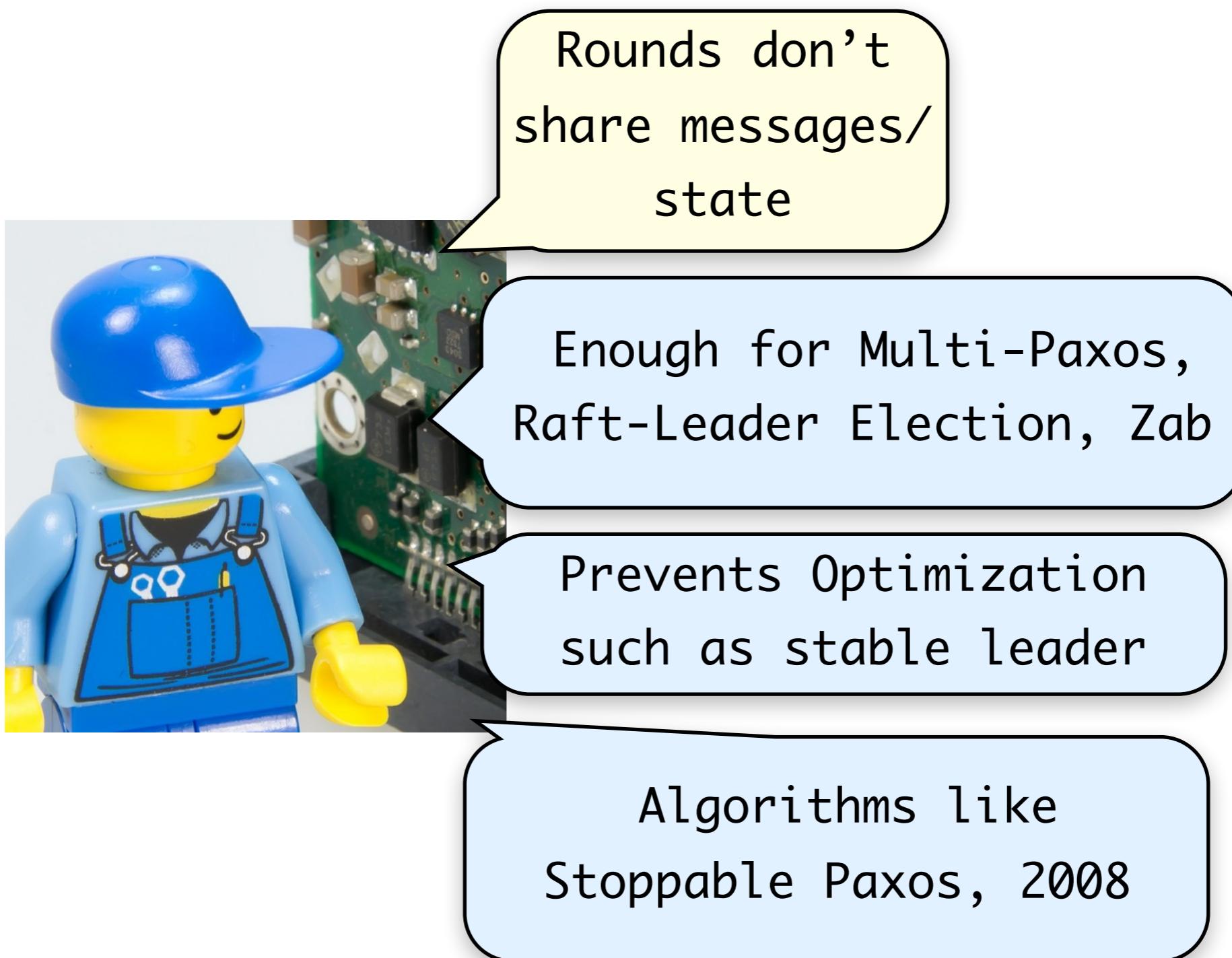
Structured Loops

Symmetric Non-Determinism

Round Non-Interference

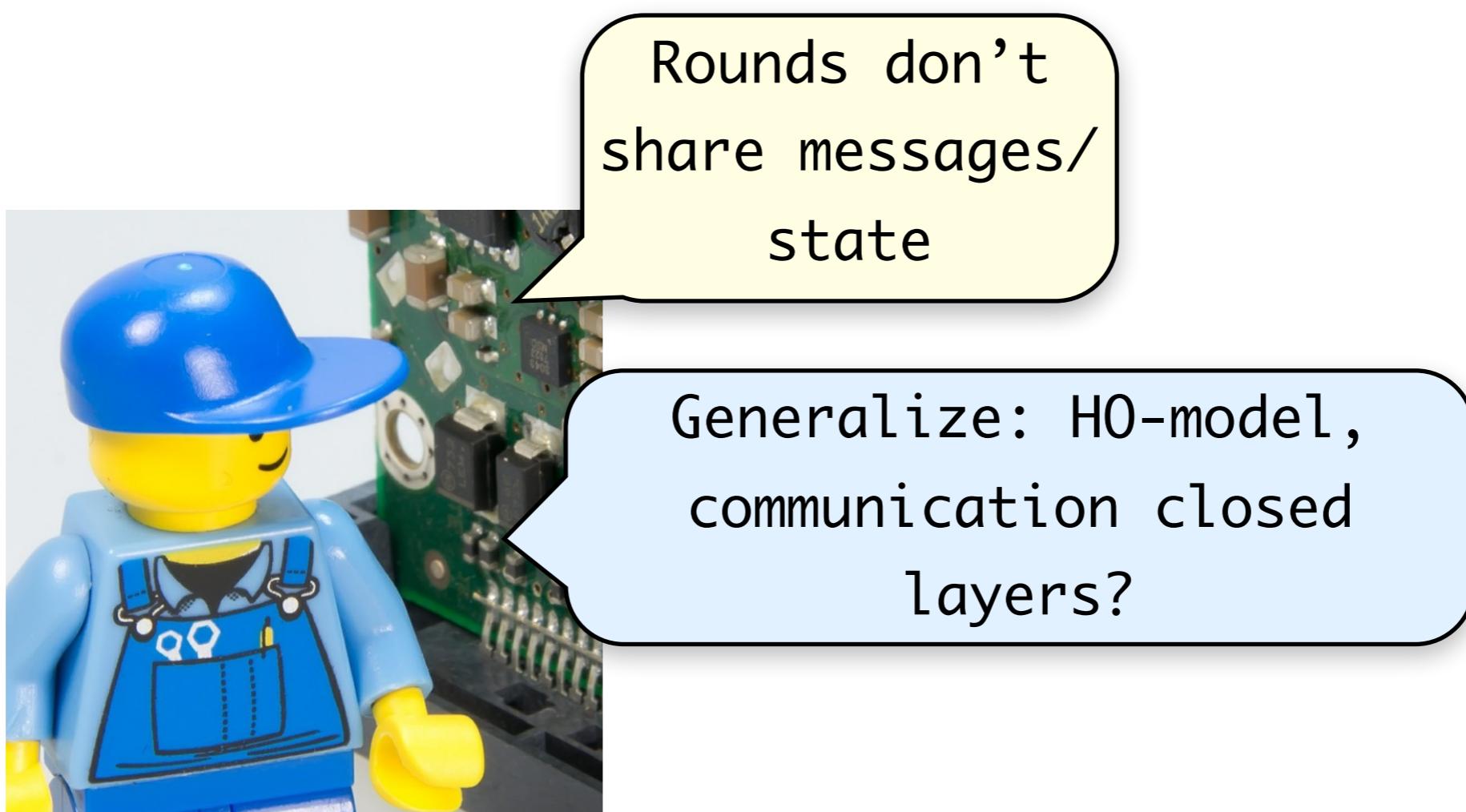
Limitations:

Round Non-Interference



Limitations:

Round Non-Interference



Future Work

Language Restrictions



Graydon Hoare
@graydon_pub

(Distributed-system folks, really go read that Canonical Sequentialization paper. Big deal.)

12:12 AM · Sep 12, 2017 · [TweetDeck](#)

Future Work

Language Restrictions



Graydon Hoare
@graydon_pub

"Figure out how programmers are currently reasoning, formalize as language restricting validity to that model, profit!" is A+ research plan



Graydon Hoare @graydon_pub · Sep 12, 2017

Yeah it's normal in many contexts, I'm just applauding a particularly pleasing example of it (that Canonical Sequentializatoon paper)



Goolong: POPL'19

Invariants

Name	#Inv Async	Time Async/ Dafny	#Inv Sync	Time Sync	
2PC	30	12.8s	3	0.04s	Reduce Invariants by 6x
Raft	50	301.6s	6	0.18s	Reduce checking time by 3 orders of magnitude
Paxos	72	1141.3s	14	1.51s	
Total	152	1455.8s	23	1.73s	

Goolong: POPL'19

Case-Studies

1.5-3x slowdown
over unverified
KV store

System	Throughput (req/ms)	
Goolong	118.5	
PSync	32.4	Multi-Paxos
Ivy-Raft*	13.5	KV Store
IronKV*	30	

*not able to run

Brisk: OOPSLA'17

Two Phase Commit in Brisk

```
coord :: Transaction -> Int -> SymSet ProcessId -> Process ()
coord transaction n nodes = do
    fold query () nodes
    n_ <- fold countVotes 0 nodes
    if n == n_ then
        forEach nodes commit ()
    else
        forEach nodes abort ()
forEach nodes expect :: Ack

where
    query () pid      = do { me <- myPid; send pid (me, transaction) }
    countVotes init nodes = do
        msg <- expect :: Vote
        case msg of
            Accept _ -> return (x + 1)
            Reject   -> return x

acceptor :: Process ()
acceptor = do
    me <- myPid
    (who, transaction) <- expect :: (ProcessId, Transaction)
    vote <- chooseVote transaction
    send who vote
```

Leftovers

Synchronous Proofs

```
for p in dbs do
    p.id<-c;
    p.val<-c.val;
for p in dbs do
    c.abort<-False;
vote <-* ?
    Commit : Abort
c.msg<-p.vote;
if msg == Abort
    abort <- True
```

Synchronous Proofs

```
for p in dbs do @Inv1  
    p.id<-c;  
    p.val<-c.val;  
  
for p in dbs do @Inv2  
    c.abort<-False;  
  
vote <-* ?  
    Commit : Abort  
  
c.msg<-p.vote;  
  
if msg == Abort  
    abort <- True
```

Synchronous Proofs

```
for p in dbs do @Inv1  
    p.id<-c;  
    p.val<-c.val;  
  
for p in dbs do @Inv2  
    c.abort<-False;  
    vote <-* ?  
        Commit : Abort  
    c.msg<-p.vote;  
    if msg == Abort  
        abort <- True
```

@Inv1=

$\forall p \in \text{dbs} : p \in \text{done} \Rightarrow p.\text{val} = c.\text{val}$

@Inv2= true

Synchronous Proofs

```
c.dec <- c.abort ?  
  Abort : Commit  
  
for p in dbs do @Inv3  
  
  p.dec<-c.dec;  
  
for p in dbs do @Inv4  
  
  if msg == Commit  
    p.value <- p.val;  
  
  _<-Ack;
```

Synchronous Proofs

```
c.dec <- c.abort ?  
  Abort : Commit  
  
for p in dbs do @Inv3  
  
  p.dec<-c.dec;  
  
for p in dbs do @Inv4  
  
  if msg == Commit  
    p.value <- p.val;  
  
  _<-Ack;
```

No case-splits!

@Inv3 = true

No network state!

@Inv4 =

$\forall p \in \text{dbs}: (p \in \text{done} \wedge c.\text{dec} = \text{Commit}) \Rightarrow p.\text{value} = c.\text{val}$

Outline

Key Idea: Pretend Synchrony

Implementation

From 2PC to Paxos

Evaluation

Limitations

Implementation

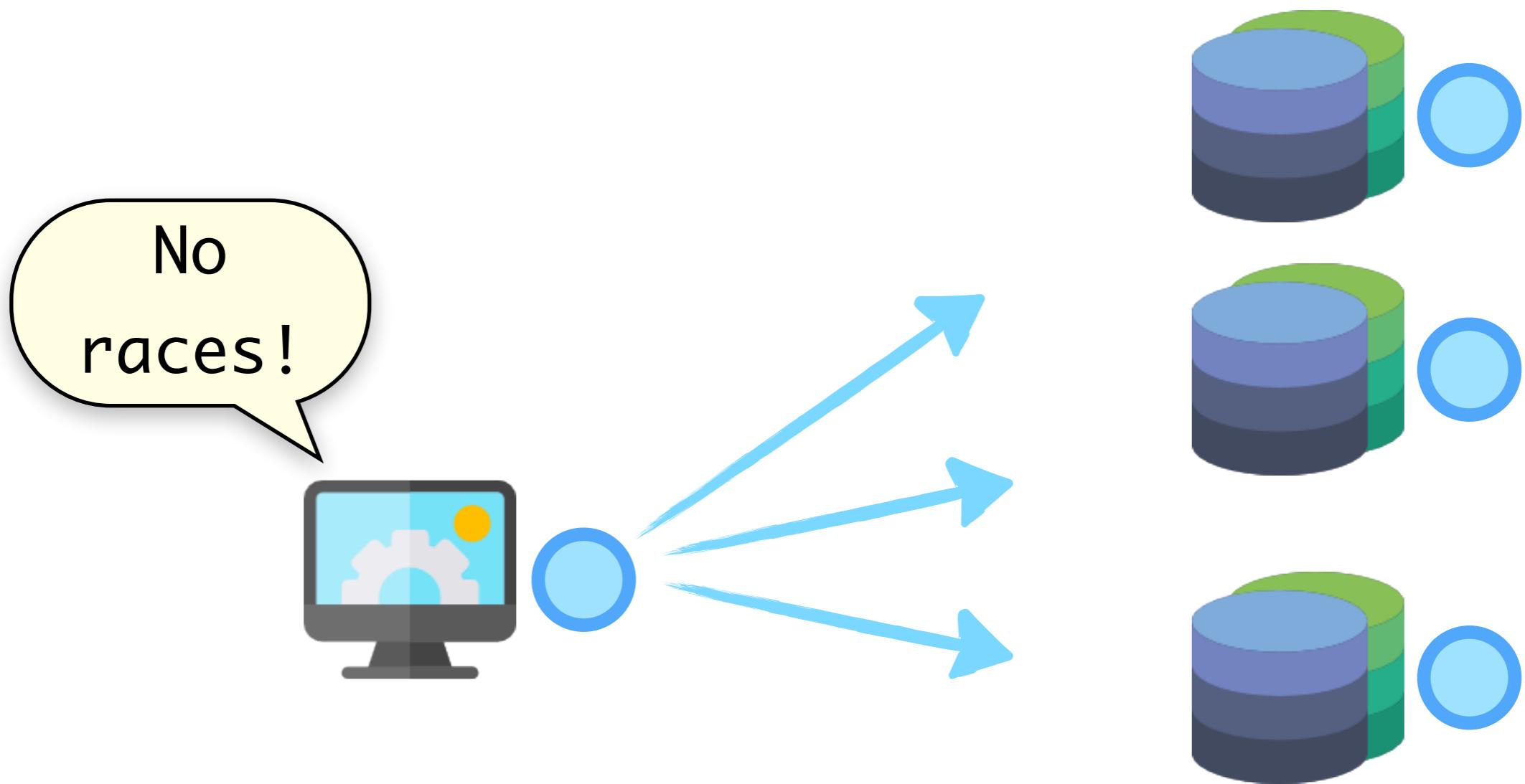
Implementation

Key ingredients

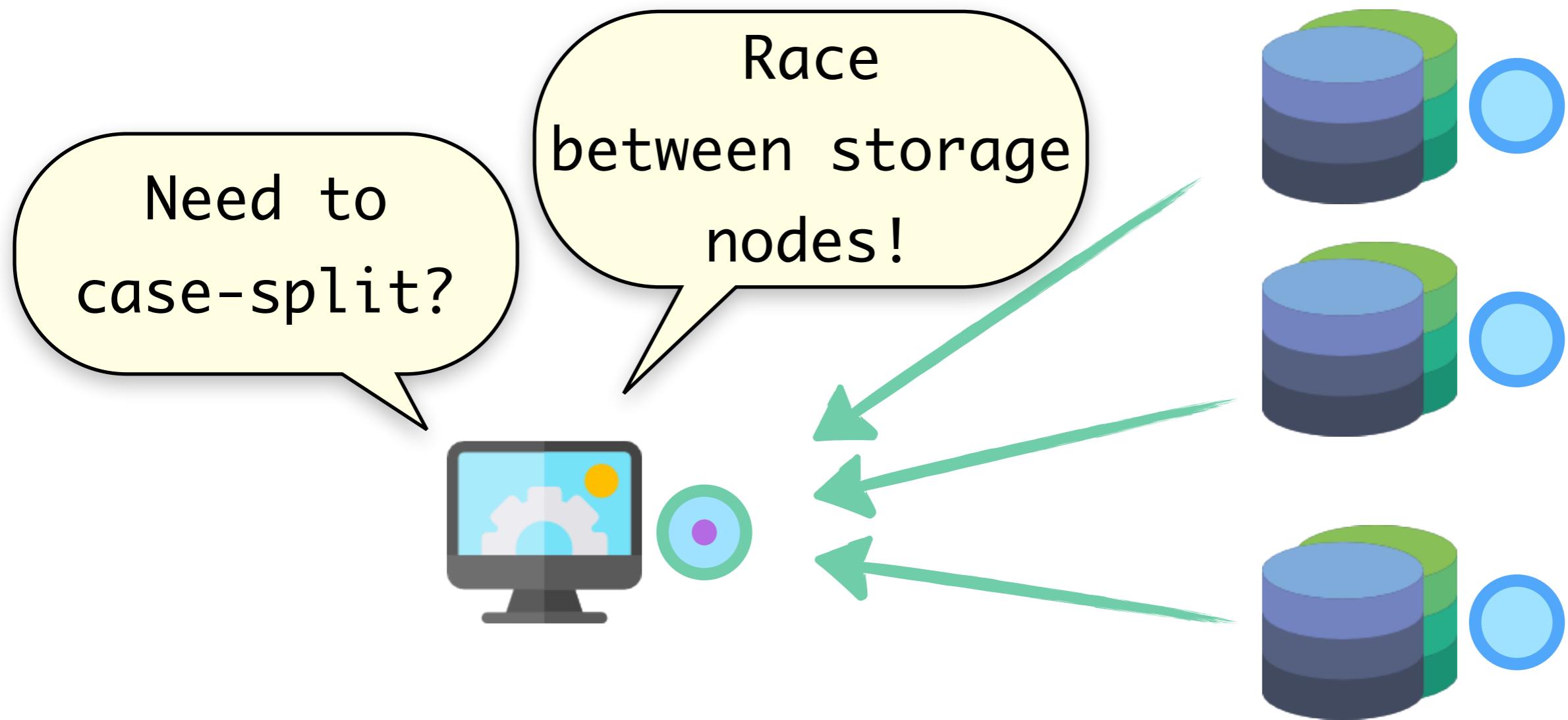
Symmetric Nondeterminism

Synchronize by Rewriting

Two-phase Commit: Phase 1

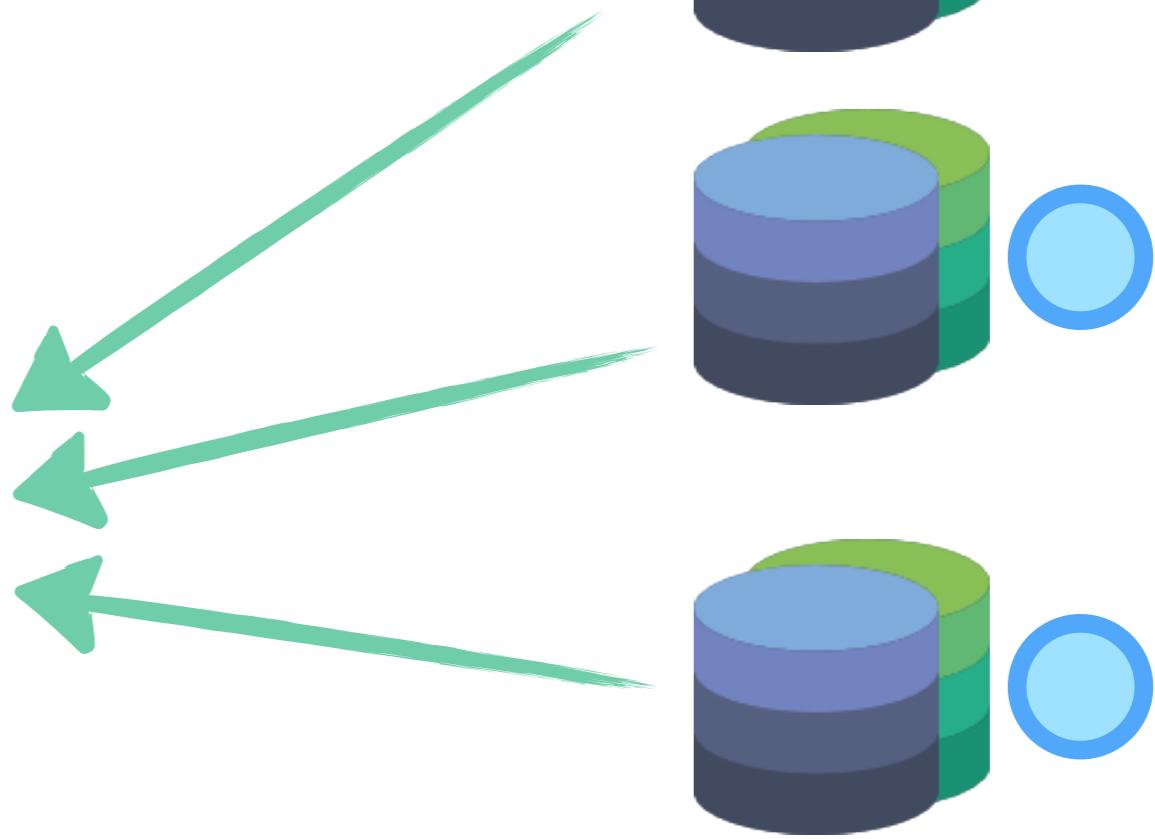
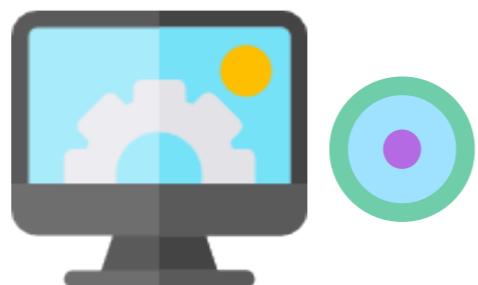


Two-phase Commit: Phase 1

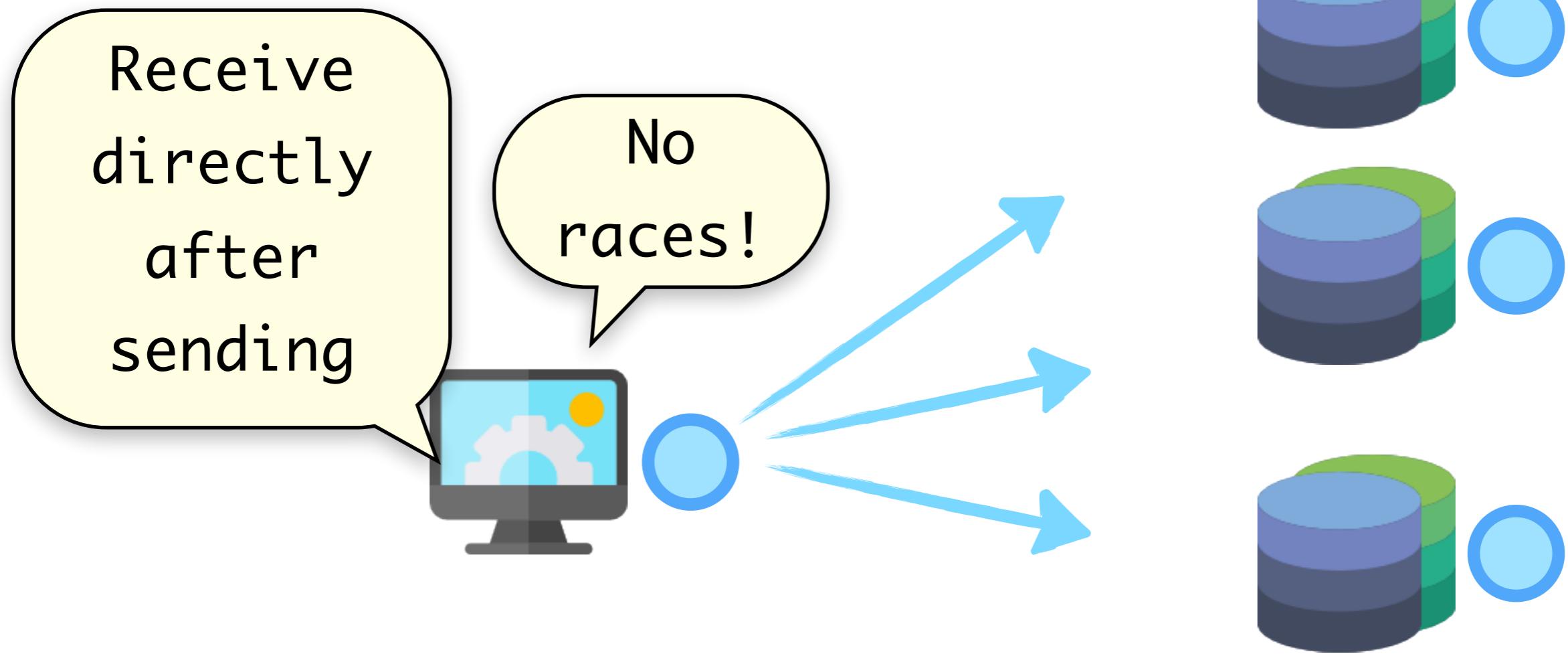


Two-phase Commit: Phase 1

No! Because races are symmetric

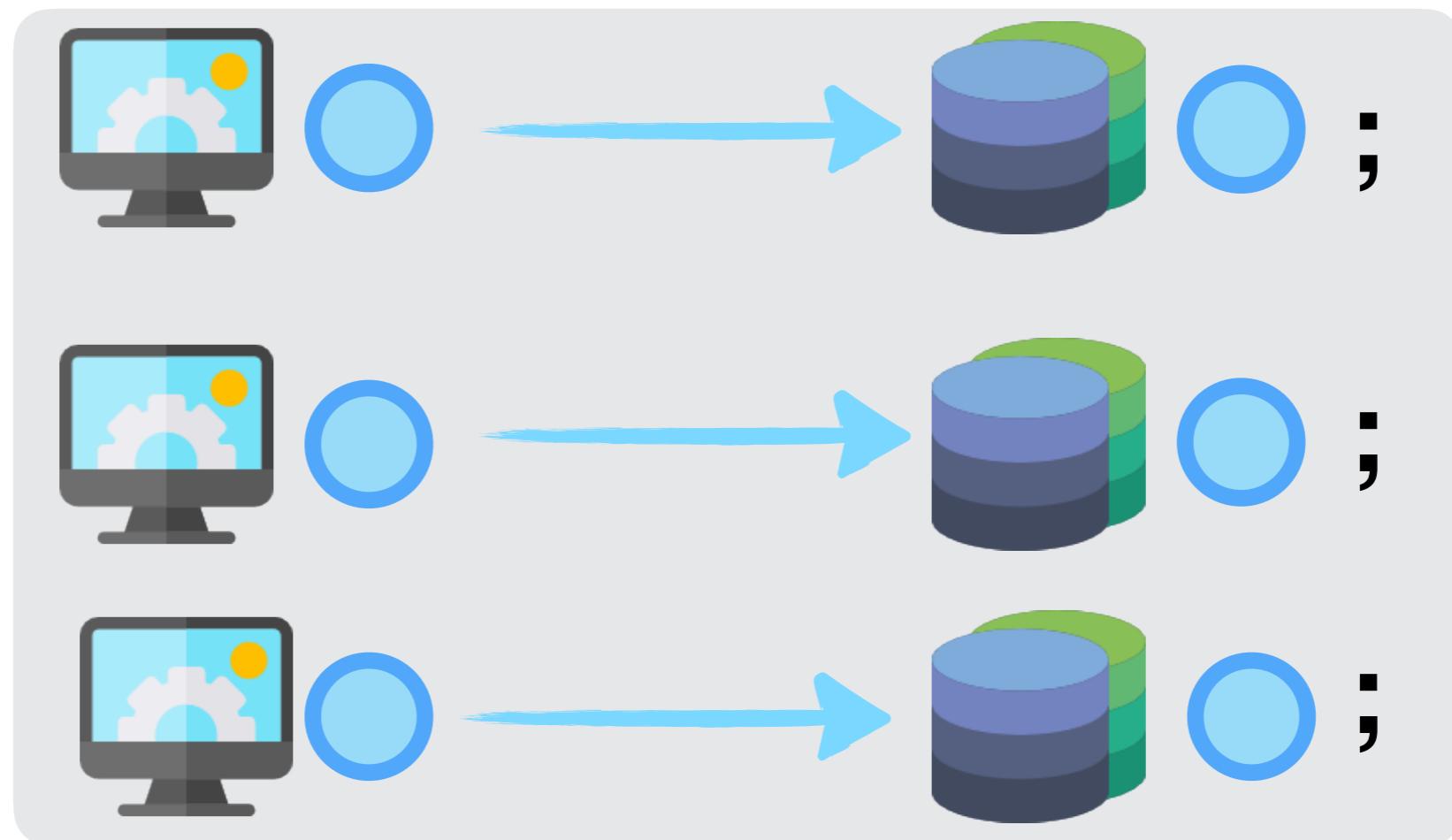


Two-phase Commit: Phase 1



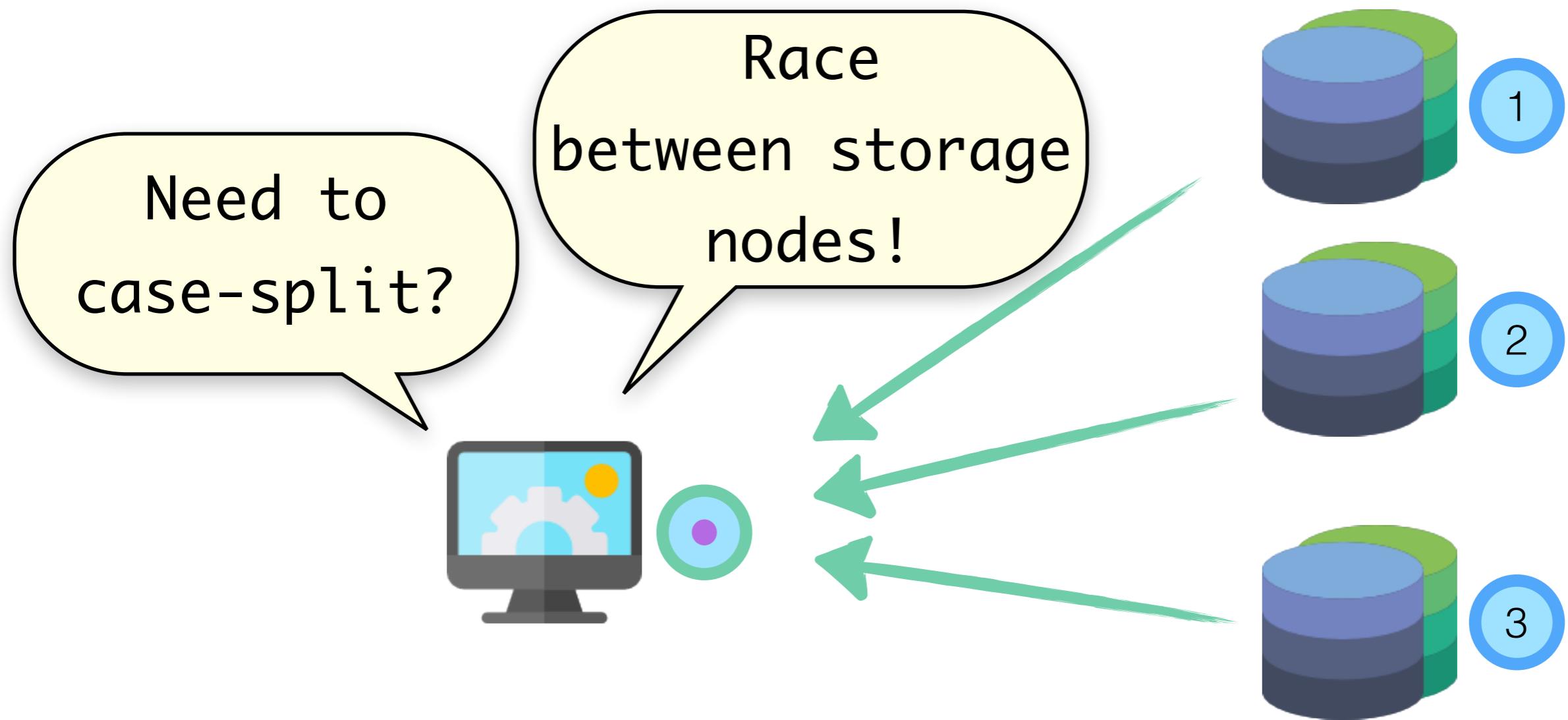
Theory of Movers [Lipton 1975]

Two-phase Commit: Phase 1

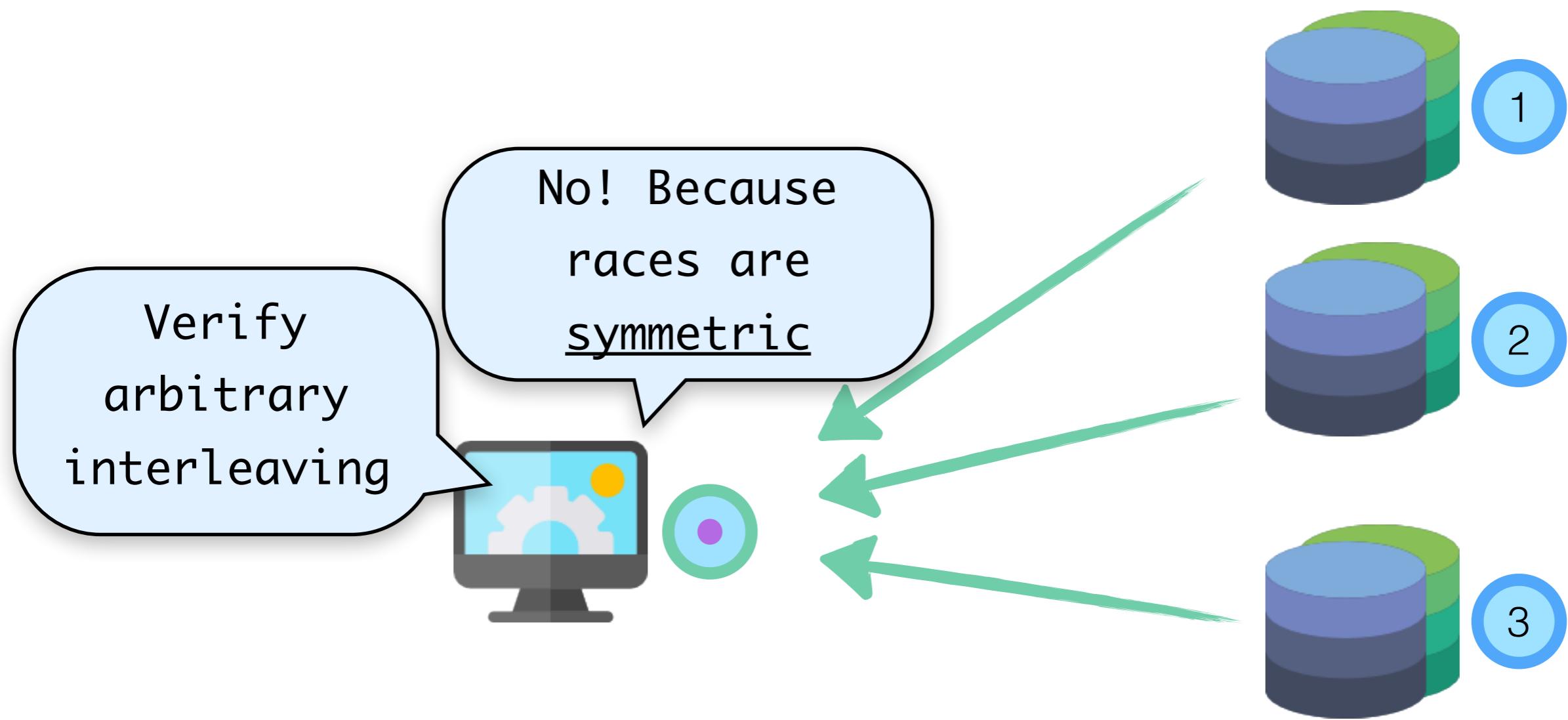


Theory of Movers [Lipton 1975]

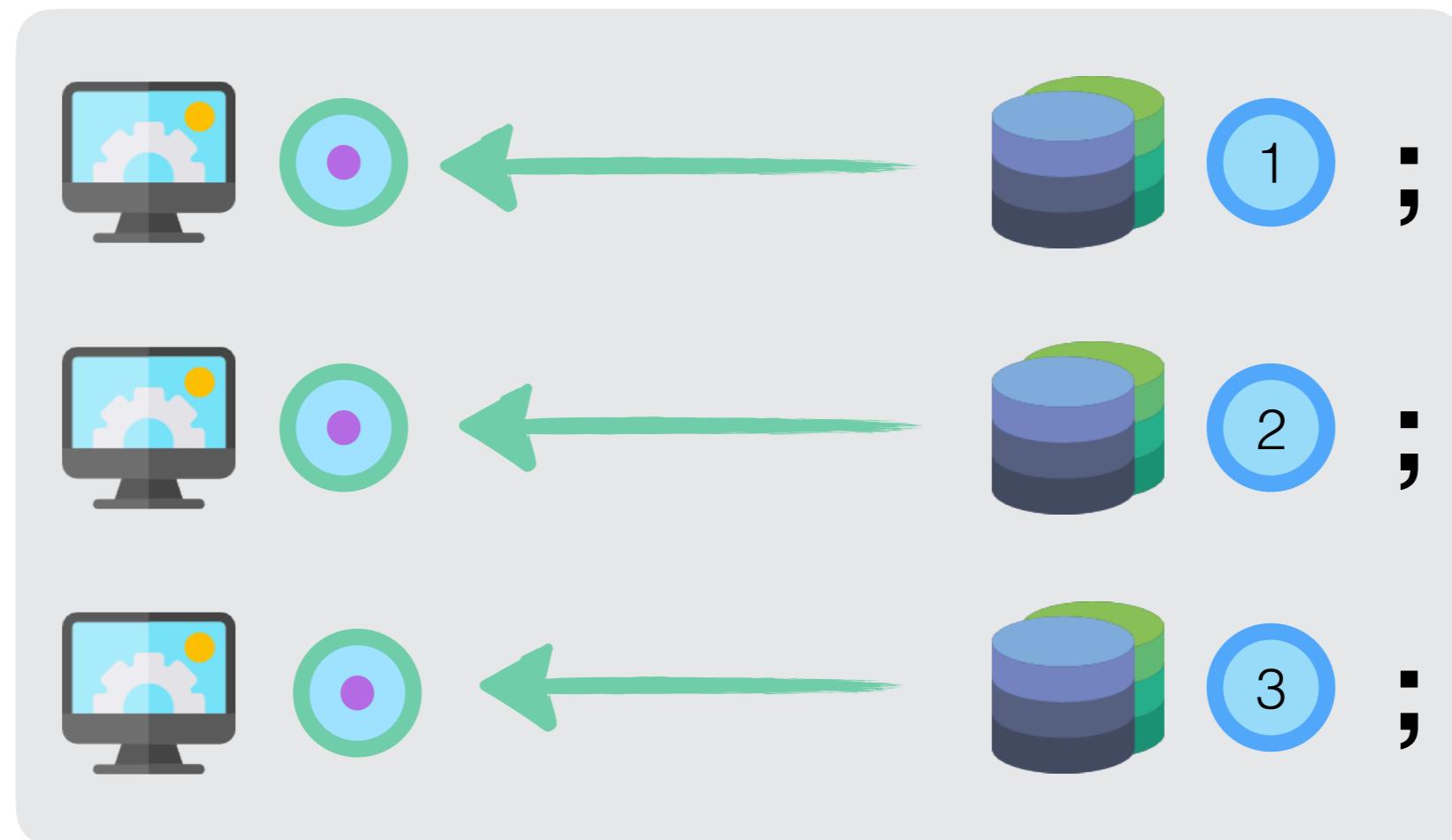
Two-phase Commit: Phase 1



Two-phase Commit: Phase 1



Two-phase Commit: Phase 1



Implementation

Key ingredients

Symmetric Nondeterminism

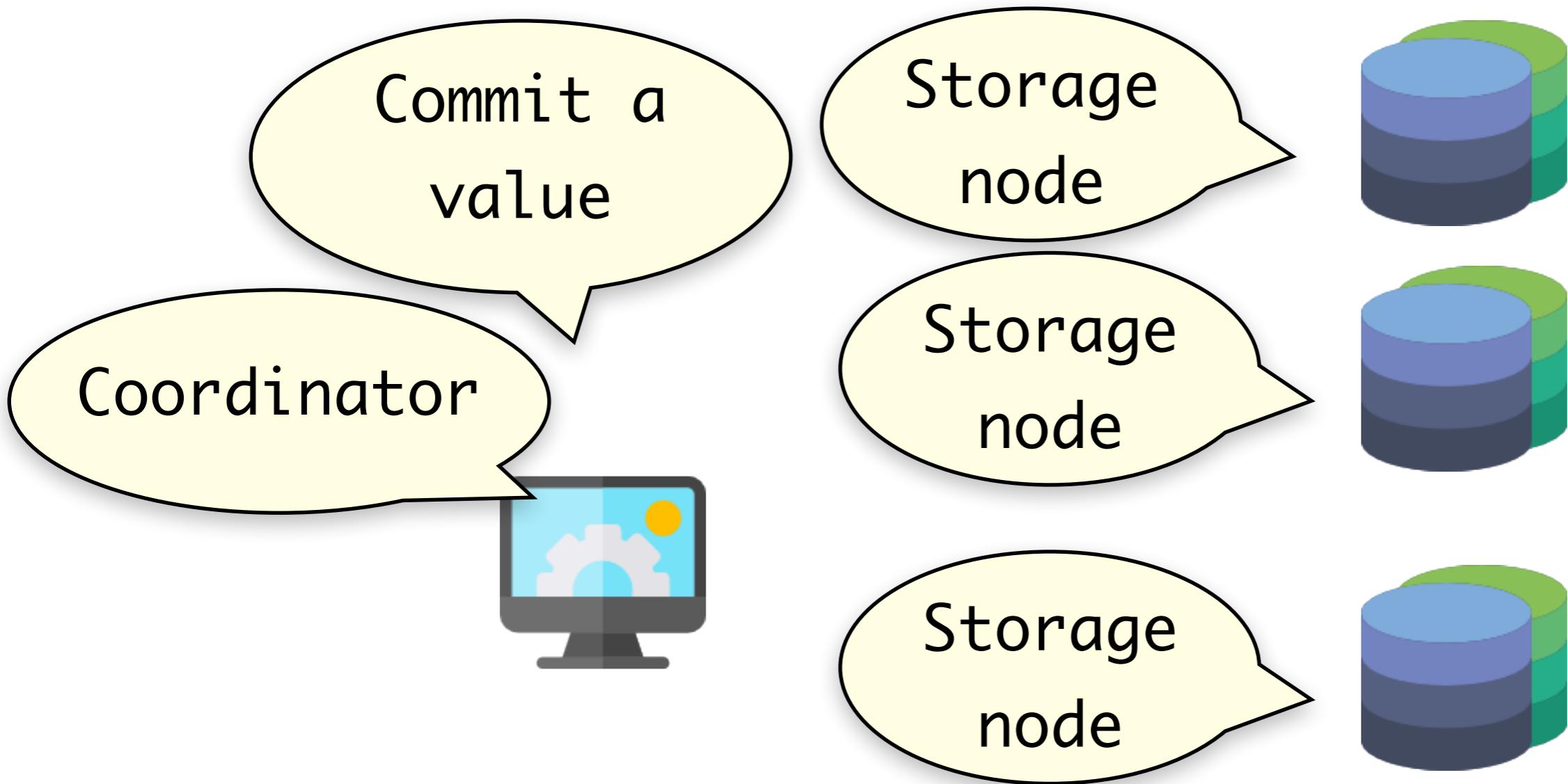
Synchronize by Rewriting

Implementation

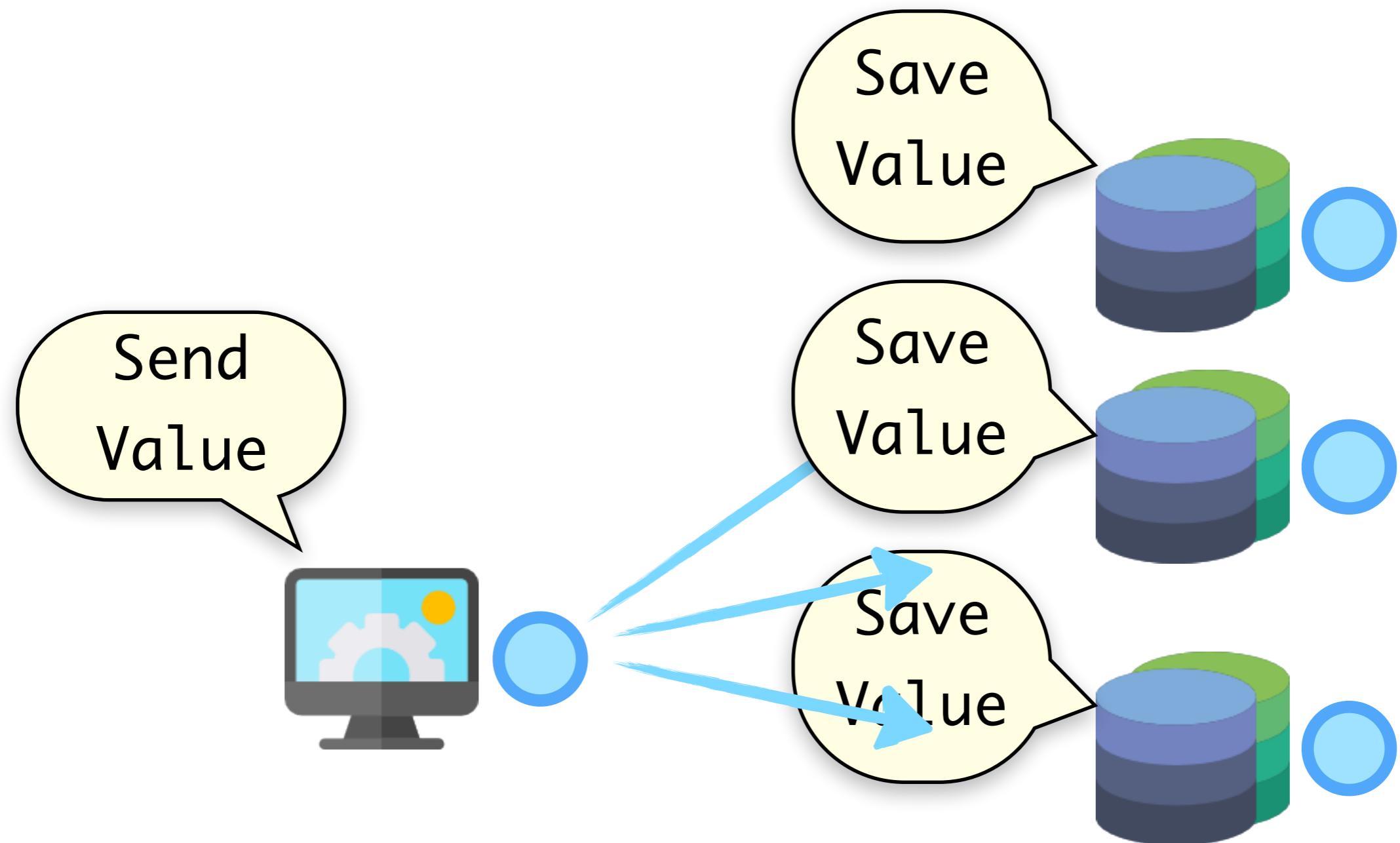
Example 2PC

Example: Two-phase Commit

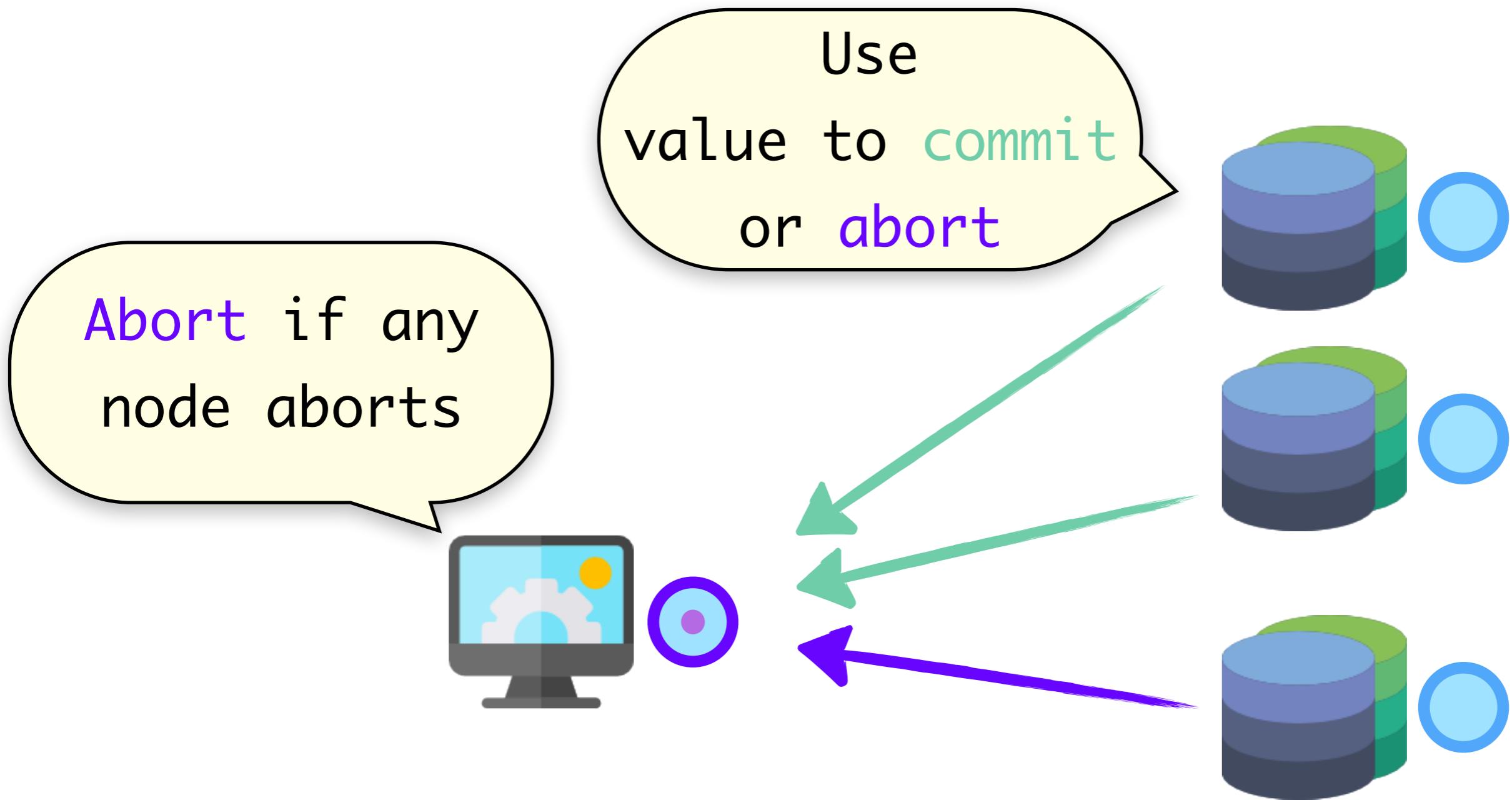
Example: Two-phase Commit



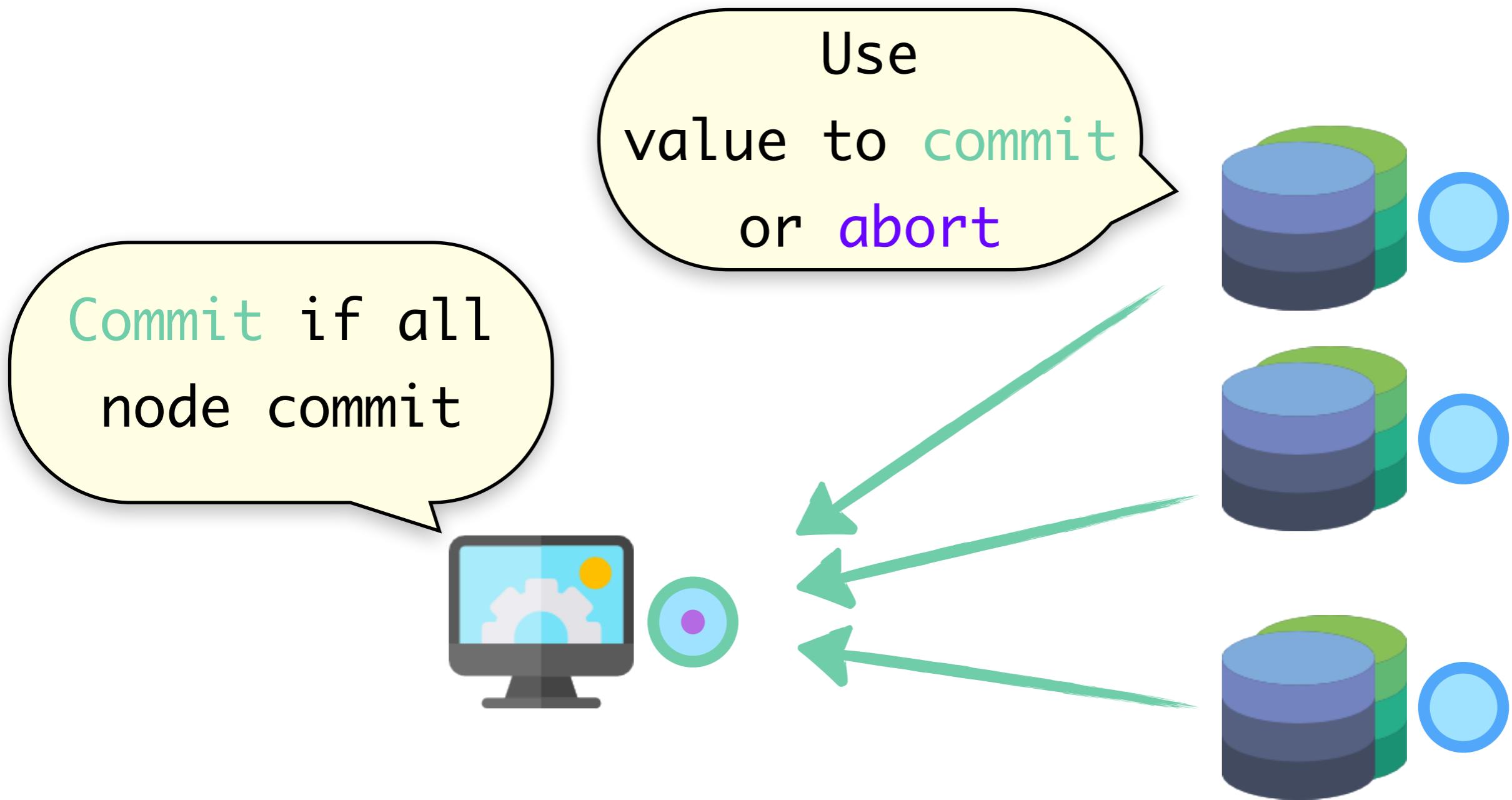
Two-phase Commit: Phase 1



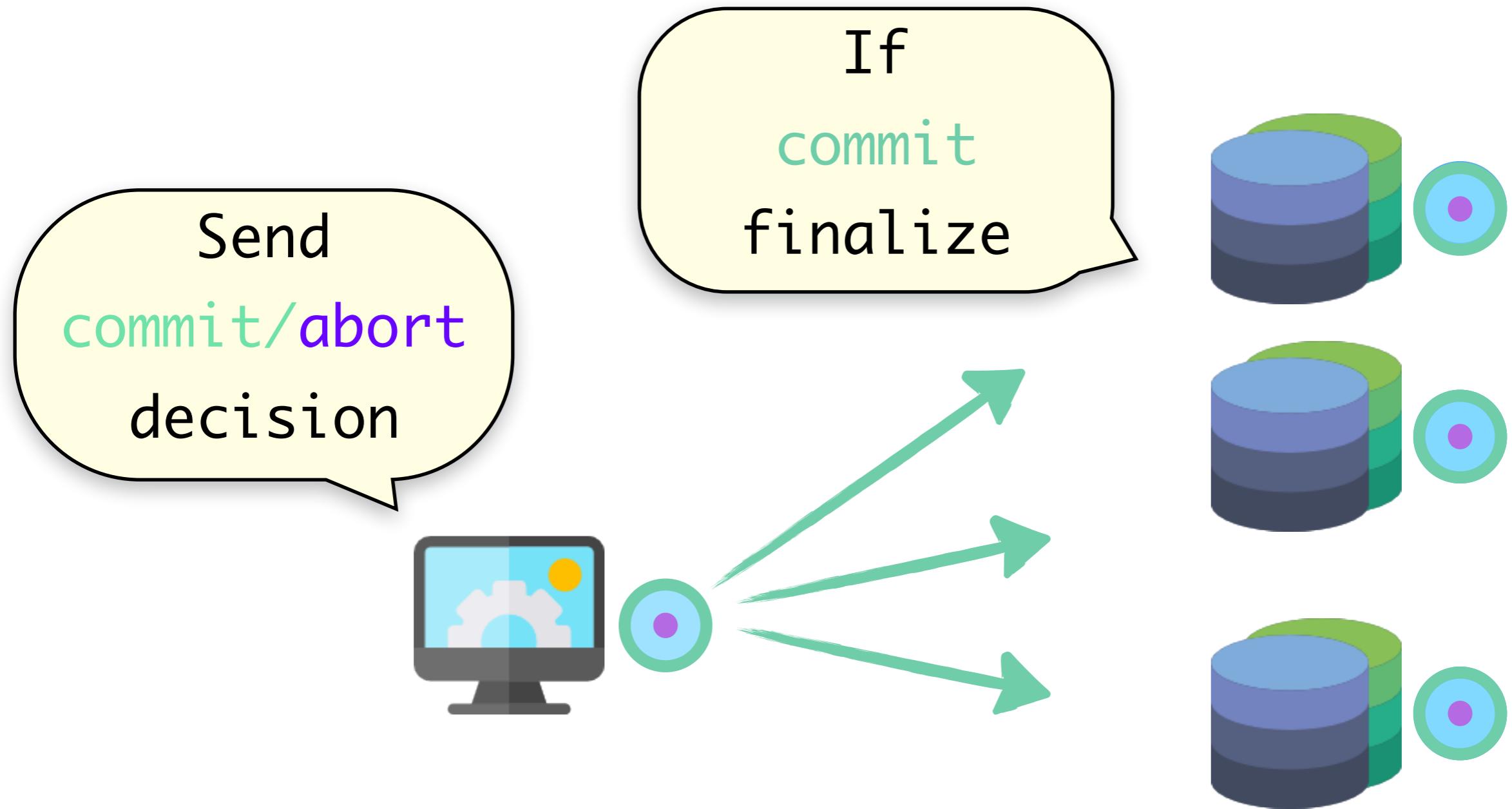
Two-phase Commit: Phase 1



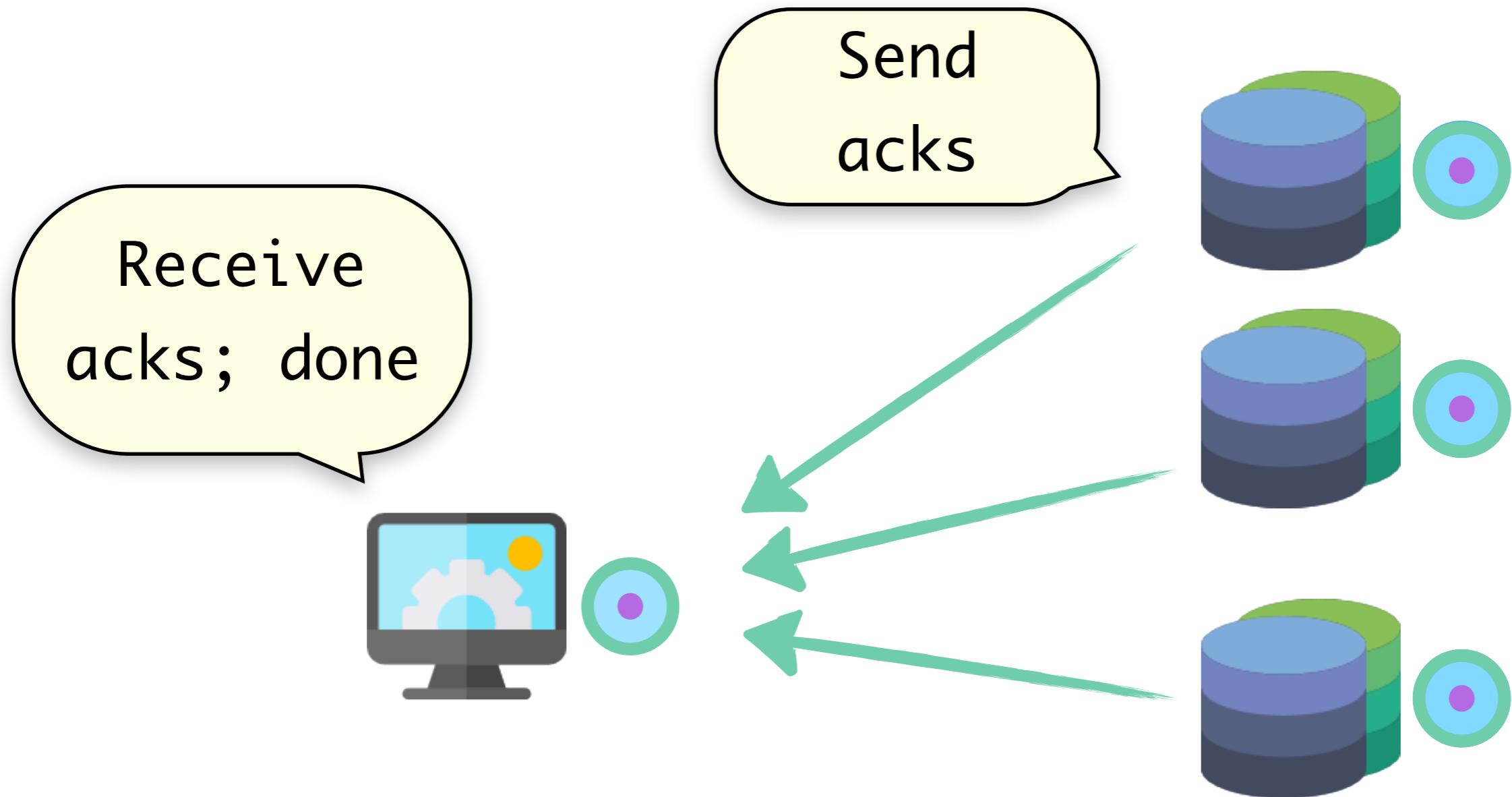
Two-phase Commit: Phase 1



Two-phase Commit: Phase 2



Two-phase Commit: Phase 2



Problem: *Asynchronous
Proofs are Hard*

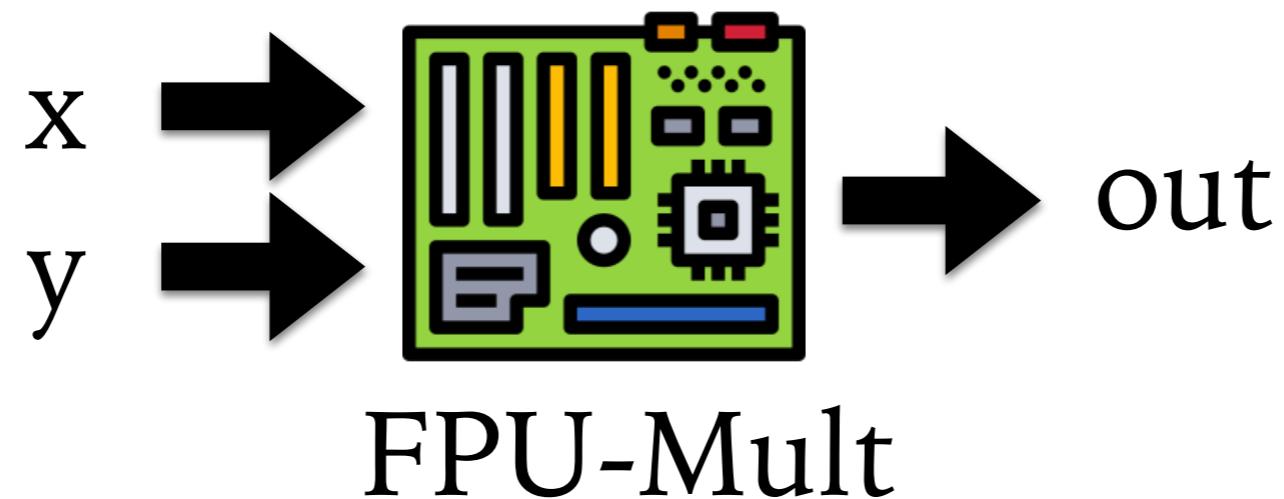
Make Proofs Easier!

Synchronous Proofs are Easy

Verilog Primer

Example FPU

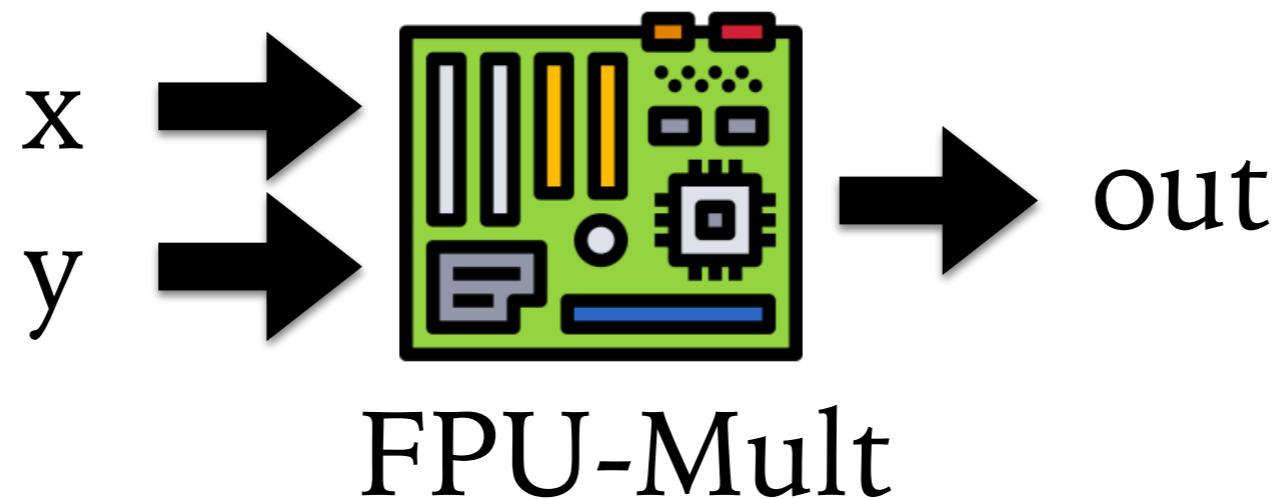
Given floats x and y



... compute x^*y

Example FPU

Given floats x and y



... but exhibits timing variability

Example FPU

```
always @(posedge clk) begin  
    if (iszzero)  
        out <= 0;  
    else if (isNaN)  
        ...  
    else  
        out <= flp_res;  
end
```

Example FPU

```
always @(posedge clk) begin  
    if (iszero)  
        out <= 0;  
    else if (isNaN)  
        ...  
    else  
        out <= flp_res;  
end
```

```
always @(posedge clk) begin  
    ...  
    flp_res <= // x*y;  
end
```

Influence set:
cycles that influenced value

Example FPU

Influence set: for $x=0$

cycle	x	y	flop_res	out
0	{0}	{0}	\emptyset	\emptyset
1	{1}	{1}	\emptyset	{0}
...				
k-1	{k-1}	{k-1}	{0}	{k-2}
k	{k}	{k}	{1}	{k-1}

Example FPU

Influence set: $x=1$

cycle	x	y	flop_res	out
0	{0}	{0}	\emptyset	\emptyset
1	{1}	{1}	\emptyset	{0}
...				
k-1	{k-1}	{k-1}	{0}	{k-2}
k	{k}	{k}	{1}	{0, k-1}

sets for out at k differ: timing variability

Example FPU

How to verify?

Produce product program, track equivalence
of influence sets through equivalence of
membership

Example FPU

The FPU takes a fast path, if x is 0

cycle	x	y	f1p_res	out
0	0	1	X	X
1	0	1	X	0
...				
k-1	0	1	0	0
k	0	1	0	0

Example FPU

The FPU takes the slow path, if x is 1

cycle	x	y	flop_res	out
0	1	1	X	X
1	0	1	X	0
...				
k-1	0	1	1	0
k	0	1	0	1

Example FPU

Influence set: all cycles that influenced value

cycle	x	y	fip_res	out
0	{0}	{0}	\emptyset	\emptyset
1	{1}	{1}	\emptyset	{0}
...				
k-1	{k-1}	{k-1}	{0}	{k-2}
k	{k}	{k}	{1}	{k-1}

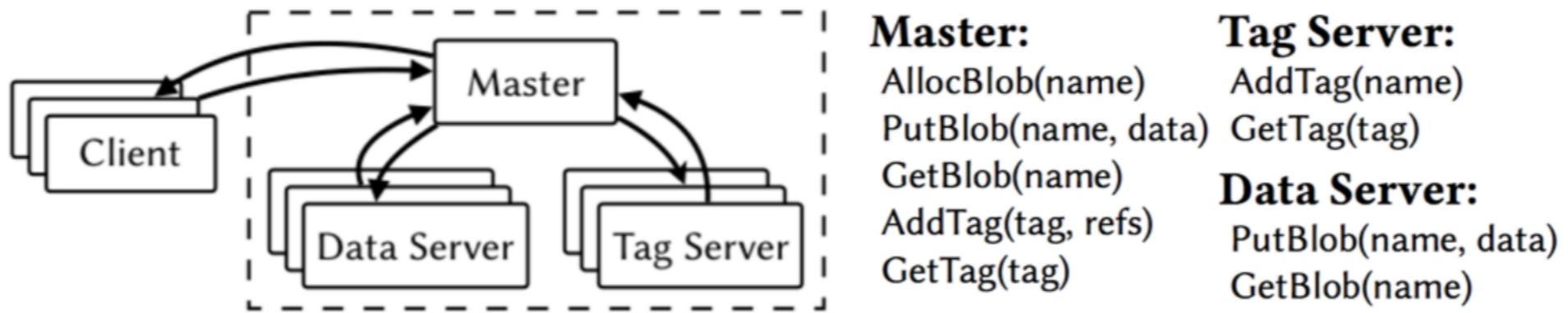
BACKUP



Results

Benchmark	GOOLONG						DAFNY					
	#LG	#LI	#A	#I	RW (s)	Chk (s)	#LI	#A	#I	#H	Chk (s)	
Two-Phase Commit	102	49	2	3	0.17	0.04	55	8	30	62	12.81	
Raft Leader Election	138	44	17	6	0.19	0.18	40	20	50	73	301.68	
Single-Decree Paxos	504	65	23	14	0.25	1.51	69	50	72	63	1141.35	
Total	744	158	42	23	0.61	1.73	164	78	152	198	1455.84	
Multi-Paxos KV	847	100	21	14	0.24	1.64	-	-	-	-	-	

BACKUP

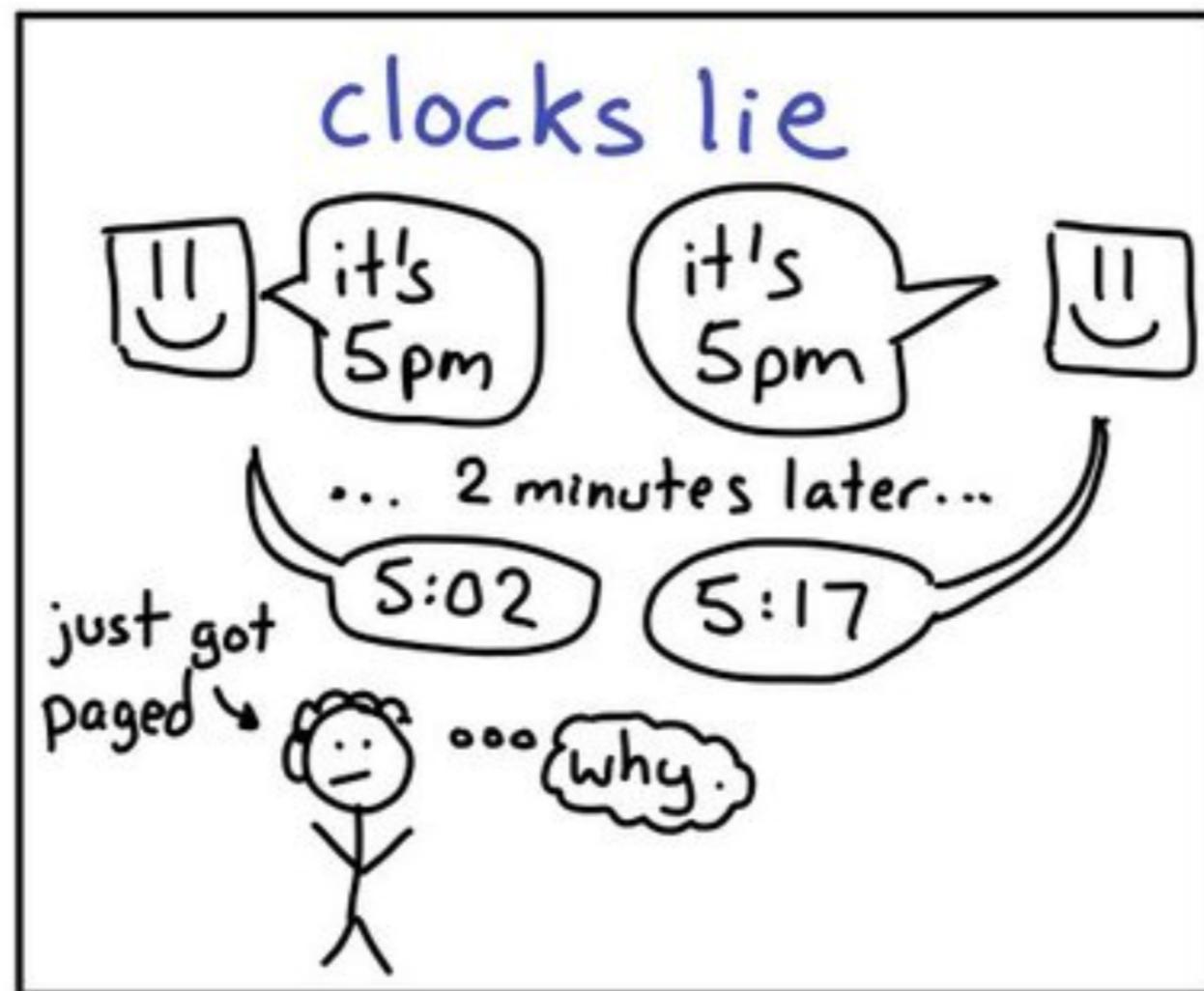


Tags
Data
Mutable Immutable

BACKUP

System	Throughput (req/ms)
<u>Goolong</u>	118.5
PSync	32.4
Ivy-Raft*	13.5
IronKV*	~ 30

BACKUP

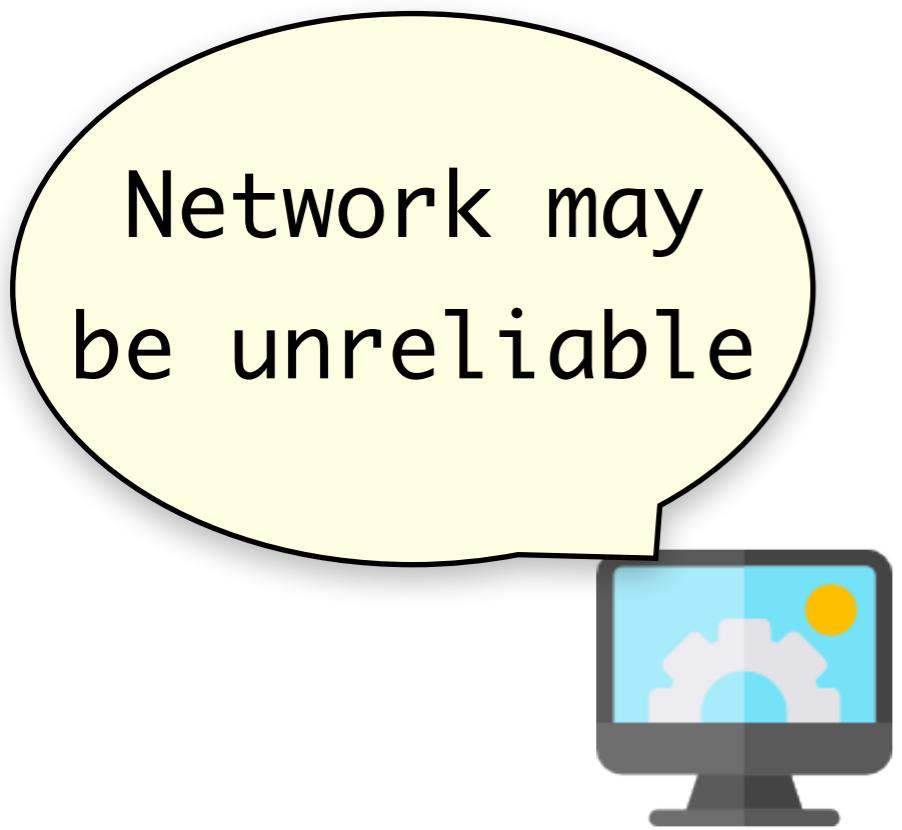


Problem 1: Asynchrony

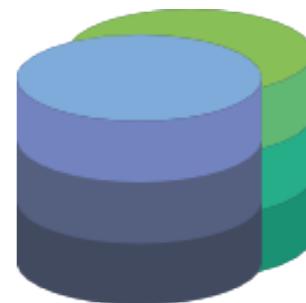
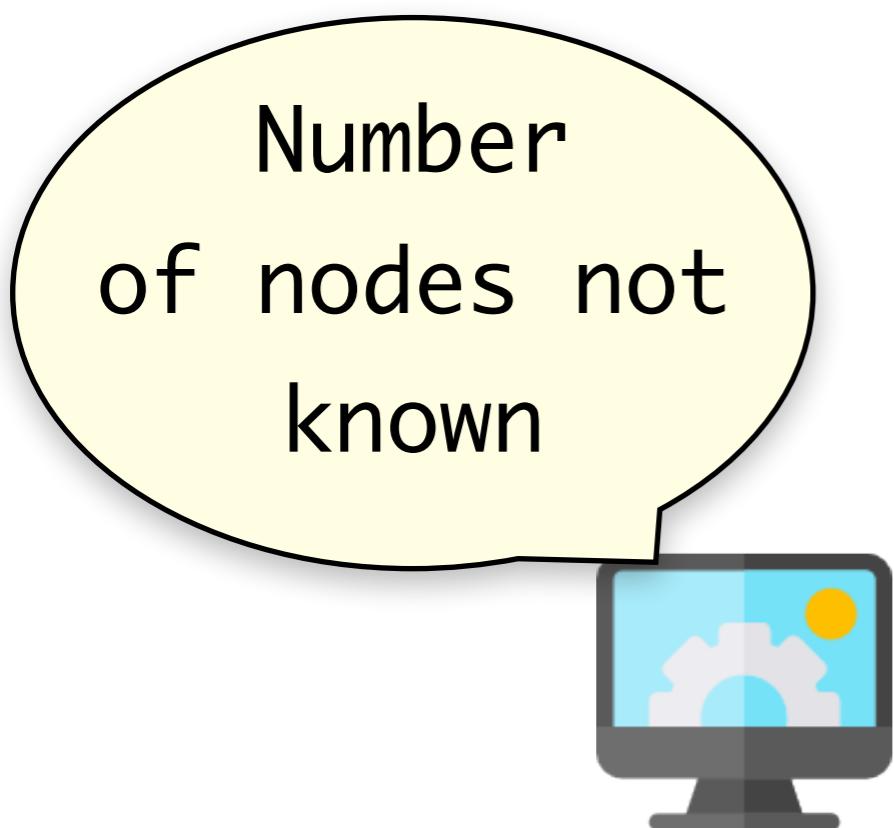
Processes/
messages have
different
speed



Problem 2: Message Drops



Problem 3: Parametrized



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How to make sure they don't?

Testing

Too
many
possibilitie

Model Checking

Infinite Number
of States:
No guarantees

Deductive
Proofs

