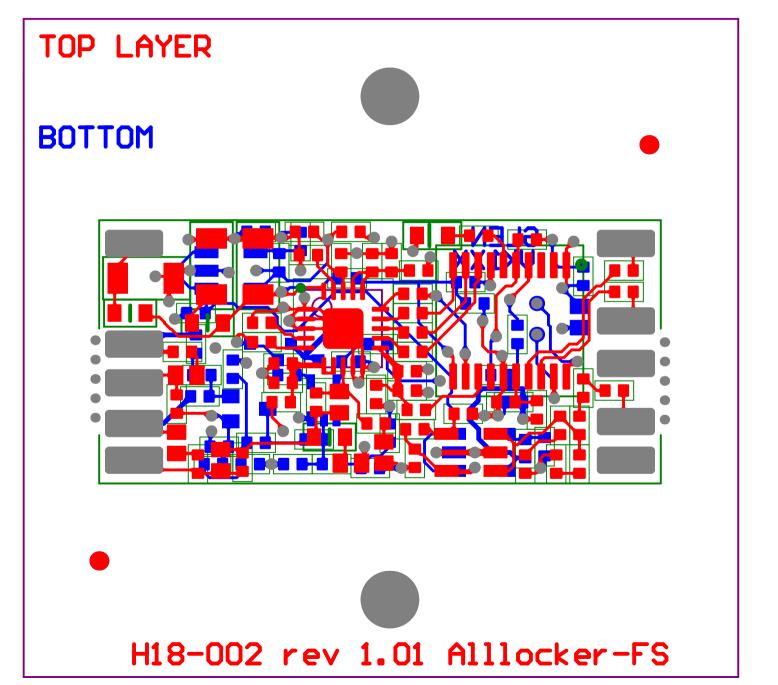


## H18-002-1.PCBDOC Outline MECH 5



TOP

## **BOTTOM**

Top layer .GTL
Plane 1 .GP1
Mid Layer L2 .G1
Bottom Layer .GBL

Outline .GM5 Tab .GM6