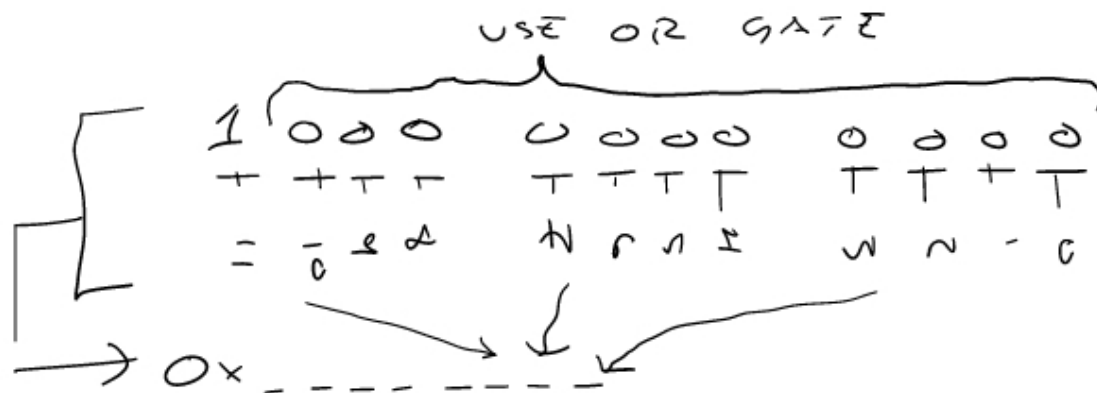


## STEP 1

\* BYPASS PLL WHILE INITIALIZING

↳ BYPASS2 IN BIT 11



0x00000800

RCC2 = | 0x00000800