STEPS   RUN THE 16MHZ CLOCK
           AT   80MHZ

① USE RBC2

② BYPASS THE PLL

③ SPECIFY FRQ IN XTAZ OT RCC

④ CLEAR PWRDN2 TO ACTIVATE PLL

⑤ ENABLE/CONFIGURE CLOCK DIVIDER
   USING SYSDIV2

⑥ WAIT FOR PLL TO STABILIZE

⑦ CONNECT/ENABLE PLL (CLEAR BYPASS2)