Туре	Function	Pin Def	Pin Number	Alternate Function
Power	GND	GND	22	
Power	GND	GND	33	
Power	GND	GND	47	
Power	Gnd	GND	54	
Power	GNDANA	GNDANA	7	
SPI Address	PA00	PA00	1	
SPI Address	PA01	PA01	2	
SPI Address	PA02	PA02	3	
SPI Address	PA03	PA03	4	
SPI Address	PA04	PA04	13	
Therm	PA05	PA05	14	AIN[13]
Slot 11	PA06	PA06	15	
Slot 11	PA07	PA07	16	
Slot 12	PA08	PA08	17	
Slot 12	PA09	PA09	18	
Slot 13	PA10	PA10	19	
Slot 13	PA11	PA11	20	
Slot 14	PA12	PA12	29	
Slot 14	PA13	PA13	30	
Slot 10	PA14	PA14	31	
Slot 10	PA15	PA15	32	
SPI	MOSI	PA16	35	
SPI	SCK	PA17	36	
SPI	SS	PA18	37	
SPI	MISO	PA19	38	
GIO Status	PA20	PA20	41	ExtInt(4)
Endstop Stat	PA21	PA21	42	ExtInt(5)
Slot 15	Pa22	Pa22	43	SDA
Slot 15	pa23	pa23	44	SCL
USB	USB_N	PA24	45	SCOM3
USB	USB_P	PA25	46	SCOM3
Slot 02	PA27	PA27	51	
Slot 02	PA28	PA28	53	
Programming	SWCLK	PA30	57	
Programming	SWDIO	PA31	58	
Therm	Pb00	Pb00		AIN(8)
Therm	PB01	PB01		AIN(9)
Therm	PB02	PB02	63	AIN(10)
Therm	PB03	PB03	64	AIN(11)
Slot 03	PB04	PB04	5	
Slot 03	PB05	PB05	6	
Slot 04	PB06	PB06	9	

Slot 04	PB07	PB07	10	
Slot 05	PB08	PB08	11	
Slot 05	PB09	PB09	12	
Slot 06	PB10	PB10	23	
Slot 06	PB11	PB11	24	
Slot 07	PB12	PB12	25	
Slot 07	PB13	PB13	26	
Slot 08	PB14	PB14	27	
Slot 08	PB15	PB15	28	
Slot 09	PB16	PB16	39	
Slot 09	PB17	PB17	40	
Slot 15	PB22	PB22	49	
Slot 15	PB23	PB23	50	
Slot 01	PB30	PB30	59	
Slot 01	PB31	PB31	60	
Programming	EDBG Reset	RESETN	52	
Power	VDDANA	VDDANA	8	
Power	Vddcore	Vddcore	55	
Power	Vddin	Vddin	56	
Power	VDDIO	VDDIO	21	
Power	VDDIO	VDDIO	34	
Power	VDDIO	VDDIO	48	