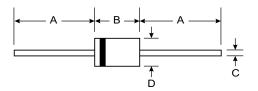


MUR140 - MUR160

1.0A SUPER-FAST RECTIFIER

Features

- Glass Passivated Die Construction
- Super-Fast Recovery Time For High Efficiency
- Low Forward Voltage Drop and High Current Capability
- Surge Overload Rating to 35A Peak
- Ideally Suited for Automated Assembly
- Plastic Material: UL Flammability Classification Rating 94V-0



Mechanical Data

- Case: Molded Plastic
- Terminals: Solder Plated Terminal -Solderable per MIL-STD-202, Method 208
- Marking: MUR140: R140
 - MUR160: R160
- Polarity: Cathode Band
- Weight: 0.35 grams (approx.)
- Mounting Position: Any

DO-41 Plastic				
Dim	Min	Max		
Α	25.40	_		
В	4.06	5.21		
С	0.71	0.864		
D	2.00	2.72		
All Dimensions in mm				

Maximum Ratings and Electrical Characteristics

@ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		MUR140	MUR160	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	400	600	V
RMS Reverse Voltage	V _{R(RMS)}	283	424	V
Average Rectified Output Current @ T _T = 120°C		1.0		Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load (JEDEC Method)		35		Α
Forward Voltage @ \mathbb{k} = 1.0A, T_J = 25°C @ I_F = 1.0A, T_J = 150°C	V _{FM}	1.25 1.05		V
Peak Reverse Current @ T _A = 25°C at Rated DC Blocking Voltage @ T _A = 150°C	I _{RM}	5. 15		μА
Reverse Recovery Time (Note 2)	t _{rr}	50		ns
Reverse Recovery Time (Note 3)	t _{rr}	75		ns
Forward Recovery Time (Note 4)		50		ns
Typical Junction Capacitance (Note 1)		45		pF
Typical Thermal Resistance, Junction to Ambient		72		K/W
Operating and Storage Temperature Range		-65 to +175		°C

Notes

- 1. Measured at 1.0MHz and applied reverse voltage of 0V DC.
- 2. Measured with $I_F = 0.5A$, $I_R = 1.0A$, $I_{rr} = 0.25A$. See Figure 5.
- 3. Measured with $I_F = 1A$, di/dt = 50A/us.
- 4. Measured with $I_F = 1.0A$, di/dt = 100A/ μ s, Duty Cycle $\leq 2.0\%$.

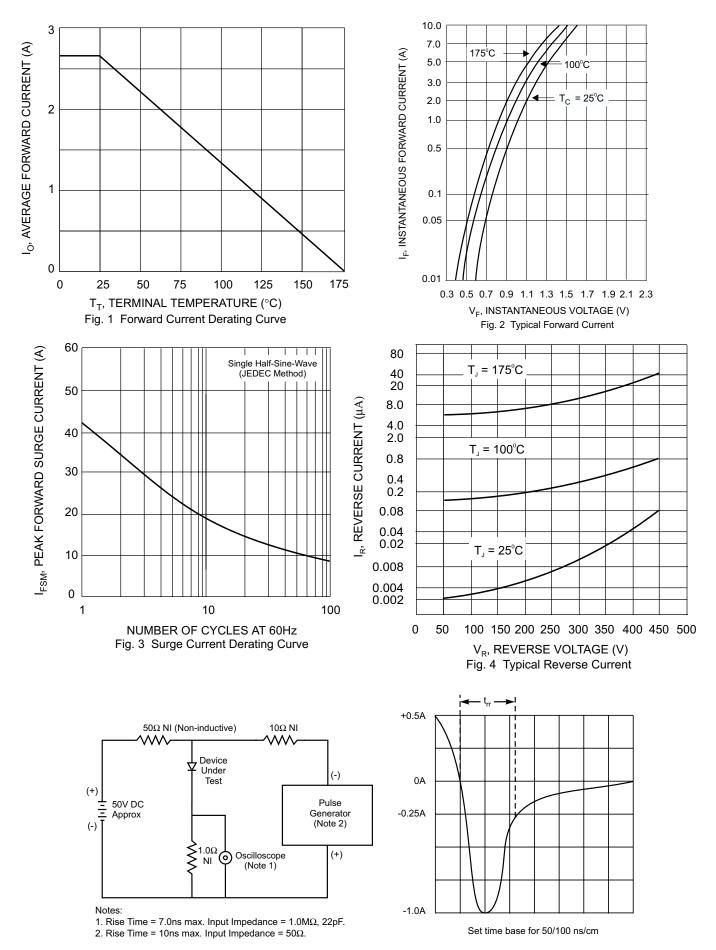


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit