

External Memory Interface

MicropendousX-LPC182x-GPS-EMI.sch

USB

MicropendousX-LPC182x-GPS-USB.sch

System

MicropendousX-LPC182x-GPS-System.sch

Position Sensors

MicropendousX-LPC182x-GPS-Position_Sensors.sch

RF

MicropendousX-LPC182x-GPS-RF.sch

LCD

MicropendousX-LPC182x-GPS-LCD.sch

Power Supply

MicropendousX-LPC182x-GPS-Power.sch

Expansion

MicropendousX-LPC182x-GPS-Expansion.sch

Vias

MicropendousX-LPC182x-GPS-Vias.sch

Notes:

- Design targets Revision 'A' of the LPC182x
- Pins with more than one function are labeled with their pin name and function(s) used
- VDD controls LPC182x, RF Transceiver, LCD Touchscreen Controller, and Motion Sensor so that these can wake up the device
- VDDIO powers microSD, GPS Module, and LCD Display along with all LPC182x IO pins so that these can be disabled in low-power modes.
- Only WAKEUP pin can wake up device from Deep Sleep and Power-Down Modes
- USB0 can wake up device only from Sleep Mode
- Power Consumption: Normal 10mA+, Sleep 5.5mA+, Other <1mA

<-- Double-click inside a sheet to go to it

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File: MicropendousX-LPC182x-GPS.sch

Sheet: /

Title: MicropendousX-LPC182x-GPS

Size: A4

Date: 8 jun 2012

Rev: 1.0

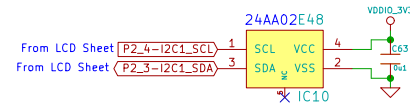
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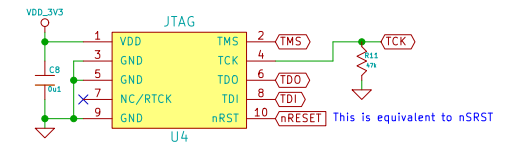
Notes:

- Run core system on always-on VDD_3V3
- Internal Rpu is minimum 45k Ohm, so for boot selection pull-down resistor, 19k is max. usable as $3.3(19/(19+45))=0.98V < V_{il}=(3.3*3)=1V$
- E48 EEPROM is used to give every node its own unique EUI-64 address

EUI-64 Node Identity

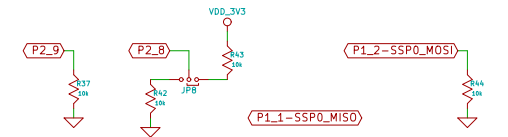


JTAG



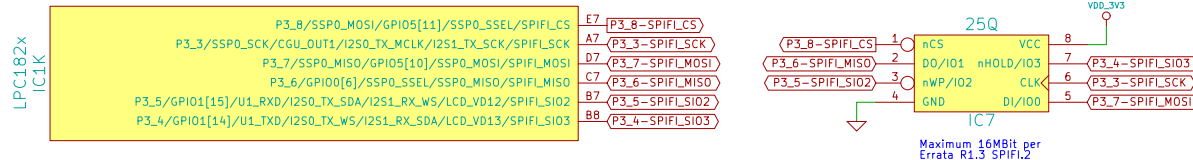
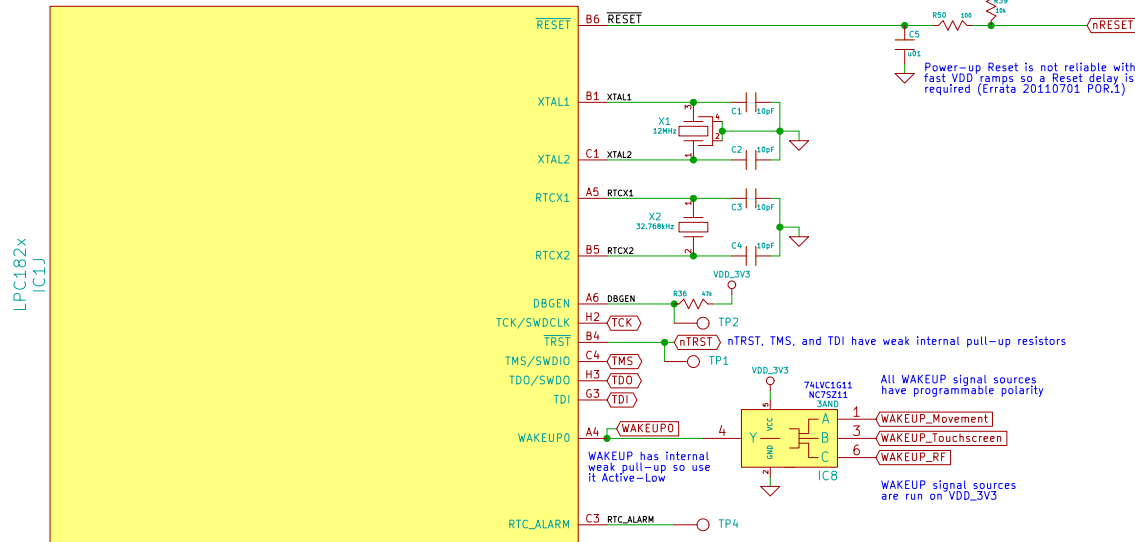
TD0 need not be pulled up since TCK pull-down ensures no random data is ever sent as TD0 is only valid when clock is valid.

Boot Mode Selection



Boot Mode	P2_9(A0)	P2_8(A8)	P1_1(A6)	P1_2(A7)
SPIFI (SPF)	LOW	LOW	HIGH	LOW
USB0 (USB)	LOW	HIGH	HIGH	LOW
USART0	LOW	LOW	LOW	LOW
EMC 8-Bit	LOW	LOW	LOW	HIGH
EMC 16-Bit	LOW	LOW	HIGH	HIGH
EMC 32-Bit	LOW	HIGH	LOW	LOW
USB1	LOW	HIGH	LOW	HIGH
SPI (SSP)	LOW	HIGH	HIGH	HIGH
USART3	HIGH	LOW	LOW	LOW

Note boot pins have weak internal pull-ups of about 50k Ohm to VDDIO
Taken from LPC18x0 Datasheet – Table 5



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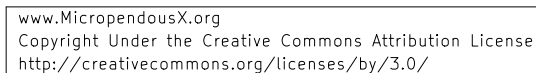
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File: MicropendousX-LPC182x-GPS-System.sch
Sheet: /System/

Title: MicropendousX-LPC182x-GPS

Size: A4 Date: 8 jun 2012 Rev: 1.0
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- Boot pins have weak internal pull-ups of about 50k Ohm to VDDIO
so VDDIO_3V3 must be enabled at the same time as VDD_3V3 at startup
- MODE_HIGH enables Burst Mode which is more efficient at lower power
- HPWR and SUSP have 2M Ohm weak pull-downs
- regulator outputs 3.3V nominal but lowering the
voltage to 3.0V will reduce power consumption and
be within all components' supply margins
- use low ESL reverse geometry X2R caps closest to device
- BAT54C reverse leakage is maximum 2uA



File: MicropendousX-LPC182x-GPS-Power.sch
Sheet: /Power Supply/

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Notes:

LPC182x
IC1A

P2_9/GPIO1[10]/CTOUT_3/U3_BAUD/EMC_A0
P2_10/GPIO0[14]/CTOUT_2/U2_TXD/EMC_A1
P2_11/GPIO1[11]/CTOUT_5/U2_RXD/EMC_A2
P2_12/GPIO1[12]/CTOUT_4/U2_UCLK/EMC_A3
P2_13/GPIO1[13]/CTIN_4/U2_DIR/EMC_A4
P1_0/GPIO0[4]/CTIN_3/SSP0_SSEL/EMC_A5
P1_1/GPIO0[8]/CTOUT_7/SSP0_MISO/EMC_A6
P1_2/GPIO0[9]/CTOUT_6/SSP0_MOSI/EMC_A7
P2_8/CTOUT_0/U3_DIR/GPIO5[7]/EMC_A8
P2_7/GPIO0[7]/CTOUT_1/U3_UCLK/T3_MAT3/EMC_A9
P2_6/U0_DIR/USB0_IND0/GPIO5[6]/CTIN_7/T3_CAP3/EMC_A10
P2_2/U0_UCLK/USB0_IND1/GPIO5[2]/CTOUT_6/T3_CAP2/EMC_A11
P2_1/U0_RXD/USB0_PWR_FAULT/GPIO5[1]/T3_CAP1/EMC_A12
P2_0/U0_TXD/USB0_PWR_EN/GPIO5[0]/T3_CAP0/ENET_MDC/EMC_A13

P1_7/GPIO1[0]/U1_DSR/CTOUT_13/USB0_PWR_EN/EMC_D0
P1_8/GPIO1[1]/U1_DTR/CTOUT_12/MMC_VOLT0/EMC_D1
P1_9/GPIO1[2]/U1_RTS/CTOUT_11/MMC_DAT0/EMC_D2
P1_10/GPIO1[3]/U1_RI/CTOUT_14/MMC_DAT1/EMC_D3
P1_11/GPIO1[4]/U1_CTS/CTOUT_15/MMC_DAT2/EMC_D4
P1_12/GPIO1[5]/U1_DCD/T0_CAP1/MMC_DAT3/EMC_D5
P1_13/GPIO1[6]/U1_TXD/T0_CAP0/MMC_CD/EMC_D6
P1_14/GPIO1[7]/U1_RXD/T0_MAT2/EMC_D7

P1_6/GPIO1[9]/CTIN_5/MMC_CMD/EMC_WE
P1_3/GPIO0[10]/CTOUT_8/USB0_IND1/SSP1_MISO/MMC_RST/EMC_OE
P1_5/GPIO1[8]/CTOUT_10/USB0_PWR_FAULT/SSP1_SSEL/MMC_POW/EMC_CS0
P1_4/GPIO0[11]/CTOUT_9/USB0_IND0/SSP1_MOSI/MMC_VOLT1/EMC_BLS0
P6_4/GPIO3[3]/CTIN_6/U0_TXD/EMC_CAS
P6_5/GPIO3[4]/CTOUT_6/U0_RXD/EMC_RAS

CLK0/EMC_CLK0/CLKOUT/MMC_CLK/EMC_CLK01/ENET_TX_CLK/ENET_REF_CLK/SSP1_SCK
CLK2/CLKOUT/MMC_CLK/I2S1_RX_SCK/I2S0_TX_MCLK/EMC_CLK23/EMC_CLK3

LPC182x
IC1E

PF_4/SSP1_SCK/GP_CLKIN/TRACECLK/I2S0_RX_SCK/LCD_CLKIN/I2S0_TX_MCLK
P3_0/I2S0_RX_MCLK/I2S0_TX_MCLK/SSP0_SCK/I2S0_RX_SCK/I2S0_TX_SCK
P3_2/CAN0_TD/USB1_IND0/GPIO5[9]/LCD_VD14/I2S0_RX_SDA/I2S0_TX_SDA
P3_1/CAN0_RD/USB1_IND1/GPIO5[8]/LCD_VD15/I2S0_RX_WS/I2S0_TX_WS

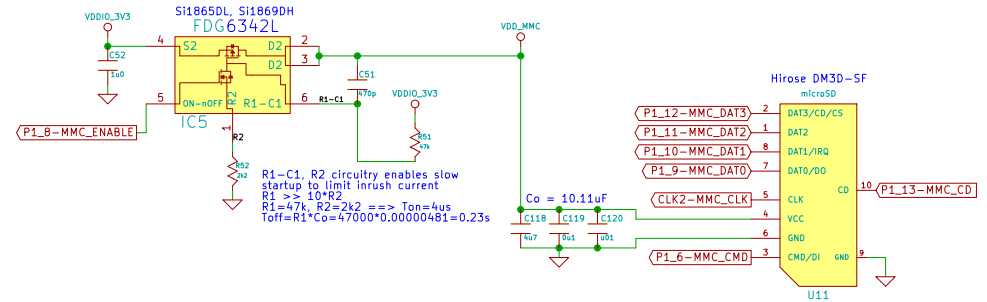
xP6_0/I2S0_RX_MCLK/I2S0_RX_SCK
xP6_2/GPIO3[1]/EMC_CKEOUT1/U0_DIR/T2_CAP1/I2S0_RX_SDA
xP6_1/GPIO3[0]/EMC_DYCS1/U0_UCLK/T2_CAP0/I2S0_RX_WS

B10 (P2_9) Used in System Sheet
E8 (P2_10-GPIO0_14) Used in RF Sheet
A9 (P2_11-GPIO1_11) Used in Power Supply Sheet
B9 (P2_12-GPIO1_12) Used in RF Sheet
A10 (P2_13-GPIO1_13) Used in RF Sheet
H1 (P1_0-SSP0_SSEL) Used in RF Sheet
K2 (P1_1-SSP0_MISO) Used in RF and System Sheets
K1 (P1_2-SSP0_MOSI) Used in RF and System Sheets
C6 (P2_8) Used in System Sheet
C10 (P2_7-ISP) TP3 P2_7 has weak internal pull-up to disable ISP
G9 (P2_6-GPIO5_6) Used in Position Sensors Sheet
F5 (P2_2-GPIO5_2) Used in Position Sensors Sheet
G7 (P2_1-U0_RXD) Used in Position Sensors Sheet
G10 (P2_0-U0_TXD) Used in Position Sensors Sheet

G4
H5 (P1_8-MMC_ENABLE)
J5 (P1_9-MMC_DAT0)
H6 (P1_10-MMC_DAT1)
J7 (P1_11-MMC_DAT2)
K7 (P1_12-MMC_DAT3)
H8 (P1_13-MMC_CD)
J8

K4 (P1_6-MMC_CMD)
J1
J4 (P1_5-GPIO1_8) Used in Position Sensors Sheet
J2 (P1_4-GPIO0_11) Used in Position Sensors Sheet
F6 (P6_4-GPIO3_3) Used in Power Supply Sheet
F9 (P6_5-GPIO3_4) Used in Power Supply Sheet

K3
K6 (CLK2-MMC_CLK)



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Sheet: /External Memory Interface/

Title: MicropendousX-LPC182x-GPS

Size: A4

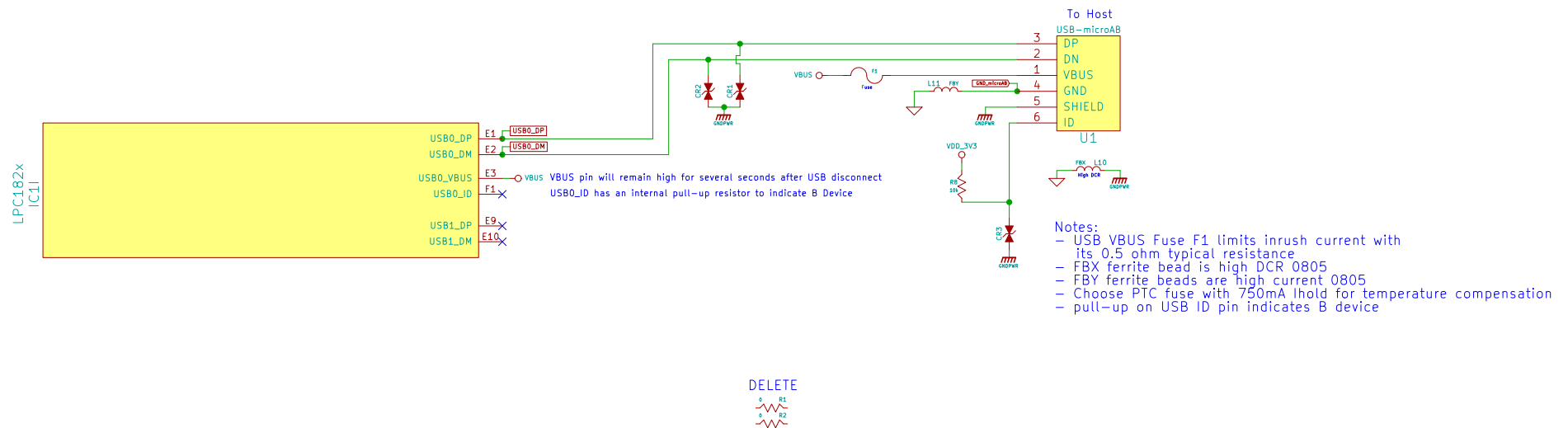
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USB Device Mode



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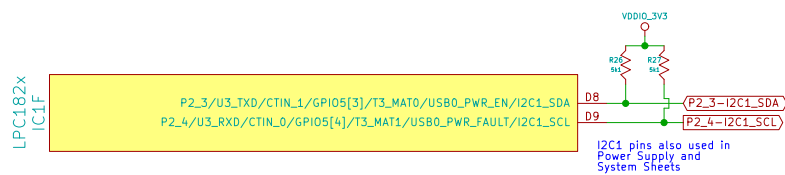
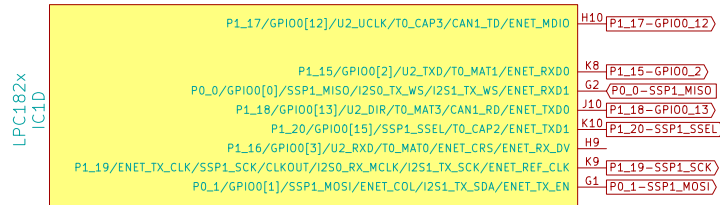
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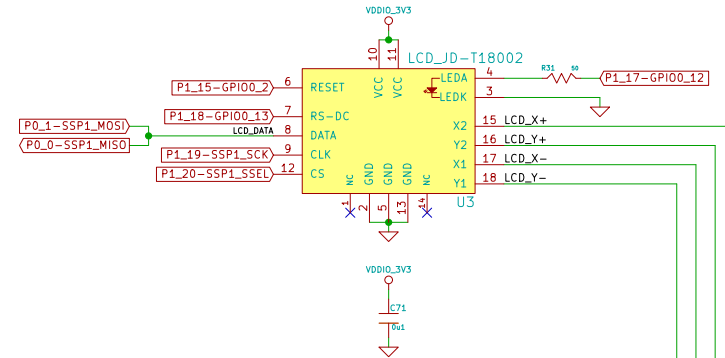
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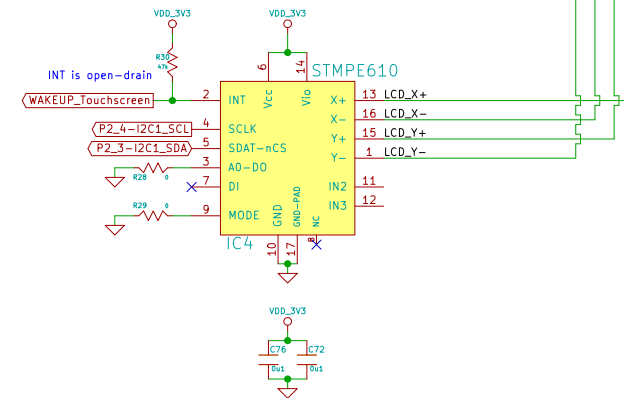
- Enable the Touchscreen controller to wake up the processor
- Run STMPE610 on always-on VDD to enable Touchscreen wakeup
- Run_LCD on VDDIO since it does not need to always be on and has leakage currents
- P1_7-GPIO0_12 is a High Drive Strength Pin that can source 32mA to the LCD Backlight LED



LCD and Resistive Touchscreen



Resistive Touchscreen Controller



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File: MicropendousX-LPC182x-GPS-LCD.sch

Sheet: /LCD/

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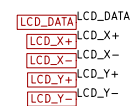
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Date: 8 jun 2012

Rev: 1.0

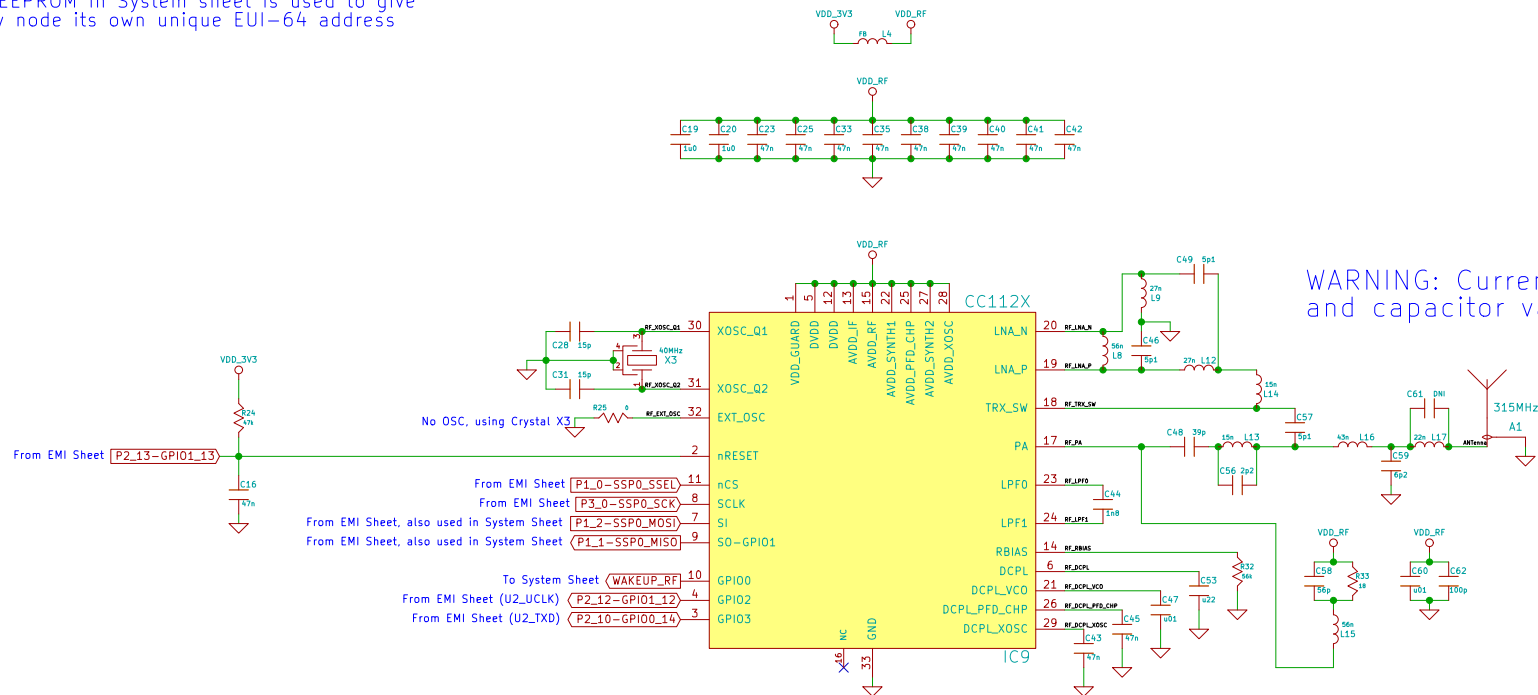
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Notes:

- Target 315MHz RF Band (Short Data Packets, No Streaming, Limited Power) for Sensor Nodes
- Use to transfer Identification and Control signals between nodes
- Refer to Linx Application Note AN-00125 for FCC15.231 A-D Overview
- CC1125 DS 1.12: $315/40=7.9$, $40*8=320$, $(320-315)=5>1\text{MHz}$ TX, similar for XOSC/2 for RX
- Use always-on VDD for power to enable Wake-On-Radio function
- MISO and MOSI are used in System sheet to define boot order
- MISO (P1_1) has a 50k pull-up
- MOSI (P1_2) has a 10k pull-down
- All capacitors should be X7R or COG/NPO
- For CC1120, use a 32MHz X3
- E48 EEPROM in System sheet is used to give every node its own unique EUI-64 address



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File: MicropendousX-LPC182x-GPS-RF.sch

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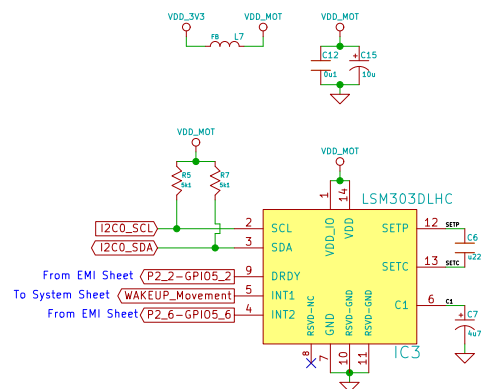
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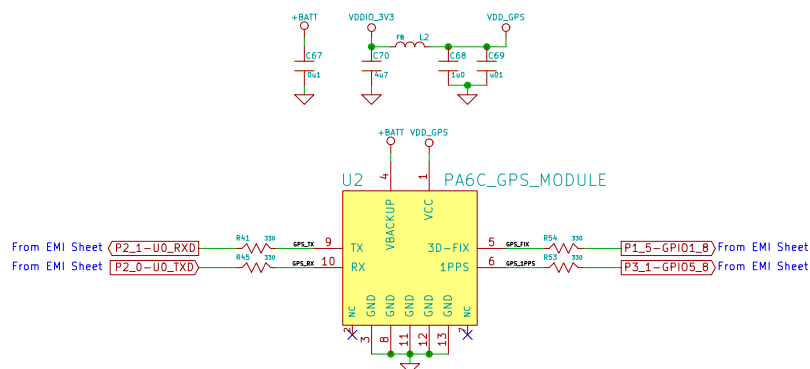
Notes:

- separate the LSM303 from all other electronics
- do not place a GND plane underneath the LSM303
- keep inductors and ferrites very far from the LSM303
- avoid ferrous materials like nickel near the LSM303
- X?R ceramic capacitor materials are ferrous, use COG/NP0 or tantalum
- set LSM303 INT1 interrupt to Active-Low wakeup the LPC182x
- LSM303 is powered by always-on VDD_3V3 to enable the LSM303 to wake up the LPC182x when in motion
- PA6C is powered by VDDIO_3V3 so that it can be turned off when the device is not in motion
- PA6C VBACKUP is 2V to 4.3V so direct Li-Ion power is OK
- The 1PPS signal is connected to an interruptable GPIO pin so that it can be used as a global timebase for RF

Motion and Orientation Sensor



GPS



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Notes:

- there isn't enough room on the PCB for a full expansion header
- these 3 analog pins and some GPIO are exposed as unmasked vias

LPC182x
IC1H

P2_5/CTIN_2/USB1_VBUS/GPIO5[5]/T3_MAT2/USB0_IN0/ADCTRIG1
ADC0_0/ADC1_0/DAC
ADC0_1/ADC1_1
ADC0_2/ADC1_2
ADC0_3/ADC1_3

D10-ADCTRIG1

A2-ADC0_0

A1-X

B3

A3-ADC0_3

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