

Guangyu Meng Resume

Education

- Washington University in St. Louis** *M.S. in Computer Science (GPA:4.0/4.0)* 01/2020-Present
- University of Southampton** *M.S. in Microelectronics System Design (Classification with Merit Degree)* 09/2011-12/2012
- Dissertation Title: Modeling an Artificial Foot and Researching the Position of Sensors
- University of Birmingham** *B.E. in Electronic Engineering (Honors Class II (division I) Degree)* 09/2010-07/2011
- Dissertation Title: Researching Bandpass Filter in High Frequency
- Huazhong University of Science & Technology** *B.E. in Electronic Science and Engineering (GPA: 3.7/4.0)* 09/2007-07/2010
- Graduate Project: Constructing a Variable Pulse Width Laser Detection System

Research

- Guangyu Meng, "Research on denoise by VAE-SRGAN", accepted by *IEEE International Conference on Control and Computer Vision (ICCCV)*
- Guangyu Meng, Zhiqian Chen, "Wavelets on Graph Neural Networks for analysis of traffic", in processing
- Guangyu Meng, Jiaming Liu, U. S. Kamilov, "Generative adversarial priors for image reconstruction", in processing

Work and Internship Experience

- Senior Software Engineer and Team Leader, Mentor Graphics Co., Ltd, Shenzhen Office** 04/2016 - 11/2019
Mentor, a Siemens business, a leader in electronic design automation to enable companies to develop electronic products faster and more cost-effectively.
- Led the project of providing high-performance system verification solutions for the world's first commercial 5G chipset designed by Huawei Technologies Co., Ltd
 - Optimized Huawei's testing code by transforming sequential execution to parallel execution, reducing runtime from 45 minutes to less than 8 minutes
 - Promoted the company's new licensed technology (Veloce Coverage App) for code coverage; gave presentations on the annual technical forum at the Silicon Valley Headquarter and took charge of the training programming for Huawei
 - Liaised between the Shenzhen office and the headquarters in Silicon Valley and Europe; travelled to Fremont as a technical support representative and guest speaker
- ASIC Design Engineer, Spreadtrum Communications Co., LTD, Shanghai Office** 01/2015-04/2016
- Built up and maintained module-level and chip-level emulation environment; leveraged popular simulation acceleration technology (like Palladium or Protium prototyping) in building modularized and scalable simulation platform
 - Developed an emulation resource allocation program in Python to facilitate resource management, resulted in 20% increase in device utilization rate; developed a Desktop GUI application to help engineers from project team to conveniently use specific commands of our systems, freed our engineers from answering basic questions repeatedly
- New Product Introduction (NPI) Engineer, Freescale Semiconductor, Inc., Shanghai Office** 01/2013-01/2015
- Responsible for product line management, new package assembly design, and packaging process optimization
 - Handled wafer mapping process; improved die bond machine capacity by 14.8% (from 3k to 3.45 die/hour)
- Software Engineer Intern, Tencent Holdings, Shenzhen Headquarter** 07/2011-08/2011
- Developed a multi-thread web crawler in Python based on Scrapy framework to gather information about the online advertising industry; wrote Python and SQL code to cleanse, format, and store data as records in MySQL database
 - Built a linear regression model to predict the 10-year growth rate of overall market size and Tencent's market share

Certificates and Awards in Artificial Intelligence

- Ranked 42/1717 in the "Global Wheat Detection" (still in processing) hosted by *Kaggle*. 05/2020-present
- Ranked the top 1% in the *Tencent Advertising Algorithm Competition* hosted by *Tencent Holdings* 03/2018-07/2018
- Obtained *Certificate of Achievement* from *Baidu* by passing the "ABC" (AI, BigData, Cloud) qualification test 09/2018
- **Technical Skills:** Python, Java, Perl, C/C++, MATLAB, VHDL, Verilog, System C, System Verilog
- GRE: 152(verbal)+170(Quantitative)+3.0(Analytical Writing) TOFEL: 100: R 28, L 26, S 22, W 24